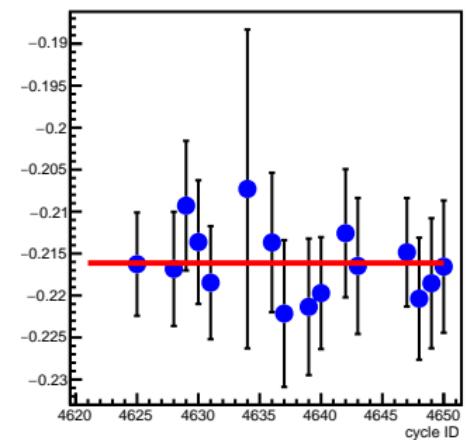
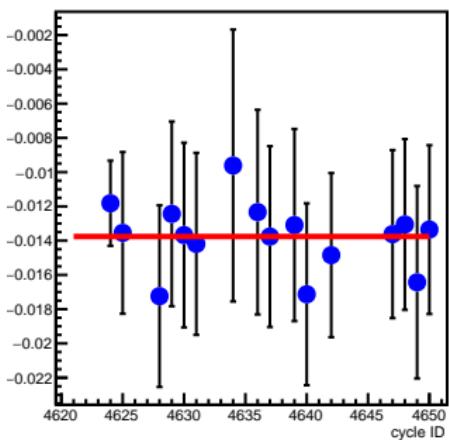


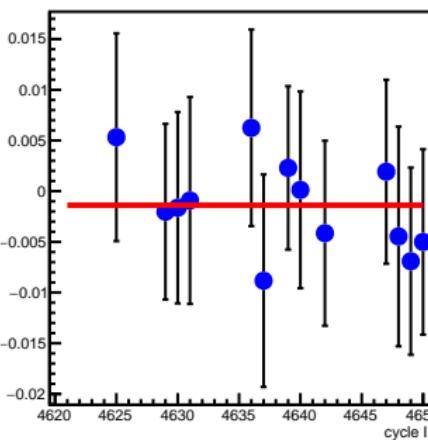
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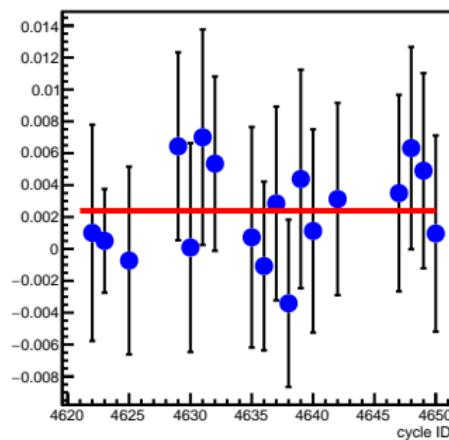
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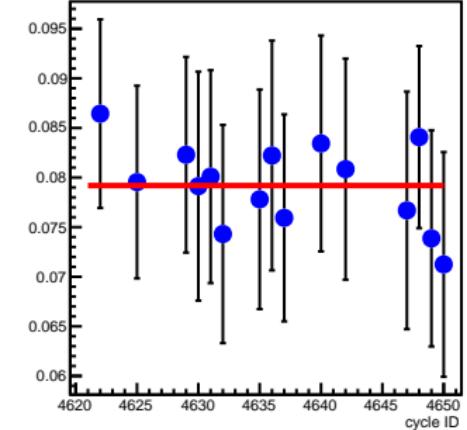
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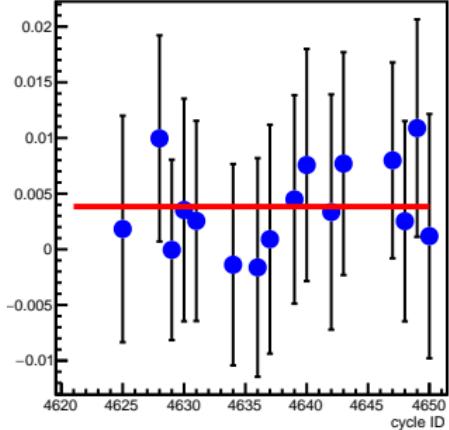
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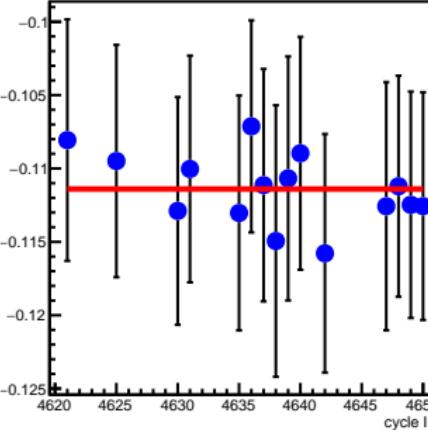
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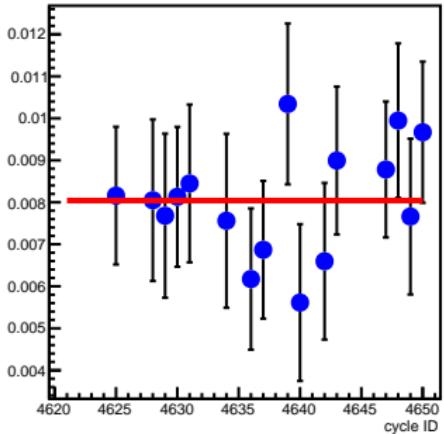
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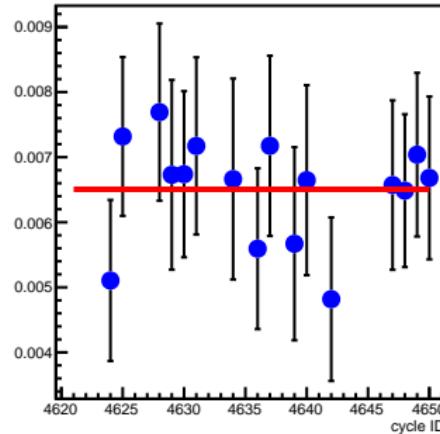
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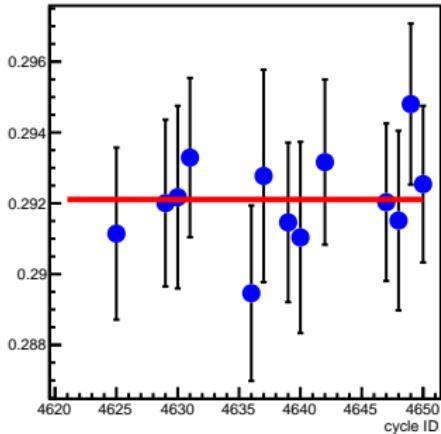
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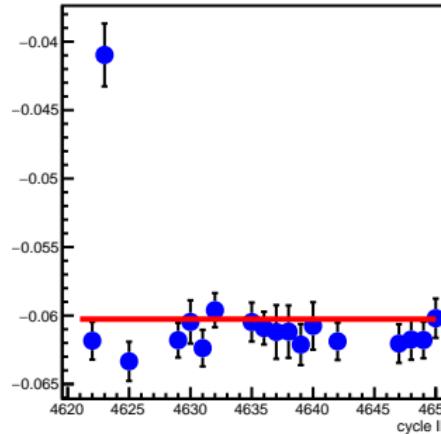
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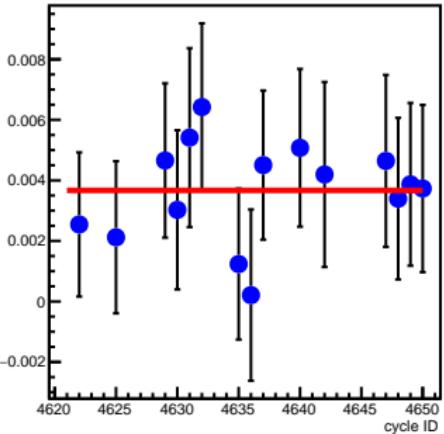
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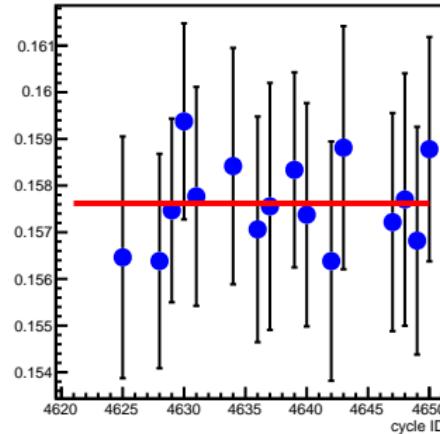
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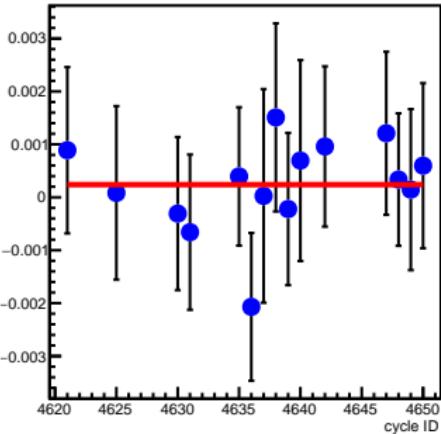
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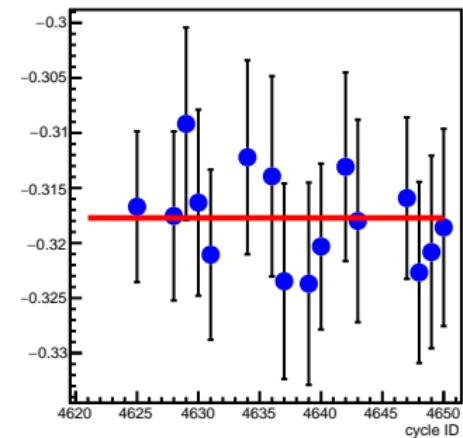
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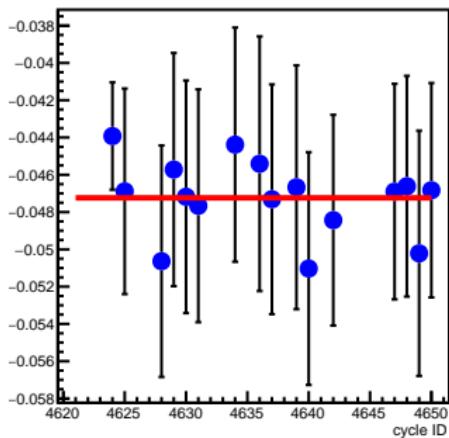
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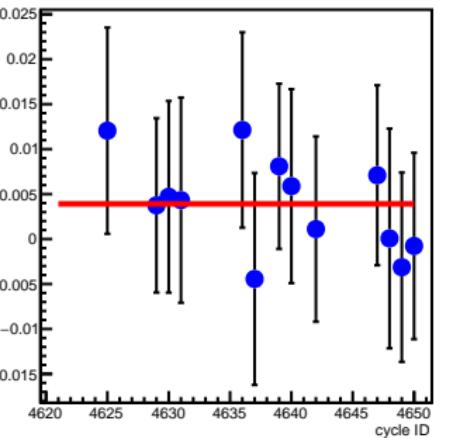
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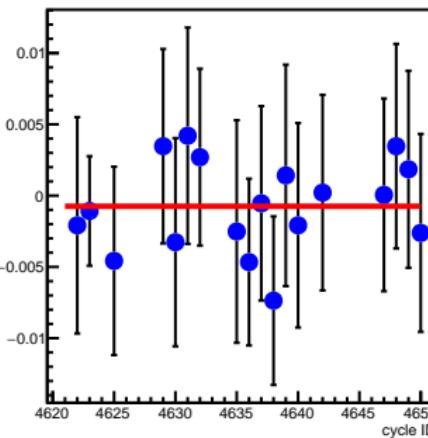
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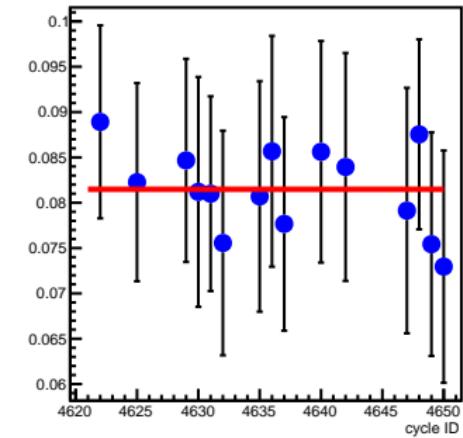
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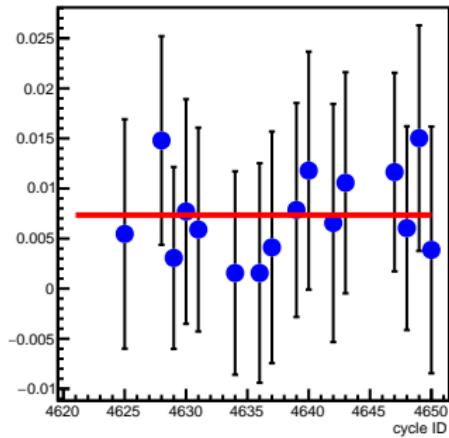
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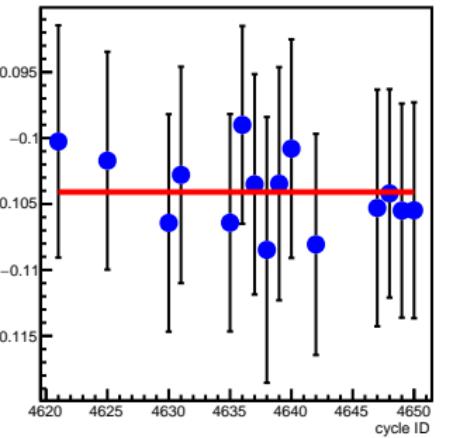
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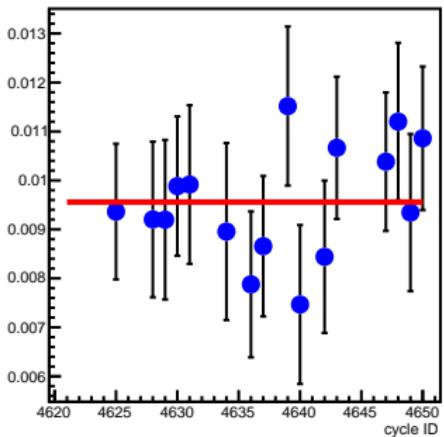
bpm4eX_coil2(um/count)



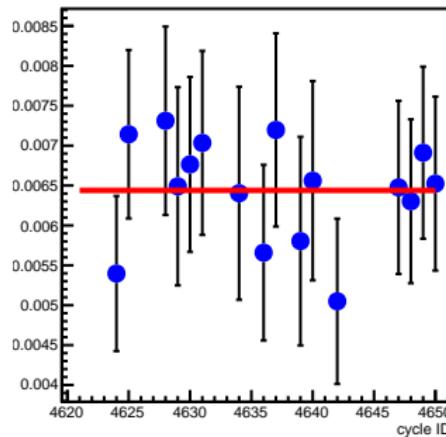
bpm4eX_coil5(um/count)



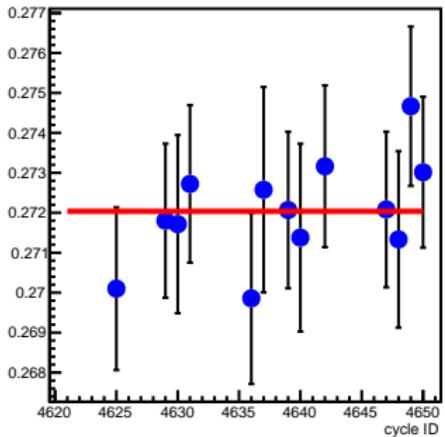
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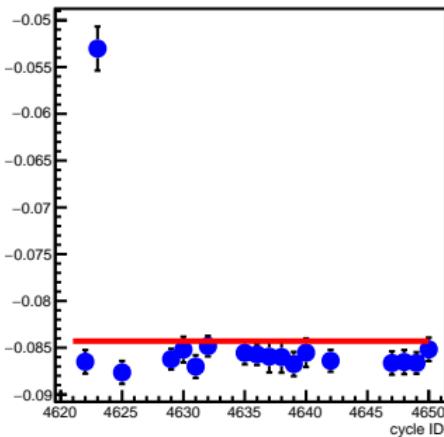
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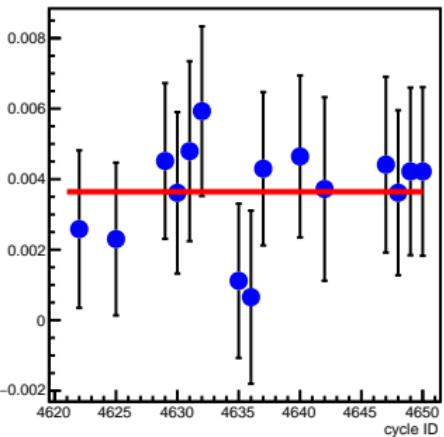
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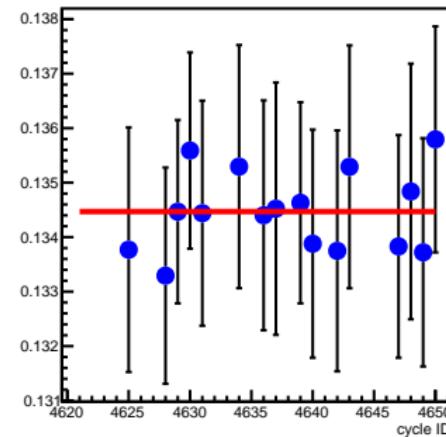
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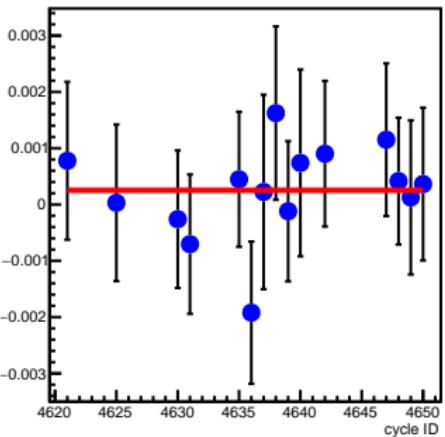
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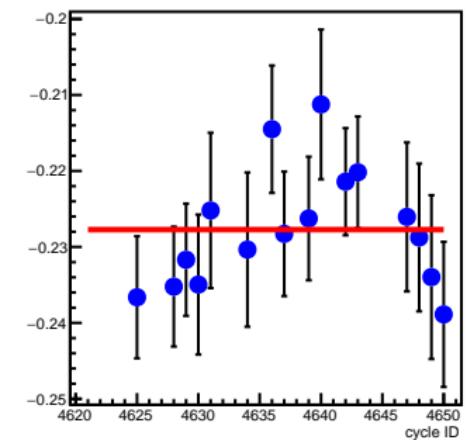
bpm4eY_coil2(um/count)



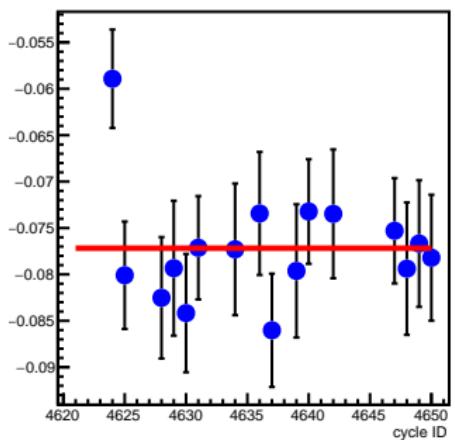
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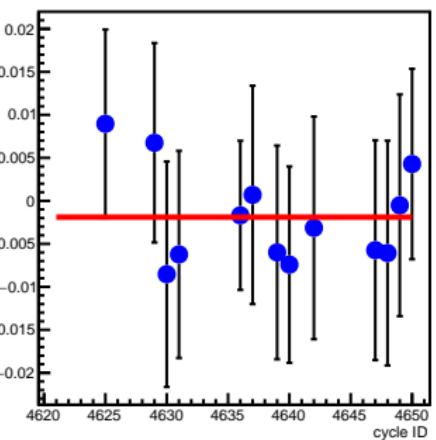
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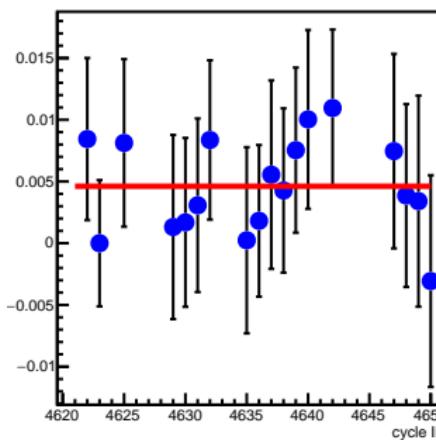
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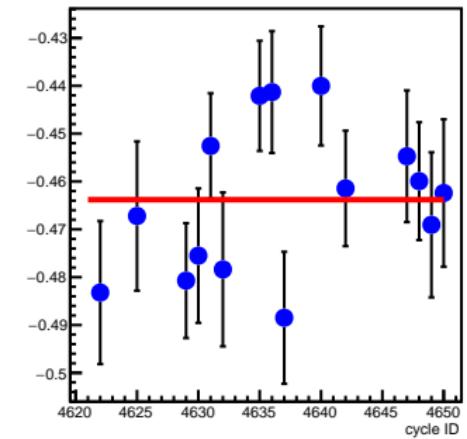
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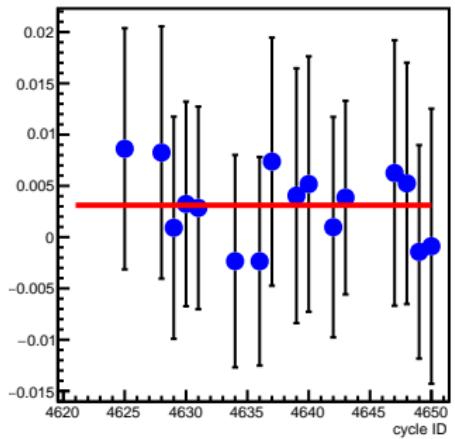
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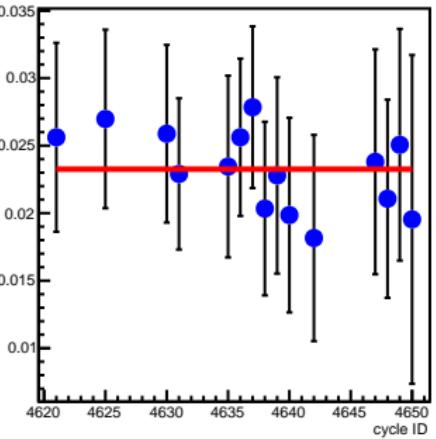
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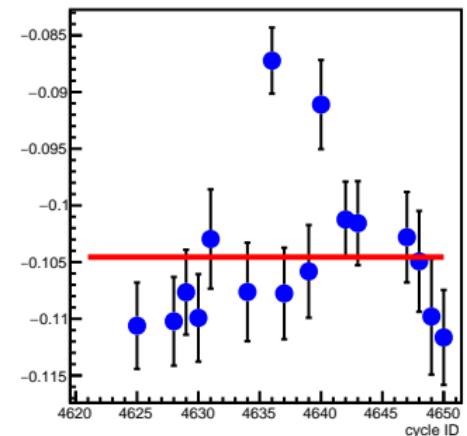
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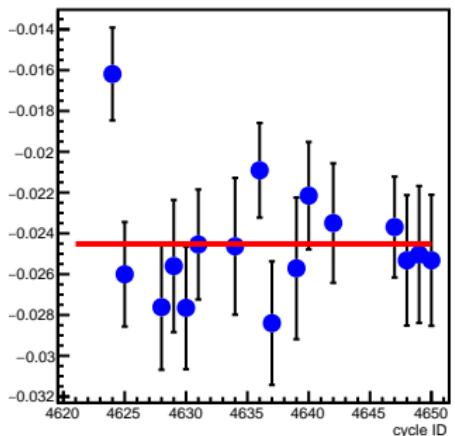
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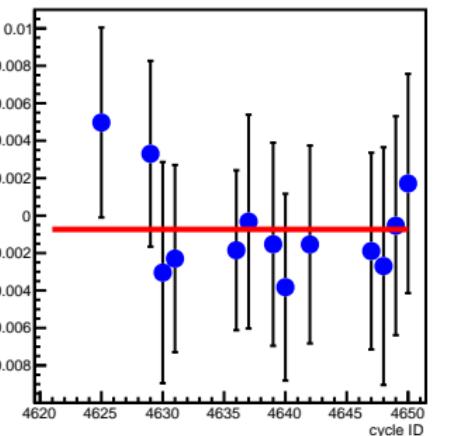
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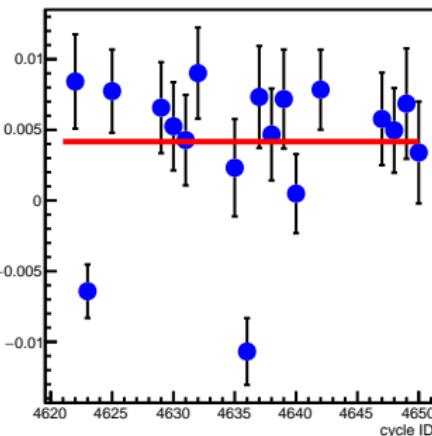
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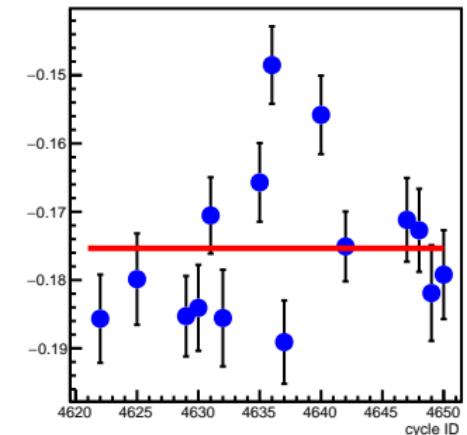
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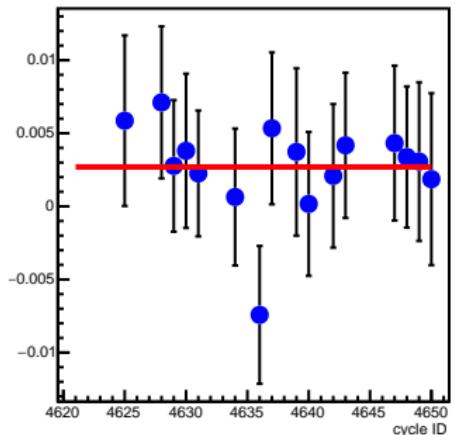
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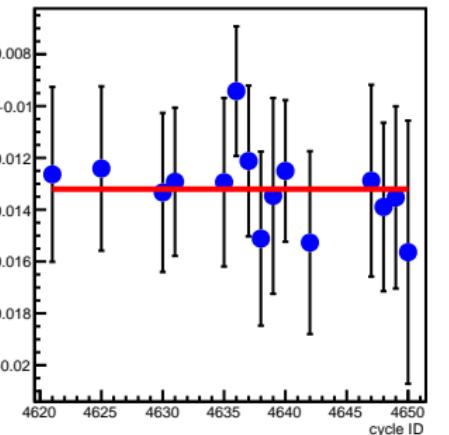
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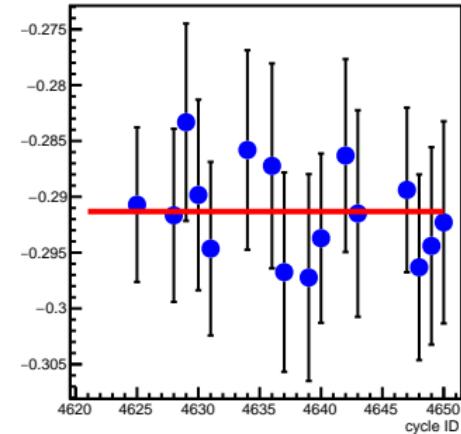
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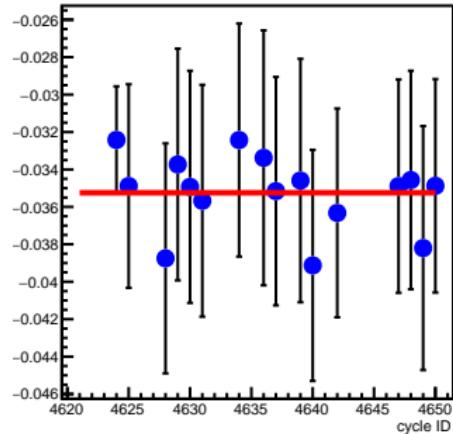
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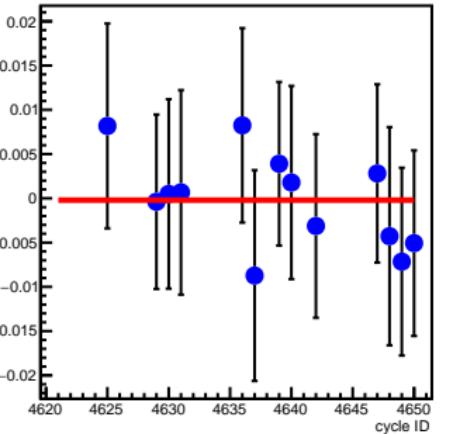
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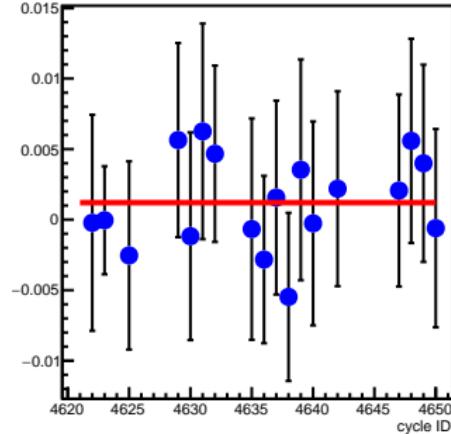
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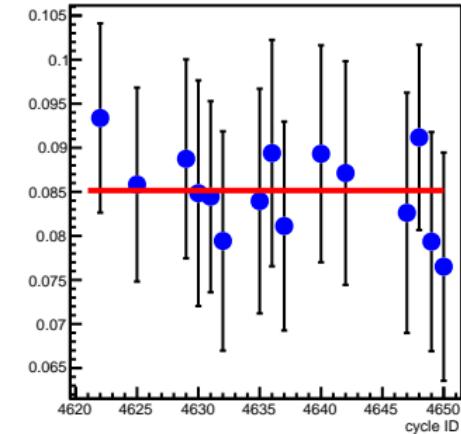
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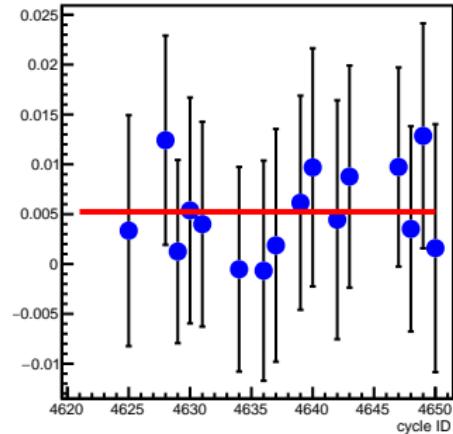
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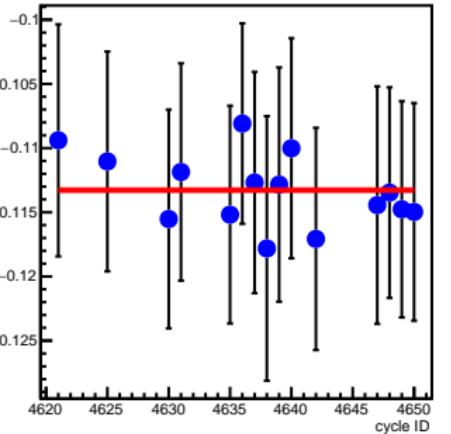
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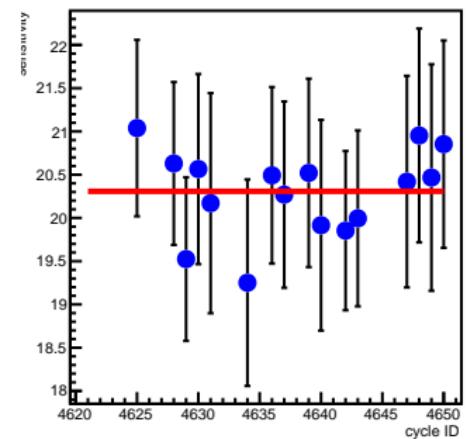
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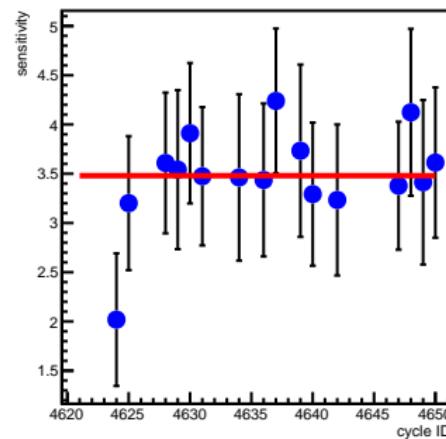
bpm4aX_coil5(um/count)



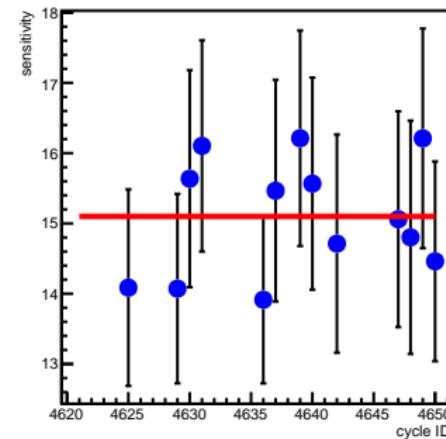
usl_coil1(ppm/count)



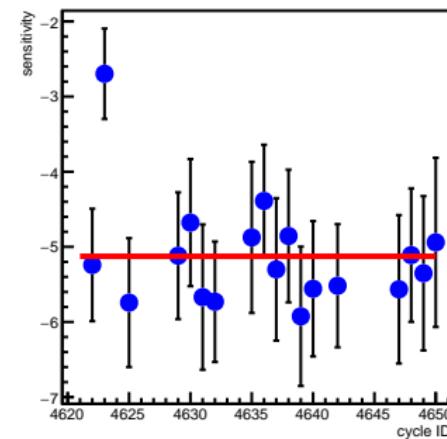
usl_coil3(ppm/count)



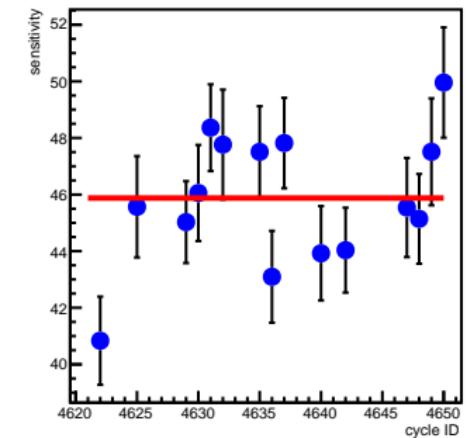
usl_coil4(ppm/count)



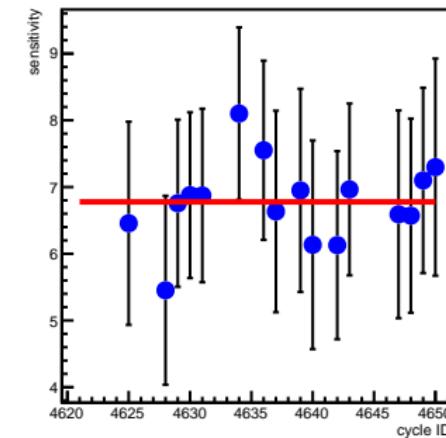
usl_coil6(ppm/count)



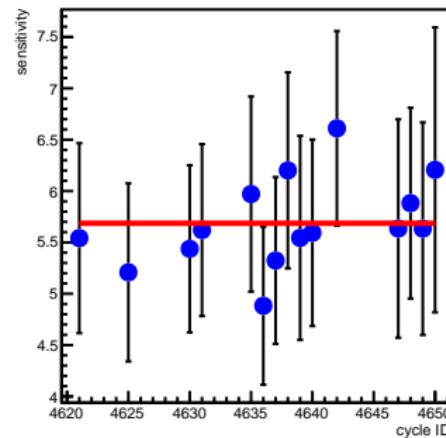
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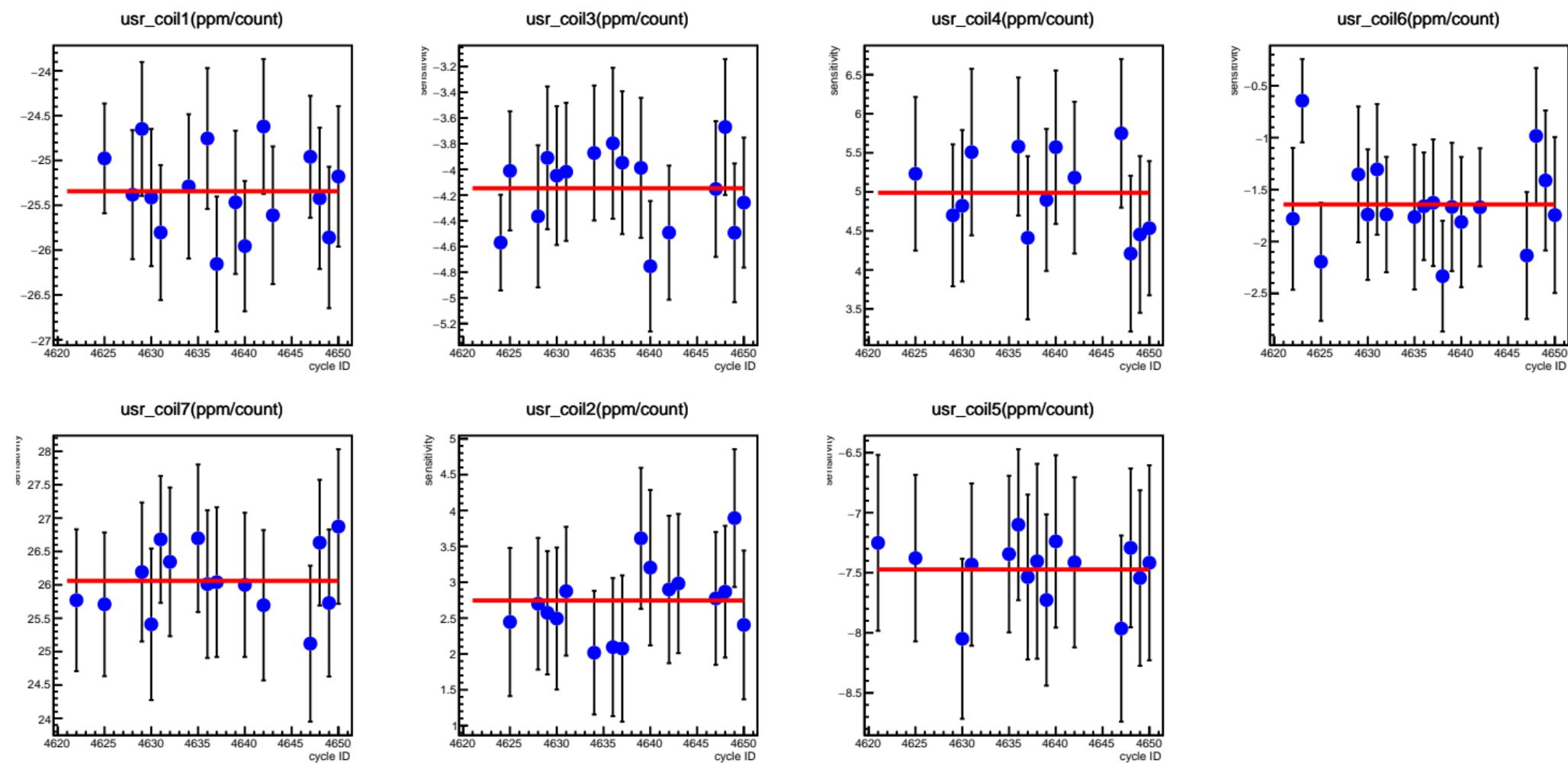


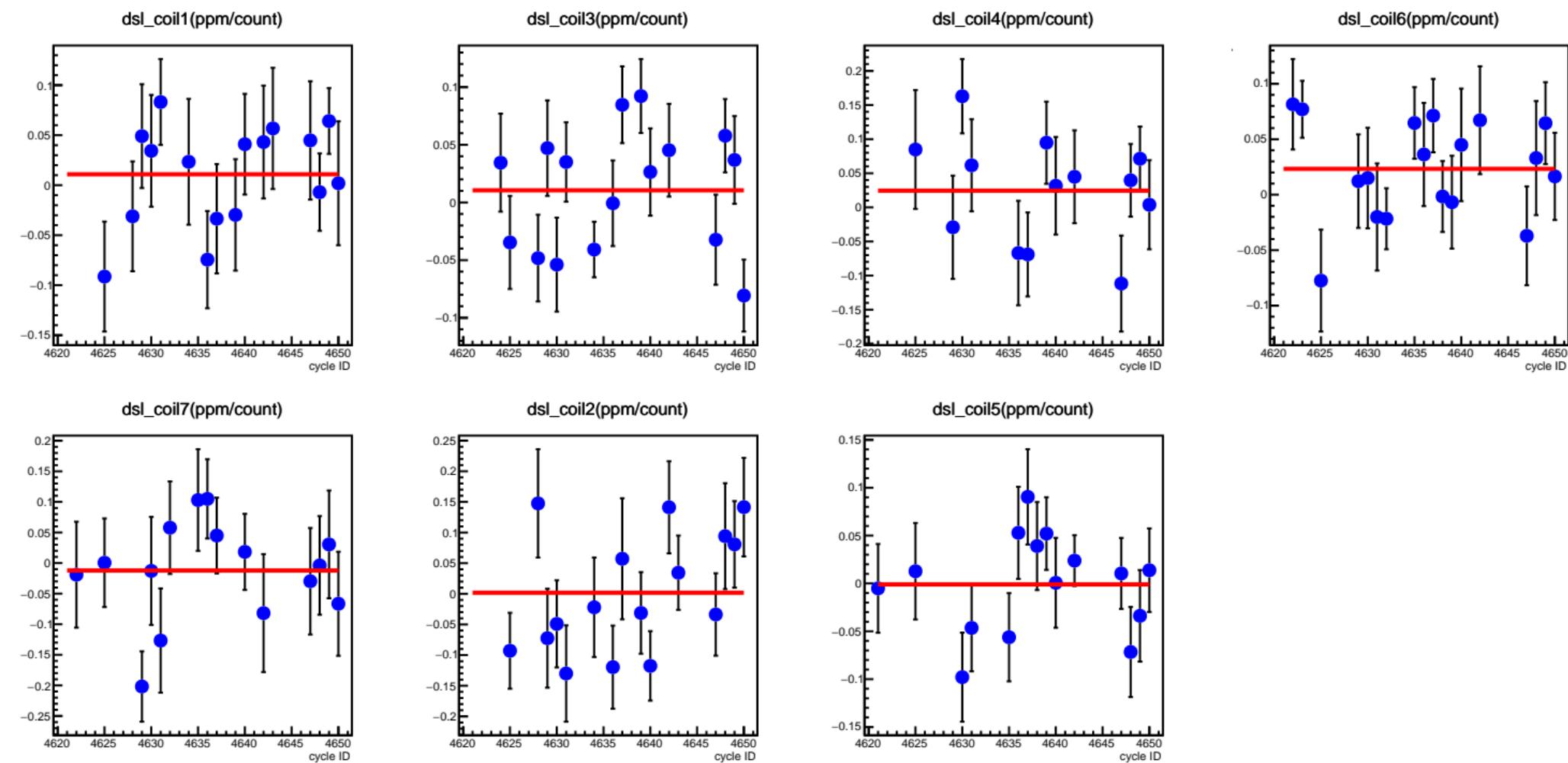
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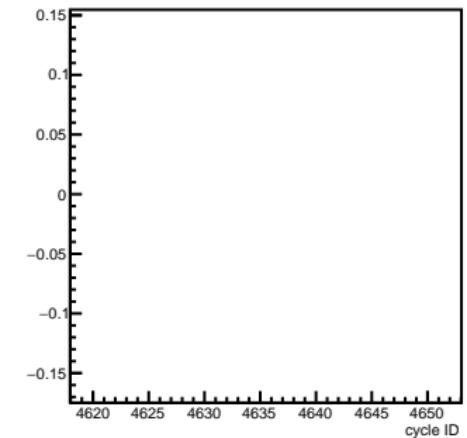
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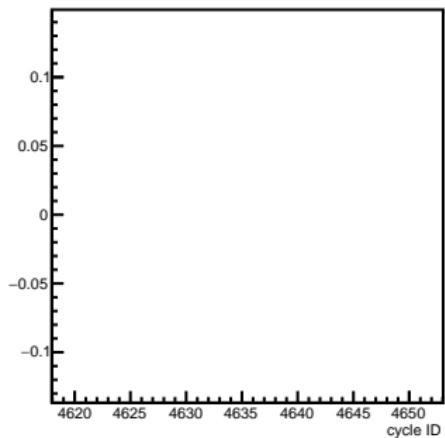




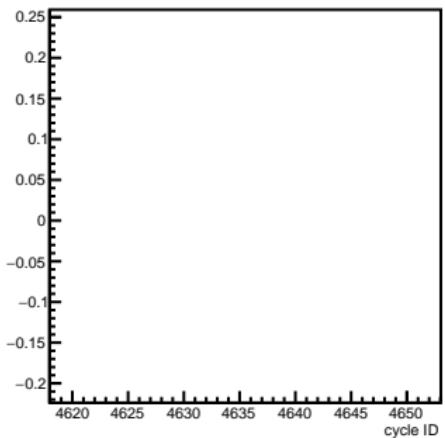
dsr_coil1(ppm/count)



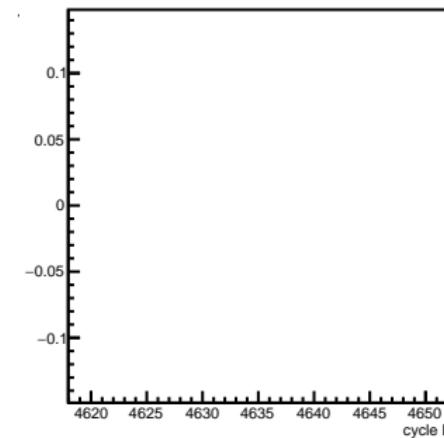
dsr_coil3(ppm/count)



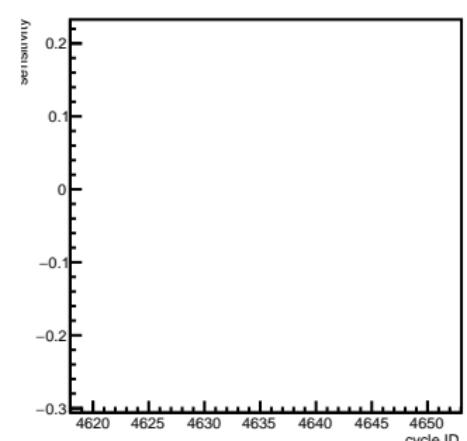
dsr_coil4(ppm/count)



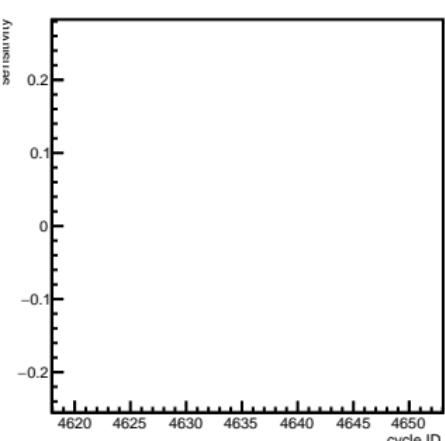
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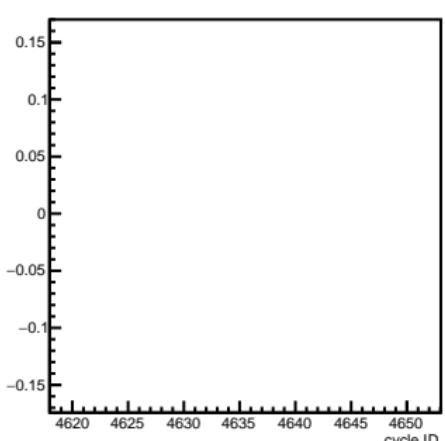
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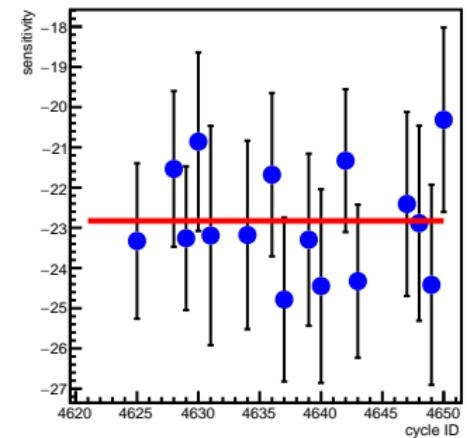
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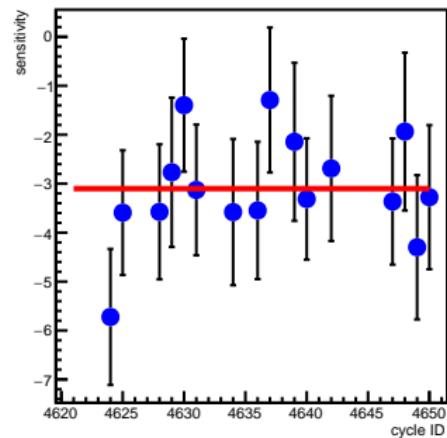
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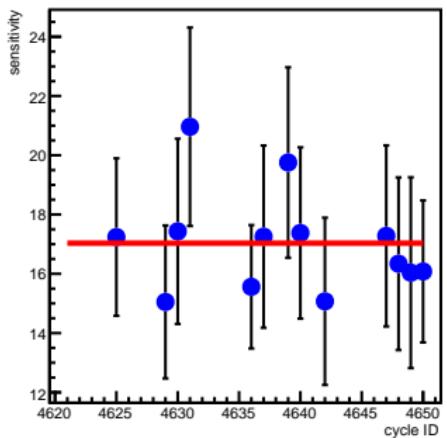
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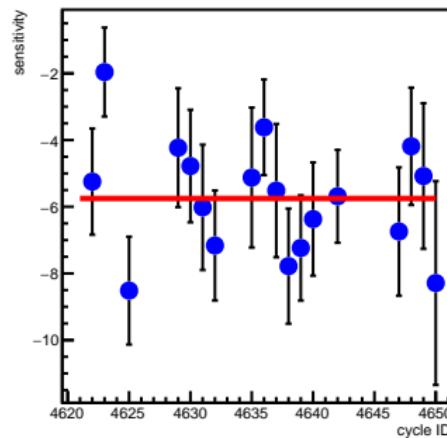
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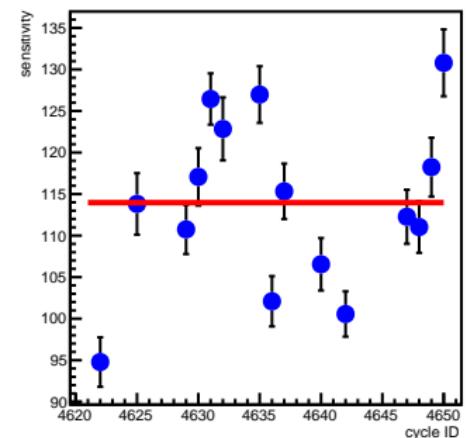
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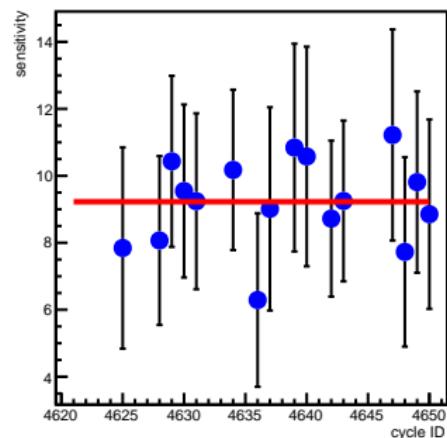
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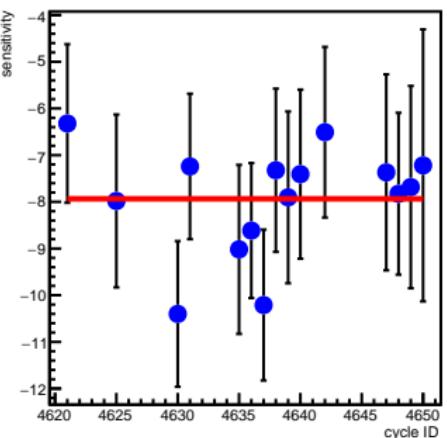
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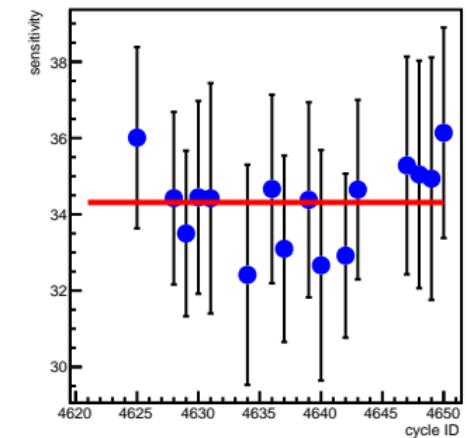
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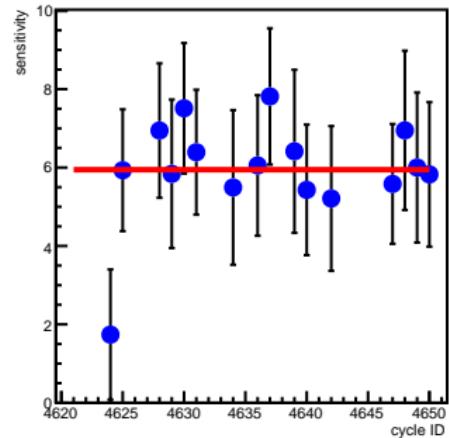
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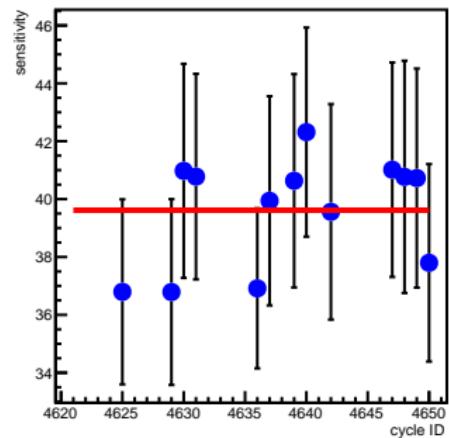
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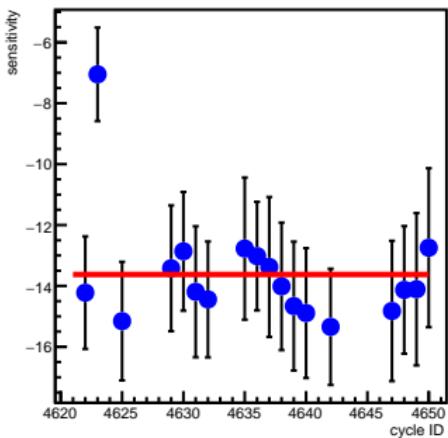
atl2_coil3(ppm/count)



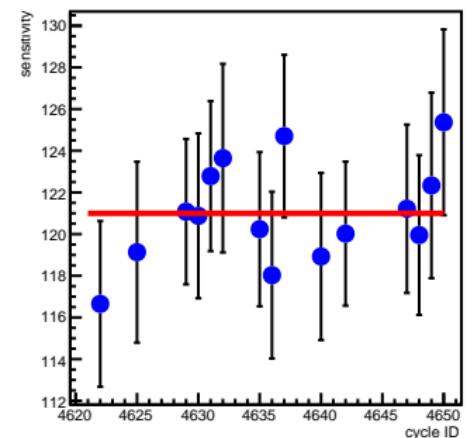
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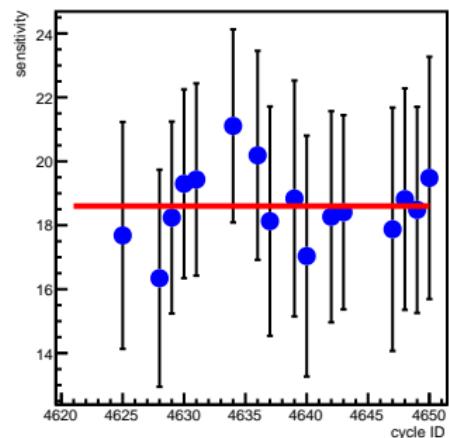
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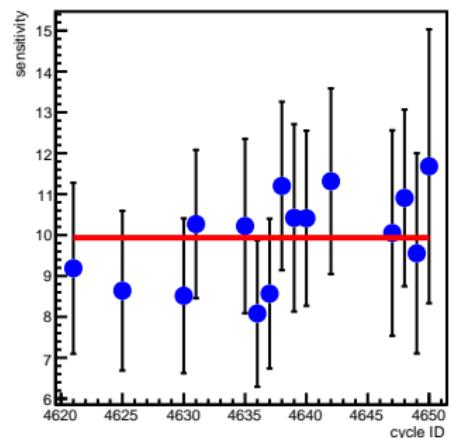
atl2_coil7(ppm/count)

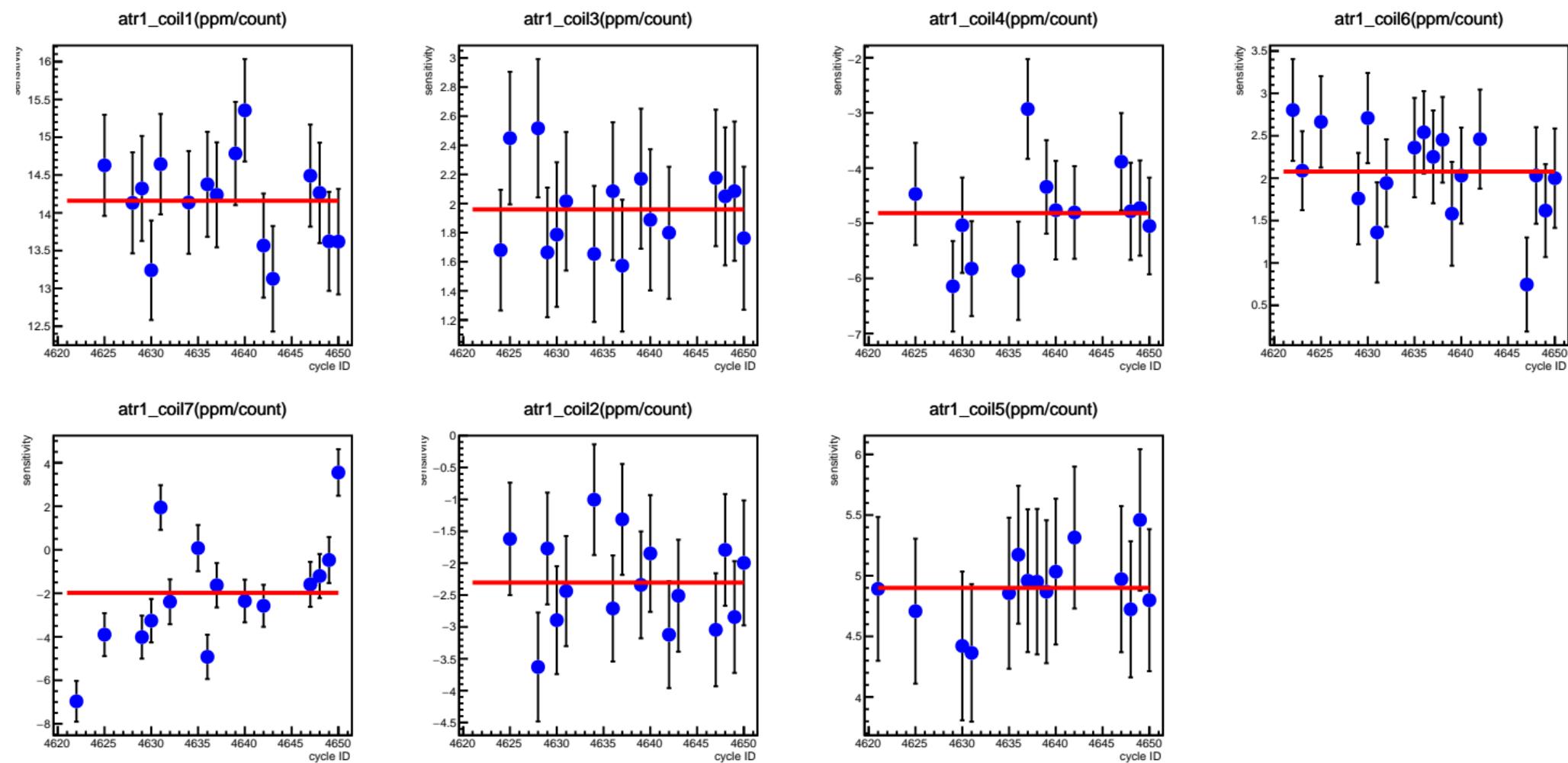


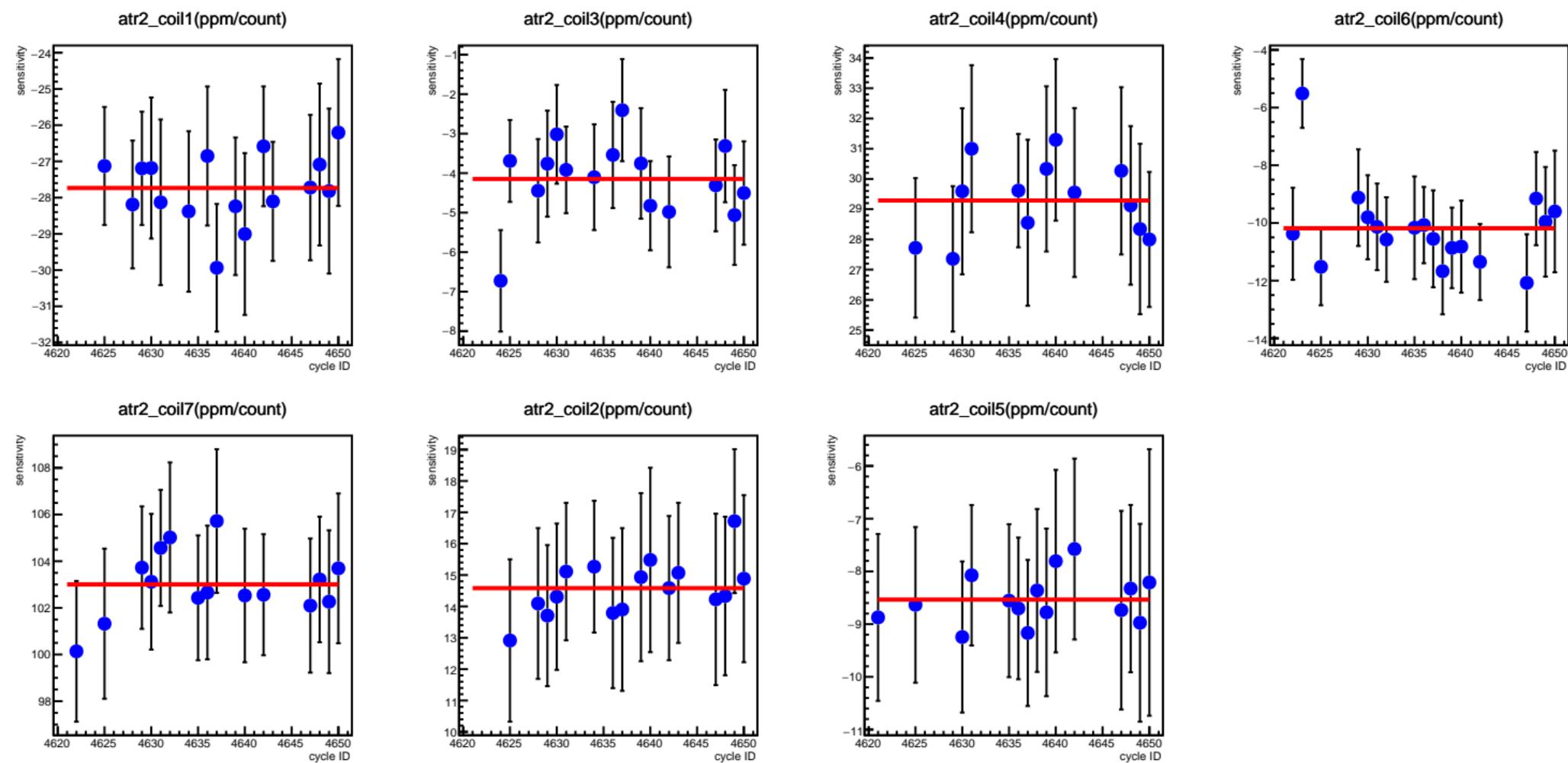
atl2_coil2(ppm/count)

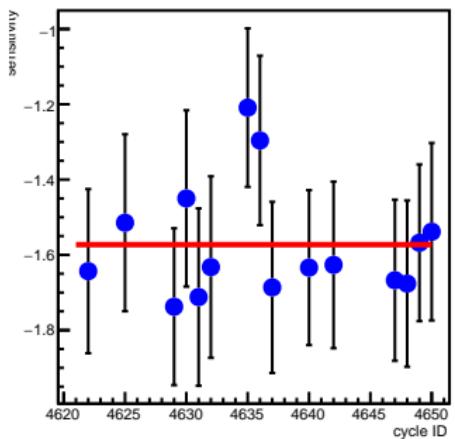
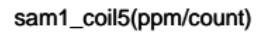
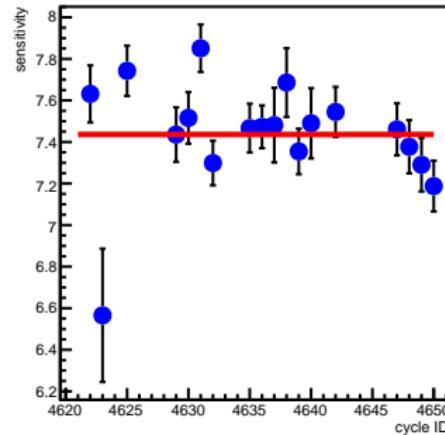
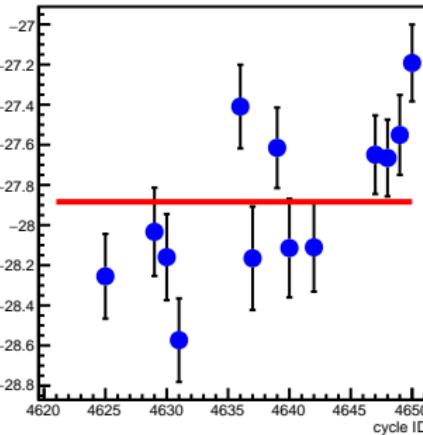
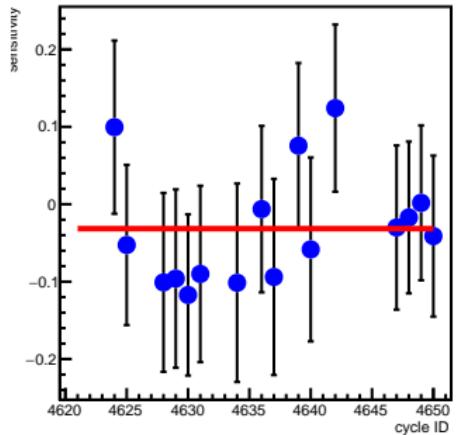
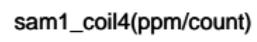
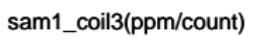
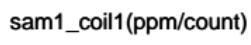


atl2_coil5(ppm/count)

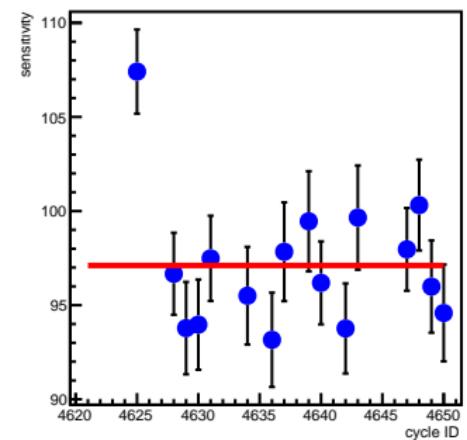




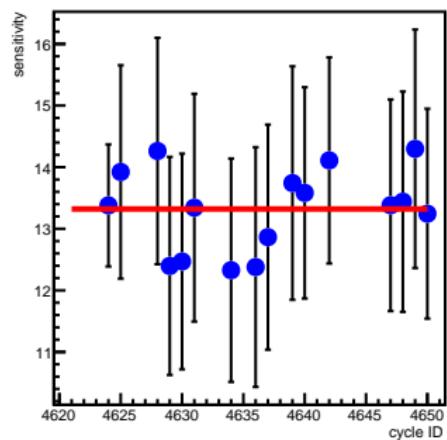




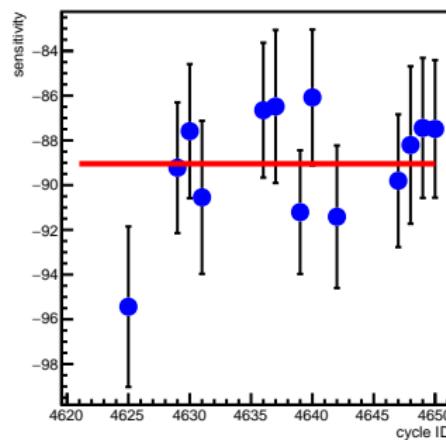
sam2_coil1(ppm/count)



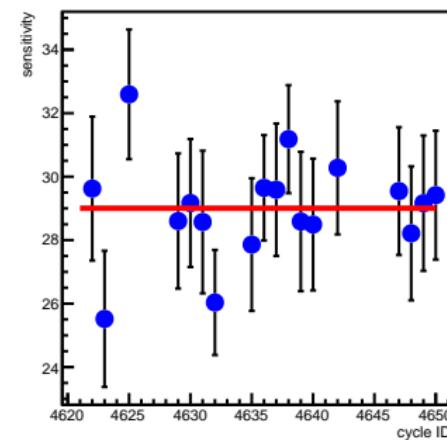
sam2_coil3(ppm/count)



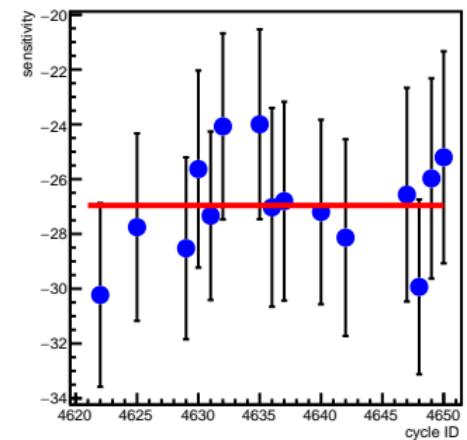
sam2_coil4(ppm/count)



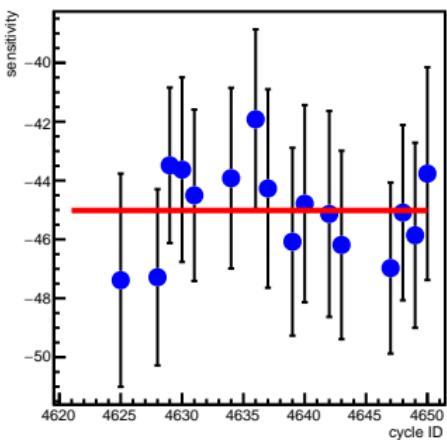
sam2_coil6(ppm/count)



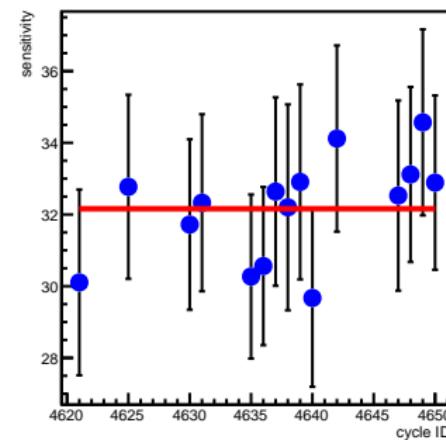
sam2_coil7(ppm/count)



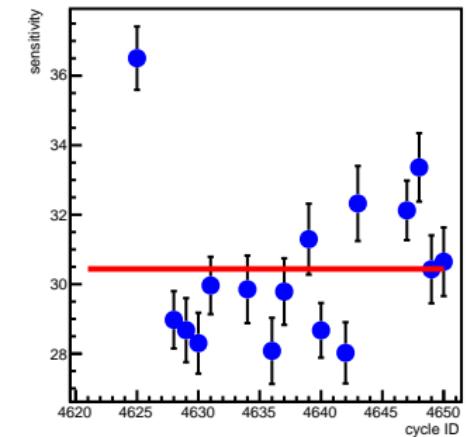
sam2_coil2(ppm/count)



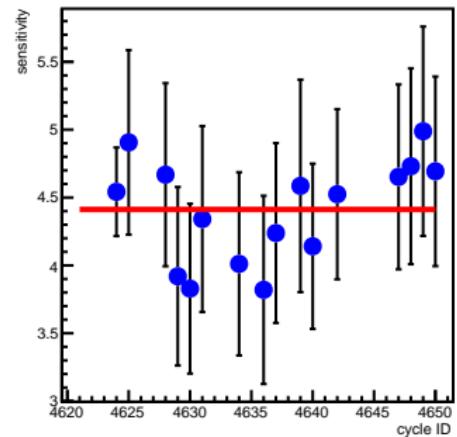
sam2_coil5(ppm/count)



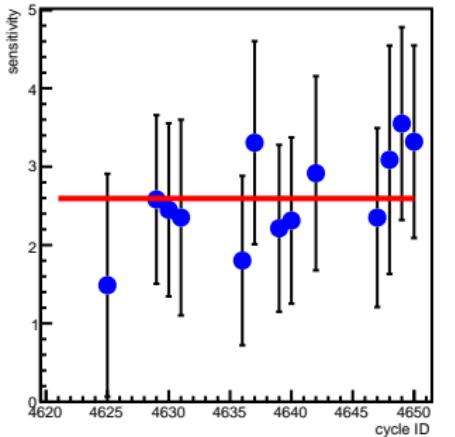
sam3_coil1(ppm/count)



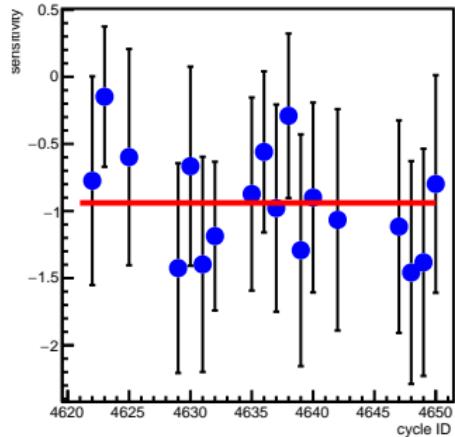
sam3_coil3(ppm/count)



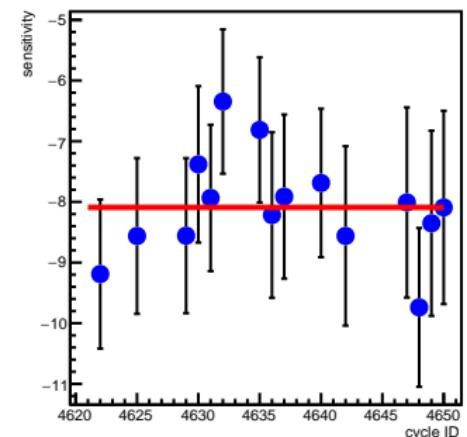
sam3_coil4(ppm/count)



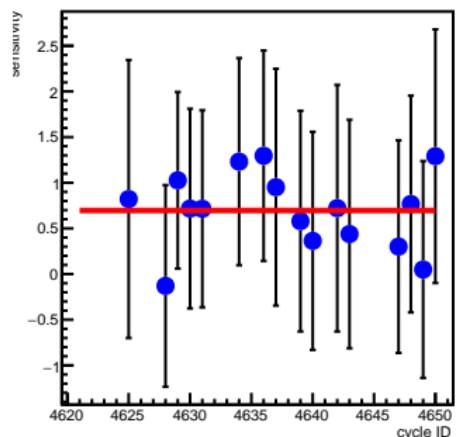
sam3_coil6(ppm/count)



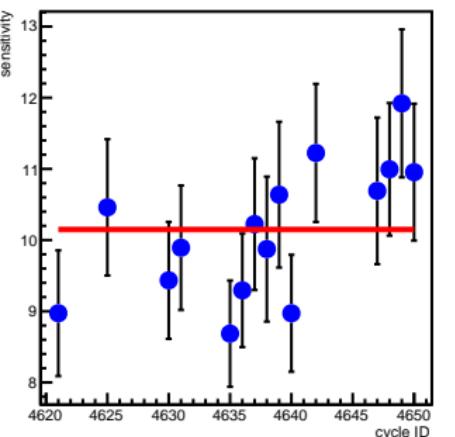
sam3_coil7(ppm/count)



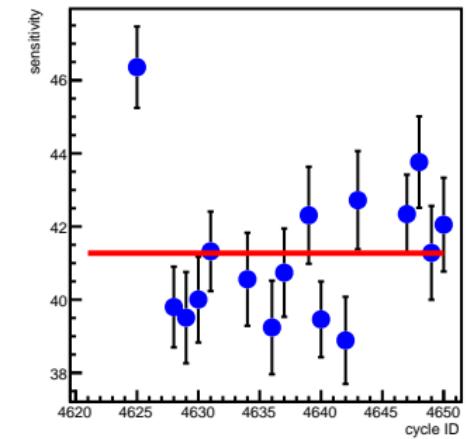
sam3_coil2(ppm/count)



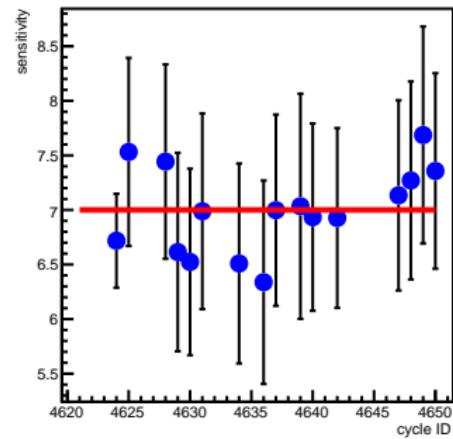
sam3_coil5(ppm/count)



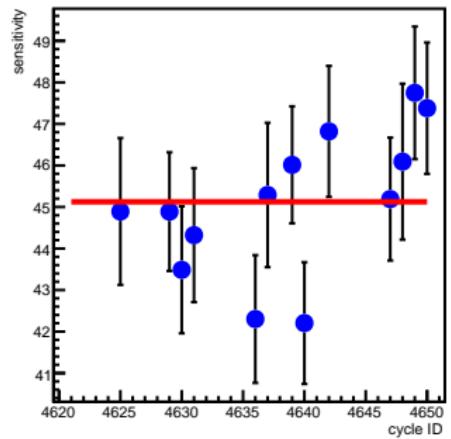
sam4_coil1(ppm/count)



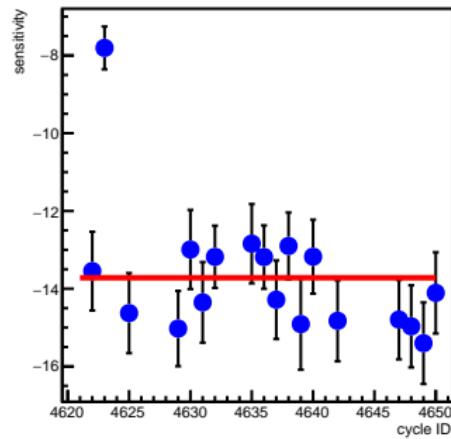
sam4_coil3(ppm/count)



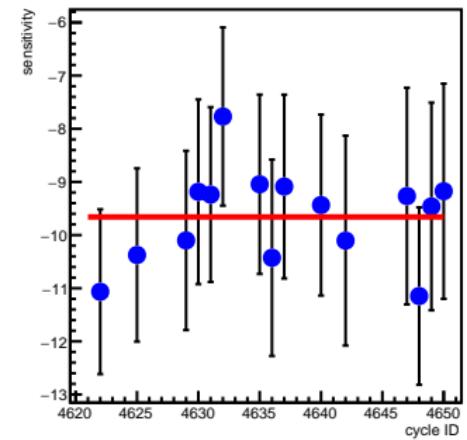
sam4_coil4(ppm/count)



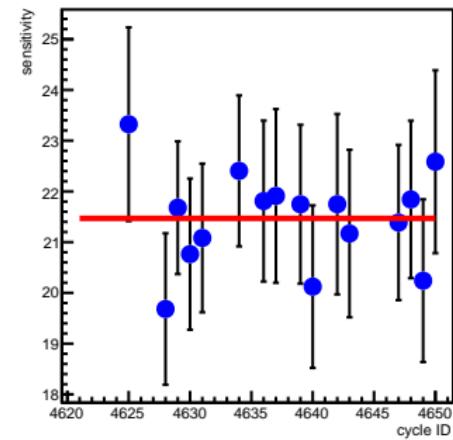
sam4_coil6(ppm/count)



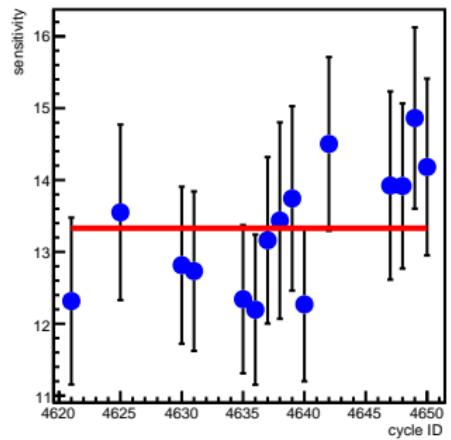
sam4_coil7(ppm/count)



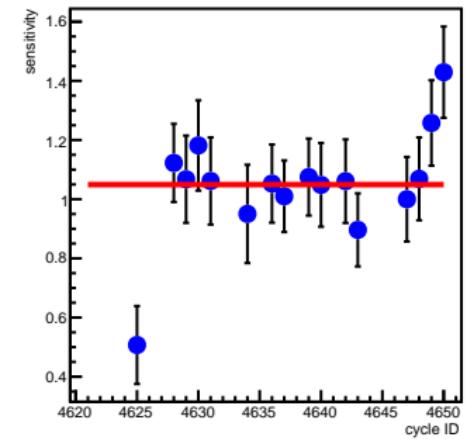
sam4_coil2(ppm/count)



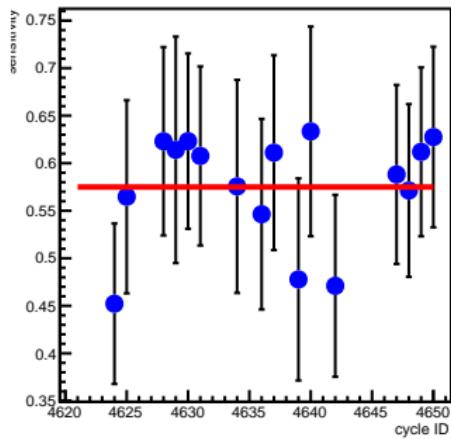
sam4_coil5(ppm/count)



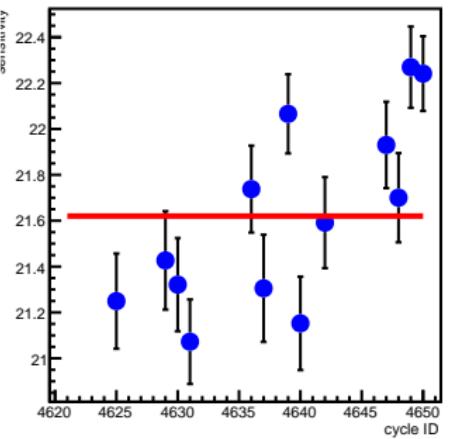
sam5_coil1(ppm/count)



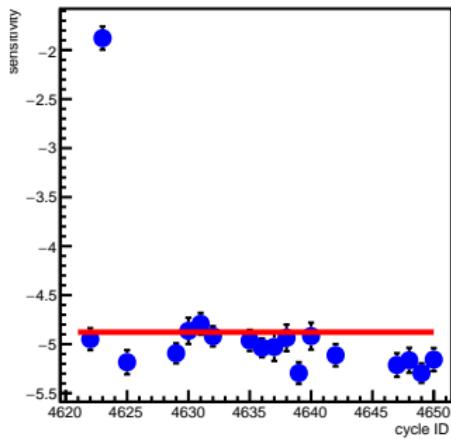
sam5_coil3(ppm/count)



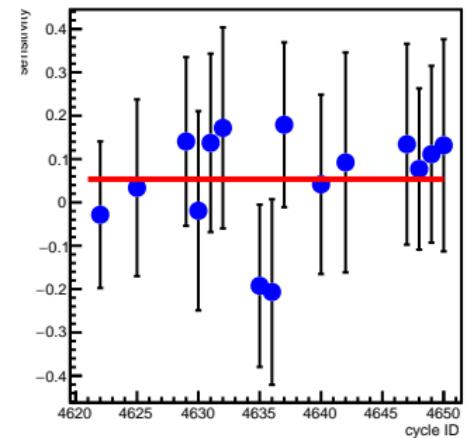
sam5_coil4(ppm/count)



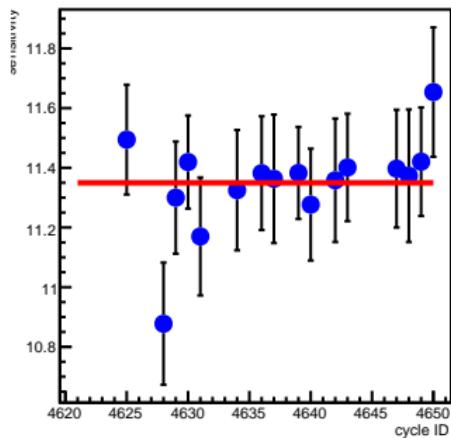
sam5_coil6(ppm/count)



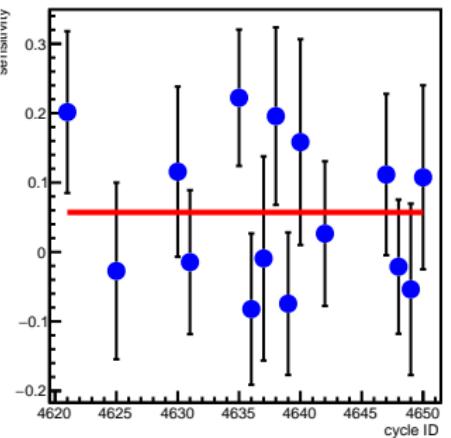
sam5_coil7(ppm/count)



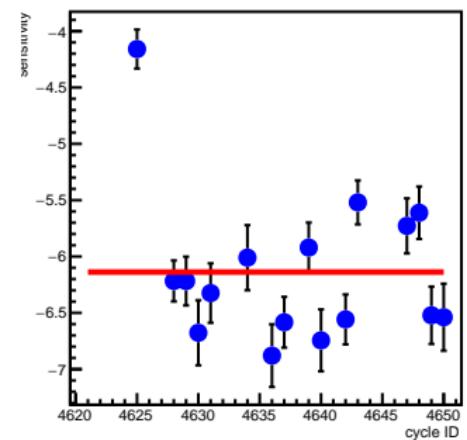
sam5_coil2(ppm/count)



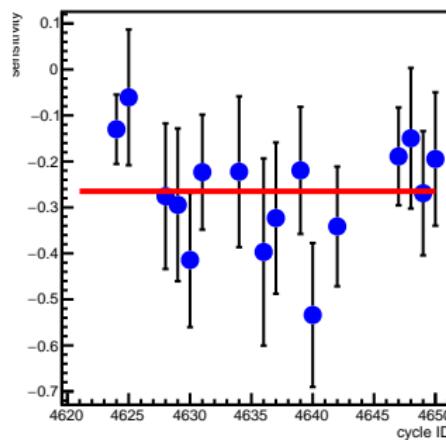
sam5_coil5(ppm/count)



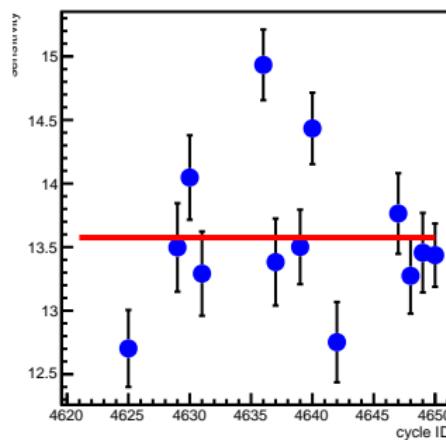
sam6_coil1(ppm/count)



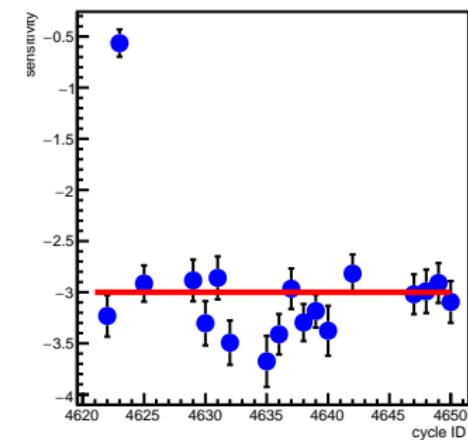
sam6_coil3(ppm/count)



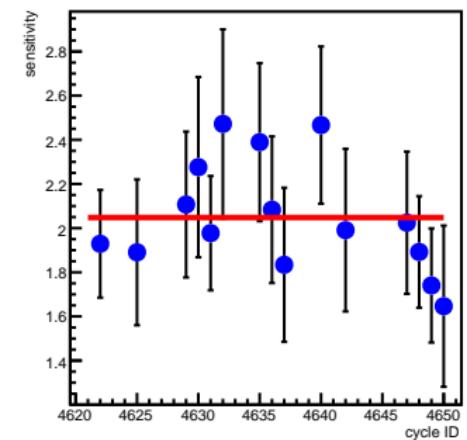
sam6_coil4(ppm/count)



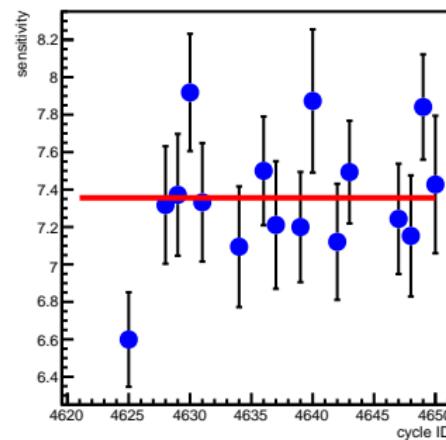
sam6_coil6(ppm/count)



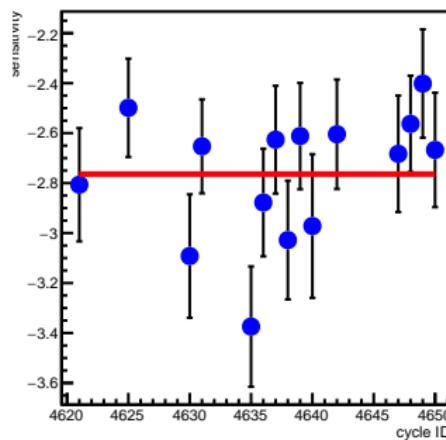
sam6_coil7(ppm/count)



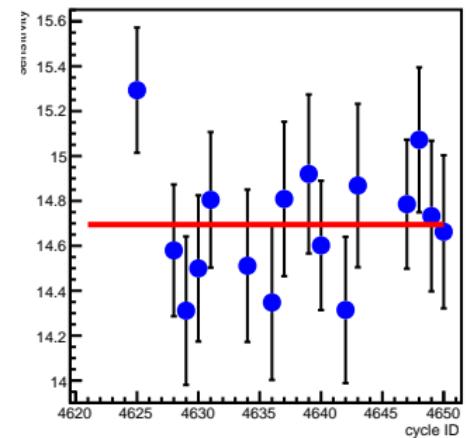
sam6_coil2(ppm/count)



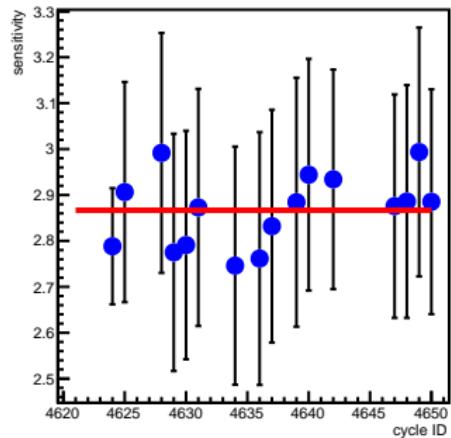
sam6_coil5(ppm/count)



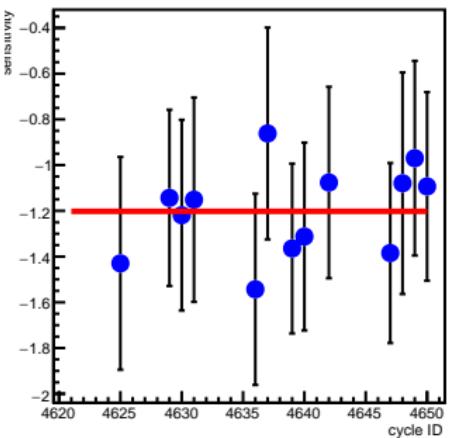
sam7_coil1(ppm/count)



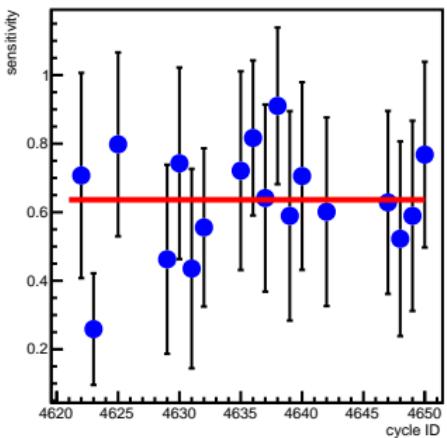
sam7_coil3(ppm/count)



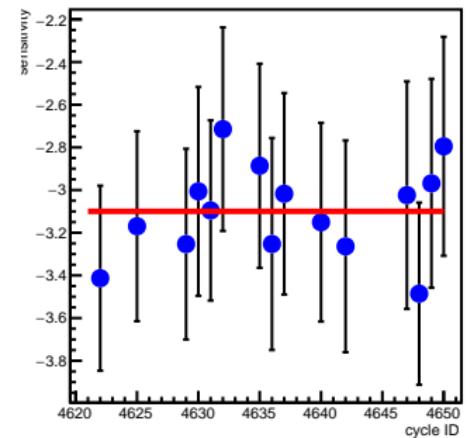
sam7_coil4(ppm/count)



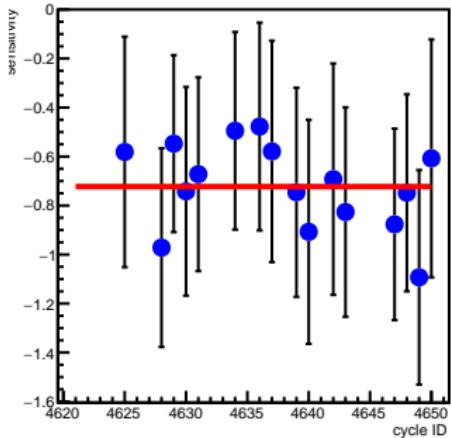
sam7_coil6(ppm/count)



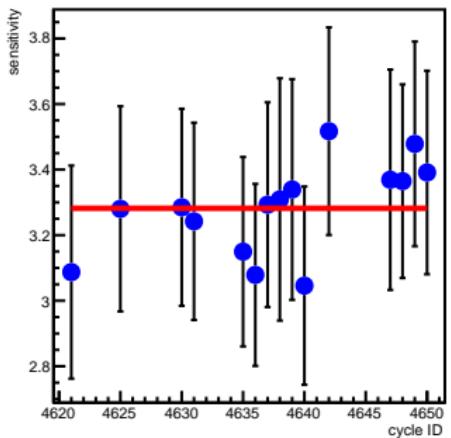
sam7_coil7(ppm/count)



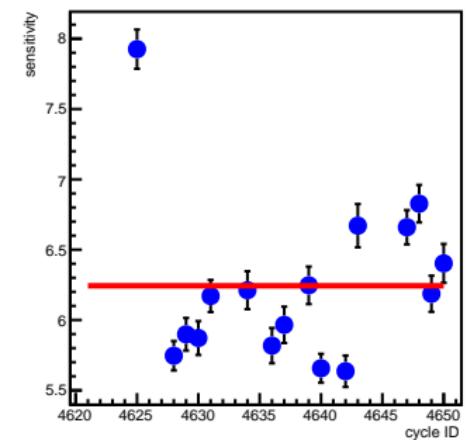
sam7_coil2(ppm/count)



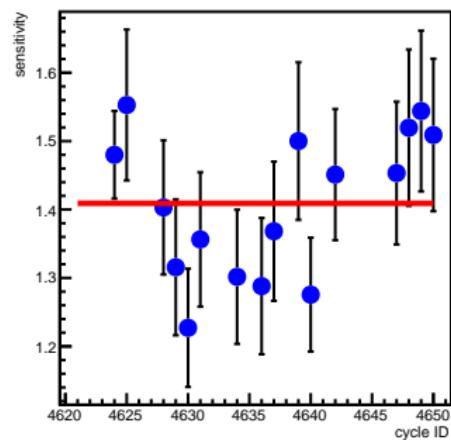
sam7_coil5(ppm/count)



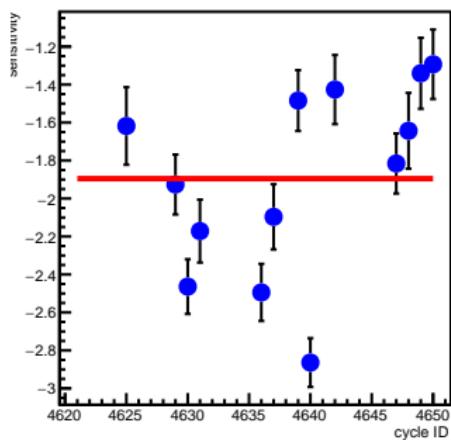
sam8_coil1(ppm/count)



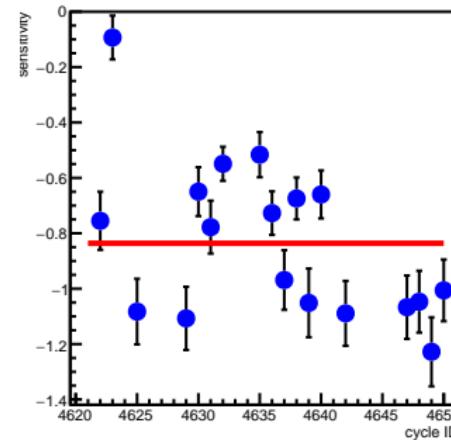
sam8_coil3(ppm/count)



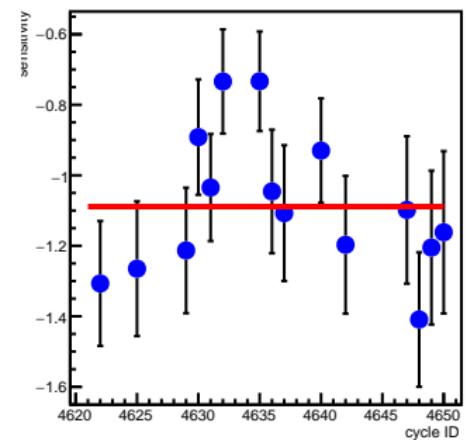
sam8_coil4(ppm/count)



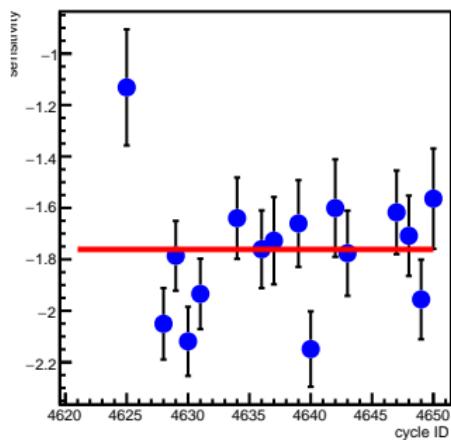
sam8_coil6(ppm/count)



sam8_coil7(ppm/count)



sam8_coil2(ppm/count)



sam8_coil5(ppm/count)

