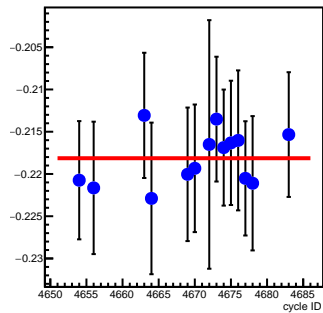
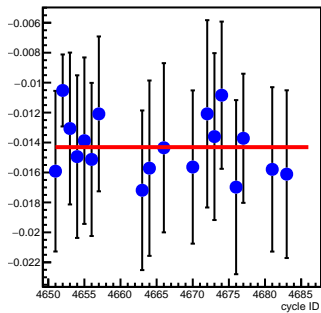


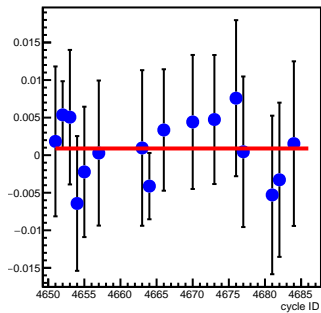
bpm1X\_coil1(um/count)



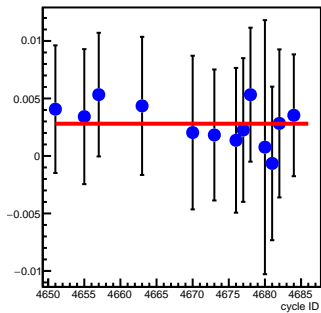
bpm1X\_coil3(um/count)



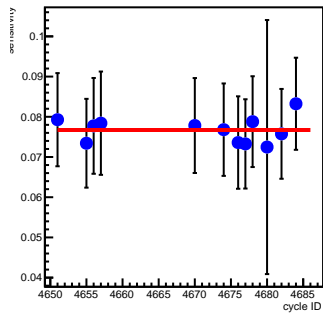
bpm1X\_coil4(um/count)



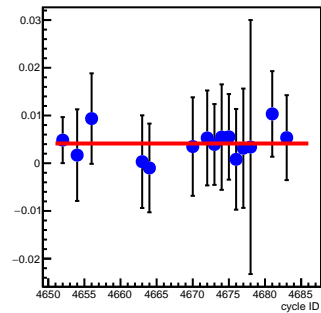
bpm1X\_coil6(um/count)



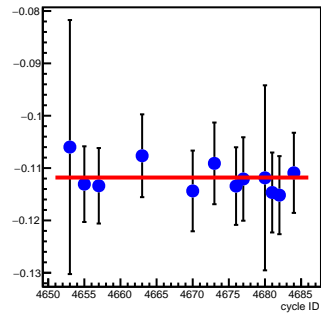
bpm1X\_coil7(um/count)



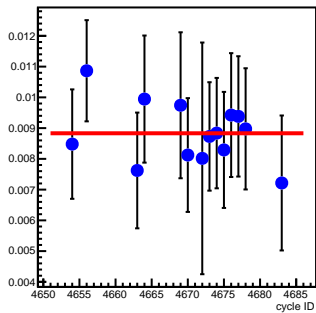
bpm1X\_coil2(um/count)



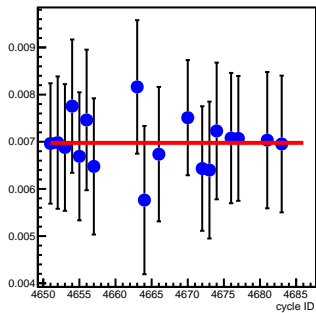
bpm1X\_coil5(um/count)



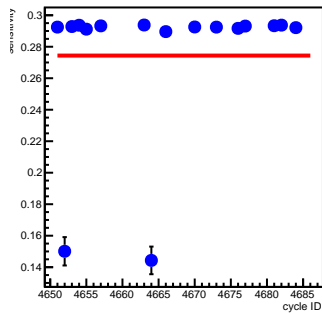
bpm4aY\_coil1(um/count)



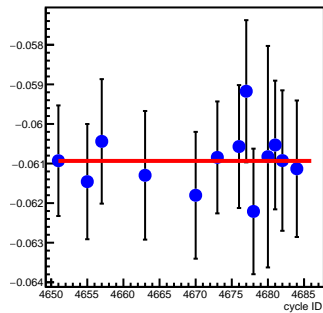
bpm4aY\_coil3(um/count)



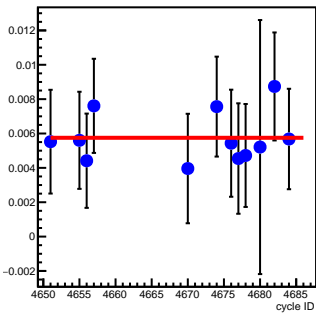
bpm4aY\_coil4(um/count)



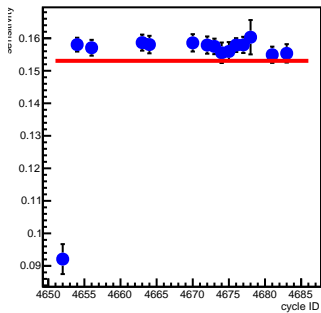
bpm4aY\_coil6(um/count)



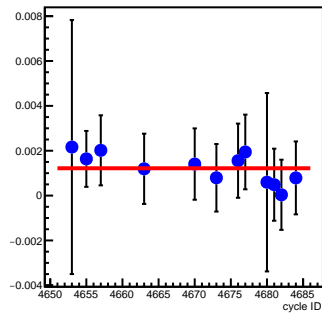
bpm4aY\_coil7(um/count)



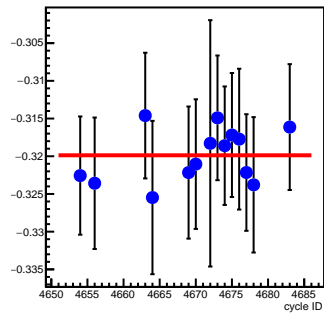
bpm4aY\_coil2(um/count)



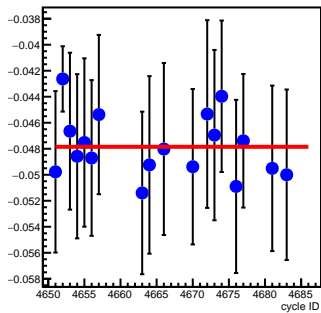
bpm4aY\_coil5(um/count)



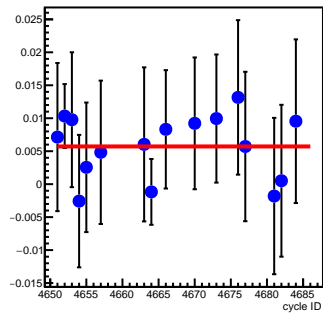
bpm4eX\_coil1(um/count)



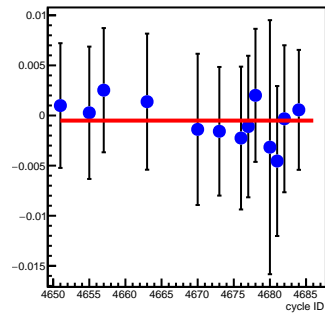
bpm4eX\_coil3(um/count)



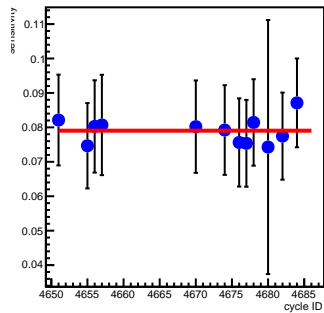
bpm4eX\_coil4(um/count)



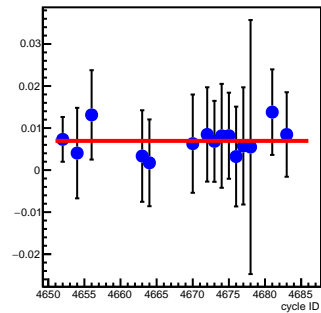
bpm4eX\_coil6(um/count)



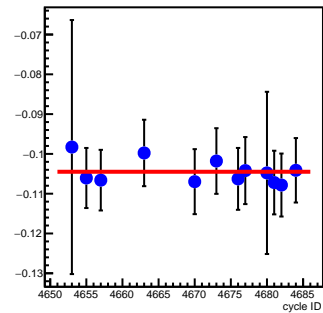
bpm4eX\_coil7(um/count)



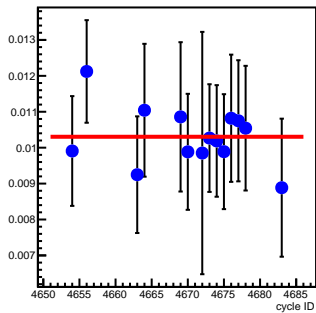
bpm4eX\_coil2(um/count)



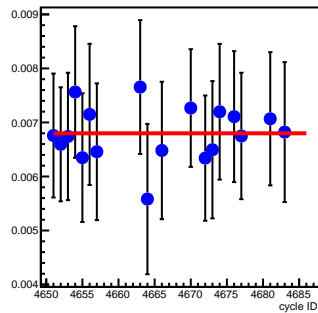
bpm4eX\_coil5(um/count)



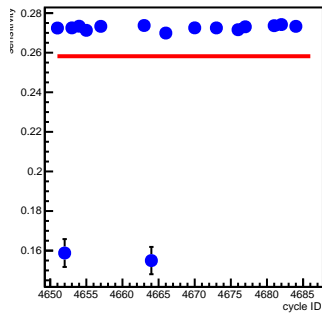
bpm4eY\_coil1(um/count)



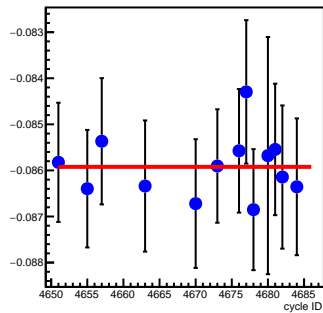
bpm4eY\_coil3(um/count)



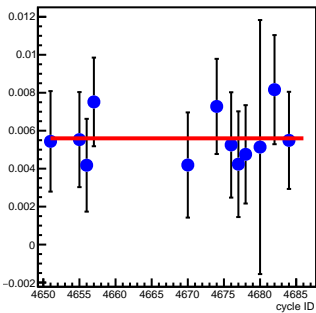
bpm4eY\_coil4(um/count)



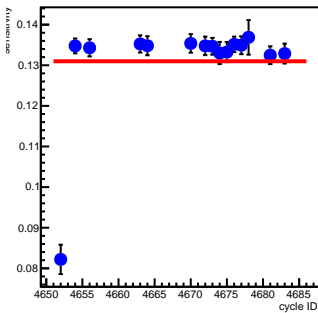
bpm4eY\_coil6(um/count)



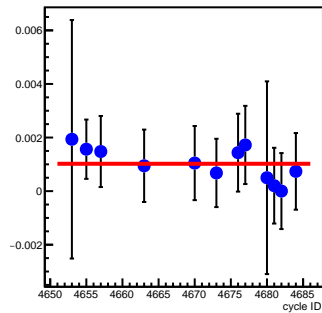
bpm4eY\_coil7(um/count)



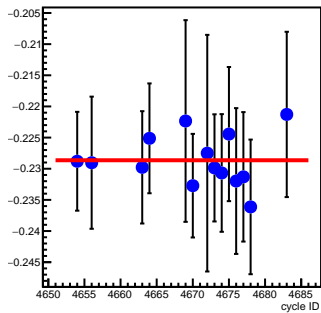
bpm4eY\_coil2(um/count)



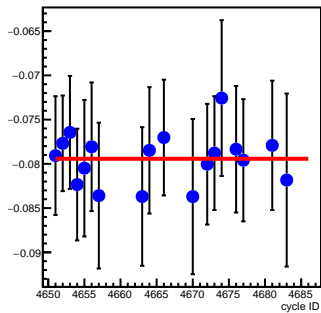
bpm4eY\_coil5(um/count)



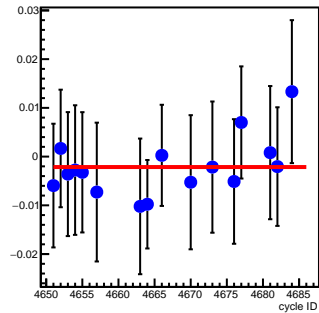
bpm12X\_coil1(um/count)



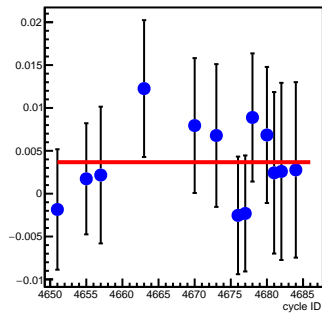
bpm12X\_coil3(um/count)



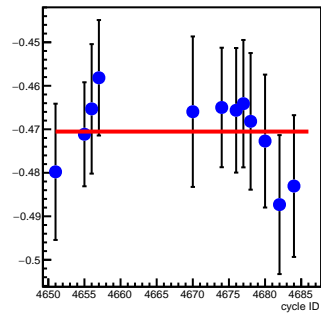
bpm12X\_coil4(um/count)



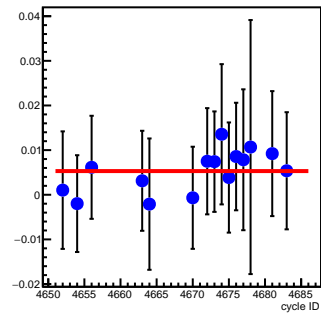
bpm12X\_coil6(um/count)



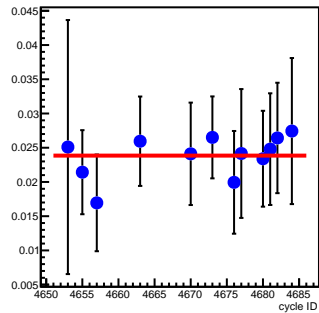
bpm12X\_coil7(um/count)



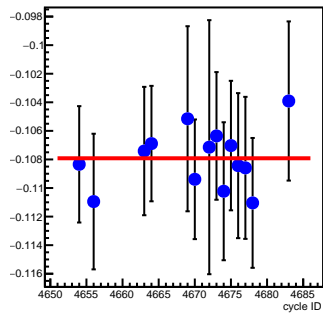
bpm12X\_coil2(um/count)



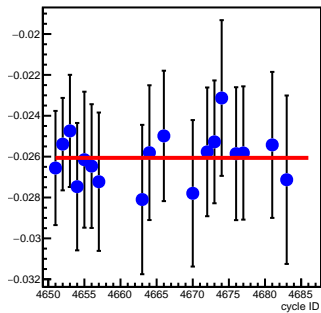
bpm12X\_coil5(um/count)



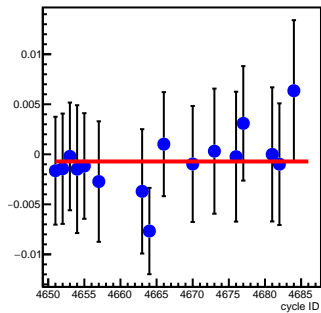
bpm11X\_coil1(um/count)



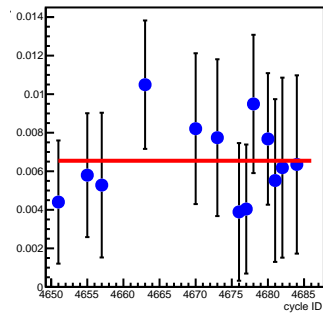
bpm11X\_coil3(um/count)



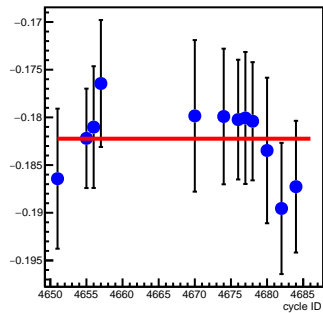
bpm11X\_coil4(um/count)



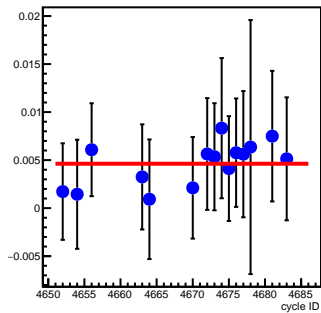
bpm11X\_coil6(um/count)



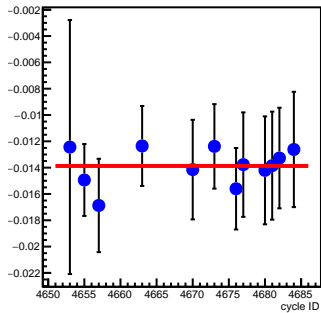
bpm11X\_coil7(um/count)



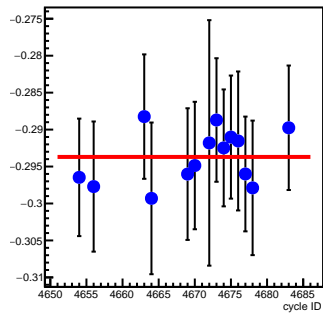
bpm11X\_coil2(um/count)



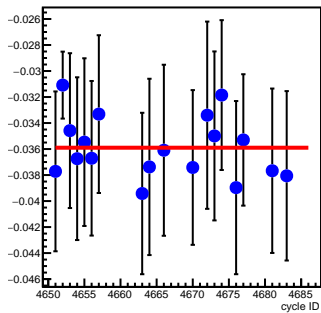
bpm11X\_coil5(um/count)



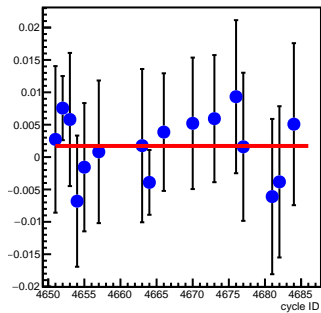
bpm4aX\_coil1(um/count)



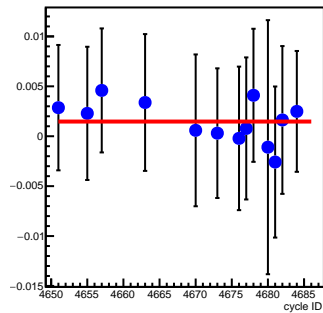
bpm4aX\_coil3(um/count)



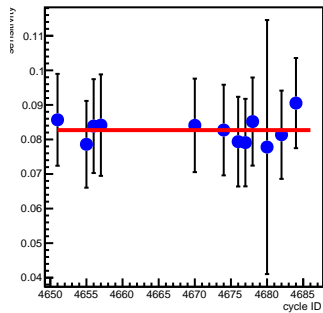
bpm4aX\_coil4(um/count)



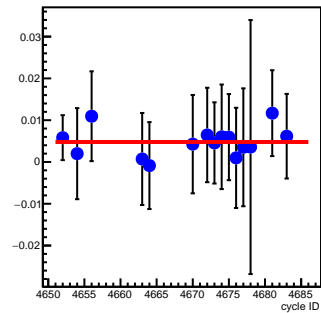
bpm4aX\_coil6(um/count)



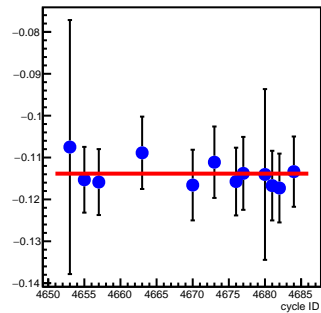
bpm4aX\_coil7(um/count)



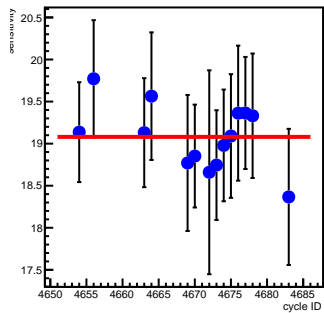
bpm4aX\_coil2(um/count)



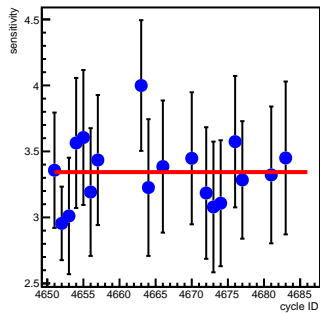
bpm4aX\_coil5(um/count)



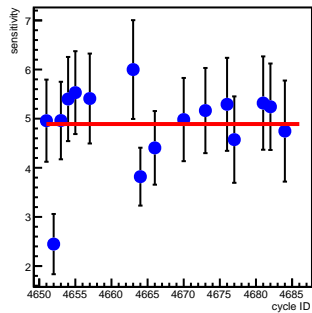
usl\_coil1(ppm/count)



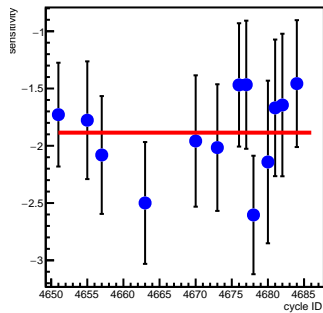
usl\_coil3(ppm/count)



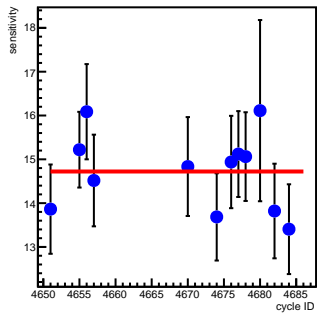
usl\_coil4(ppm/count)



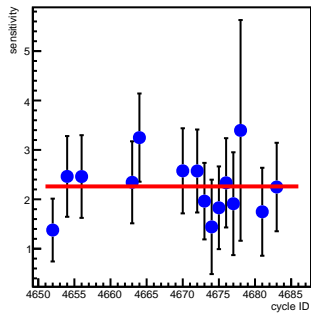
usl\_coil6(ppm/count)



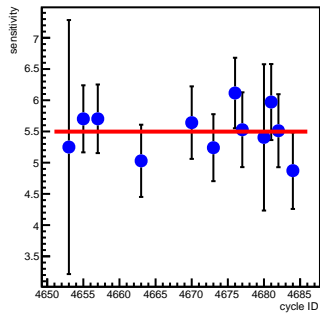
usl\_coil7(ppm/count)



usl\_coil2(ppm/count)

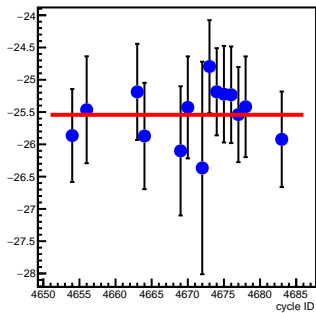


usl\_coil5(ppm/count)

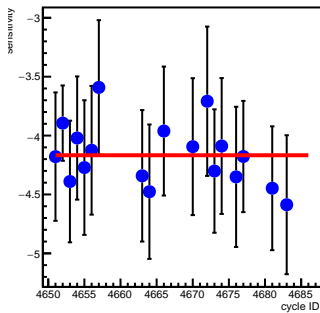




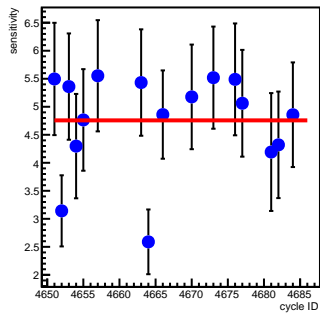
usr\_coil1(ppm/count)



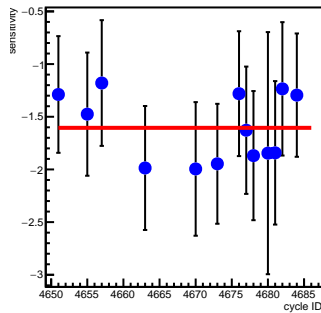
usr\_coil3(ppm/count)



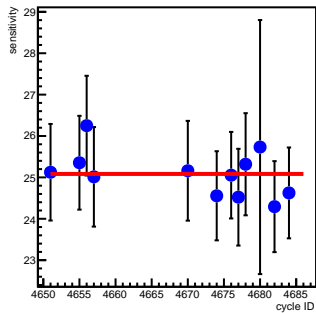
usr\_coil4(ppm/count)



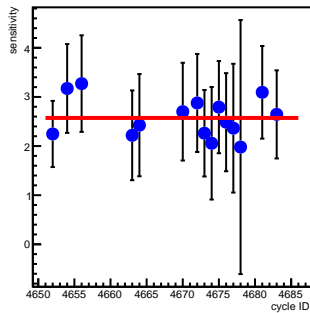
usr\_coil6(ppm/count)



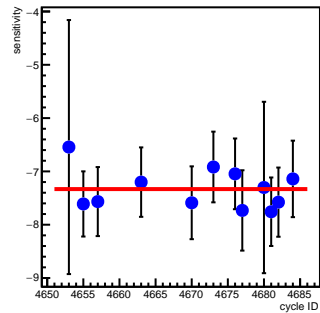
usr\_coil7(ppm/count)



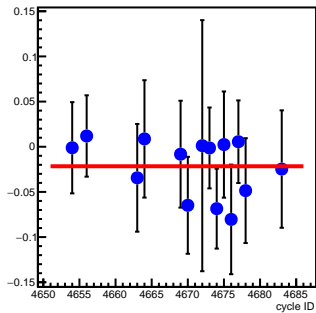
usr\_coil2(ppm/count)



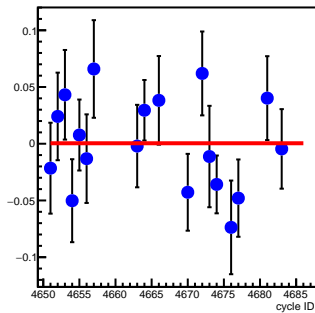
usr\_coil5(ppm/count)



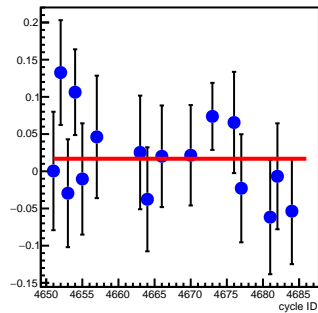
dsl\_coil1(ppm/count)



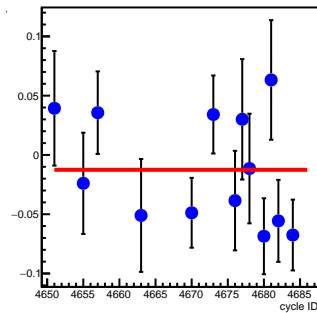
dsl\_coil3(ppm/count)



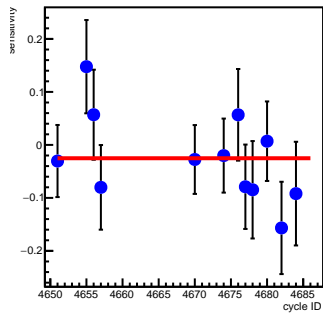
dsl\_coil4(ppm/count)



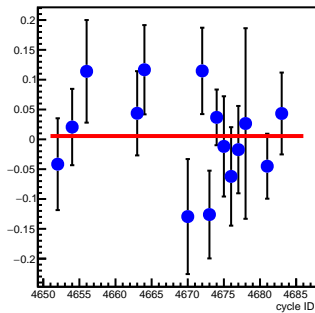
dsl\_coil6(ppm/count)



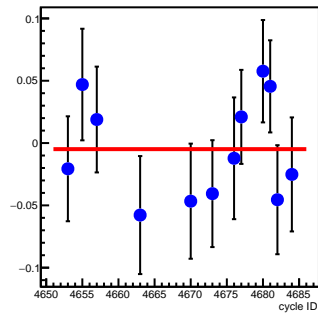
dsl\_coil7(ppm/count)



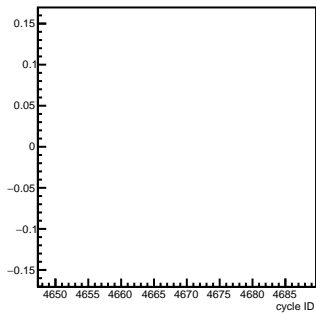
dsl\_coil2(ppm/count)



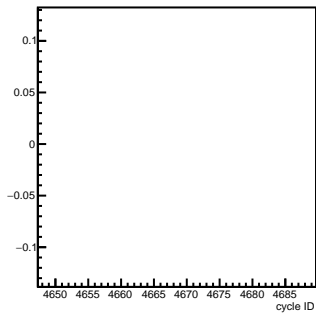
dsl\_coil5(ppm/count)



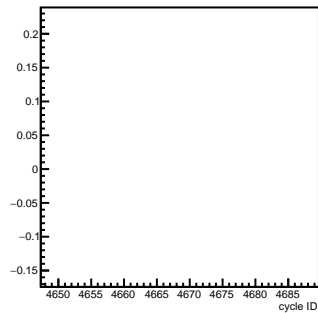
dsr\_coil1(ppm/count)



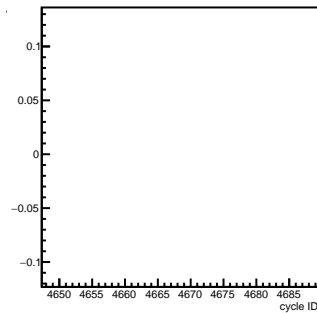
dsr\_coil3(ppm/count)



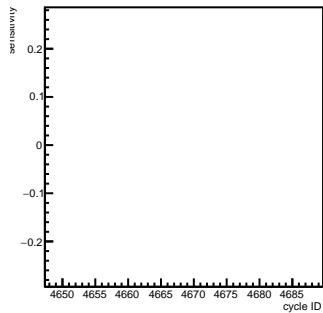
dsr\_coil4(ppm/count)



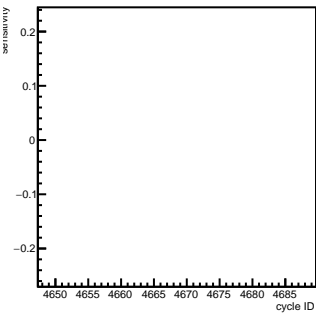
dsr\_coil6(ppm/count)



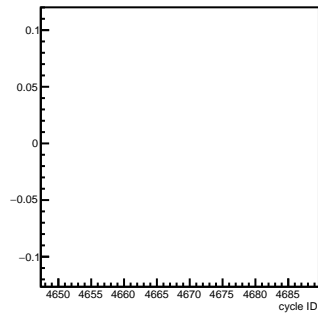
dsr\_coil7(ppm/count)



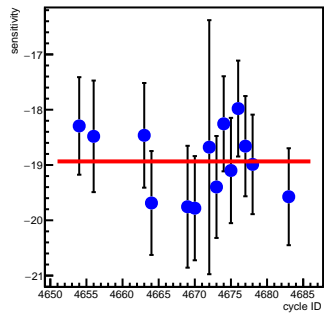
dsr\_coil2(ppm/count)



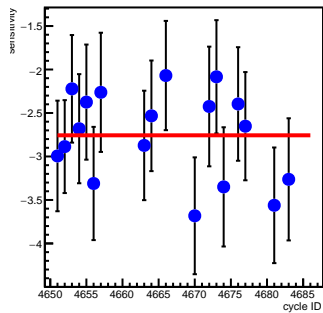
dsr\_coil5(ppm/count)



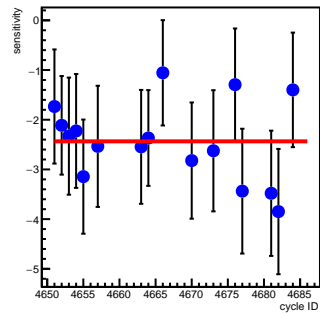
at1\_coil1(ppm/count)



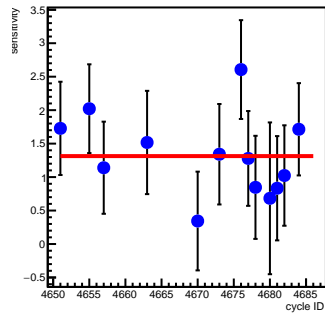
at1\_coil3(ppm/count)



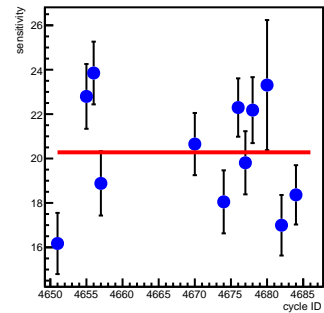
at1\_coil4(ppm/count)



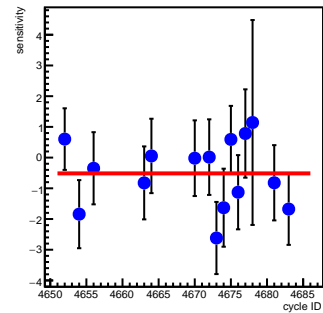
at1\_coil6(ppm/count)



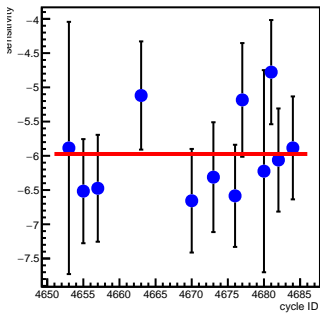
at1\_coil7(ppm/count)



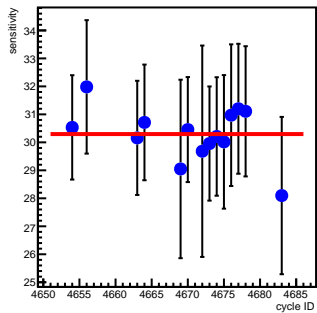
at1\_coil2(ppm/count)



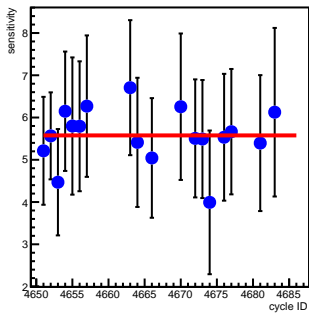
at1\_coil5(ppm/count)



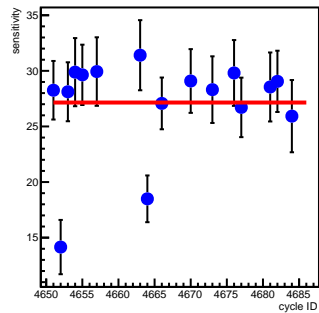
at2\_coil1(ppm/count)



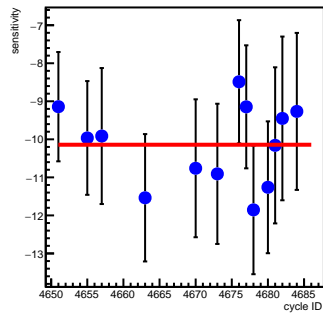
at2\_coil3(ppm/count)



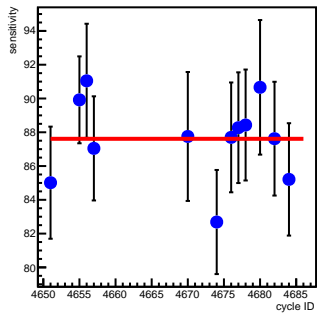
at2\_coil4(ppm/count)



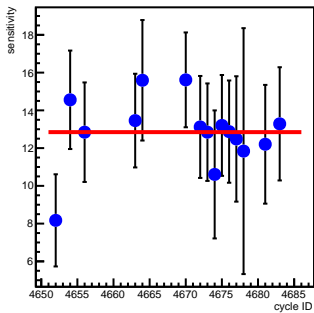
at2\_coil6(ppm/count)



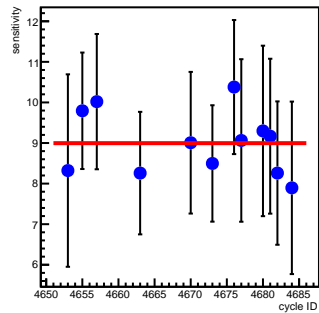
at2\_coil7(ppm/count)



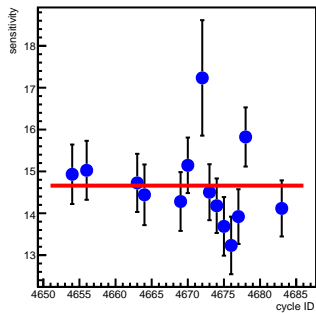
at2\_coil2(ppm/count)



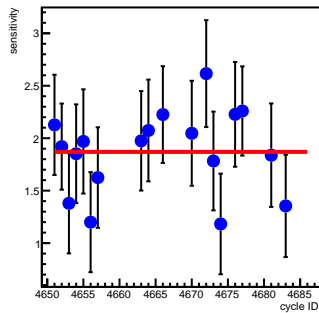
at2\_coil5(ppm/count)



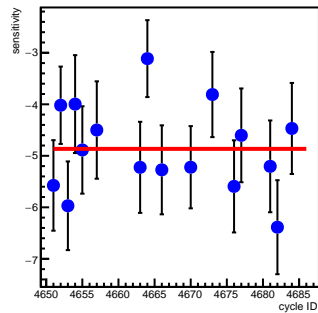
atr1\_coil1(ppm/count)



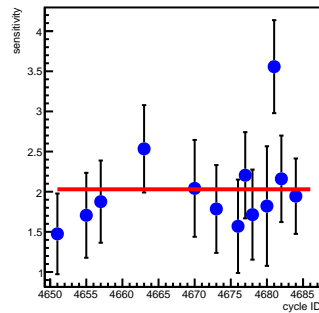
atr1\_coil3(ppm/count)



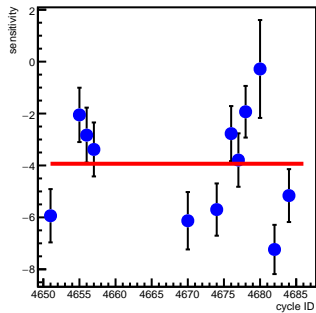
atr1\_coil4(ppm/count)



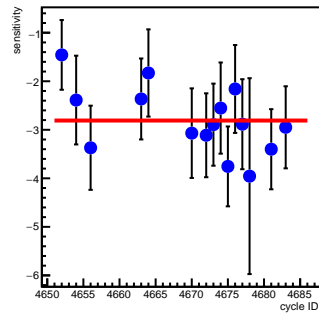
atr1\_coil6(ppm/count)



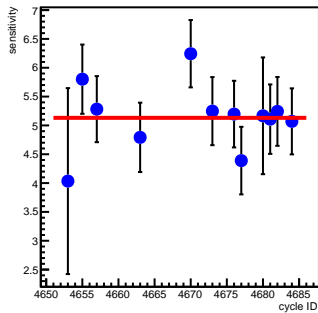
atr1\_coil7(ppm/count)



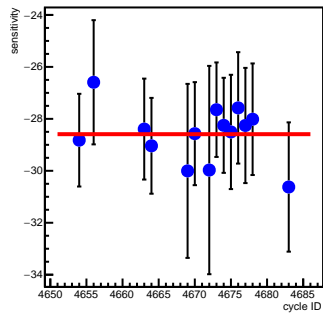
atr1\_coil2(ppm/count)



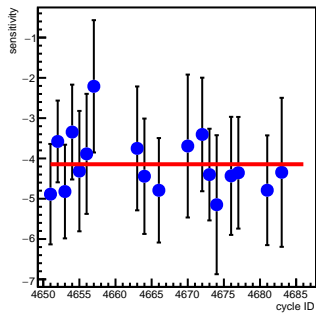
atr1\_coil5(ppm/count)



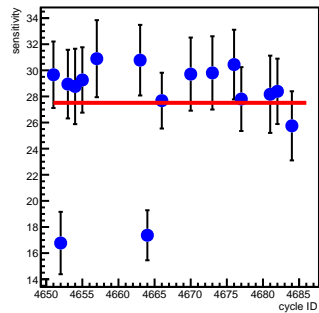
atr2\_coil1(ppm/count)



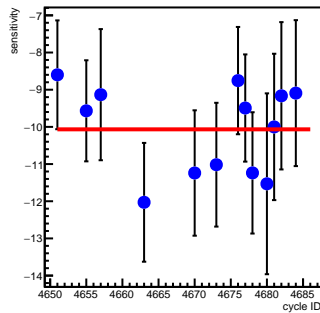
atr2\_coil3(ppm/count)



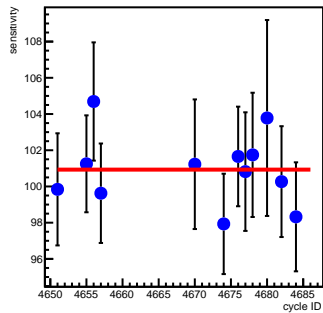
atr2\_coil4(ppm/count)



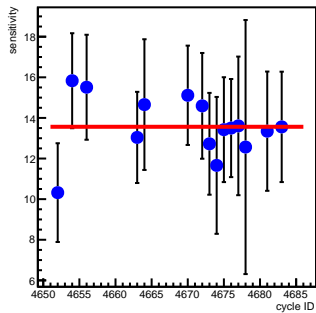
atr2\_coil6(ppm/count)



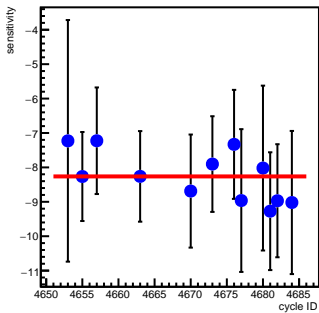
atr2\_coil7(ppm/count)



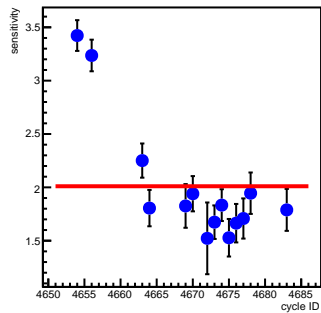
atr2\_coil2(ppm/count)



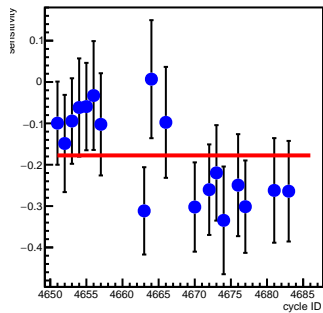
atr2\_coil5(ppm/count)



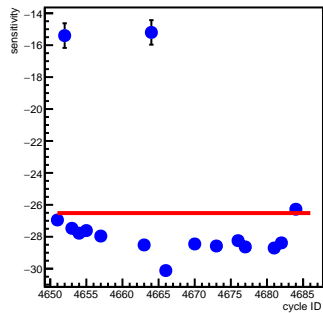
sam1\_coil1(ppm/count)



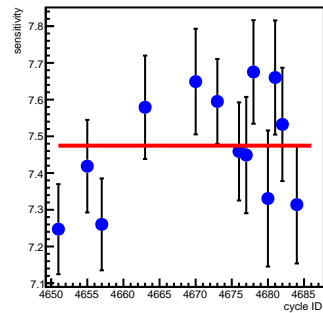
sam1\_coil3(ppm/count)



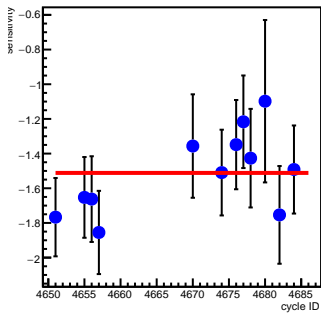
sam1\_coil4(ppm/count)



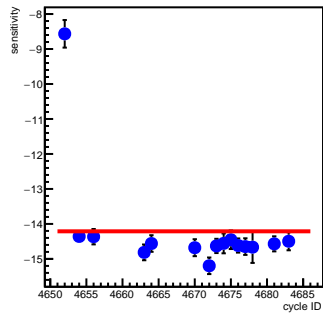
sam1\_coil6(ppm/count)



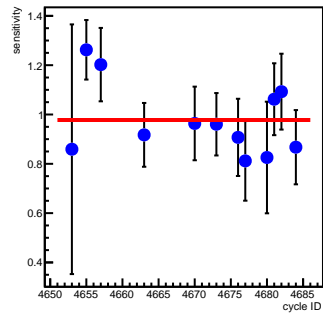
sam1\_coil7(ppm/count)



sam1\_coil2(ppm/count)

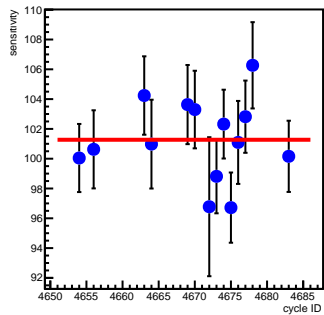


sam1\_coil5(ppm/count)

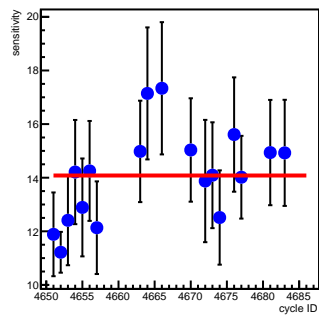




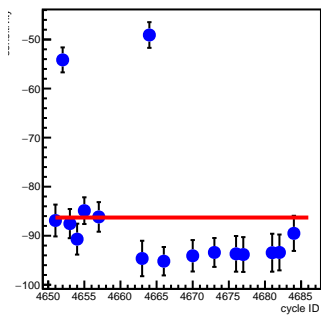
sam2\_coil1(ppm/count)



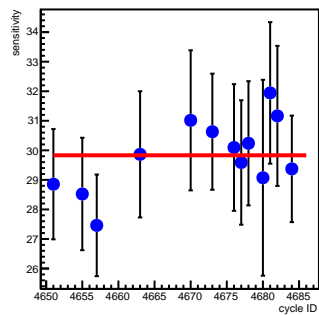
sam2\_coil3(ppm/count)



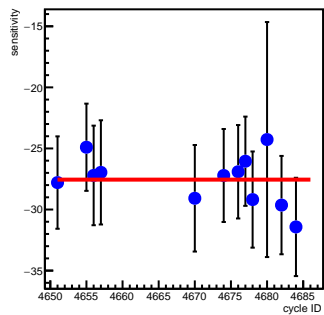
sam2\_coil4(ppm/count)



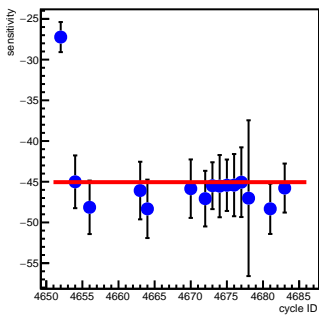
sam2\_coil6(ppm/count)



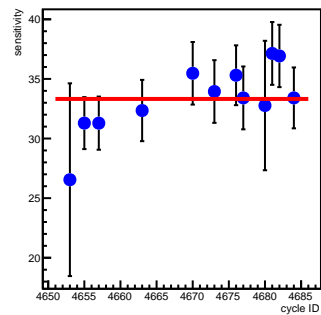
sam2\_coil7(ppm/count)



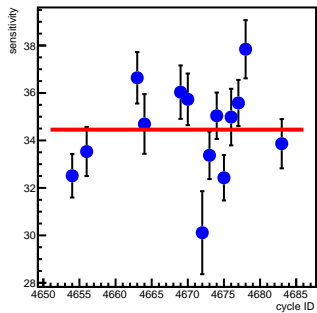
sam2\_coil2(ppm/count)



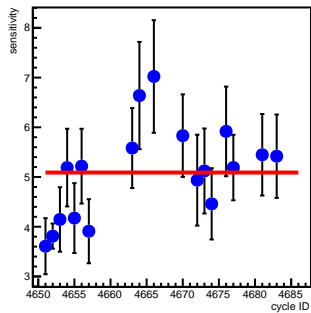
sam2\_coil5(ppm/count)



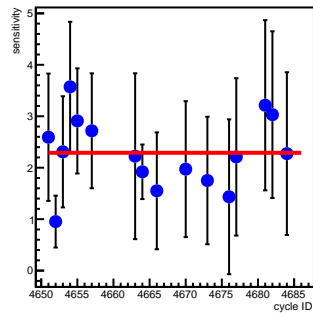
sam3\_coil1(ppm/count)



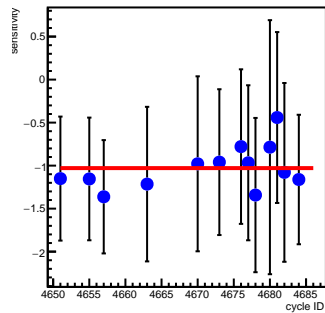
sam3\_coil3(ppm/count)



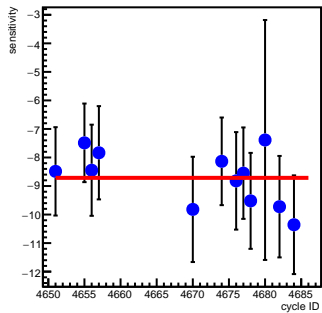
sam3\_coil4(ppm/count)



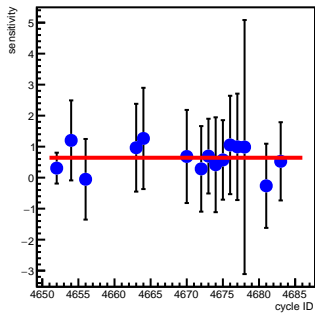
sam3\_coil6(ppm/count)



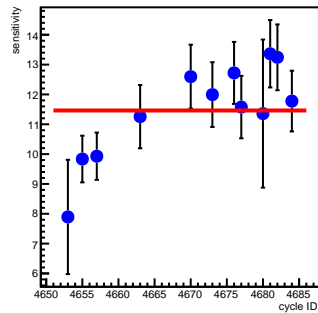
sam3\_coil7(ppm/count)



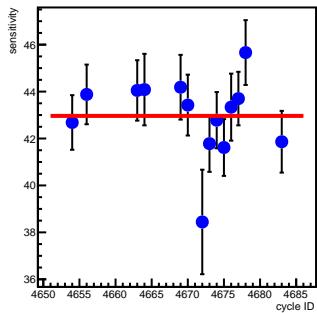
sam3\_coil2(ppm/count)



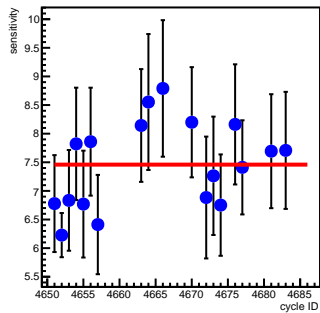
sam3\_coil5(ppm/count)



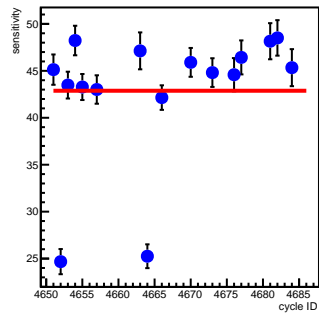
sam4\_coil1(ppm/count)



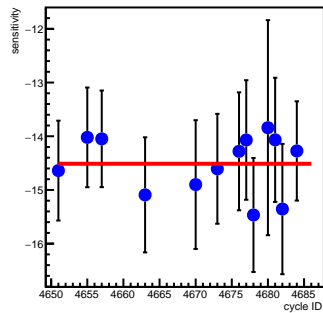
sam4\_coil3(ppm/count)



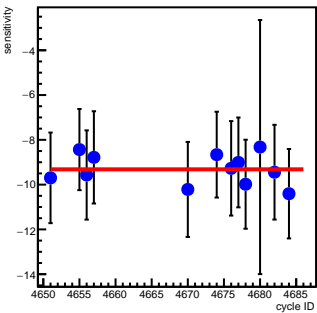
sam4\_coil4(ppm/count)



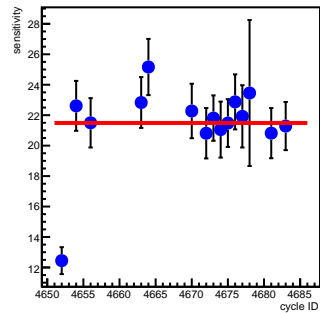
sam4\_coil6(ppm/count)



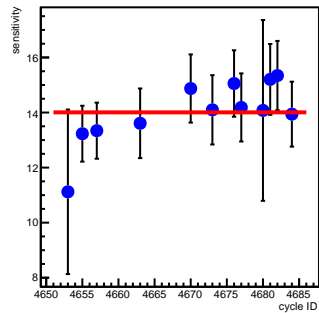
sam4\_coil7(ppm/count)



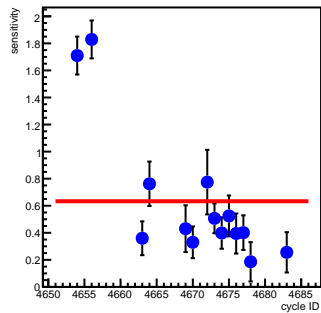
sam4\_coil2(ppm/count)



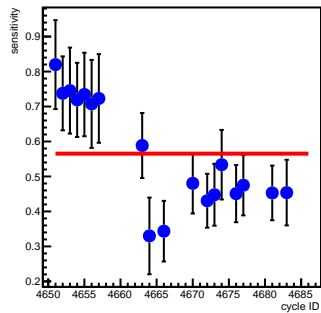
sam4\_coil5(ppm/count)



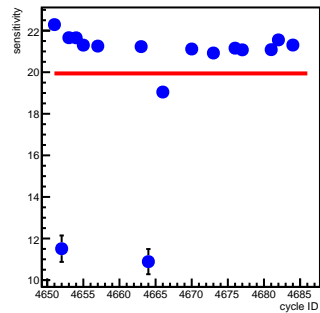
sam5\_coil1(ppm/count)



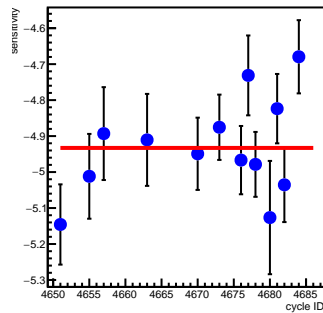
sam5\_coil3(ppm/count)



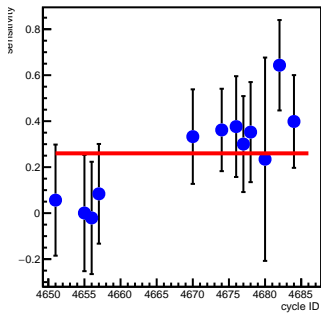
sam5\_coil4(ppm/count)



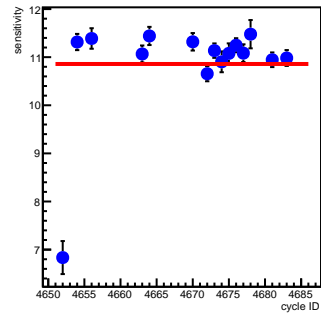
sam5\_coil6(ppm/count)



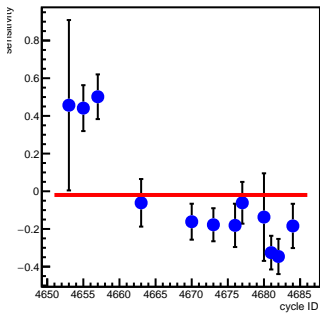
sam5\_coil7(ppm/count)



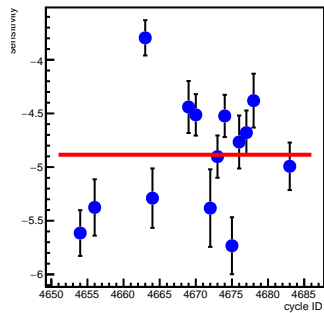
sam5\_coil2(ppm/count)



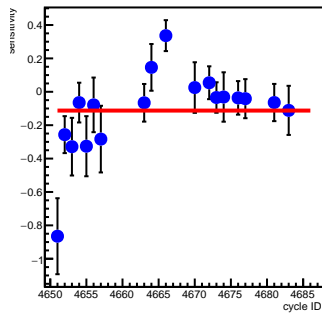
sam5\_coil5(ppm/count)



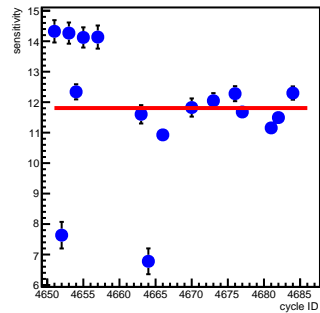
sam6\_coil1(ppm/count)



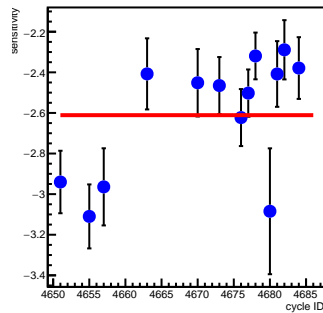
sam6\_coil3(ppm/count)



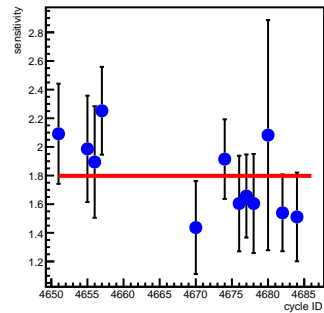
sam6\_coil4(ppm/count)



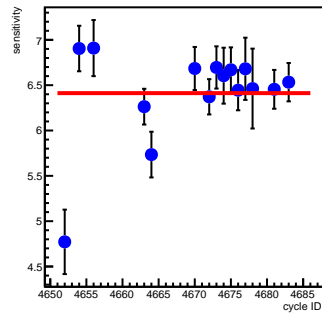
sam6\_coil6(ppm/count)



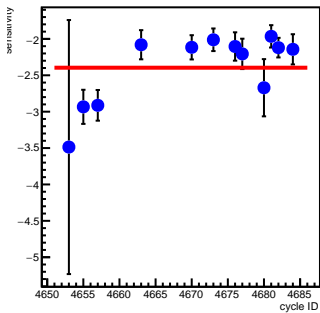
sam6\_coil7(ppm/count)



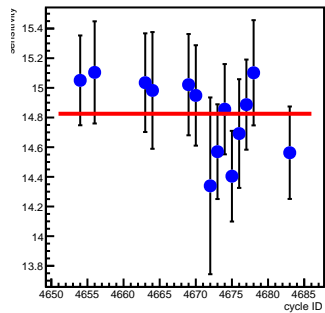
sam6\_coil2(ppm/count)



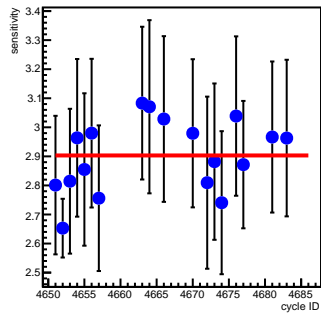
sam6\_coil5(ppm/count)



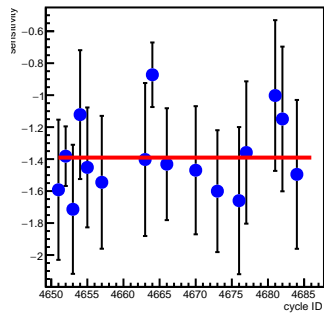
sam7\_coil1(ppm/count)



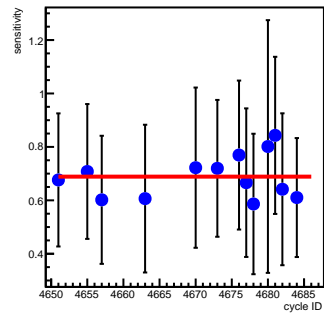
sam7\_coil3(ppm/count)



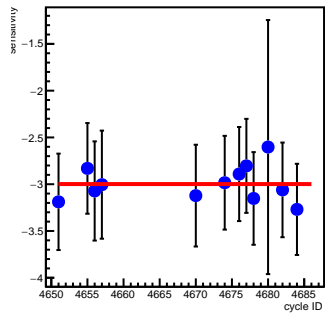
sam7\_coil4(ppm/count)



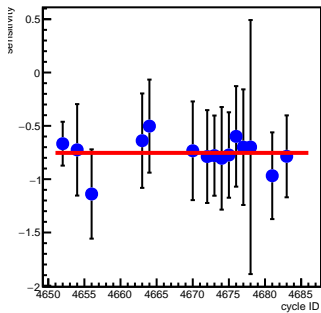
sam7\_coil6(ppm/count)



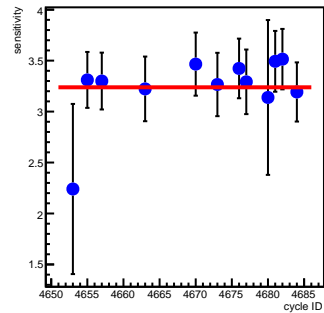
sam7\_coil7(ppm/count)



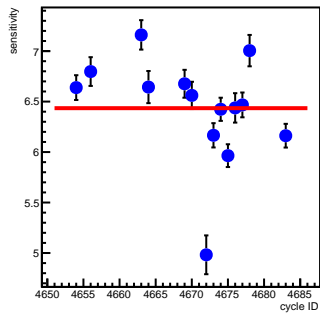
sam7\_coil2(ppm/count)



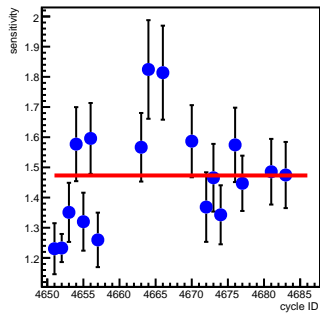
sam7\_coil5(ppm/count)



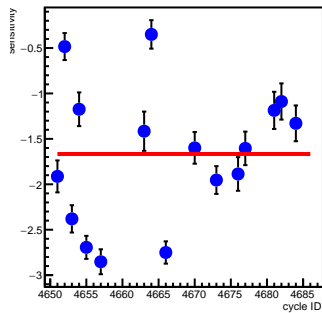
sam8\_coil1(ppm/count)



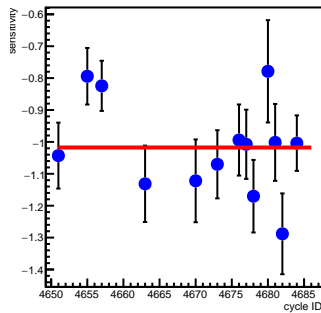
sam8\_coil3(ppm/count)



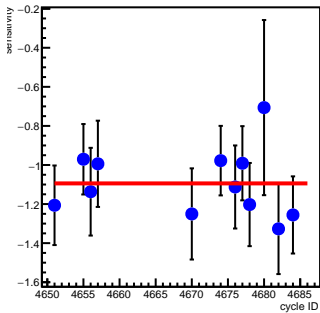
sam8\_coil4(ppm/count)



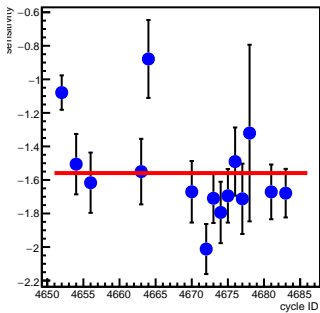
sam8\_coil6(ppm/count)



sam8\_coil7(ppm/count)



sam8\_coil2(ppm/count)



sam8\_coil5(ppm/count)

