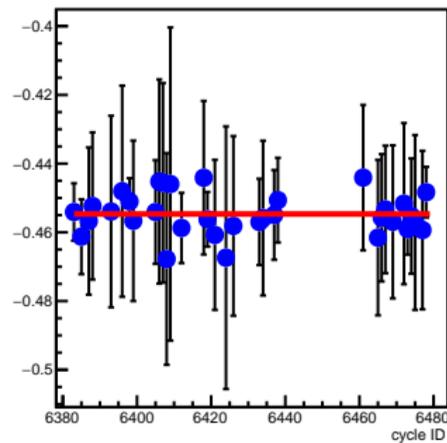
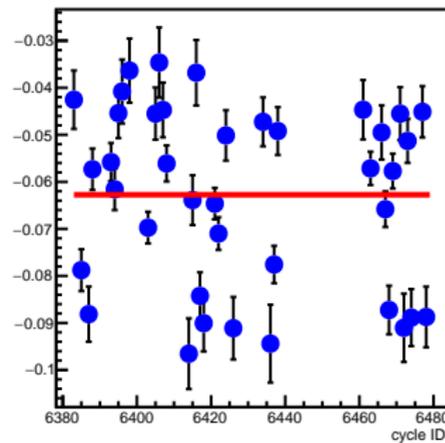


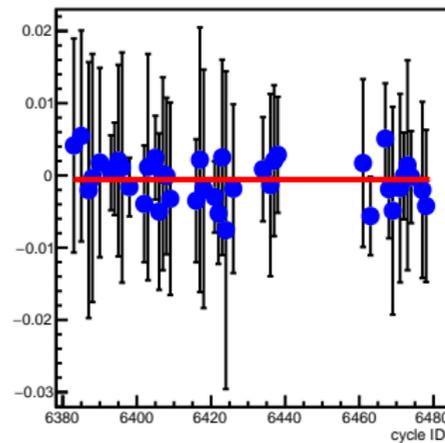
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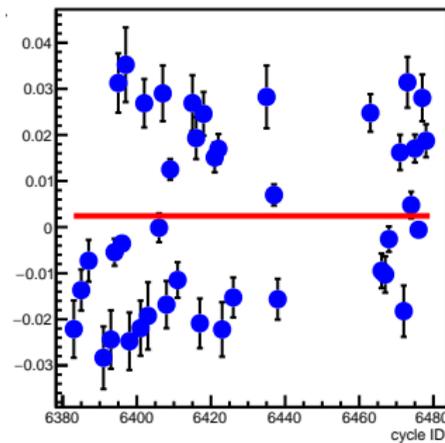
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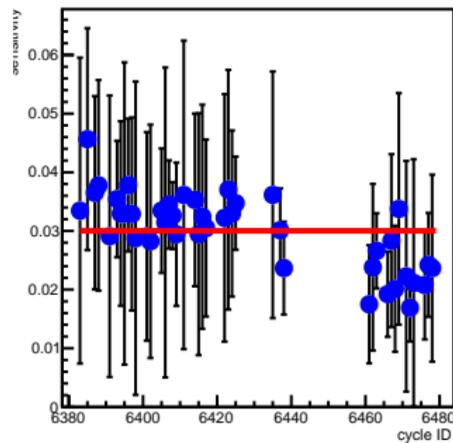
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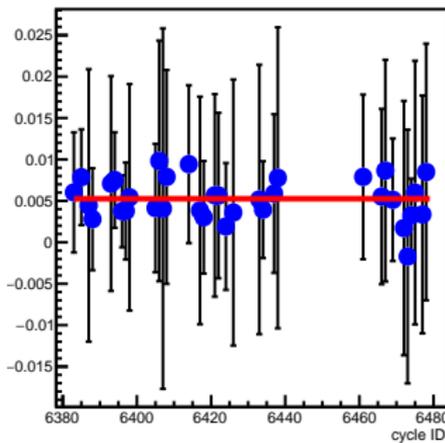
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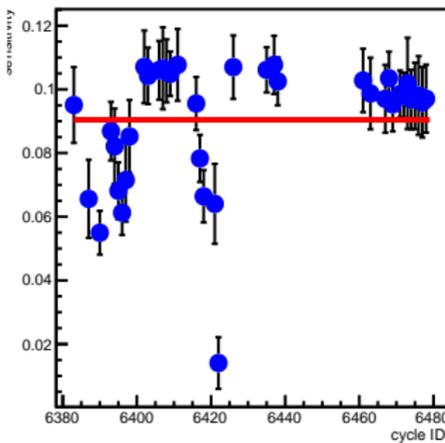
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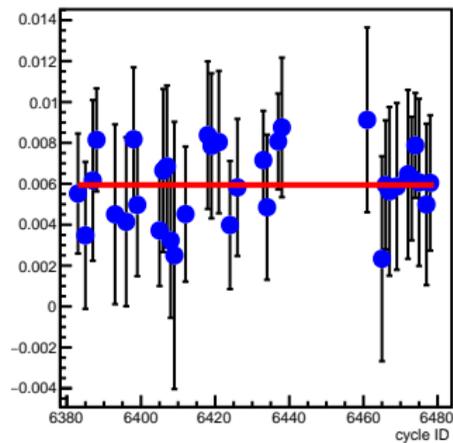
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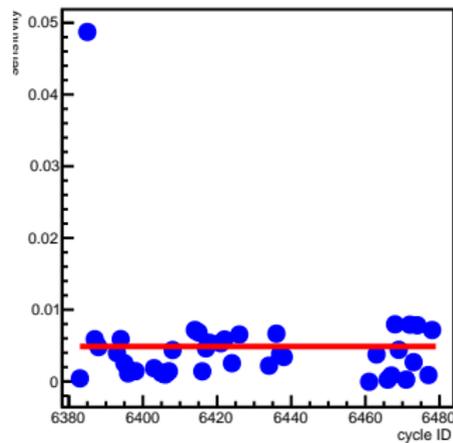
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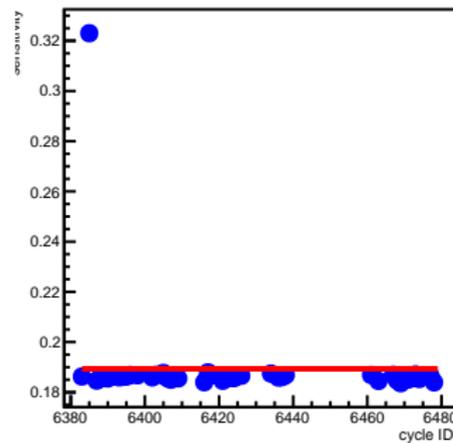
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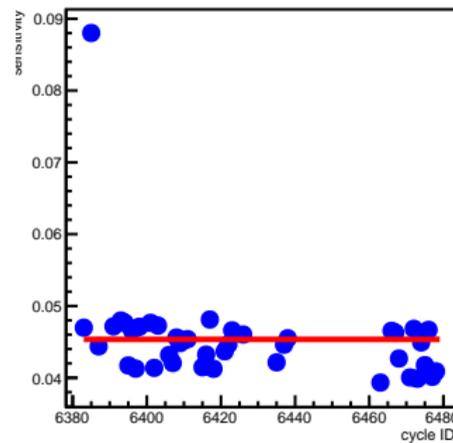
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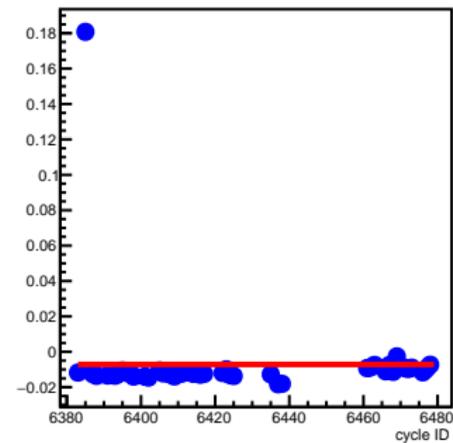
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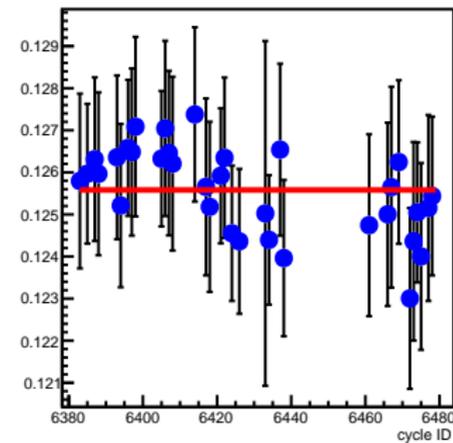
bpm4aY_coil6(um/count)



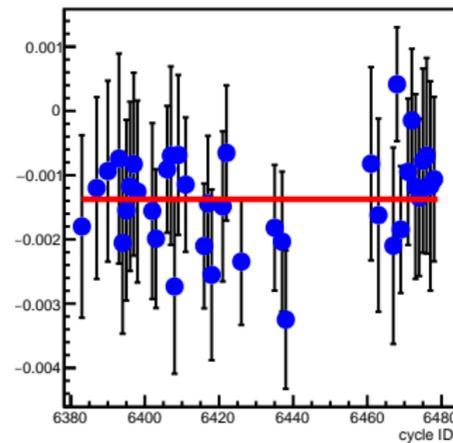
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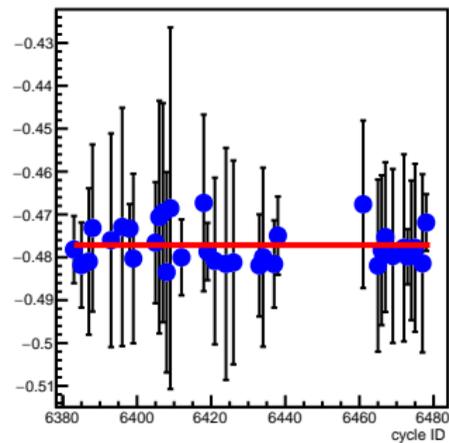
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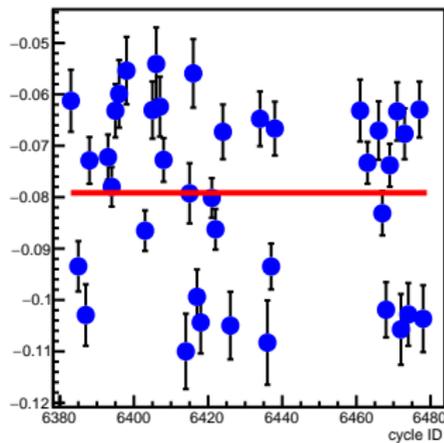
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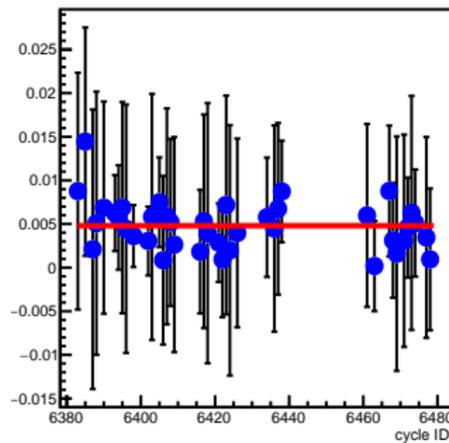
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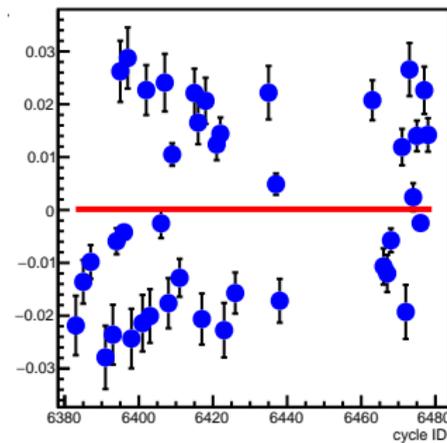
bpm4eX_coil3(um/count)



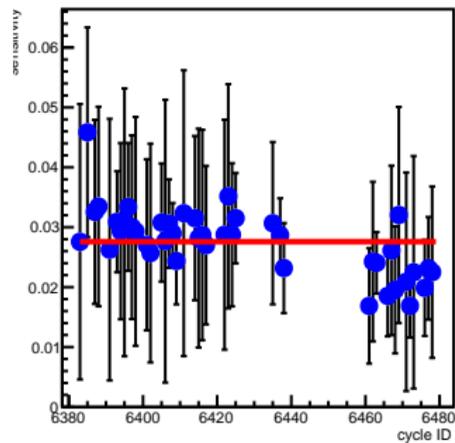
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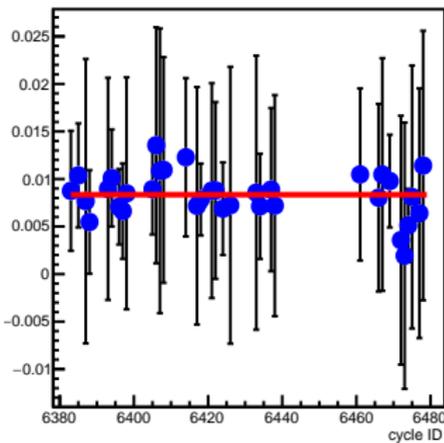
bpm4eX_coil6(um/count)



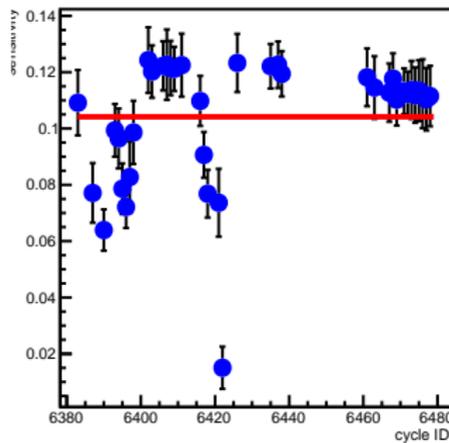
bpm4eX_coil7(um/count)



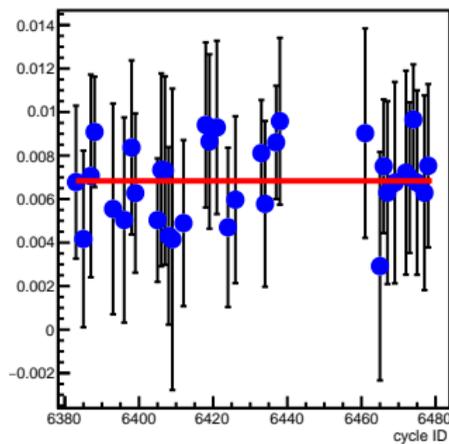
bpm4eX_coil2(um/count)



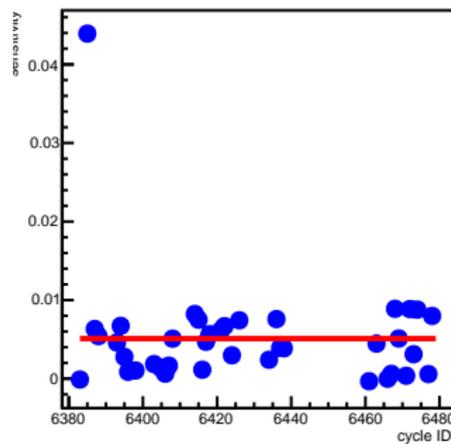
bpm4eX_coil5(um/count)



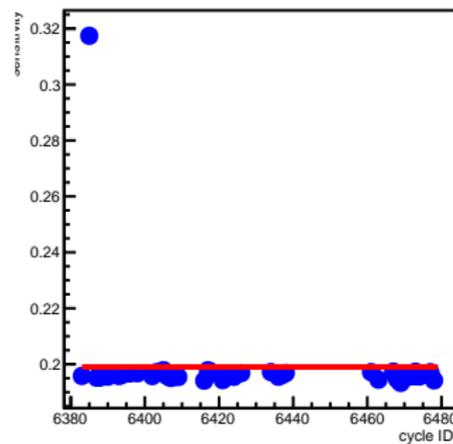
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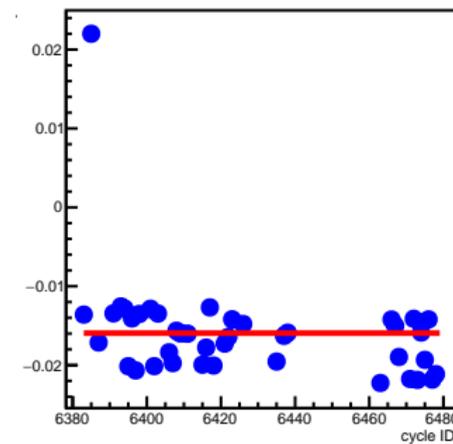
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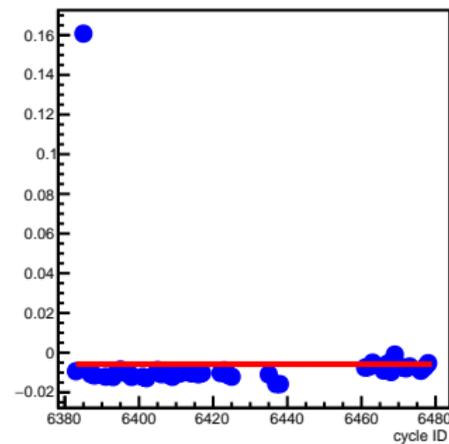
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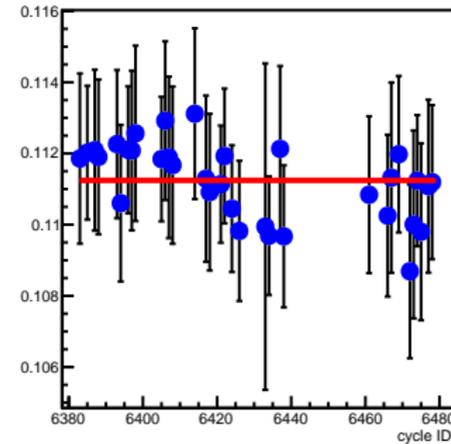
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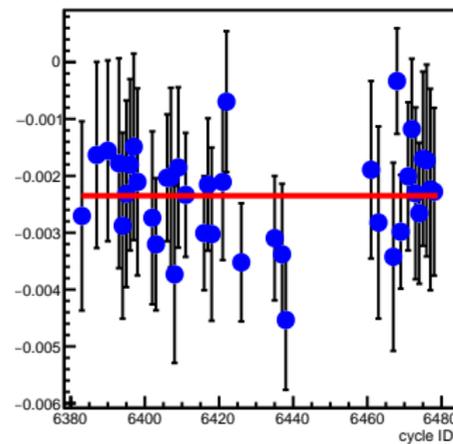
bpm4eY_coil7(um/count)



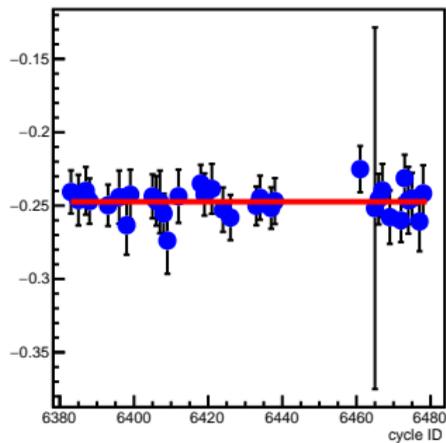
bpm4eY_coil2(um/count)



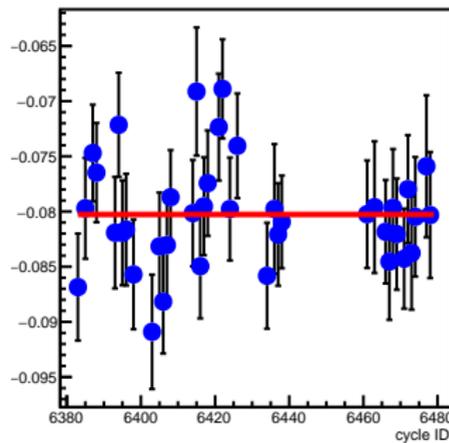
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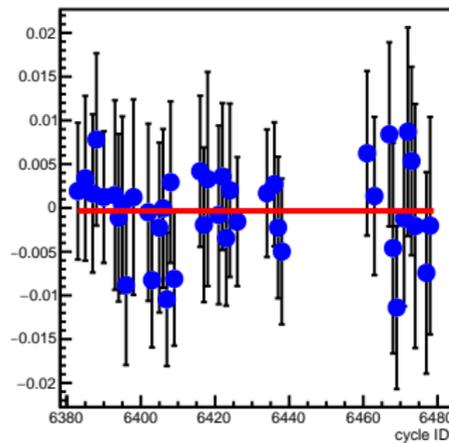
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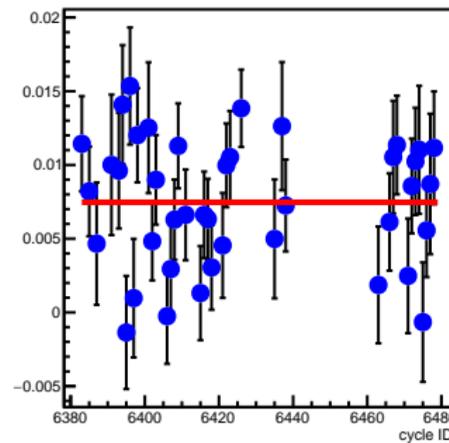
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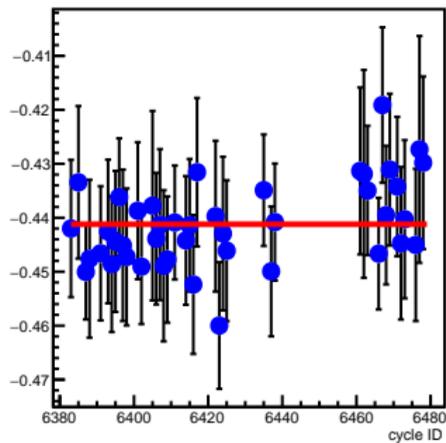
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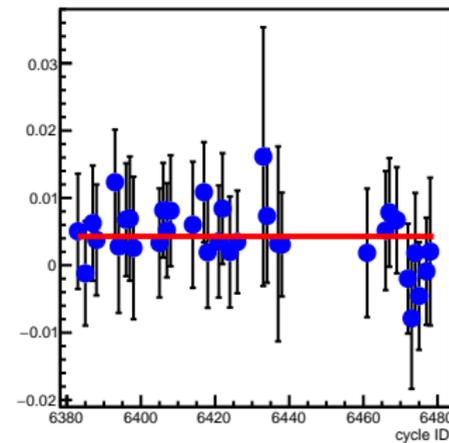
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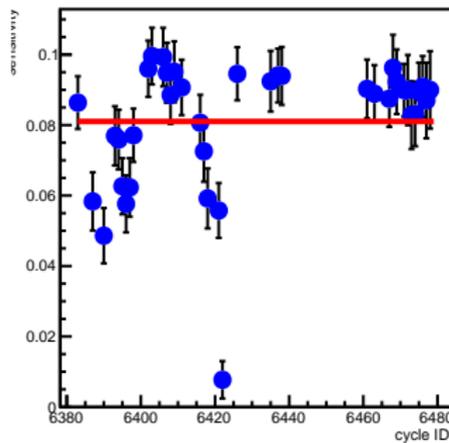
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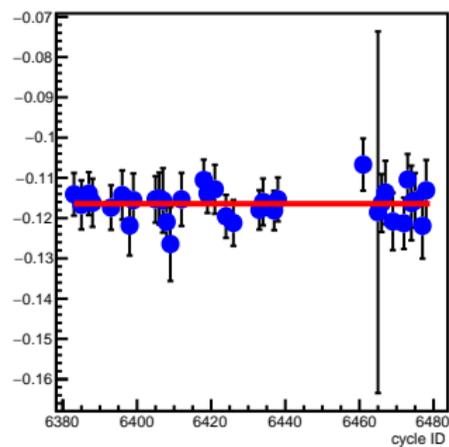
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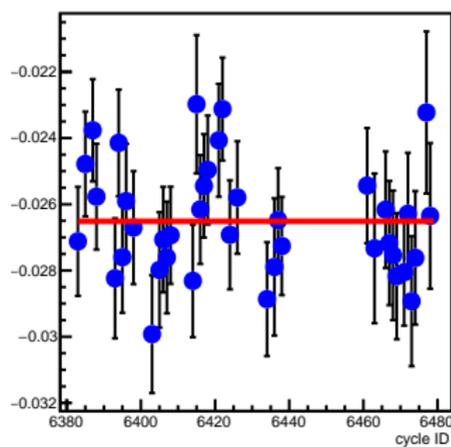
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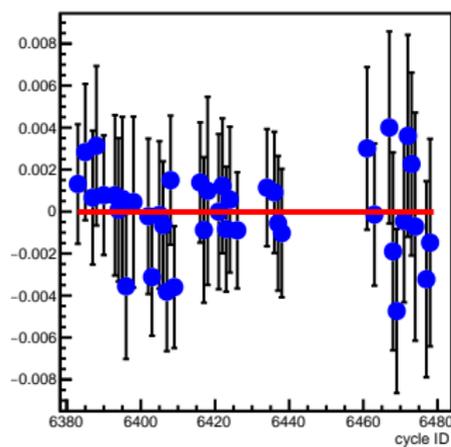
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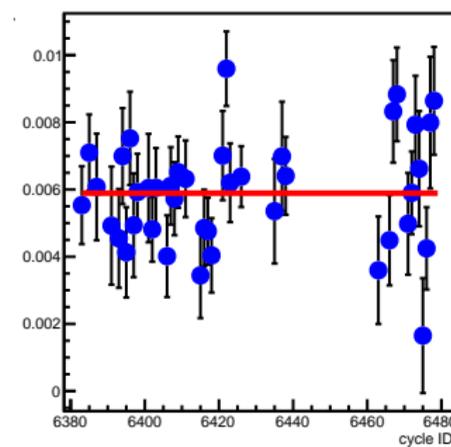
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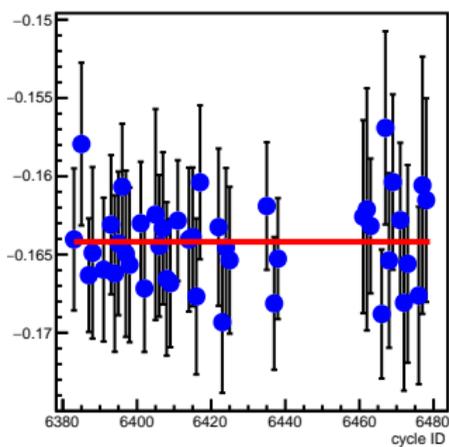
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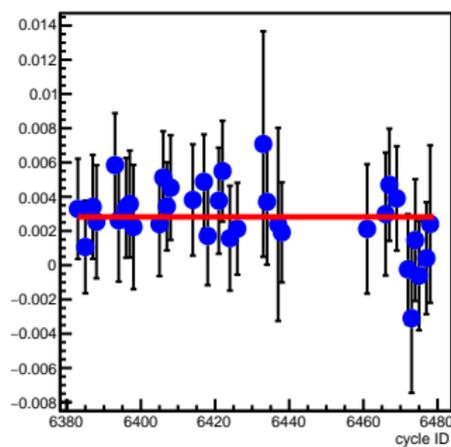
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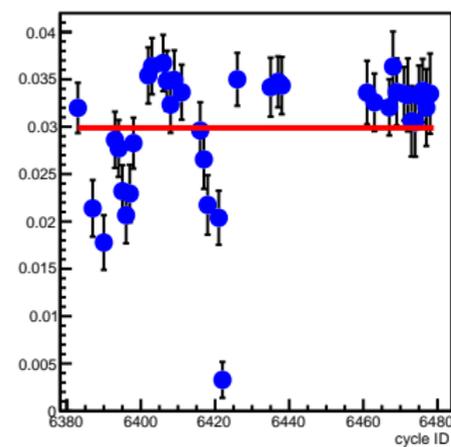
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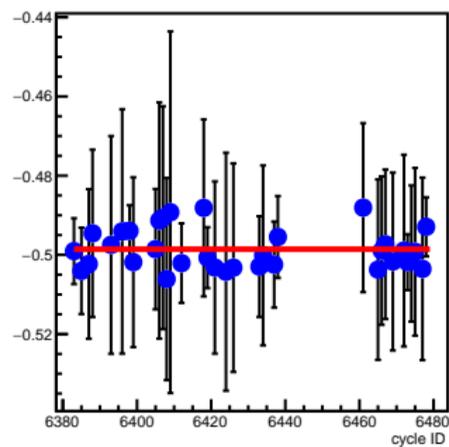
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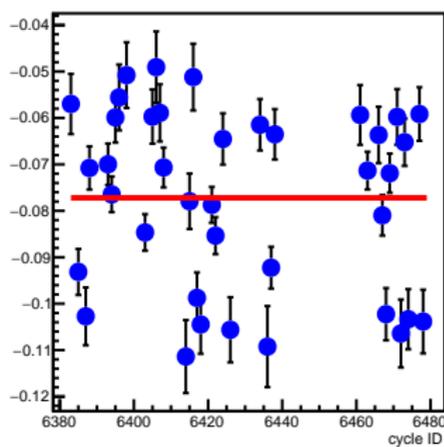
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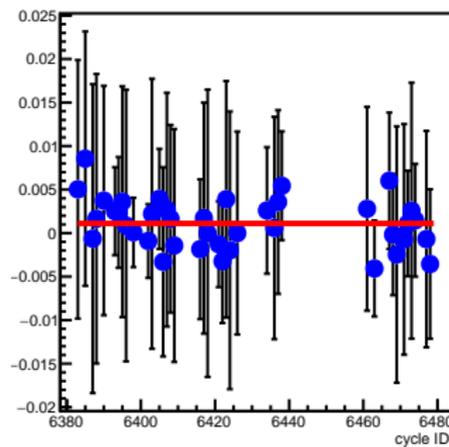
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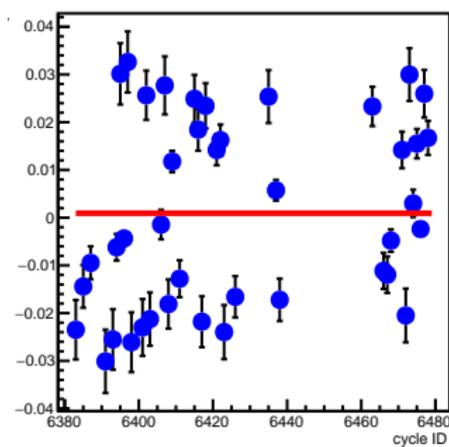
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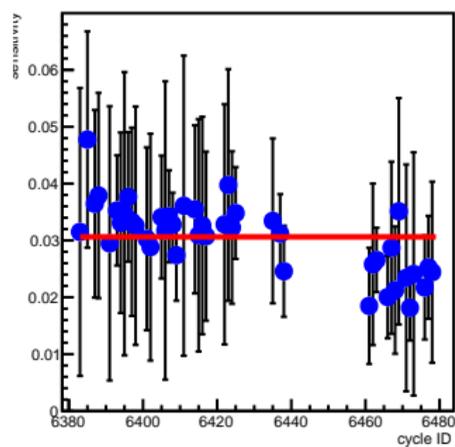
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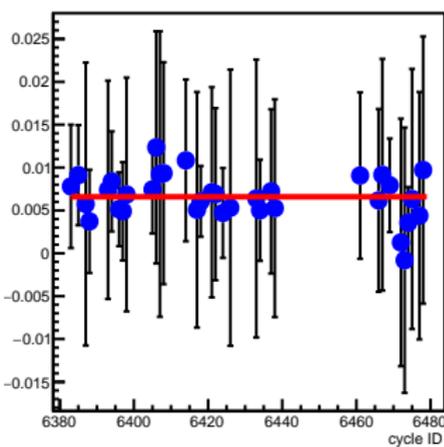
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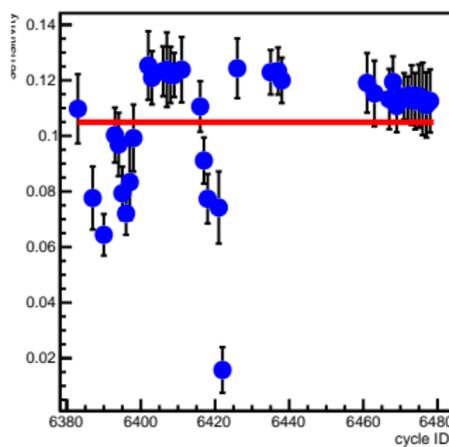
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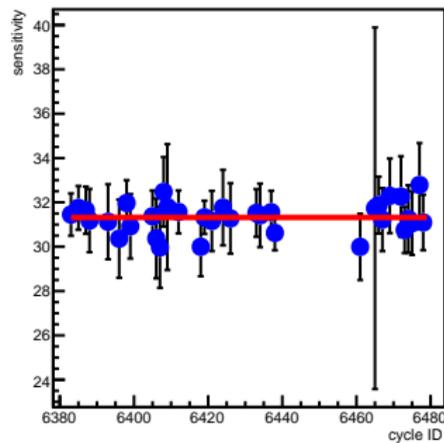
bpm4aX_coil2(um/count)



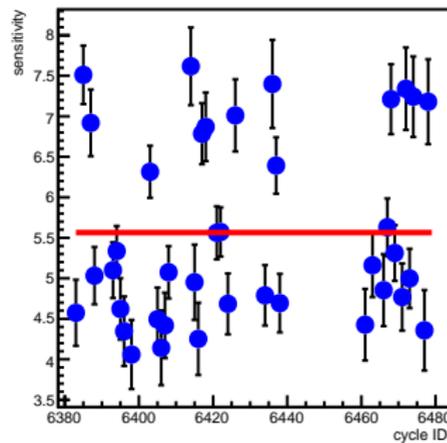
bpm4aX_coil5(um/count)



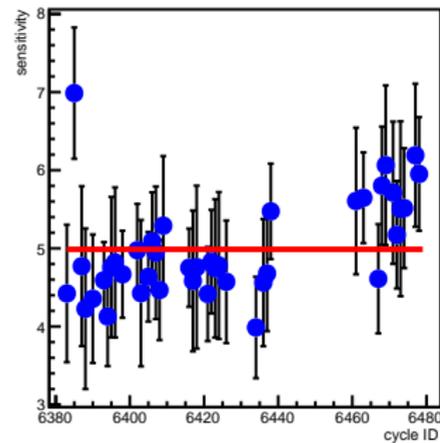
usl_coil1(ppm/count)



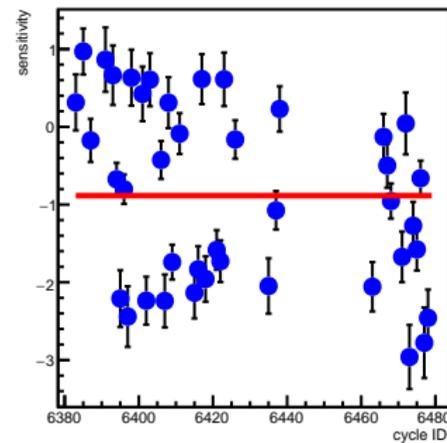
usl_coil3(ppm/count)



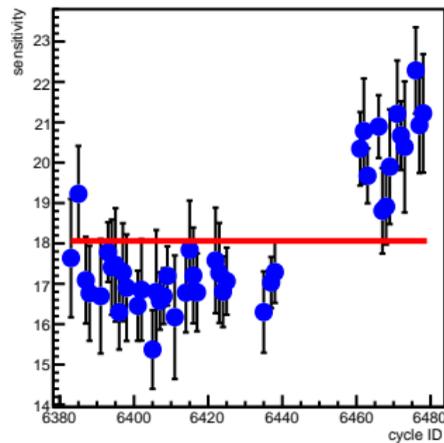
usl_coil4(ppm/count)



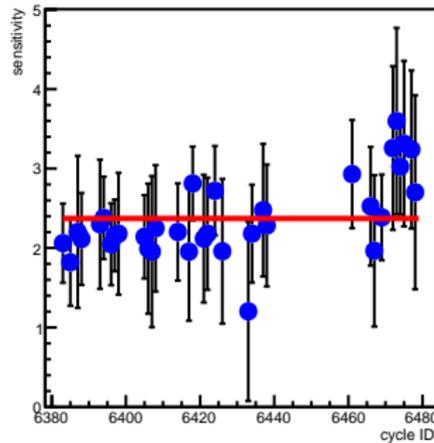
usl_coil6(ppm/count)



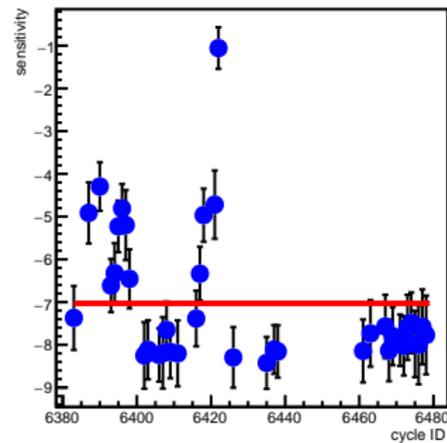
usl_coil7(ppm/count)



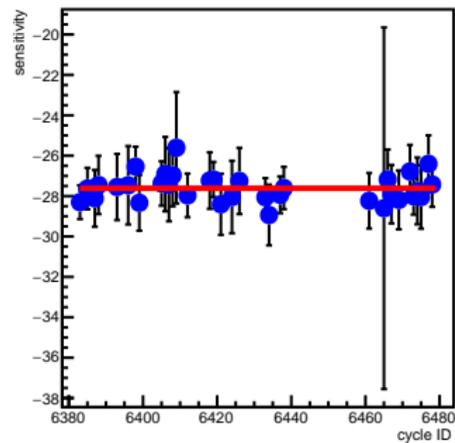
usl_coil2(ppm/count)



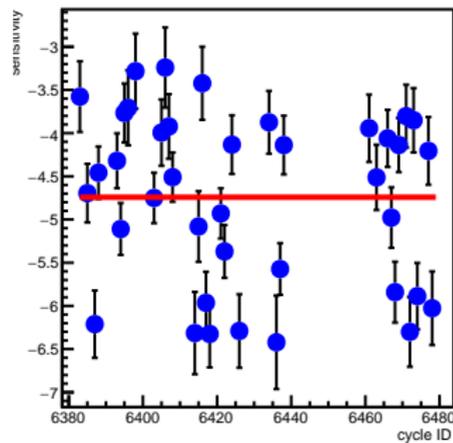
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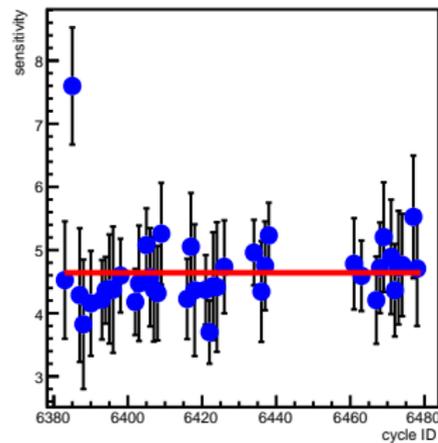
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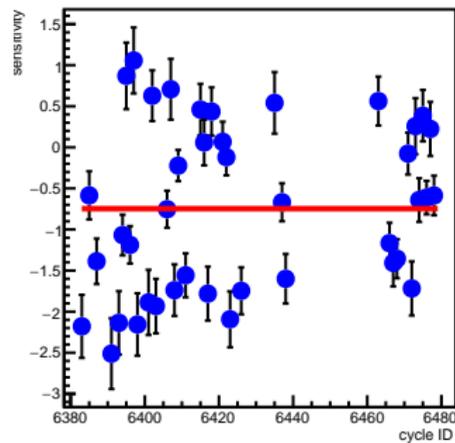
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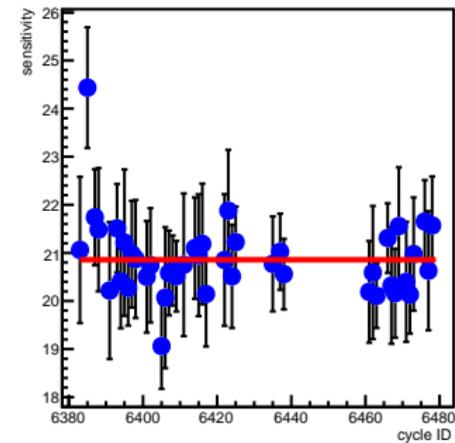
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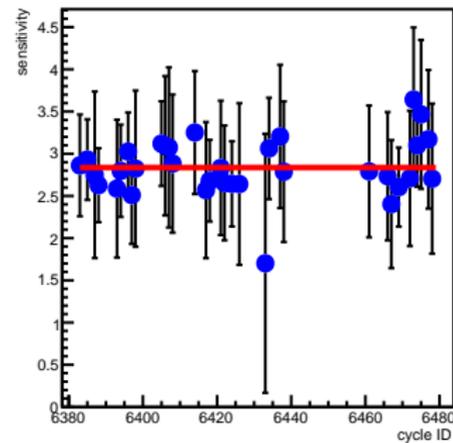
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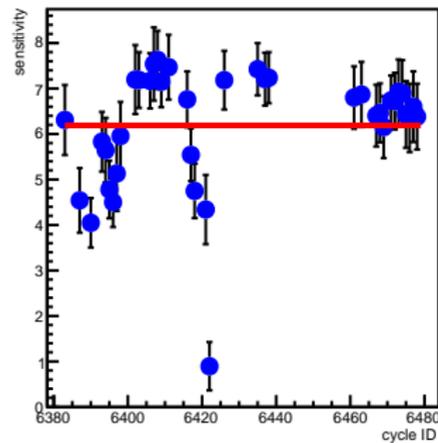
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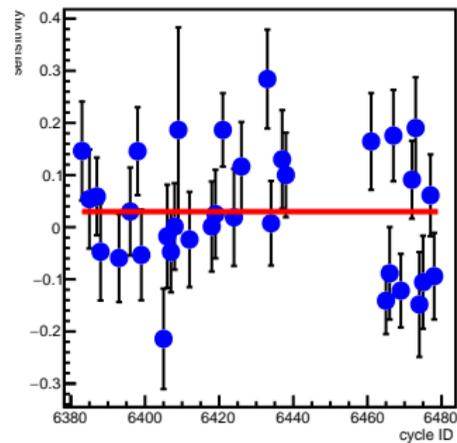
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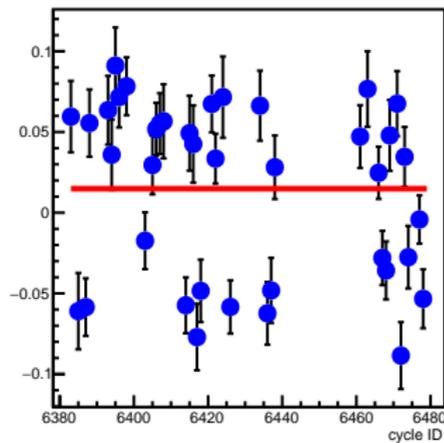
usr_coil5(ppm/count)



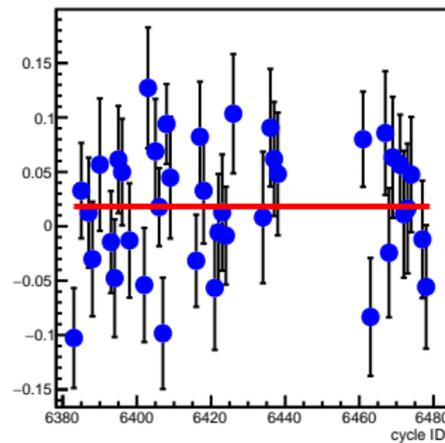
dsl_coil1(ppm/count)



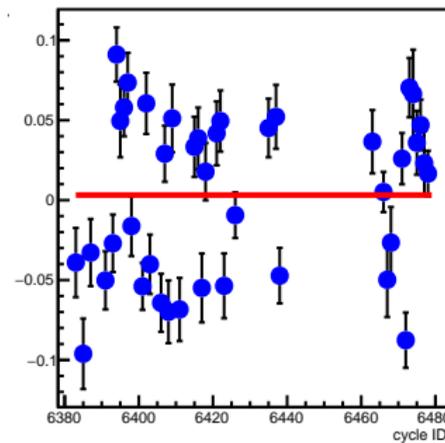
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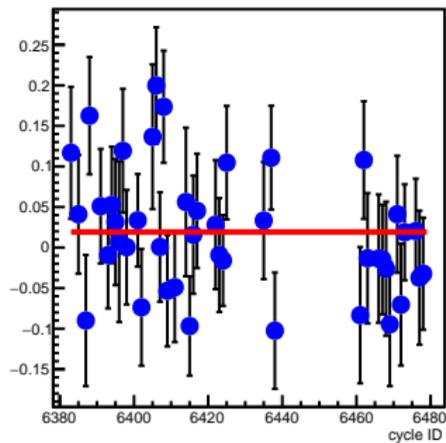
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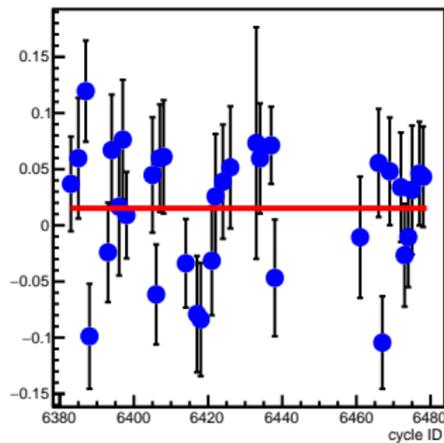
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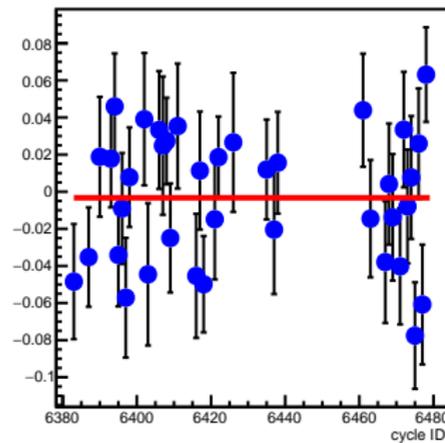
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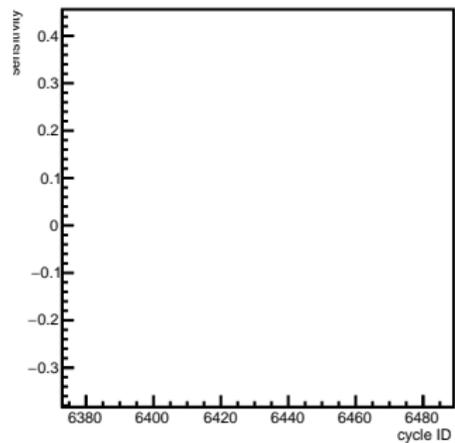
dsl_coil2(ppm/count)



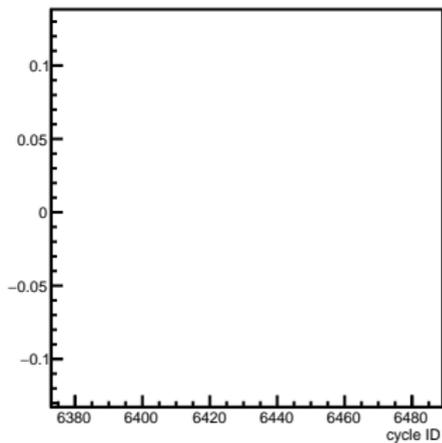
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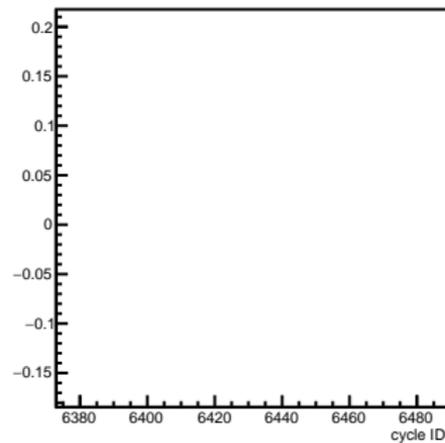
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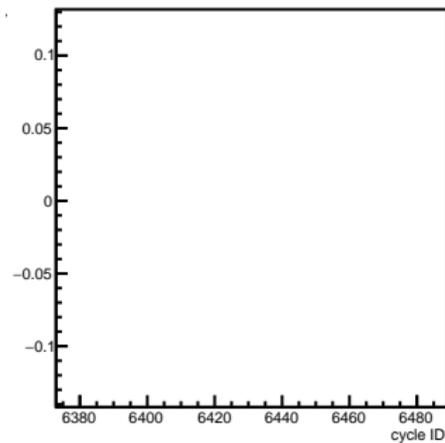
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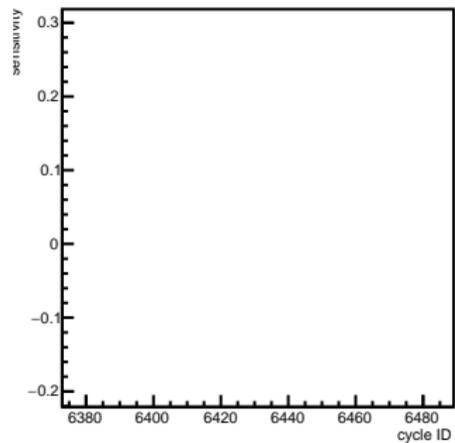
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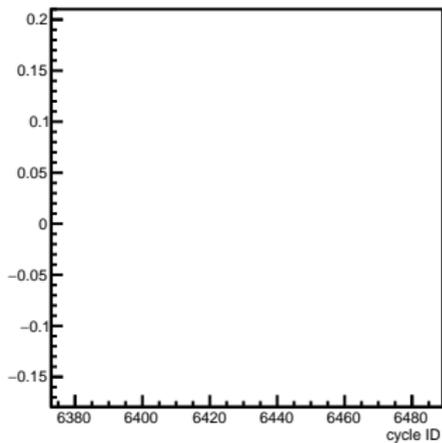
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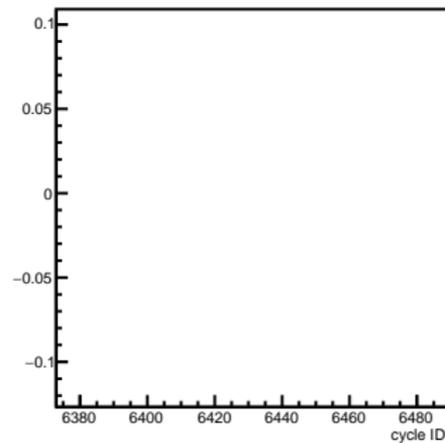
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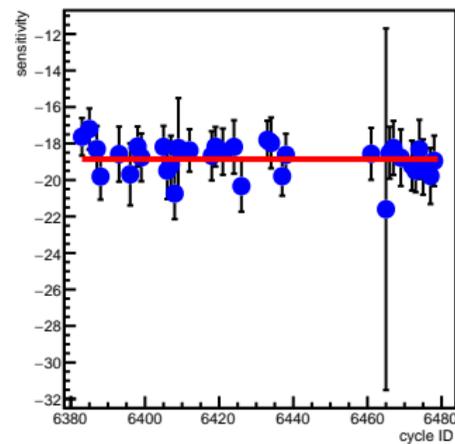
dsr_coil2(ppm/count)



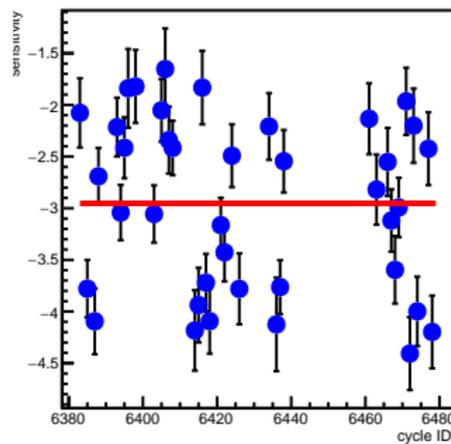
dsr_coil5(ppm/count)



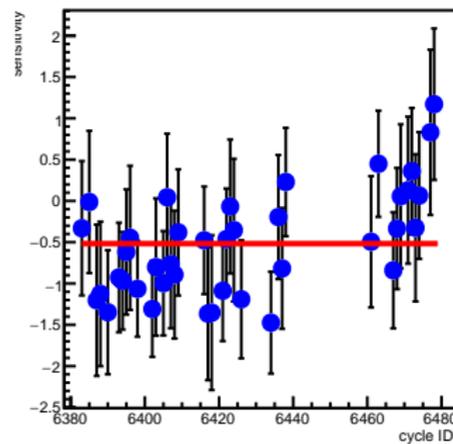
at1_coil1(ppm/count)



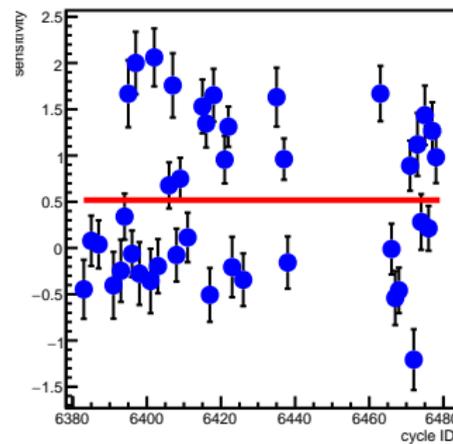
at1_coil3(ppm/count)



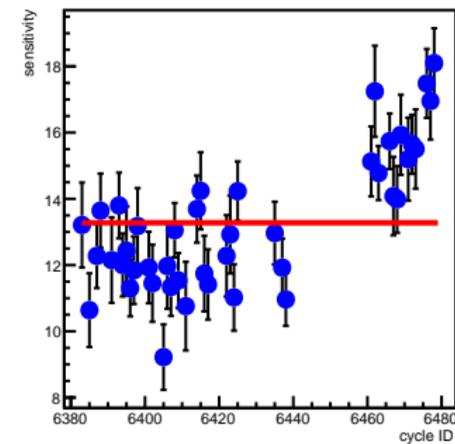
at1_coil4(ppm/count)



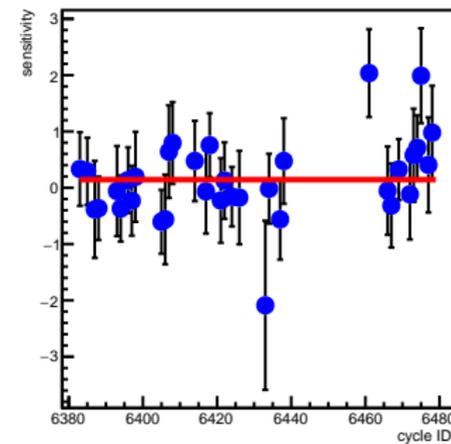
at1_coil6(ppm/count)



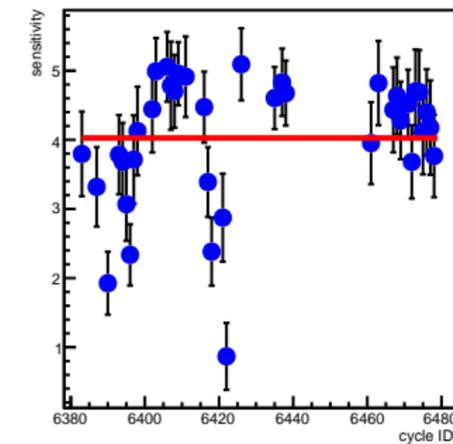
at1_coil7(ppm/count)



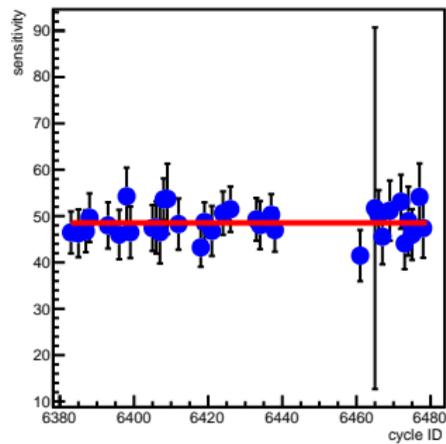
at1_coil2(ppm/count)



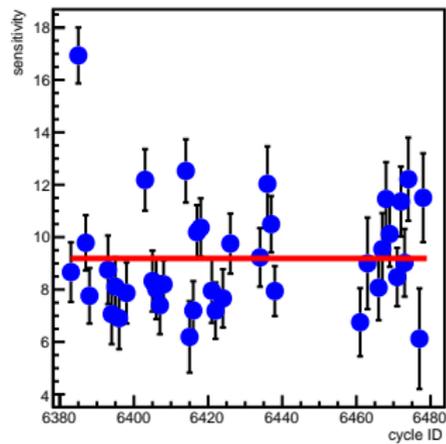
at1_coil5(ppm/count)



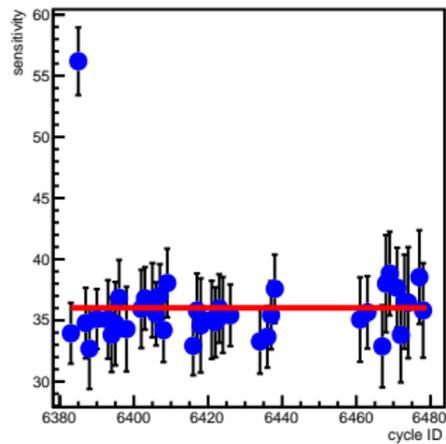
at2_coi1(ppm/count)



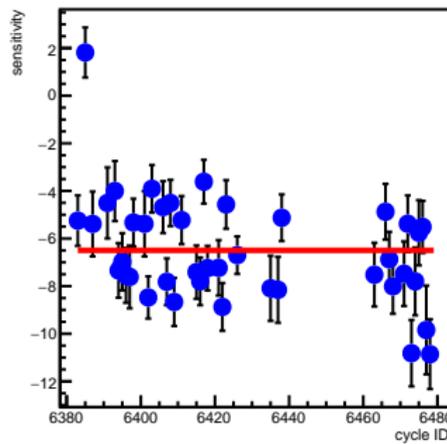
at2_coi3(ppm/count)



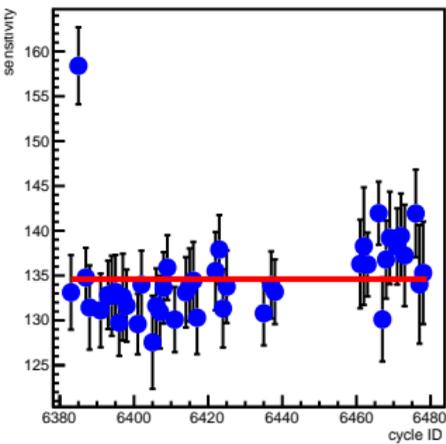
at2_coi4(ppm/count)



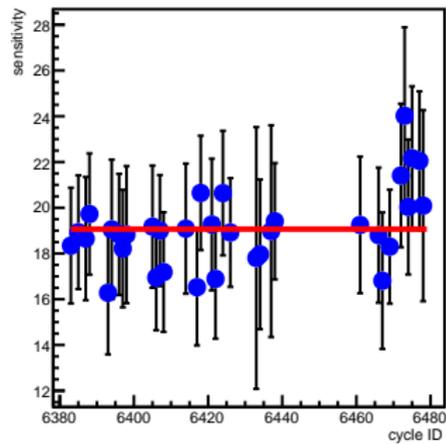
at2_coi6(ppm/count)



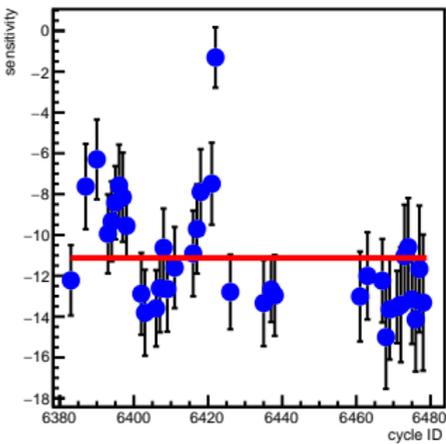
at2_coi7(ppm/count)



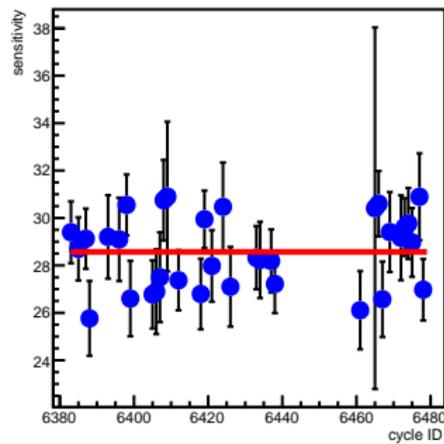
at2_coi2(ppm/count)



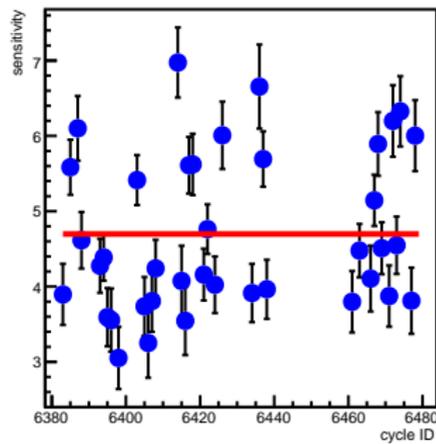
at2_coi5(ppm/count)



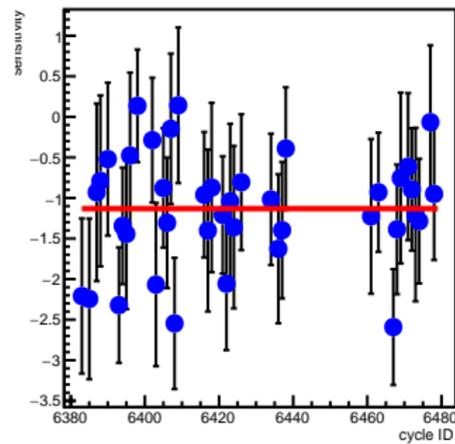
atr1_coil1(ppm/count)



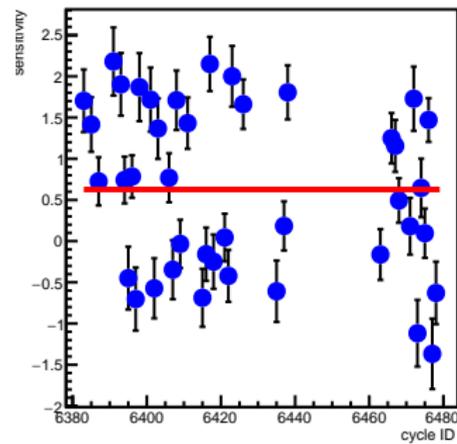
atr1_coil3(ppm/count)



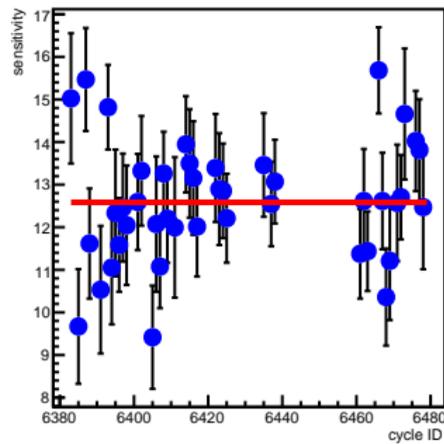
atr1_coil4(ppm/count)



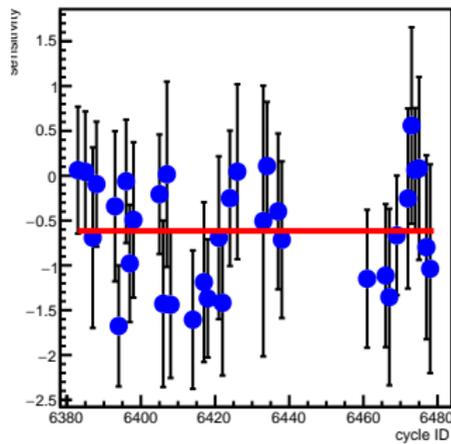
atr1_coil6(ppm/count)



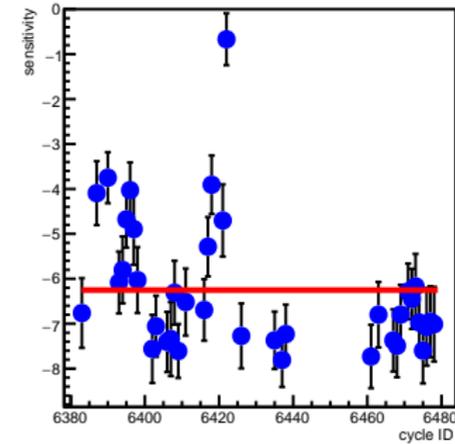
atr1_coil7(ppm/count)



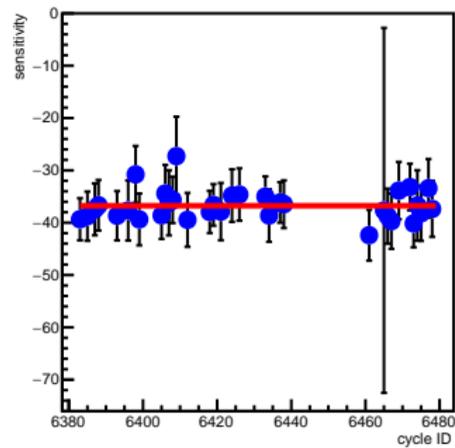
atr1_coil2(ppm/count)



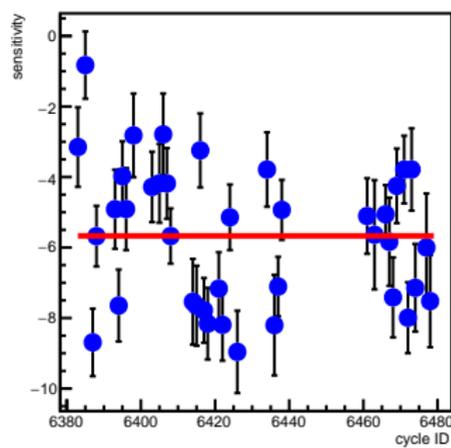
atr1_coil5(ppm/count)



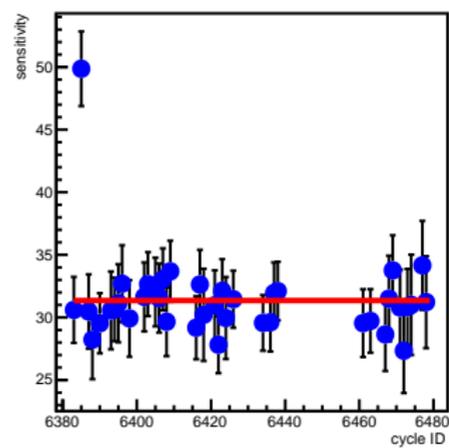
atr2_coil1(ppm/count)



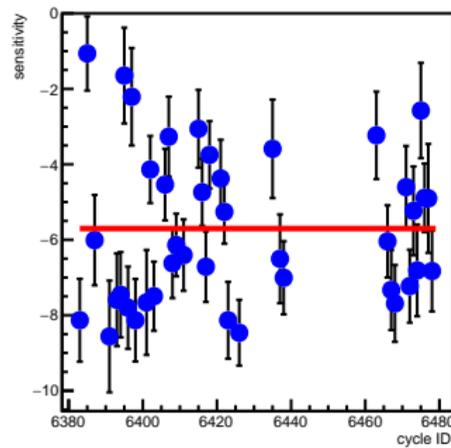
atr2_coil3(ppm/count)



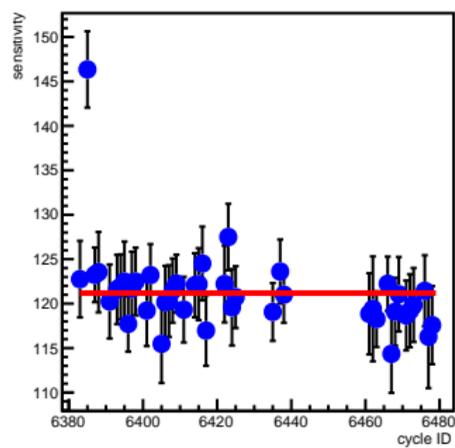
atr2_coil4(ppm/count)



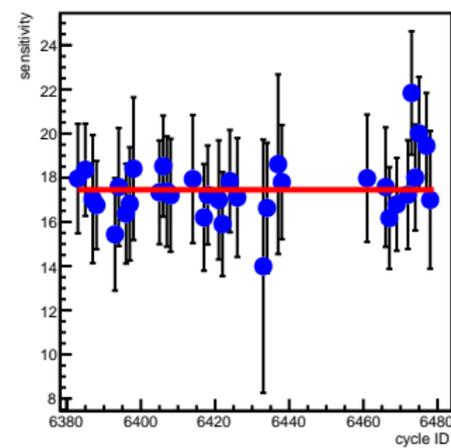
atr2_coil6(ppm/count)



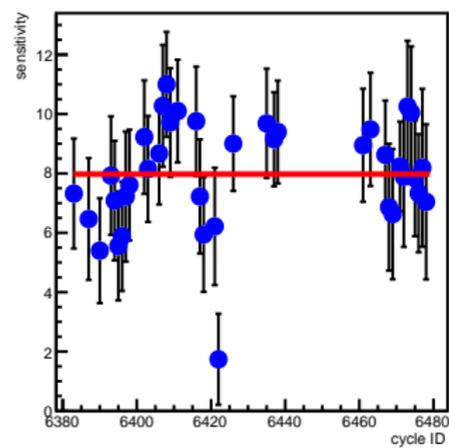
atr2_coil7(ppm/count)



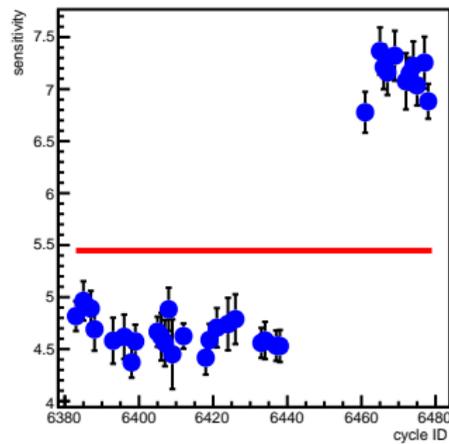
atr2_coil2(ppm/count)



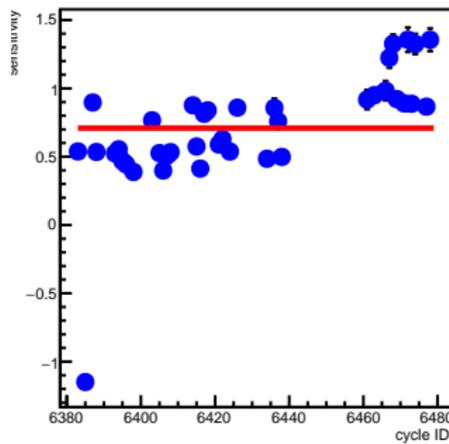
atr2_coil5(ppm/count)



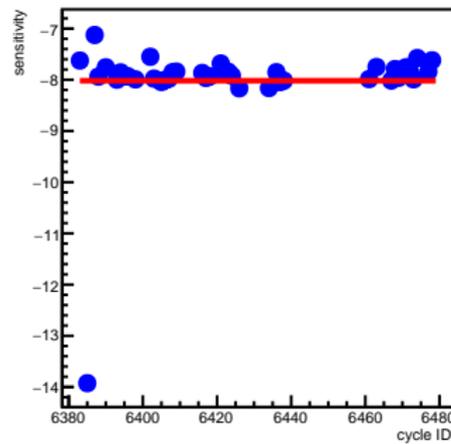
sam1_coil1(ppm/count)



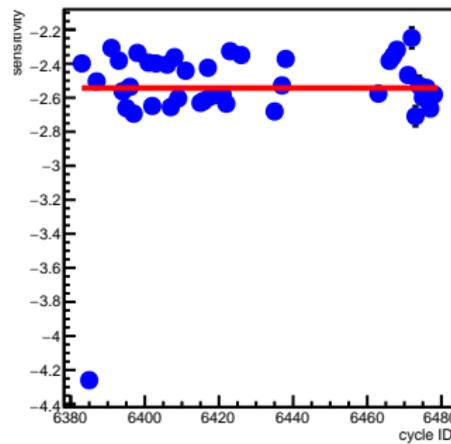
sam1_coil3(ppm/count)



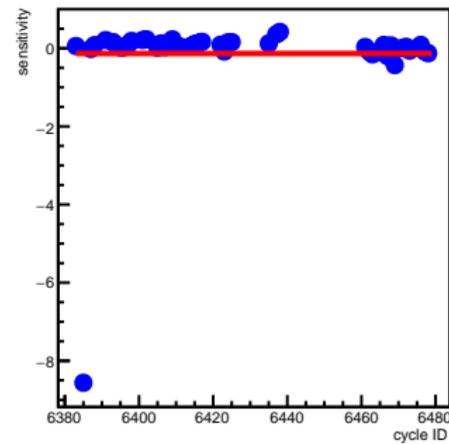
sam1_coil4(ppm/count)



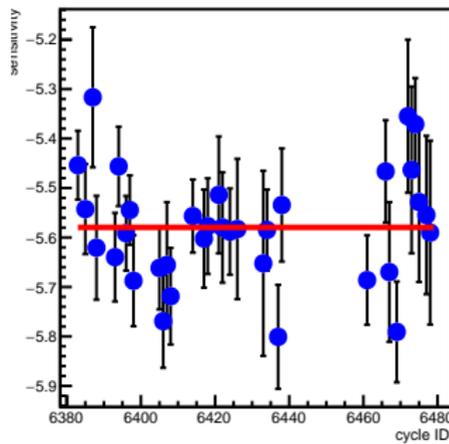
sam1_coil6(ppm/count)



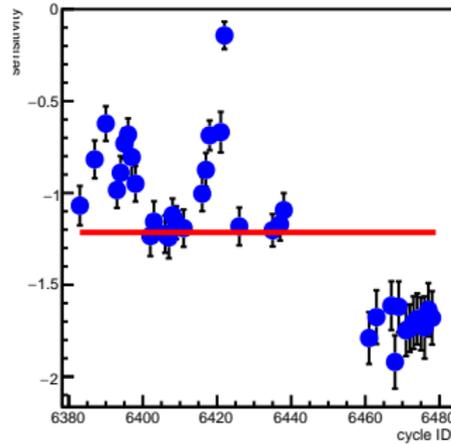
sam1_coil7(ppm/count)



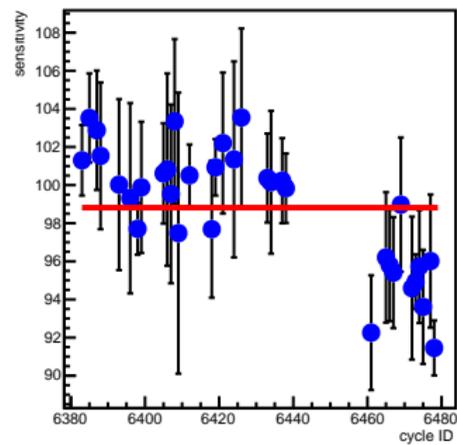
sam1_coil2(ppm/count)



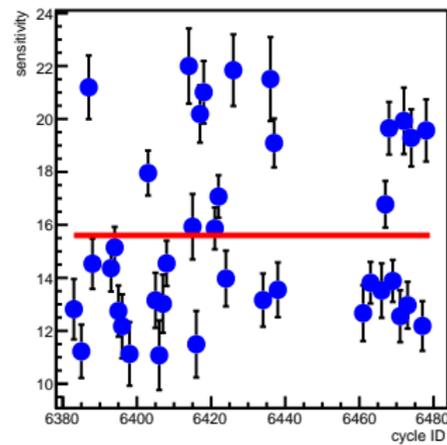
sam1_coil5(ppm/count)



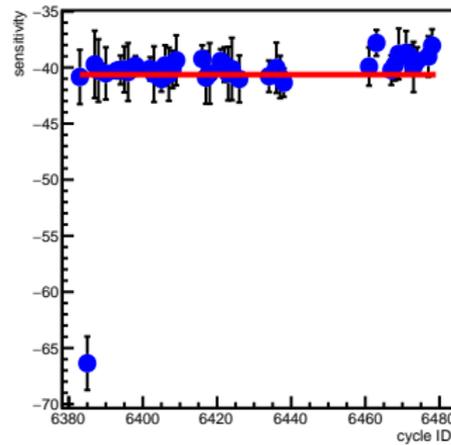
sam2_coil1(ppm/count)



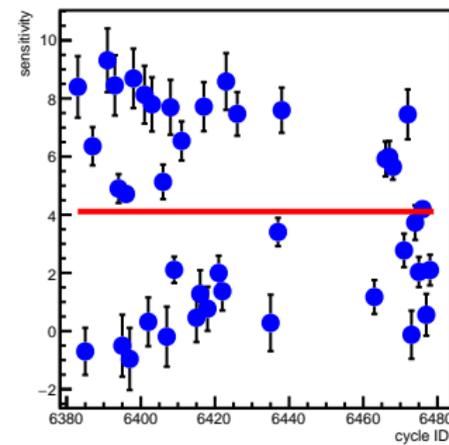
sam2_coil3(ppm/count)



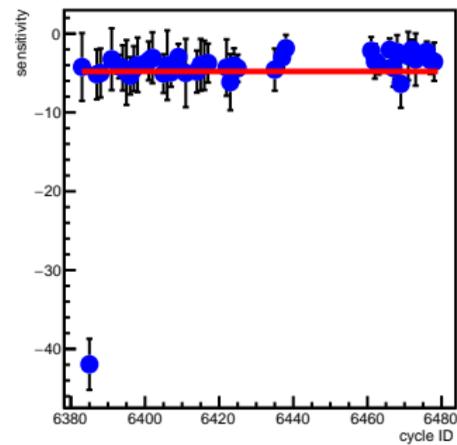
sam2_coil4(ppm/count)



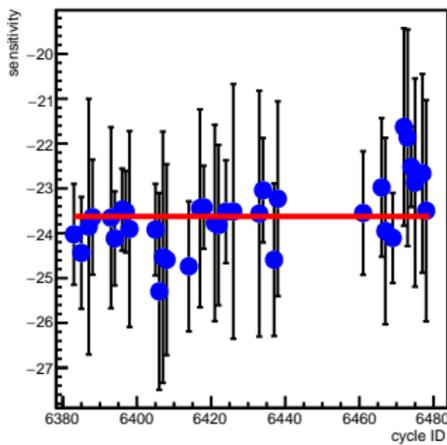
sam2_coil6(ppm/count)



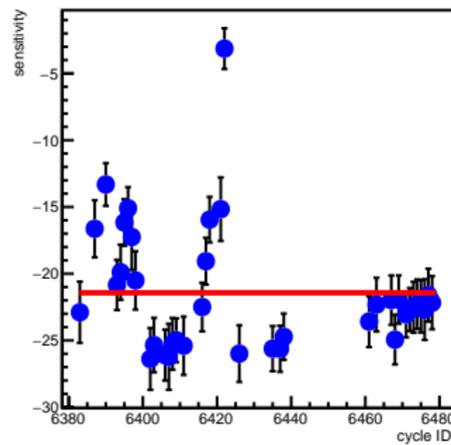
sam2_coil7(ppm/count)



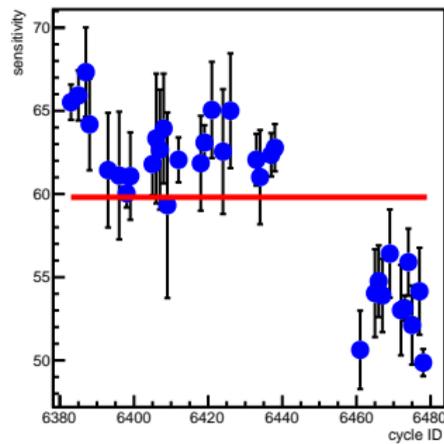
sam2_coil2(ppm/count)



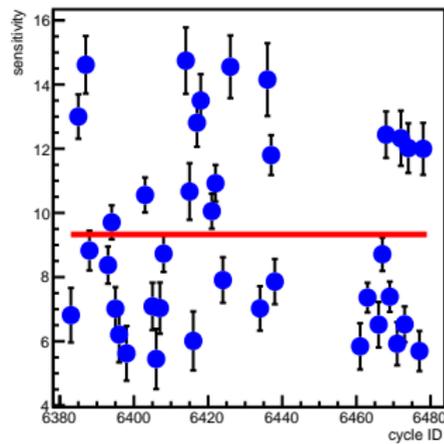
sam2_coil5(ppm/count)



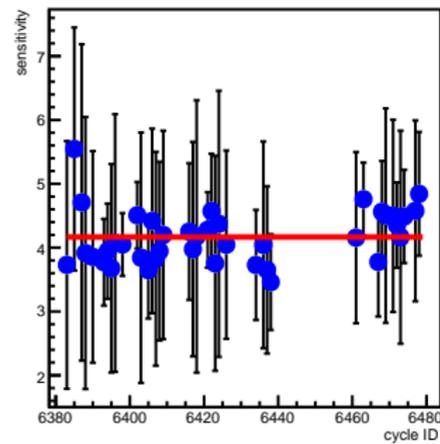
sam3_coil1(ppm/count)



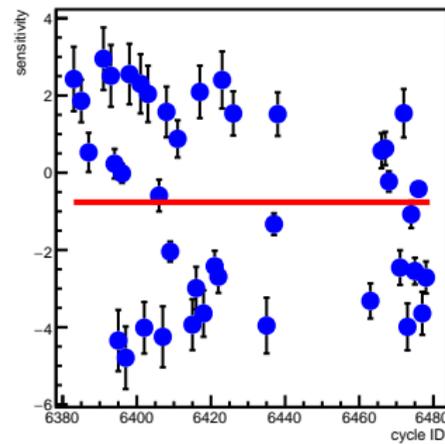
sam3_coil3(ppm/count)



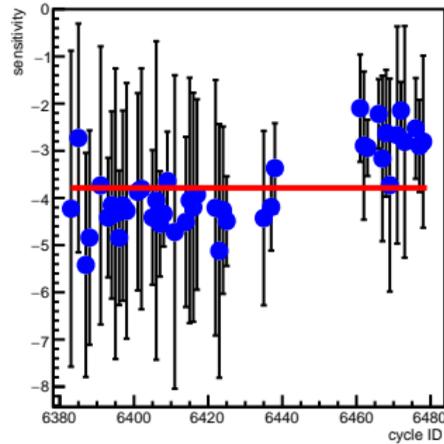
sam3_coil4(ppm/count)



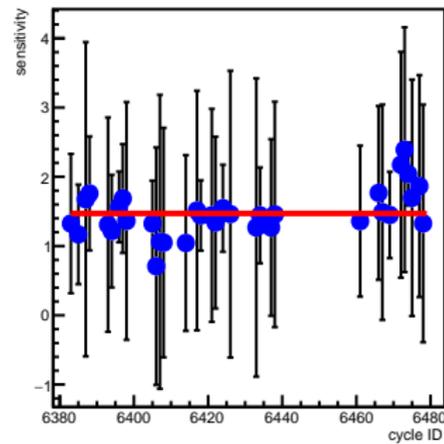
sam3_coil6(ppm/count)



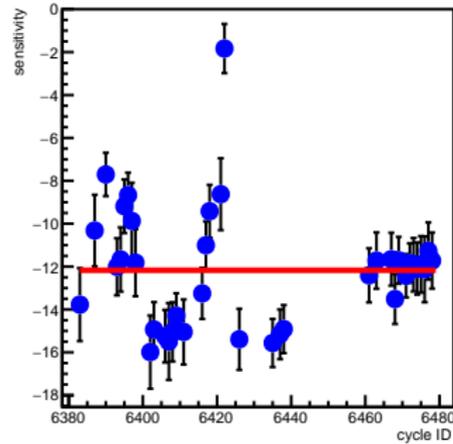
sam3_coil7(ppm/count)



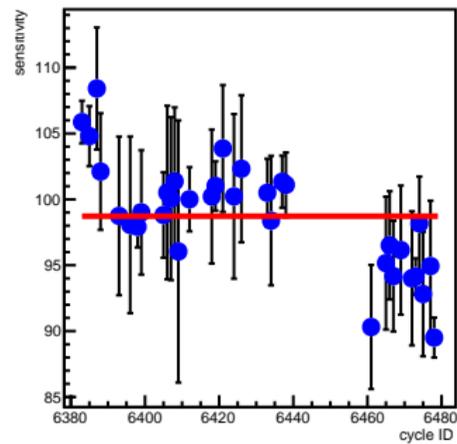
sam3_coil2(ppm/count)



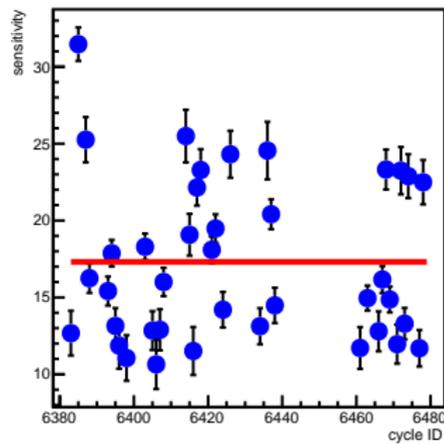
sam3_coil5(ppm/count)



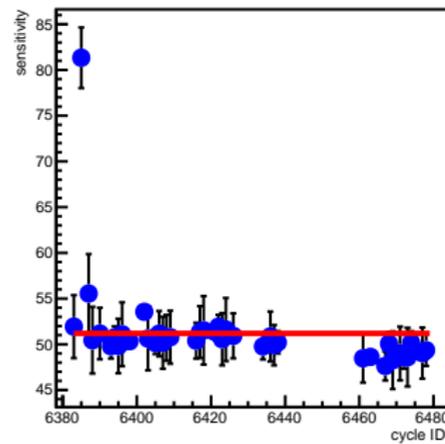
sam4_coil1(ppm/count)



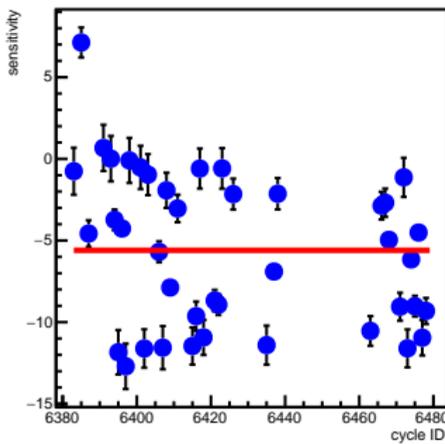
sam4_coil3(ppm/count)



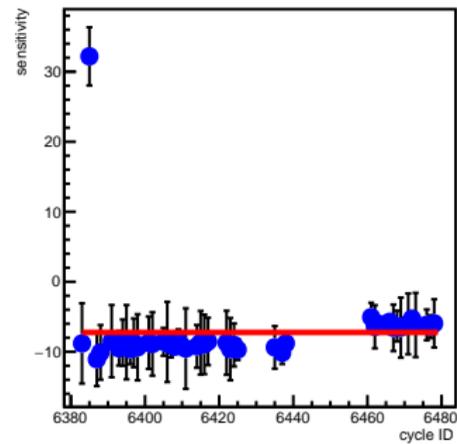
sam4_coil4(ppm/count)



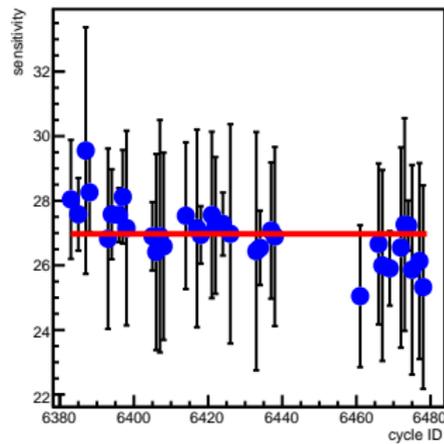
sam4_coil6(ppm/count)



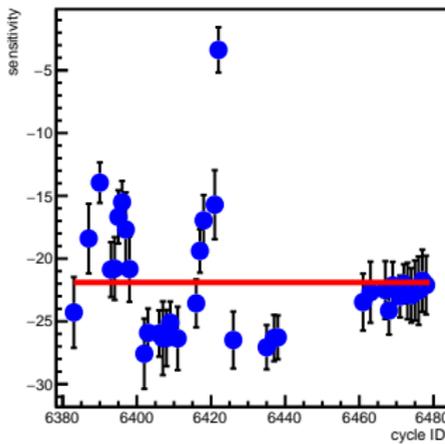
sam4_coil7(ppm/count)



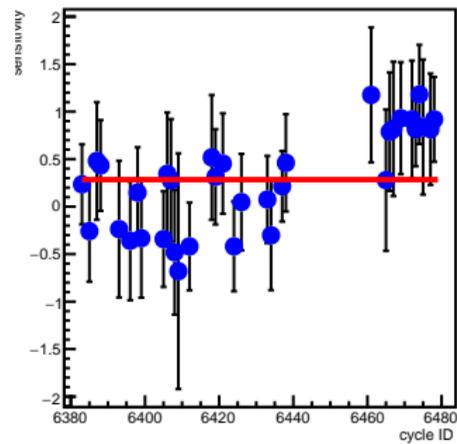
sam4_coil2(ppm/count)



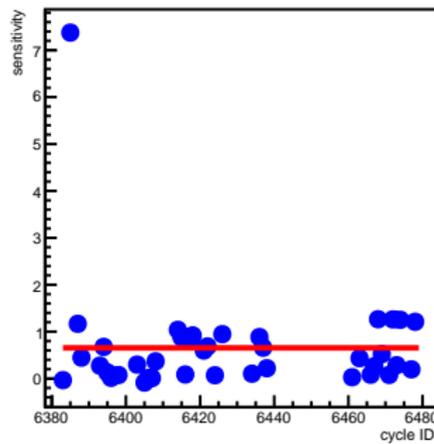
sam4_coil5(ppm/count)



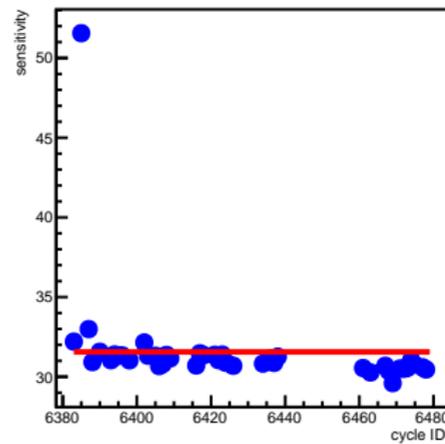
sam5_coil1(ppm/count)



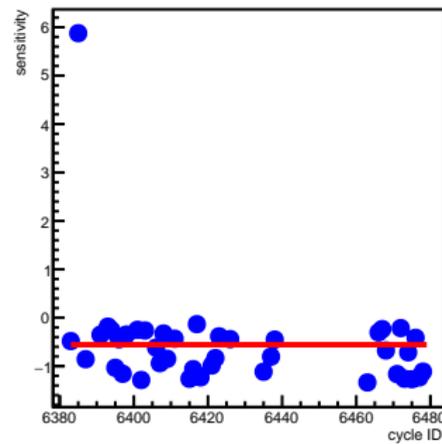
sam5_coil3(ppm/count)



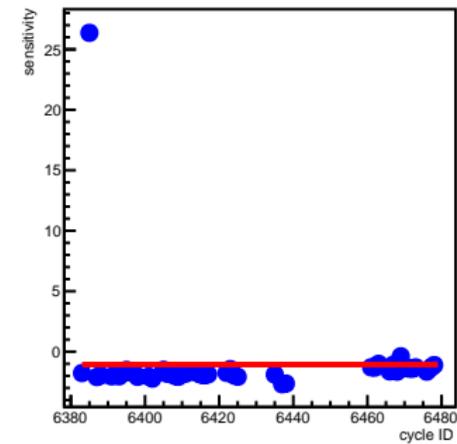
sam5_coil4(ppm/count)



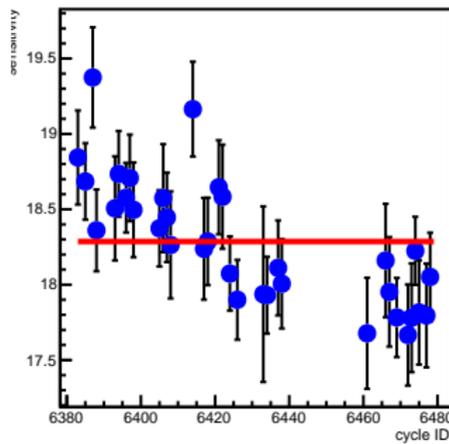
sam5_coil6(ppm/count)



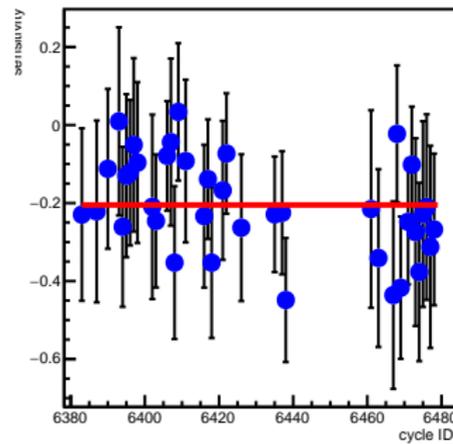
sam5_coil7(ppm/count)



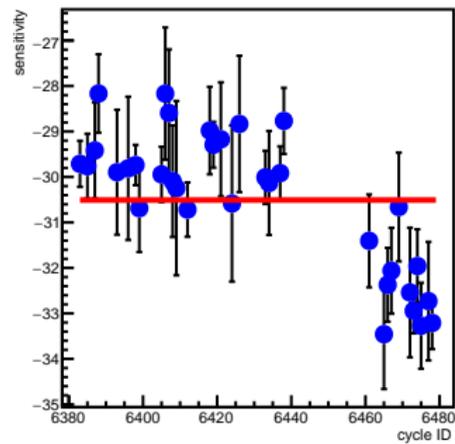
sam5_coil2(ppm/count)



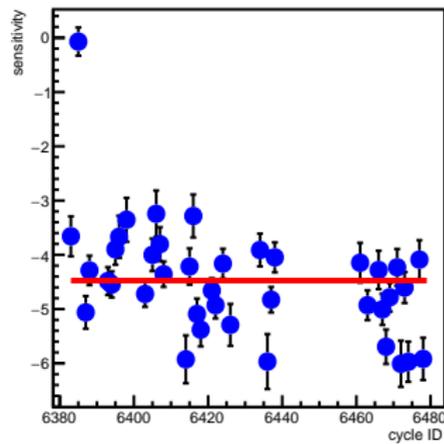
sam5_coil5(ppm/count)



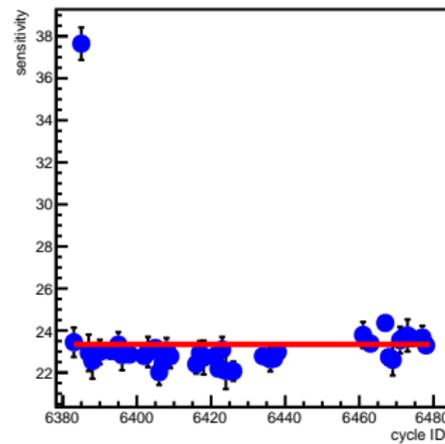
sam6_coil1(ppm/count)



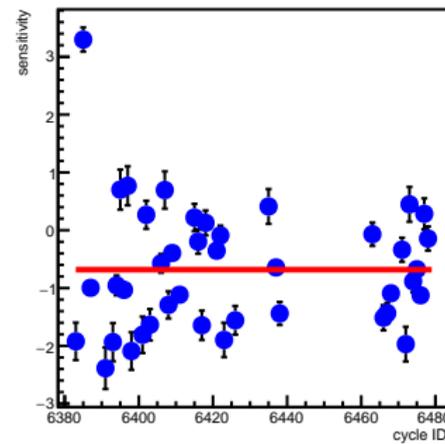
sam6_coil3(ppm/count)



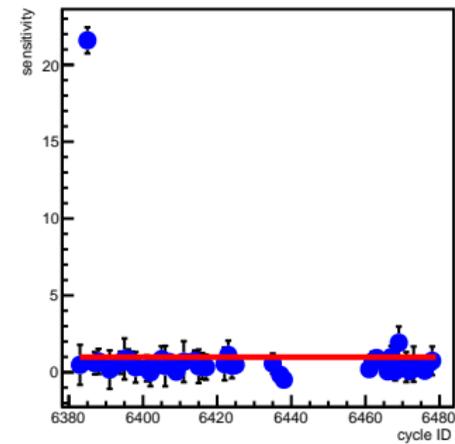
sam6_coil4(ppm/count)



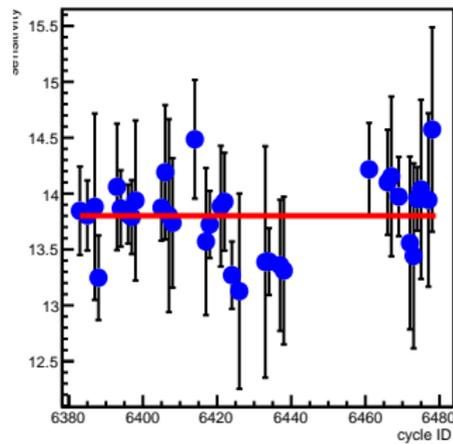
sam6_coil6(ppm/count)



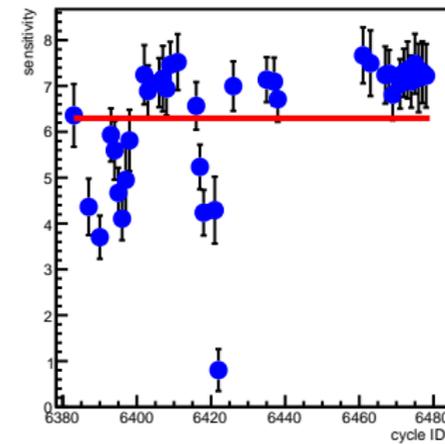
sam6_coil7(ppm/count)



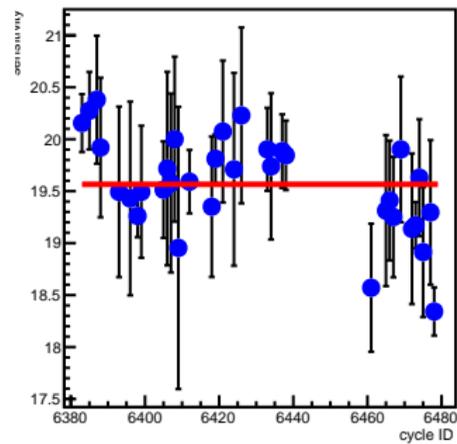
sam6_coil2(ppm/count)



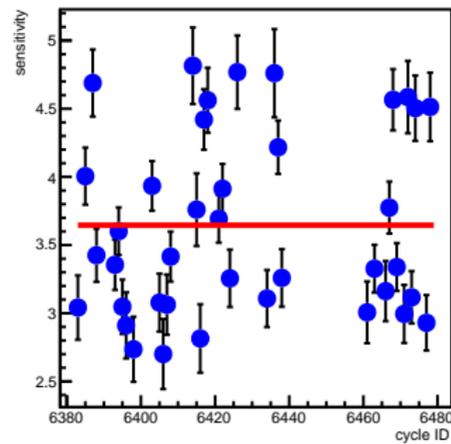
sam6_coil5(ppm/count)



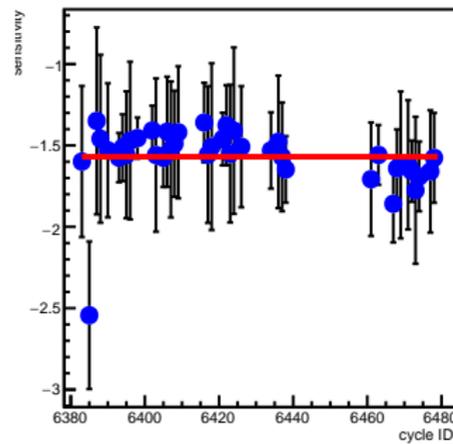
sam7_coil1(ppm/count)



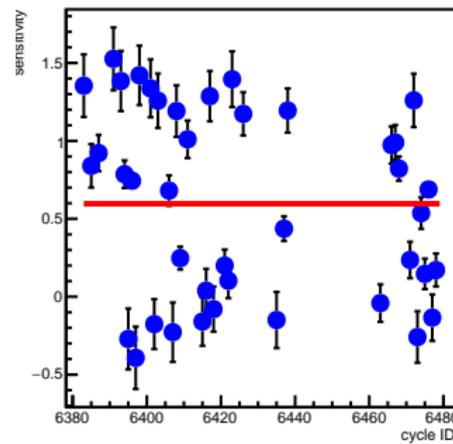
sam7_coil3(ppm/count)



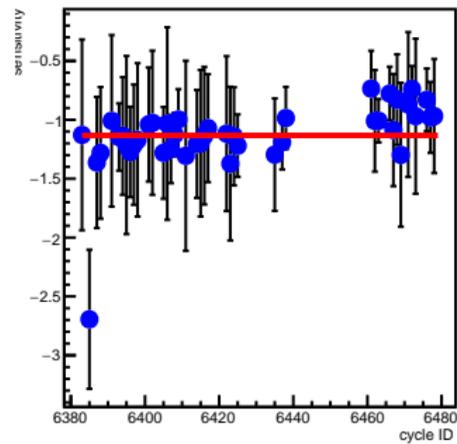
sam7_coil4(ppm/count)



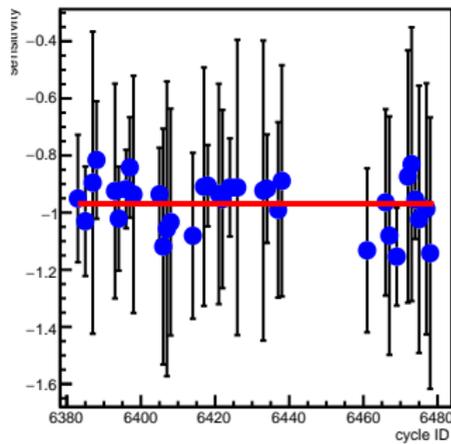
sam7_coil6(ppm/count)



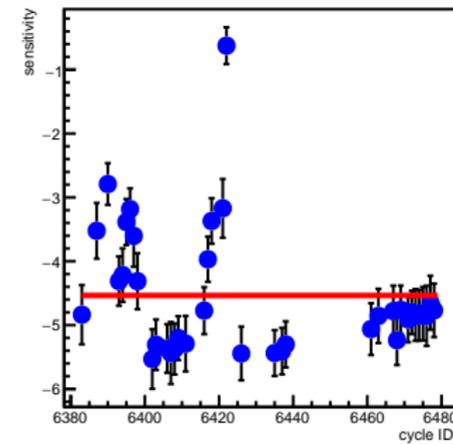
sam7_coil7(ppm/count)



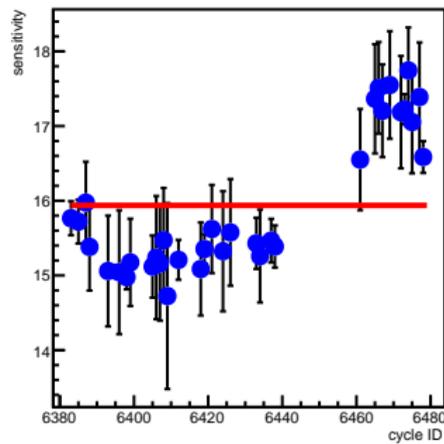
sam7_coil2(ppm/count)



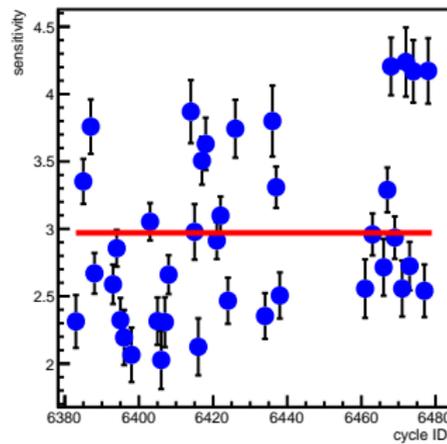
sam7_coil5(ppm/count)



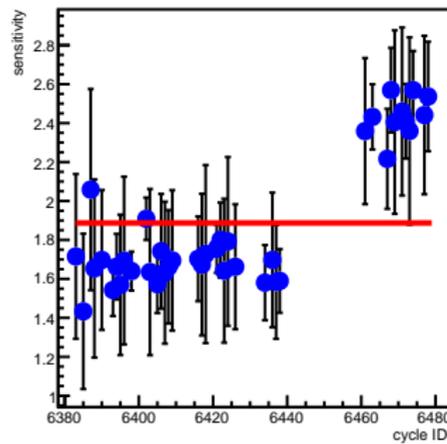
sam8_coil1(ppm/count)



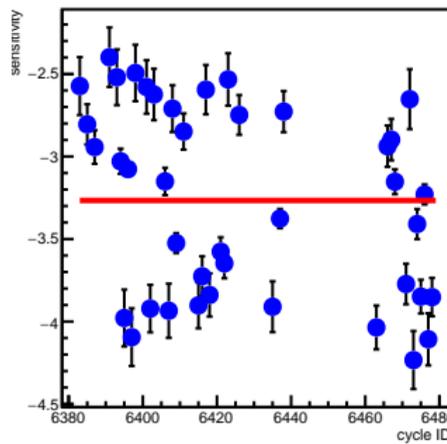
sam8_coil3(ppm/count)



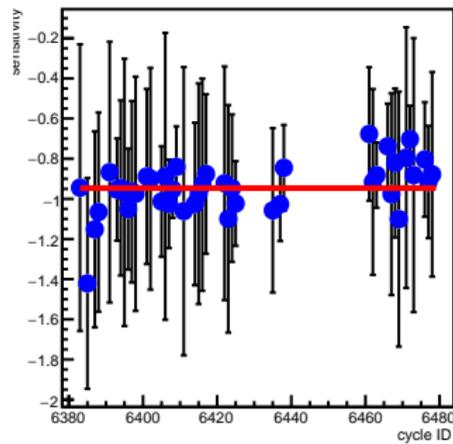
sam8_coil4(ppm/count)



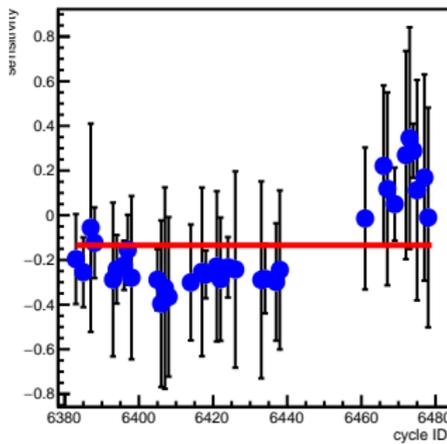
sam8_coil6(ppm/count)



sam8_coil7(ppm/count)



sam8_coil2(ppm/count)



sam8_coil5(ppm/count)

