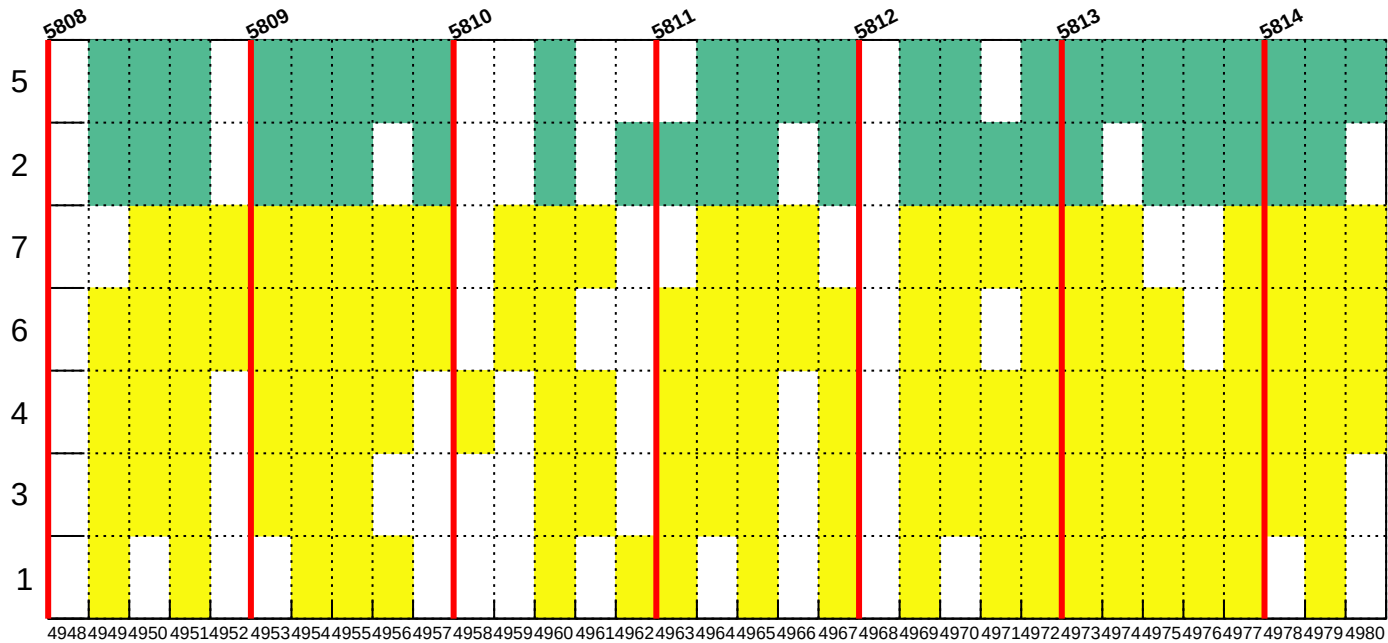
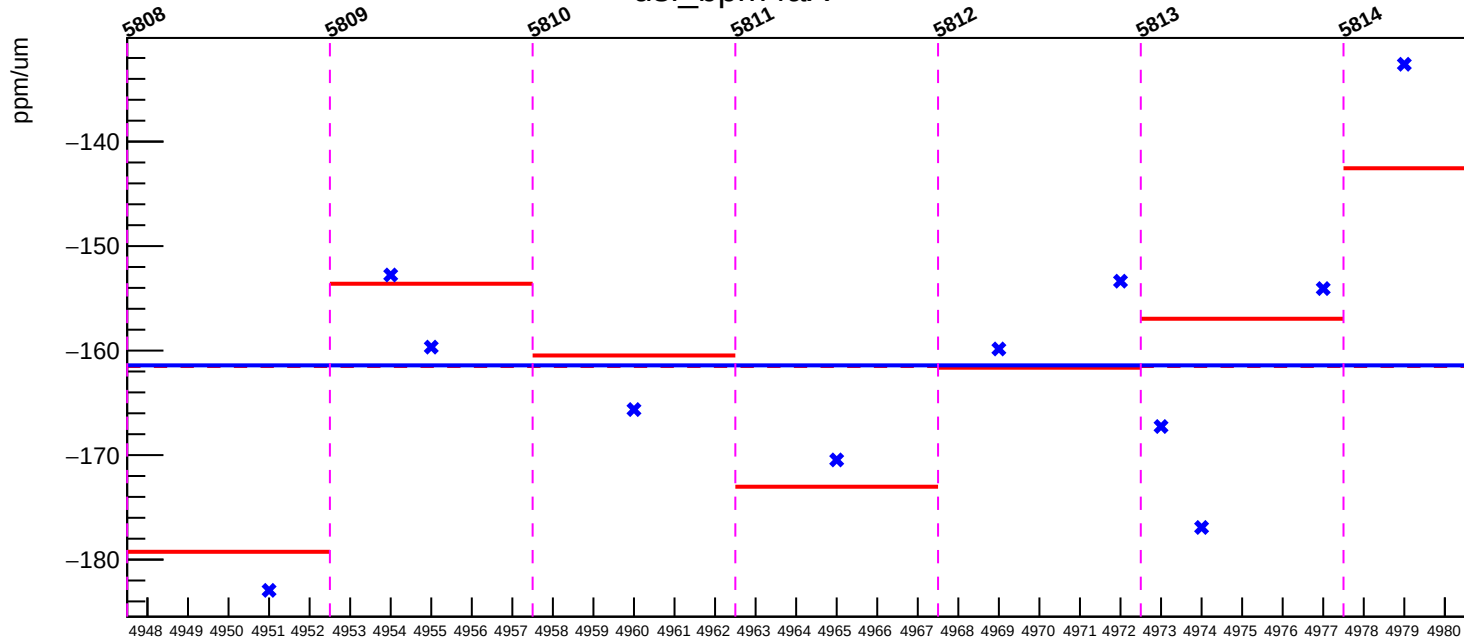
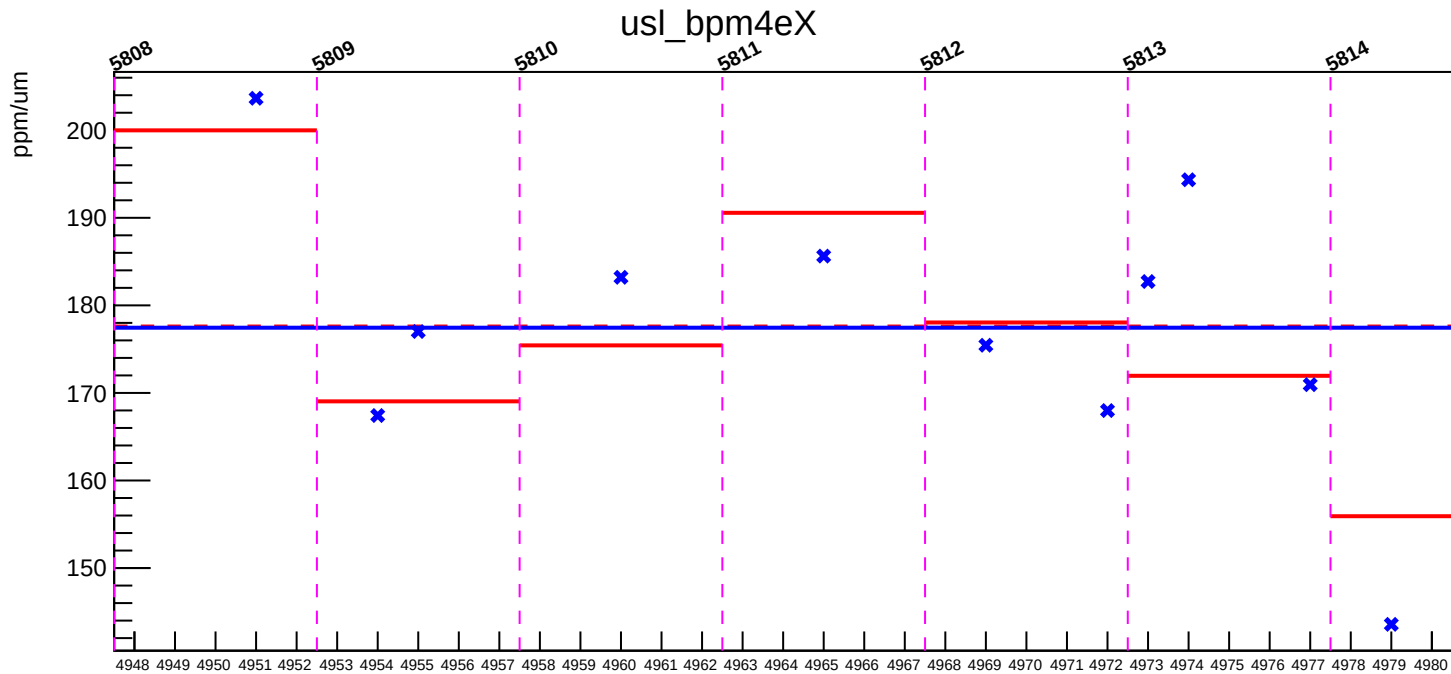
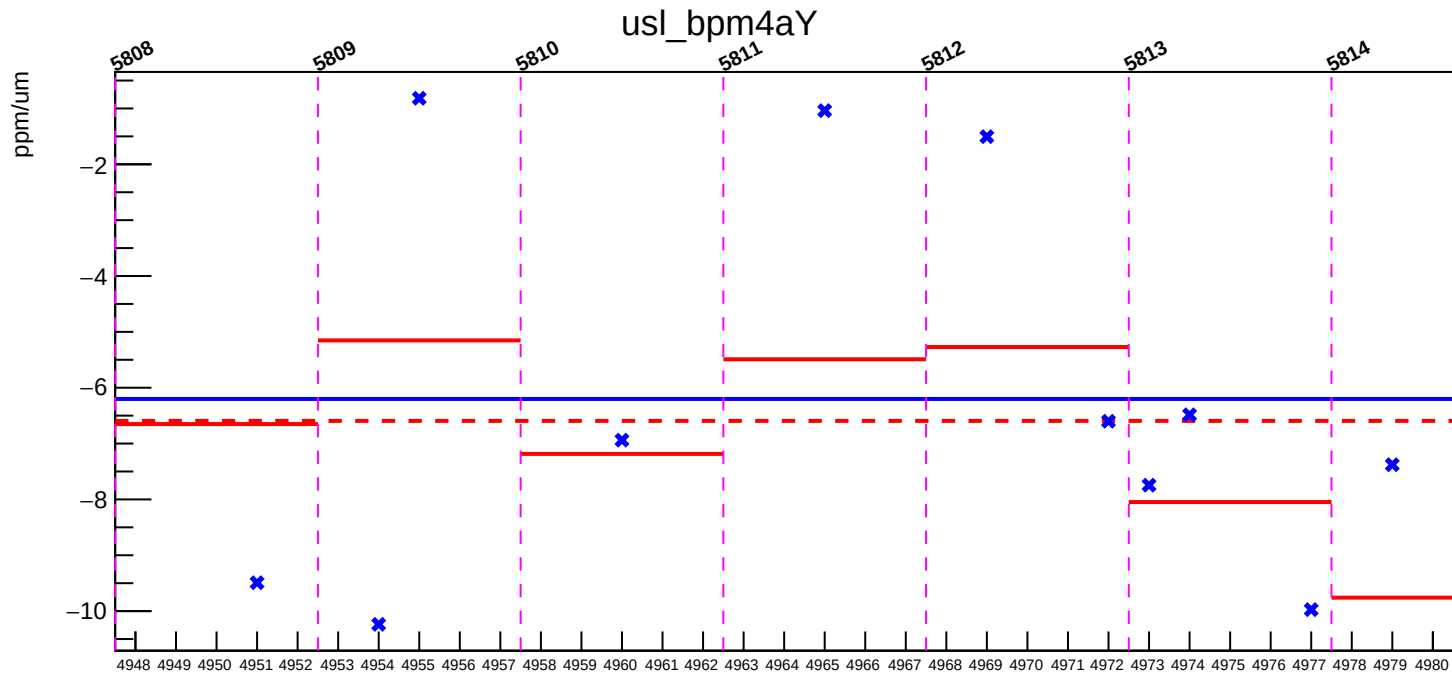


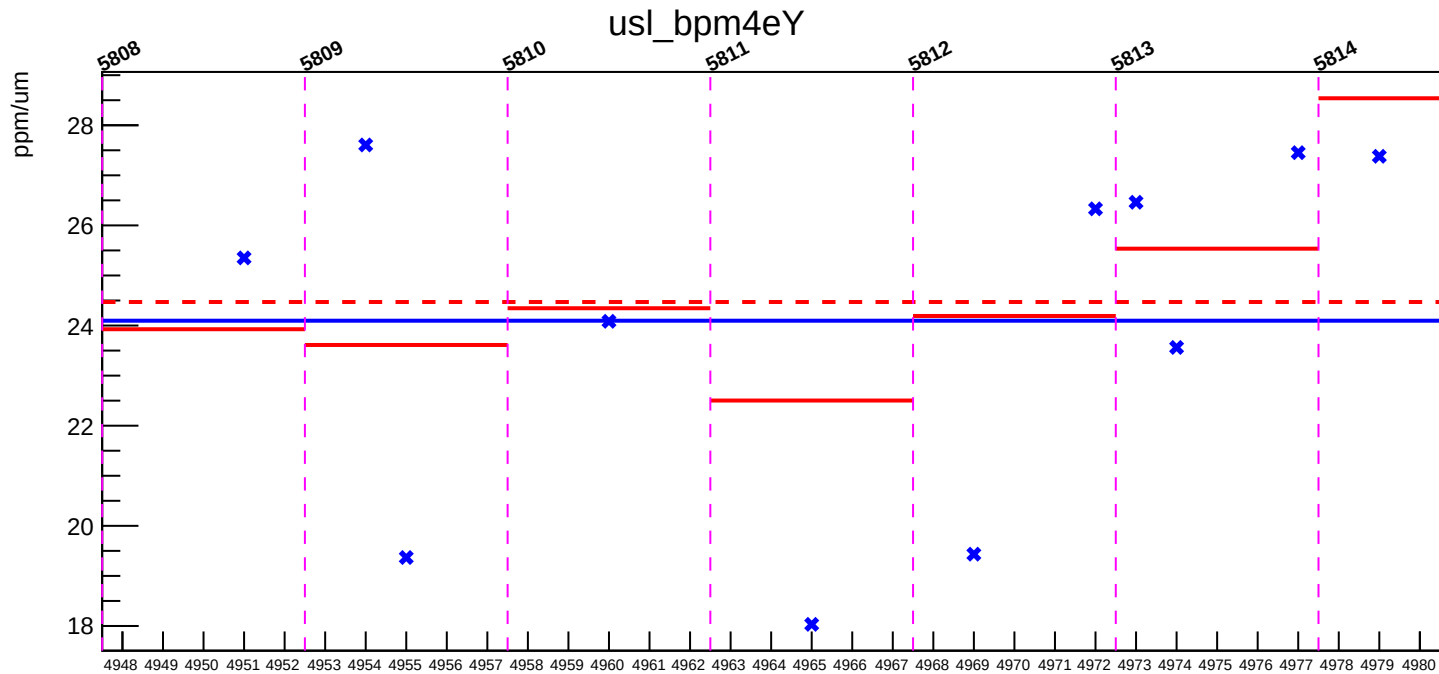


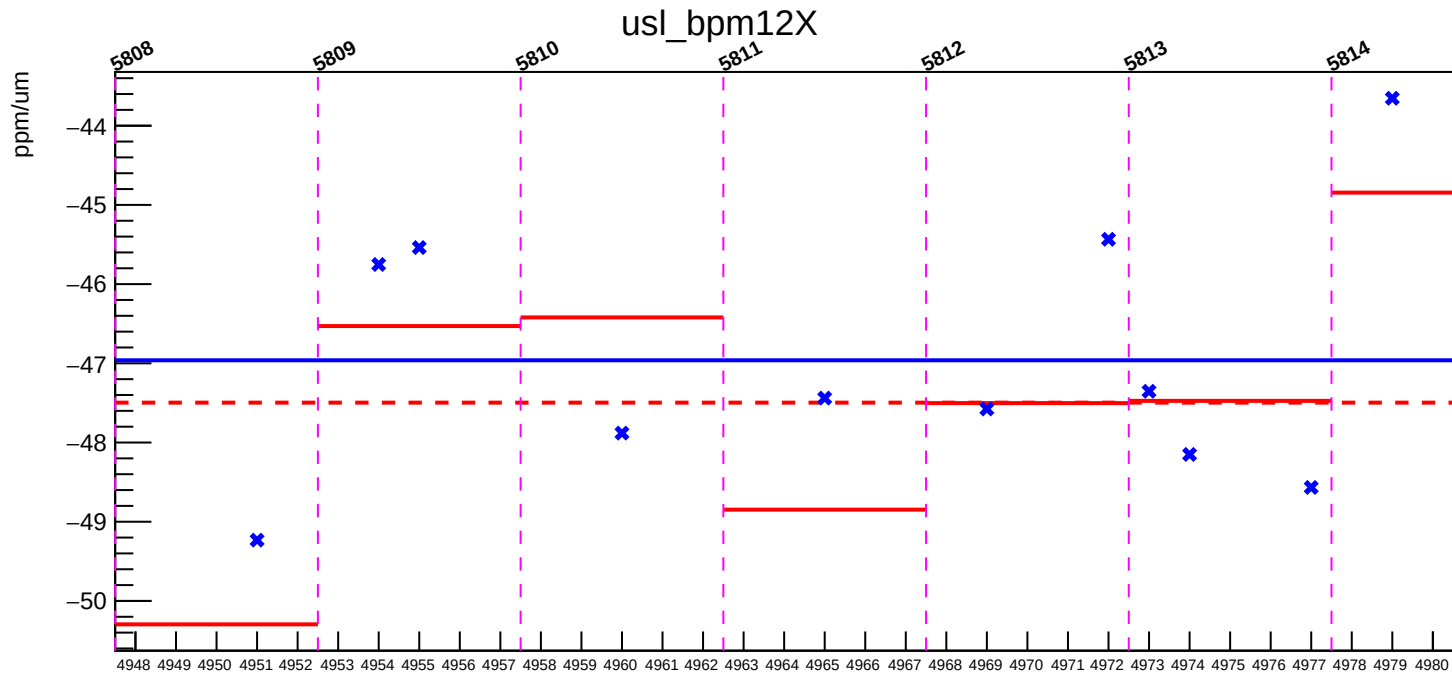
usl_bpm4aX



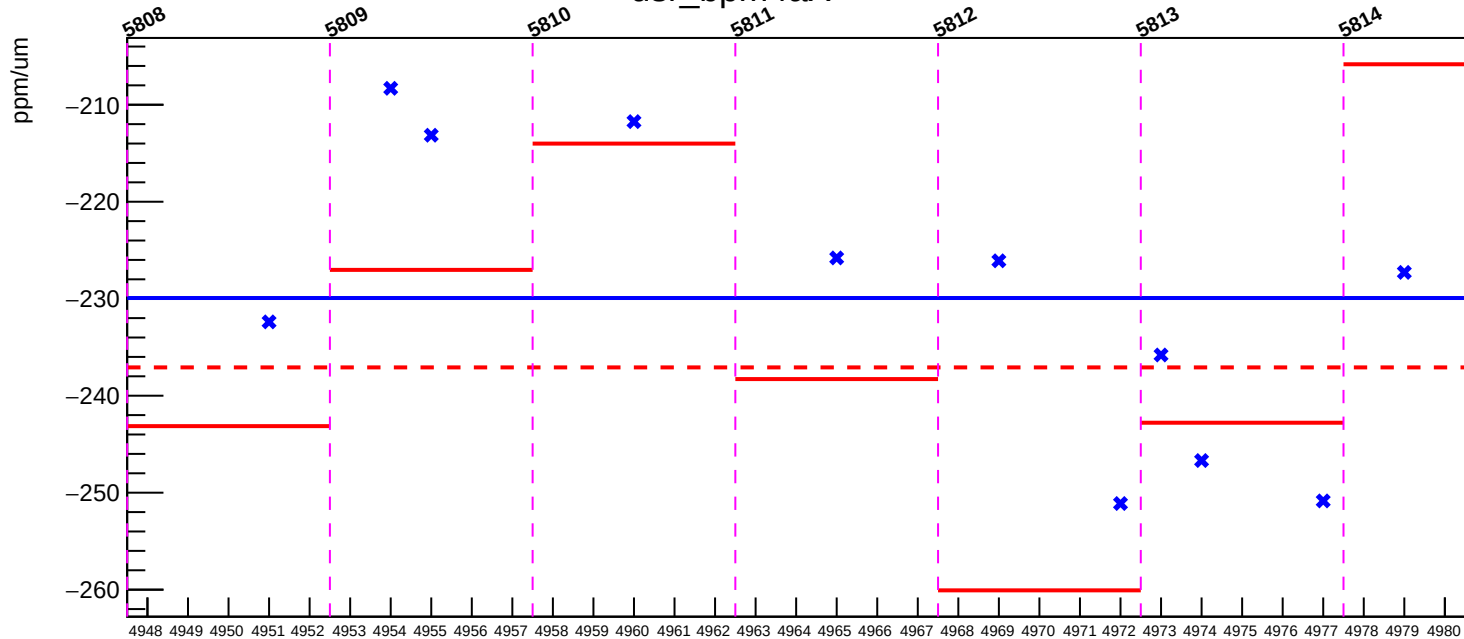


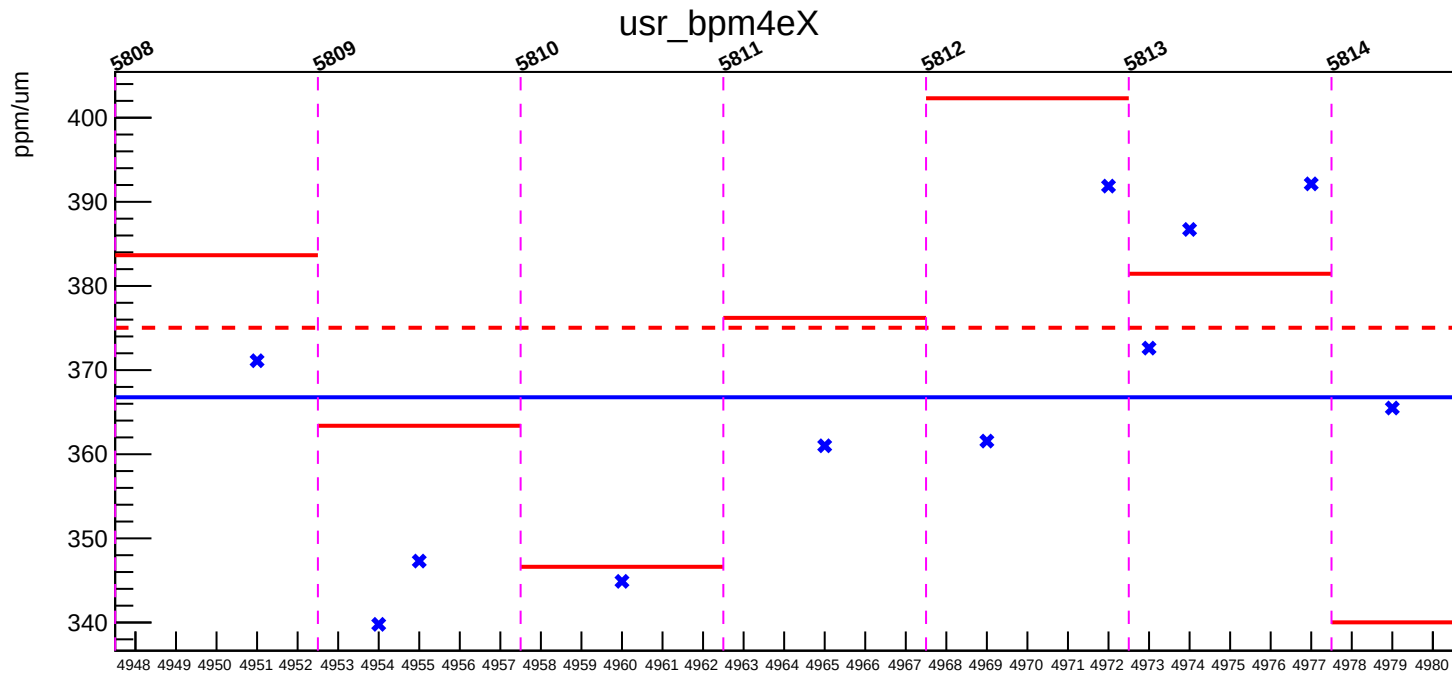


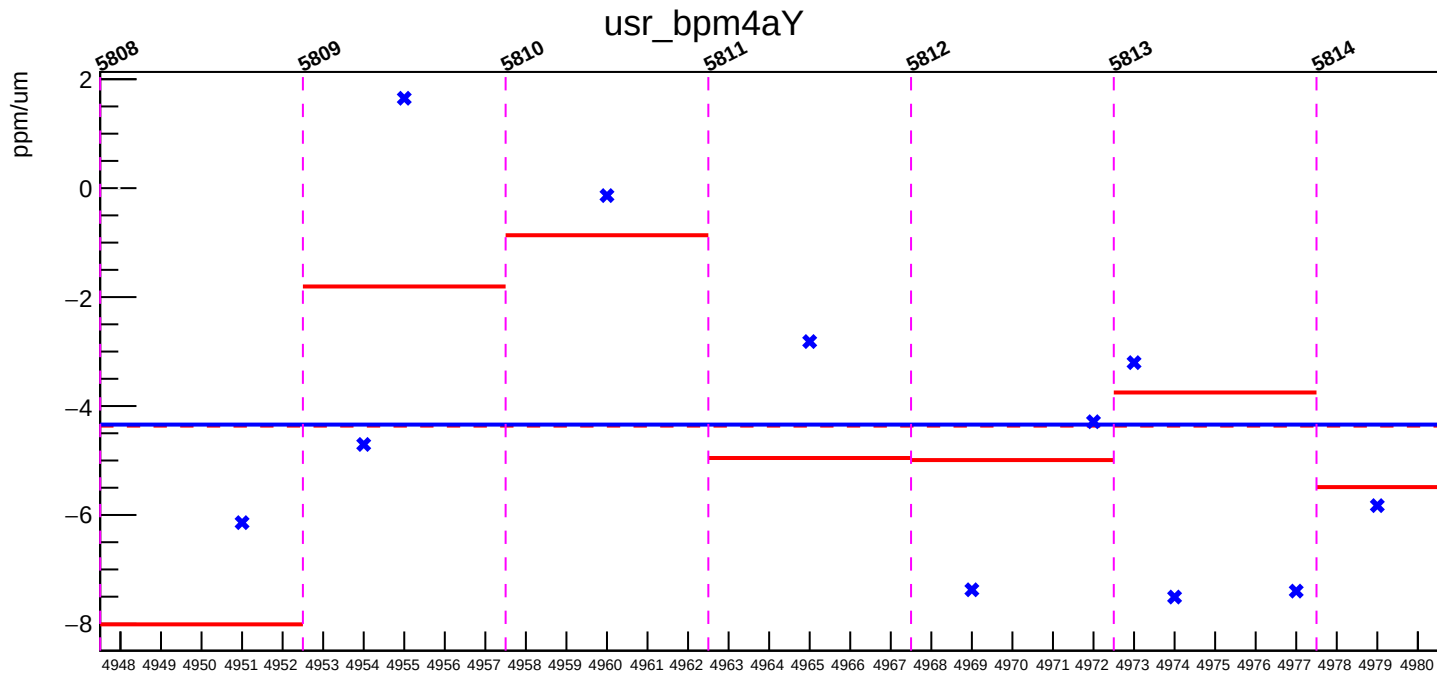


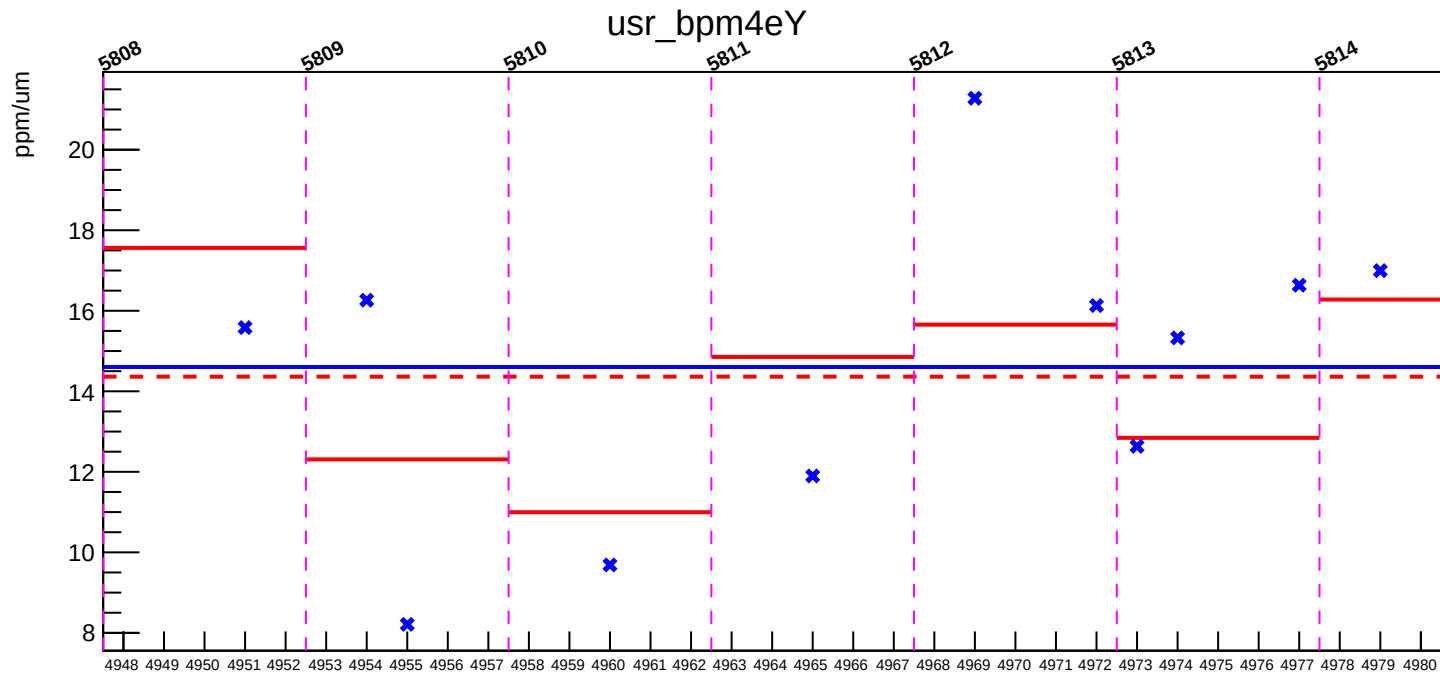


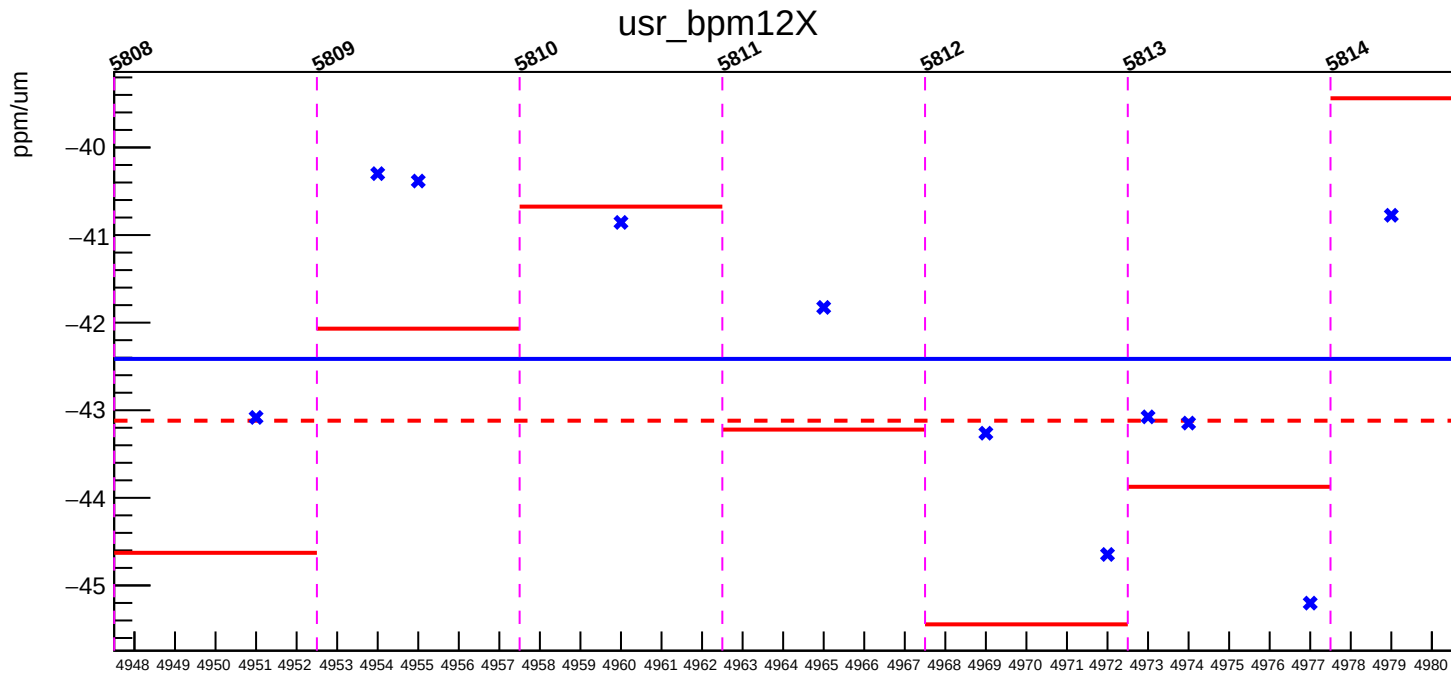
usr_bpm4aX

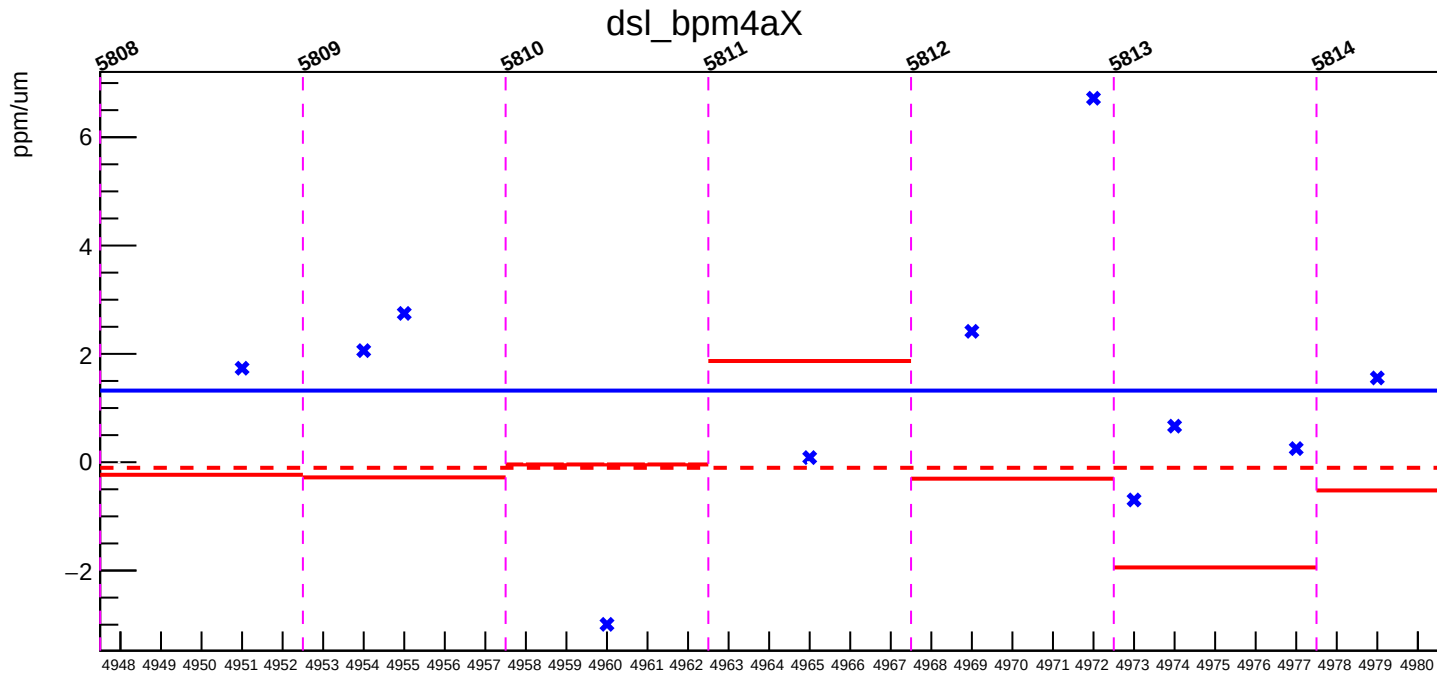


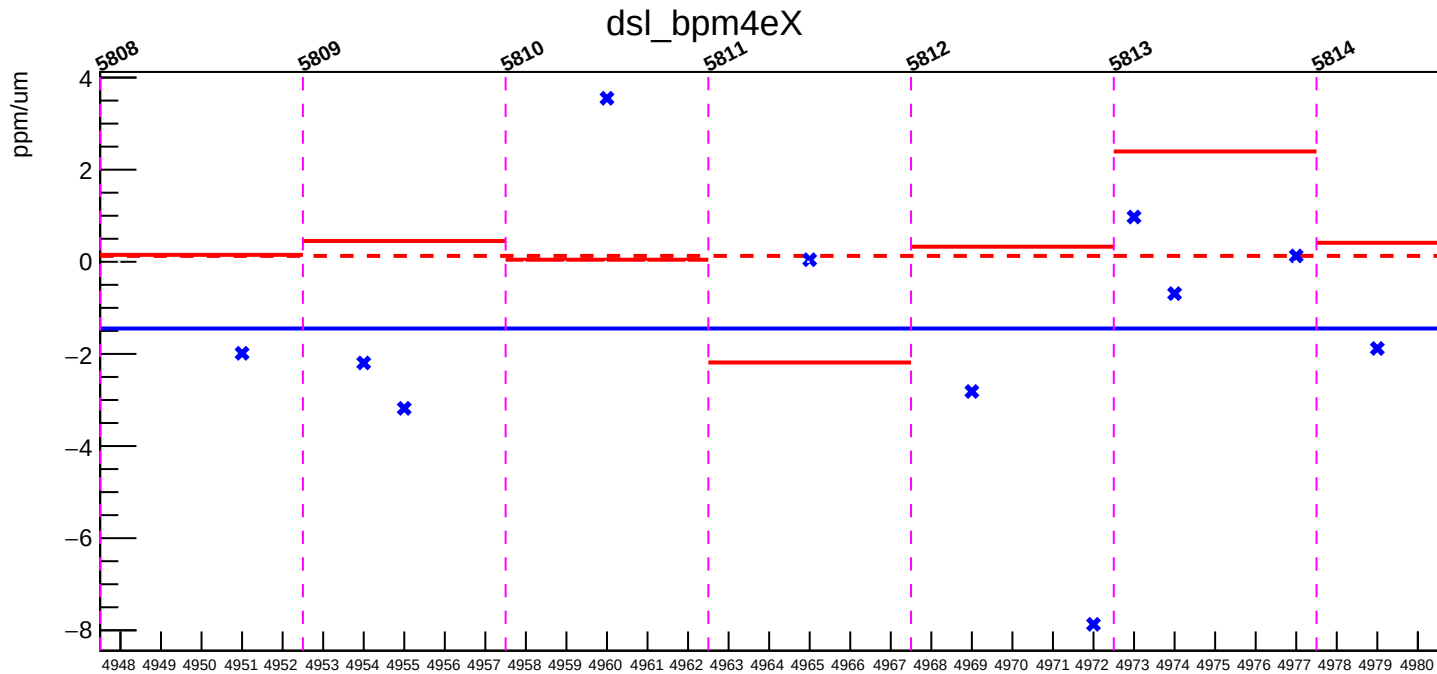




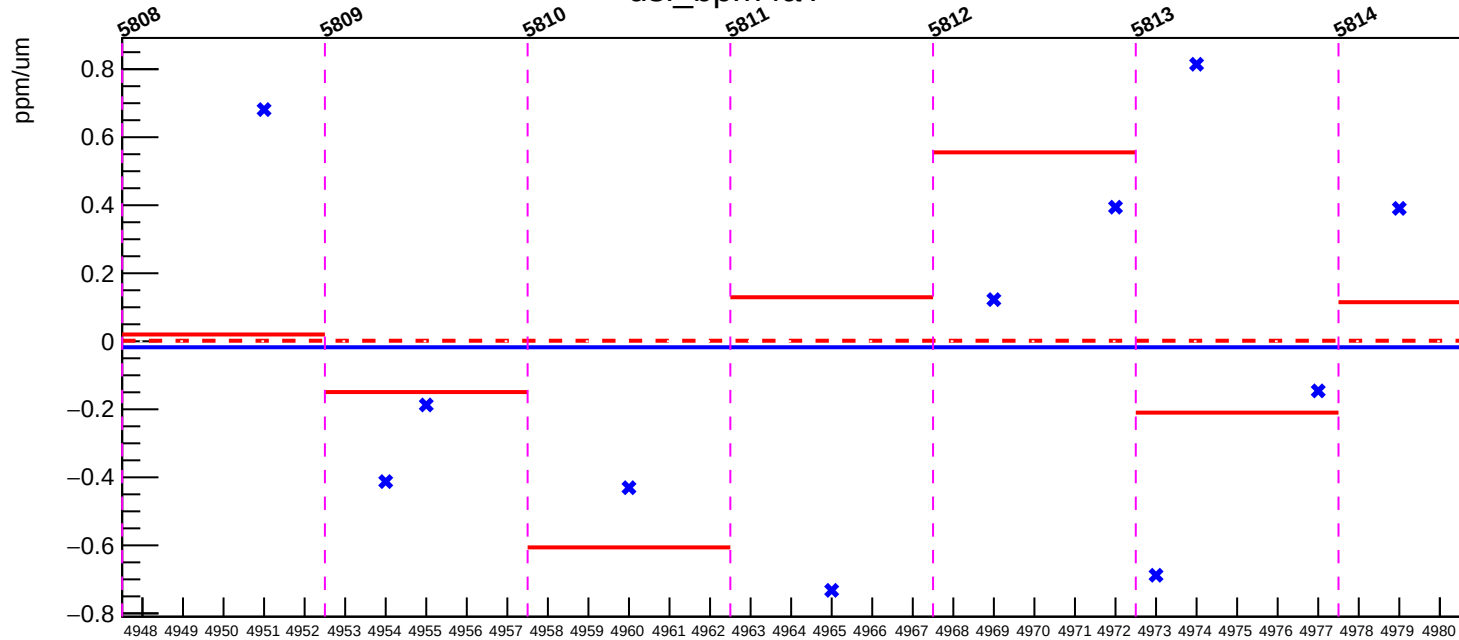


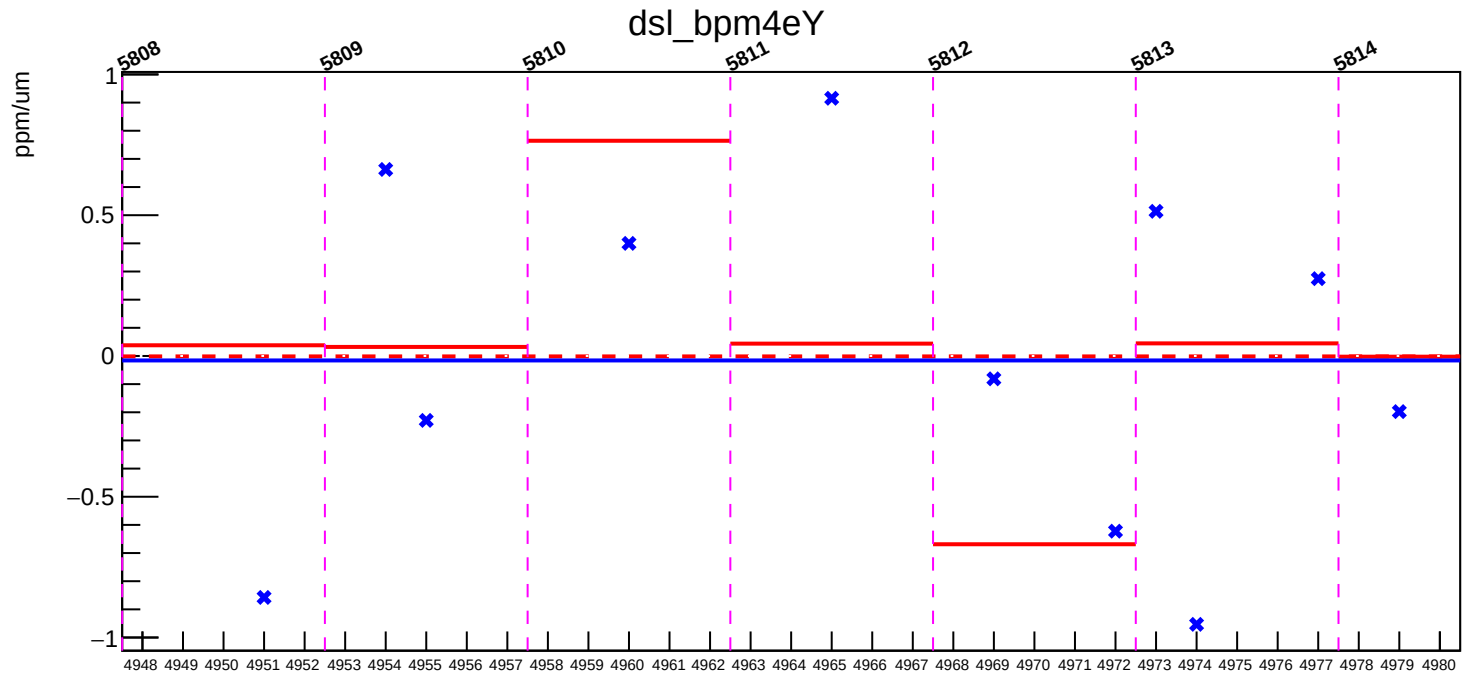




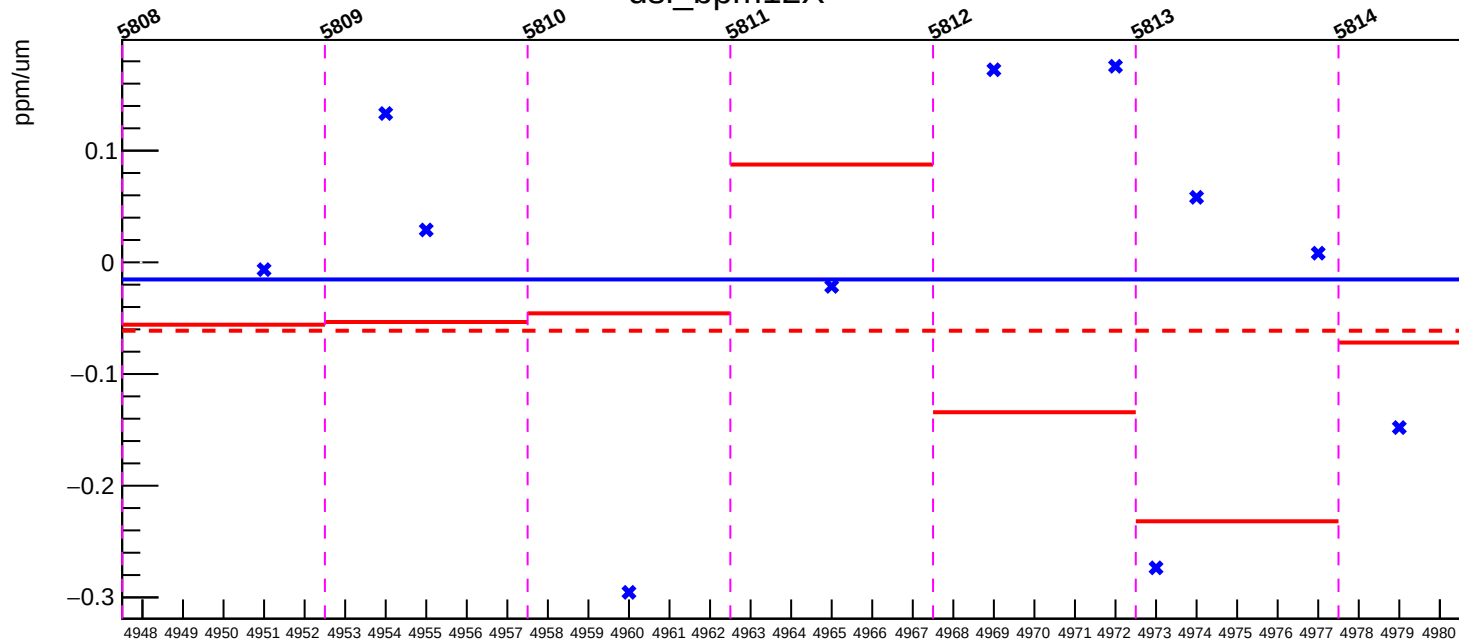


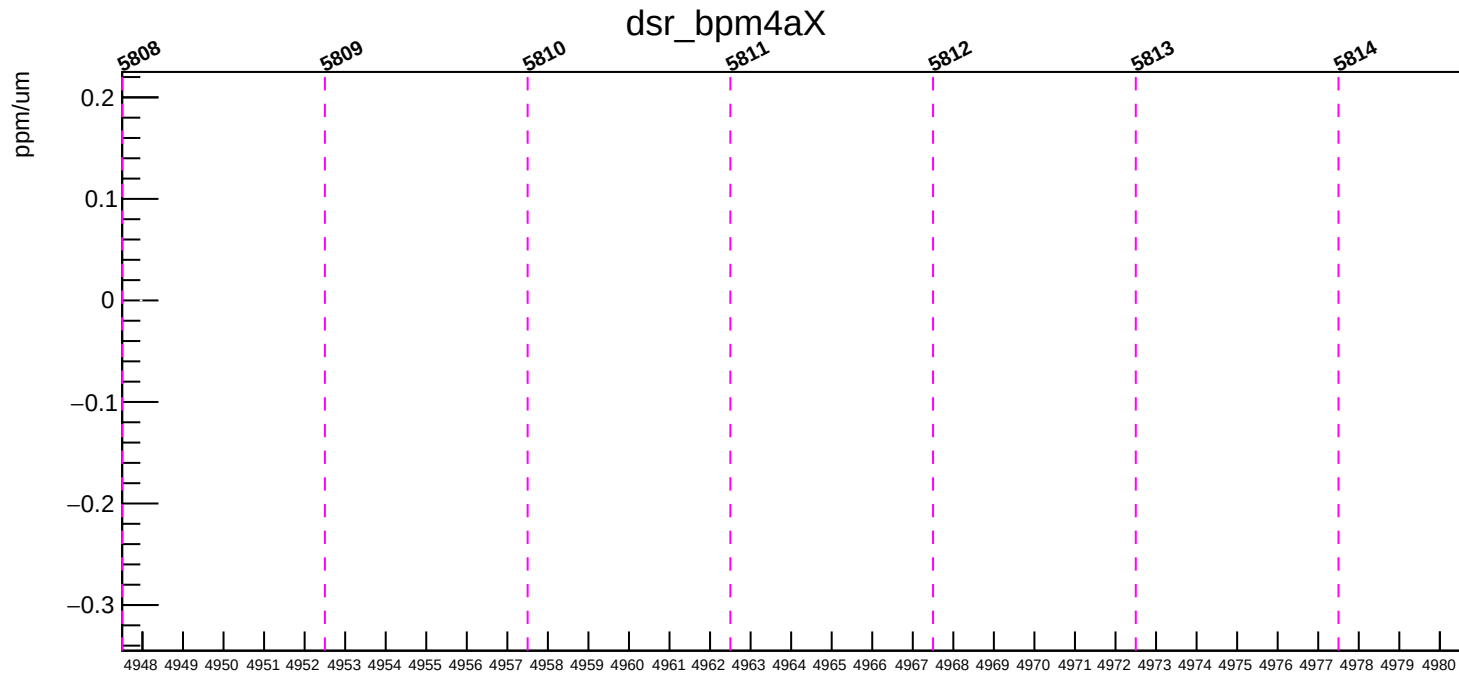
dsl_bpm4aY

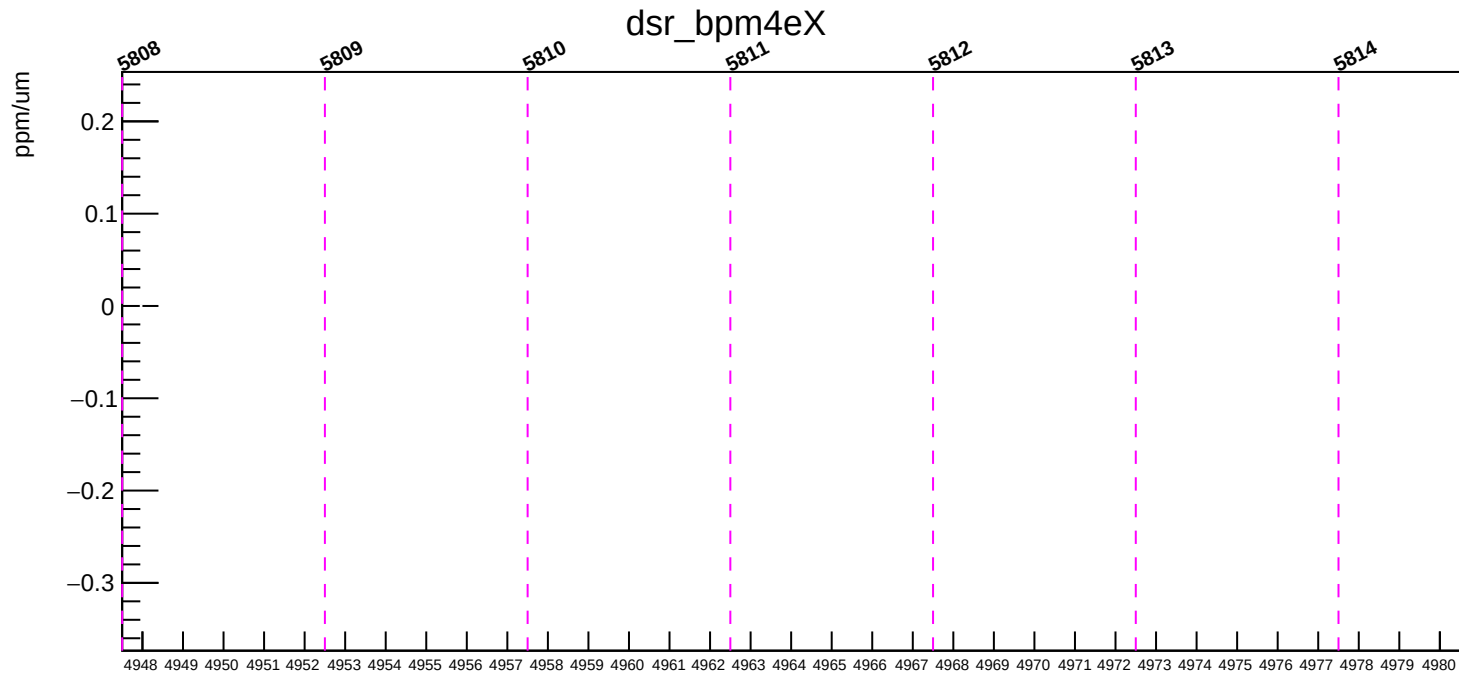




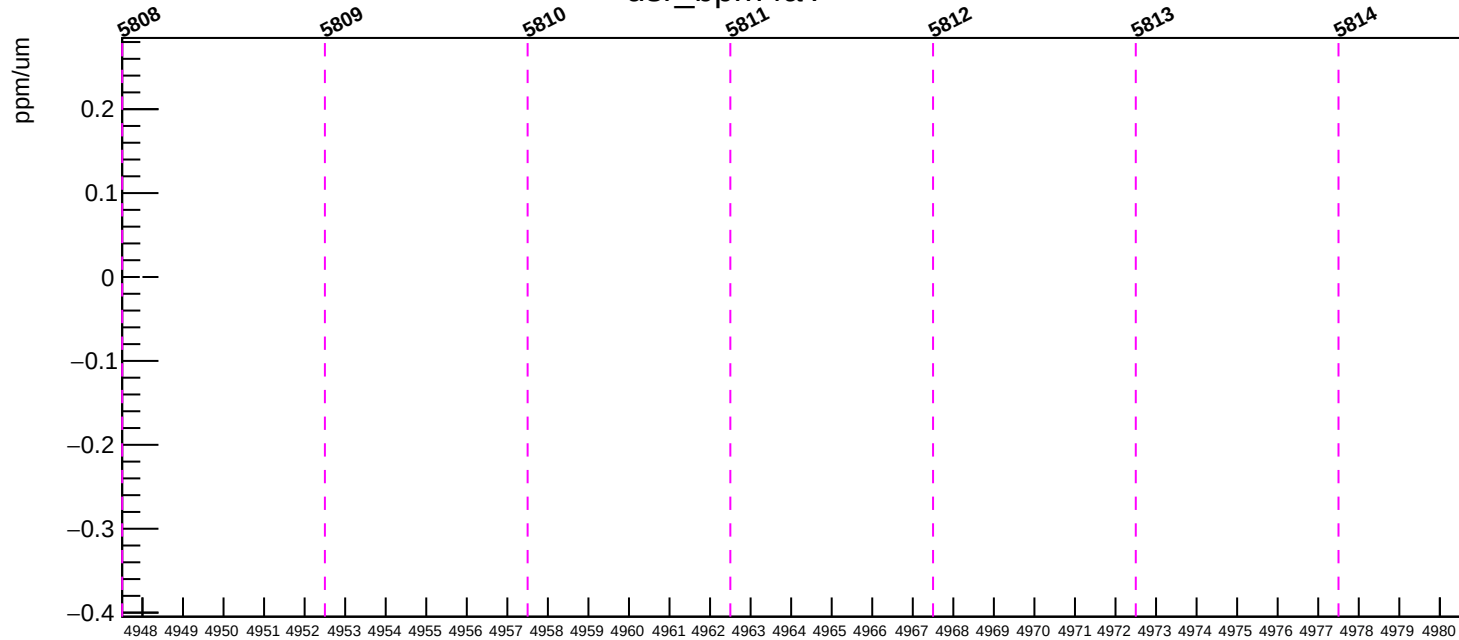
dsl_bpm12X

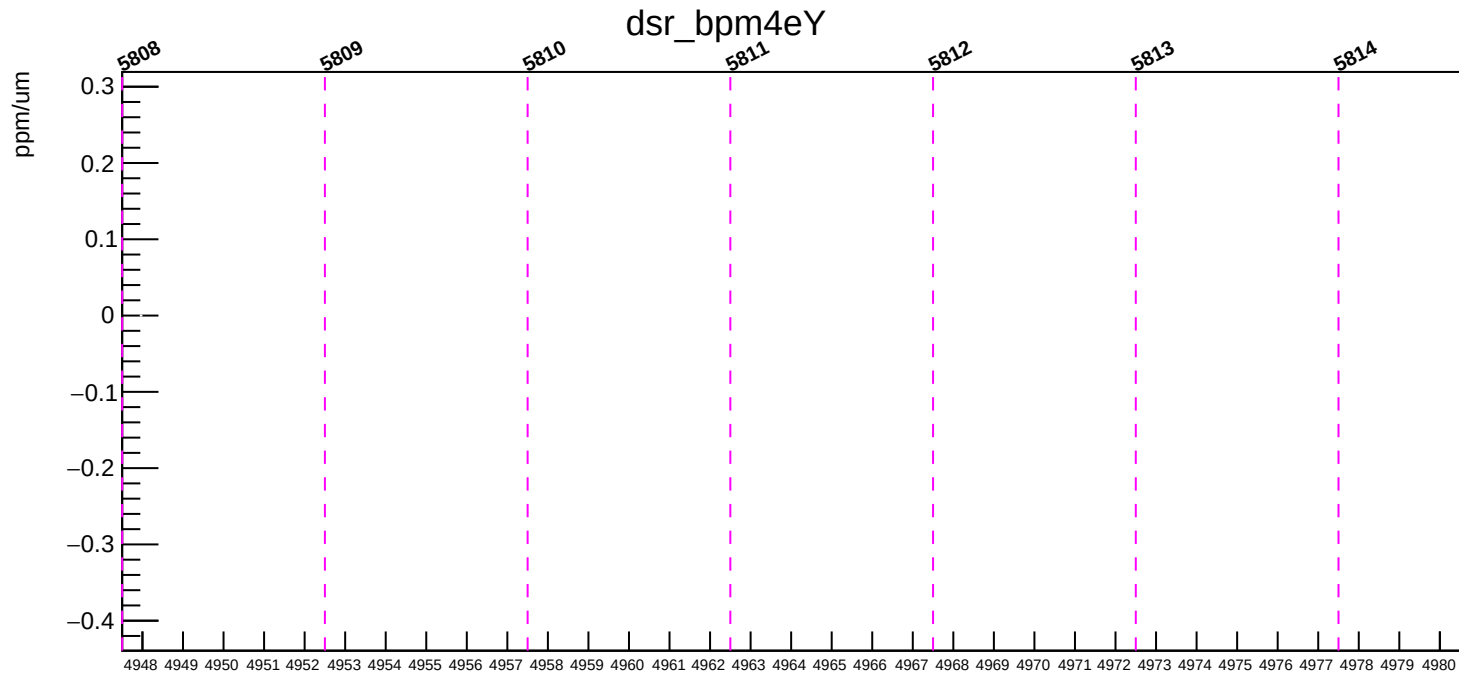


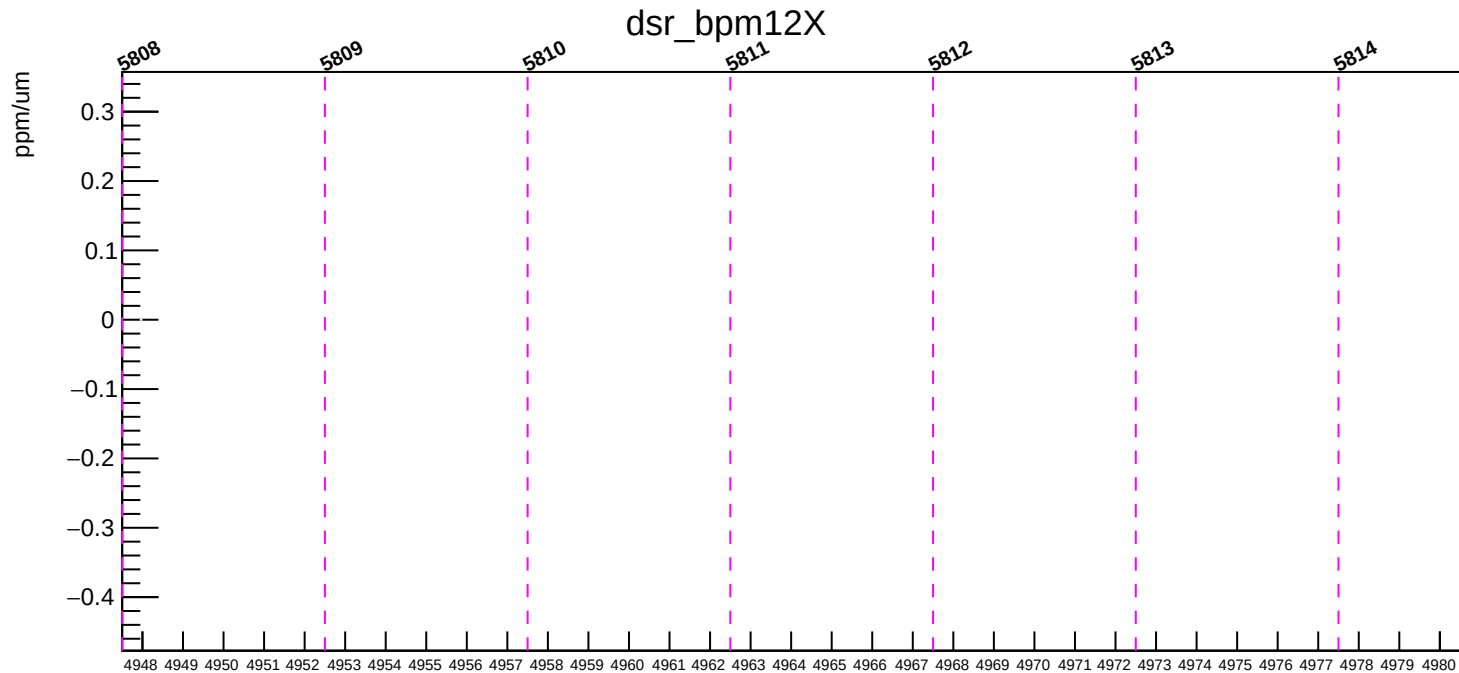


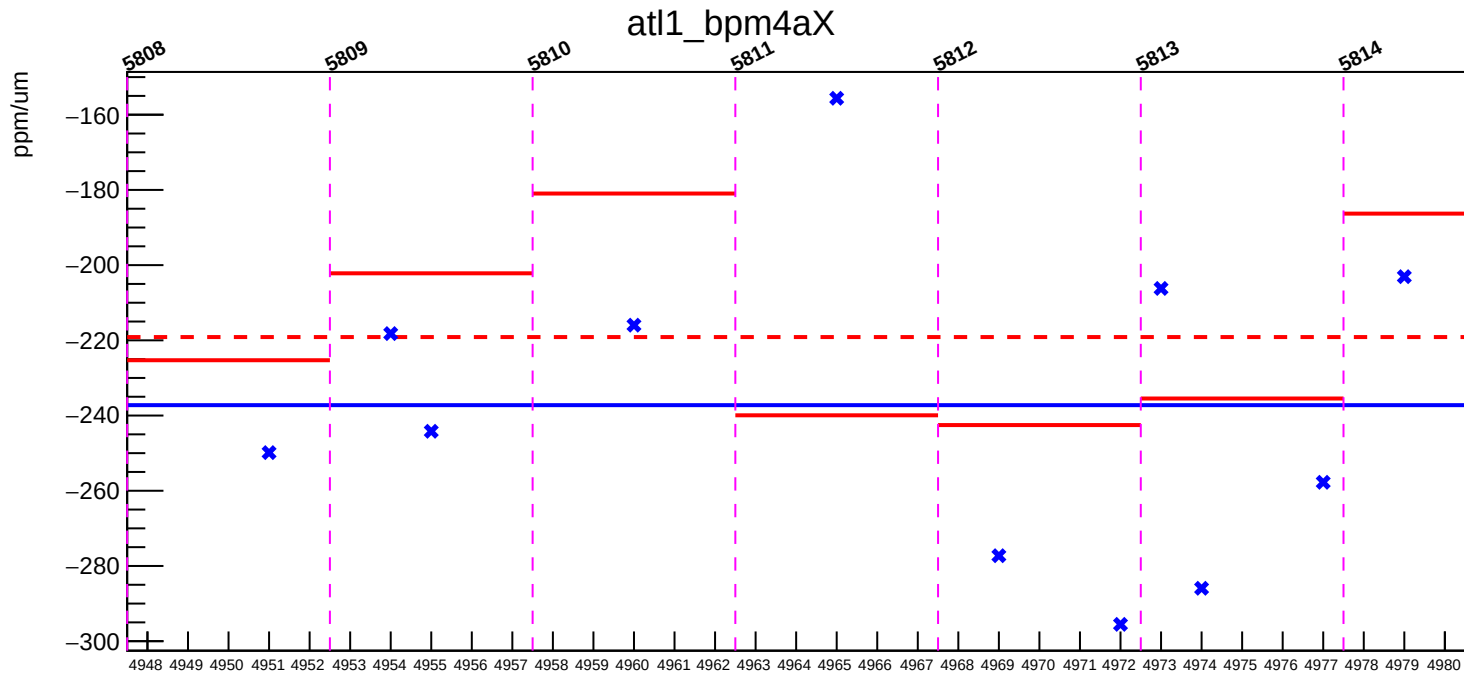


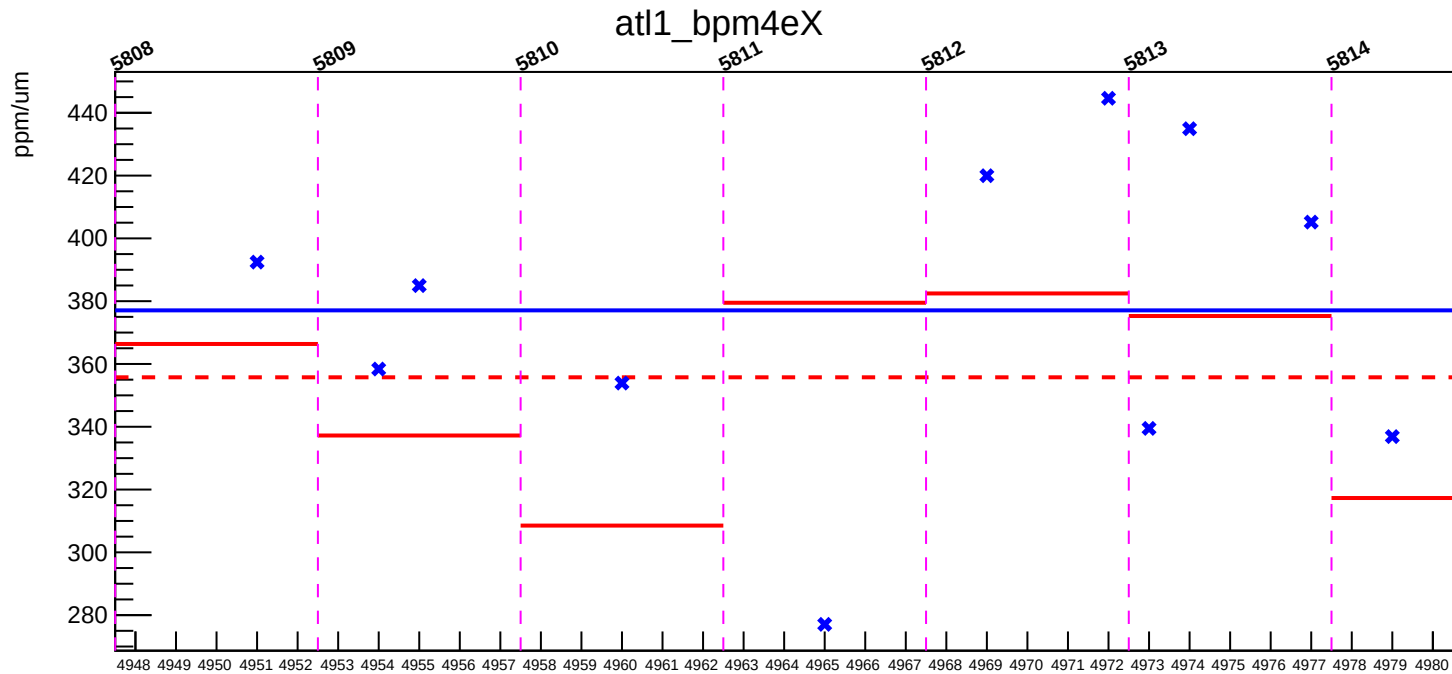
dsr_bpm4aY

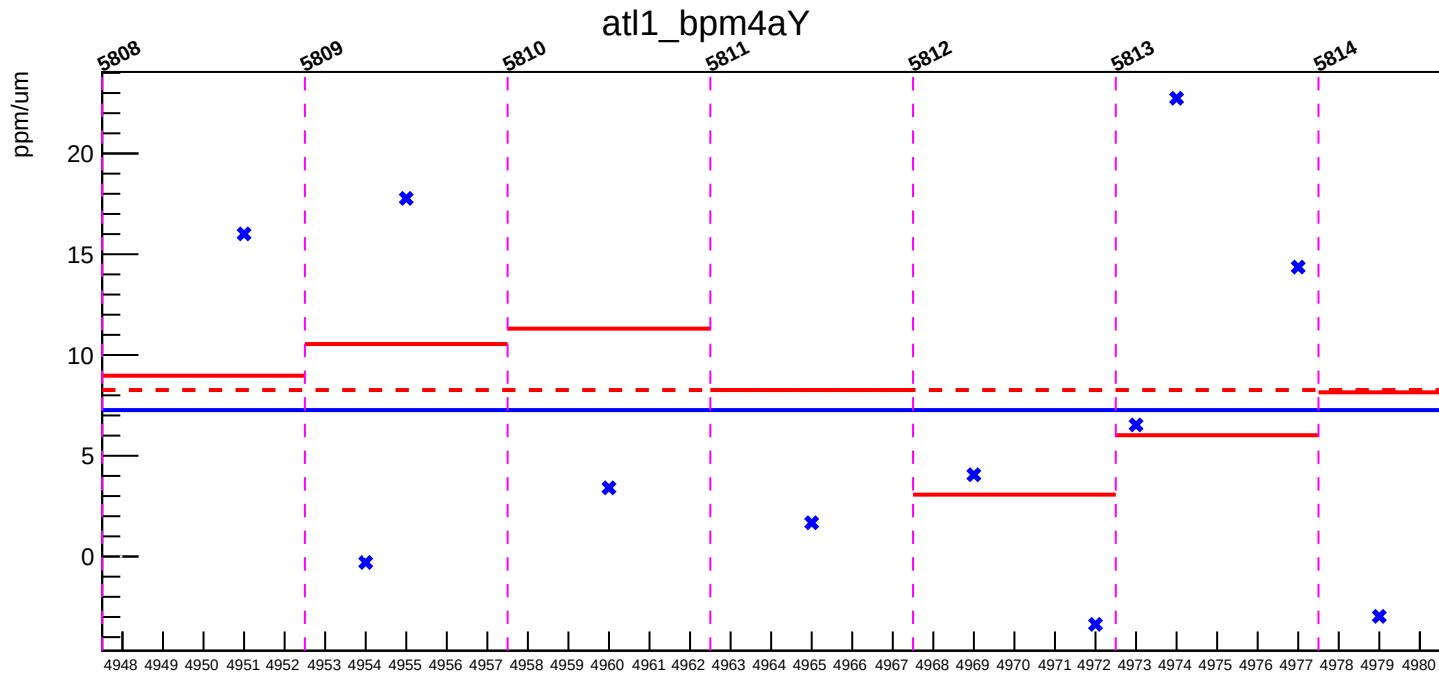


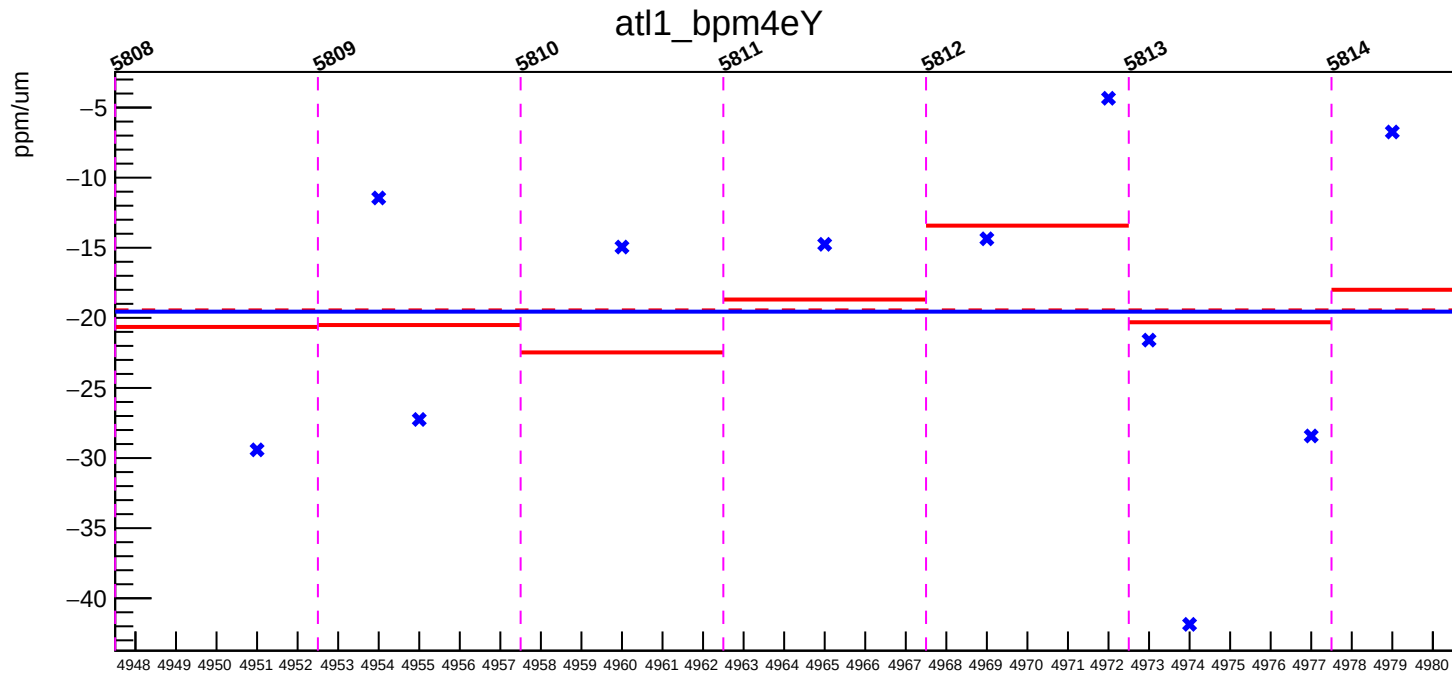


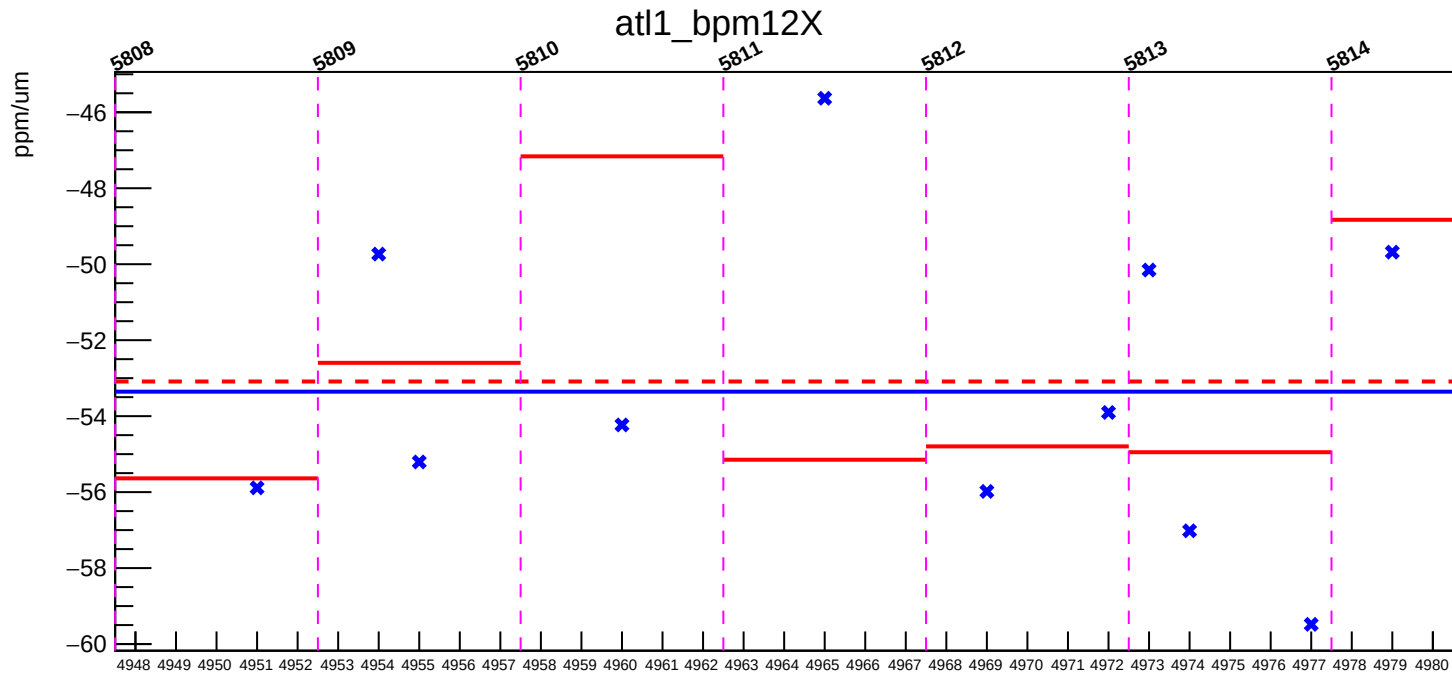




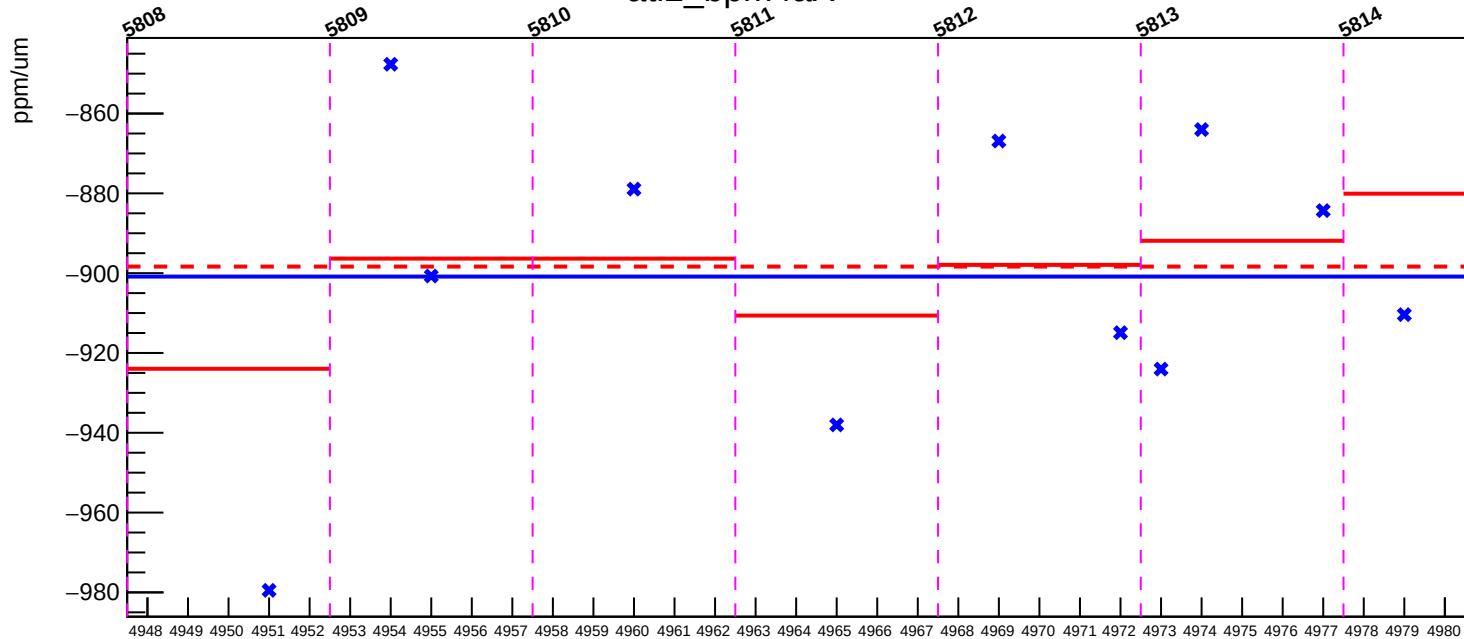


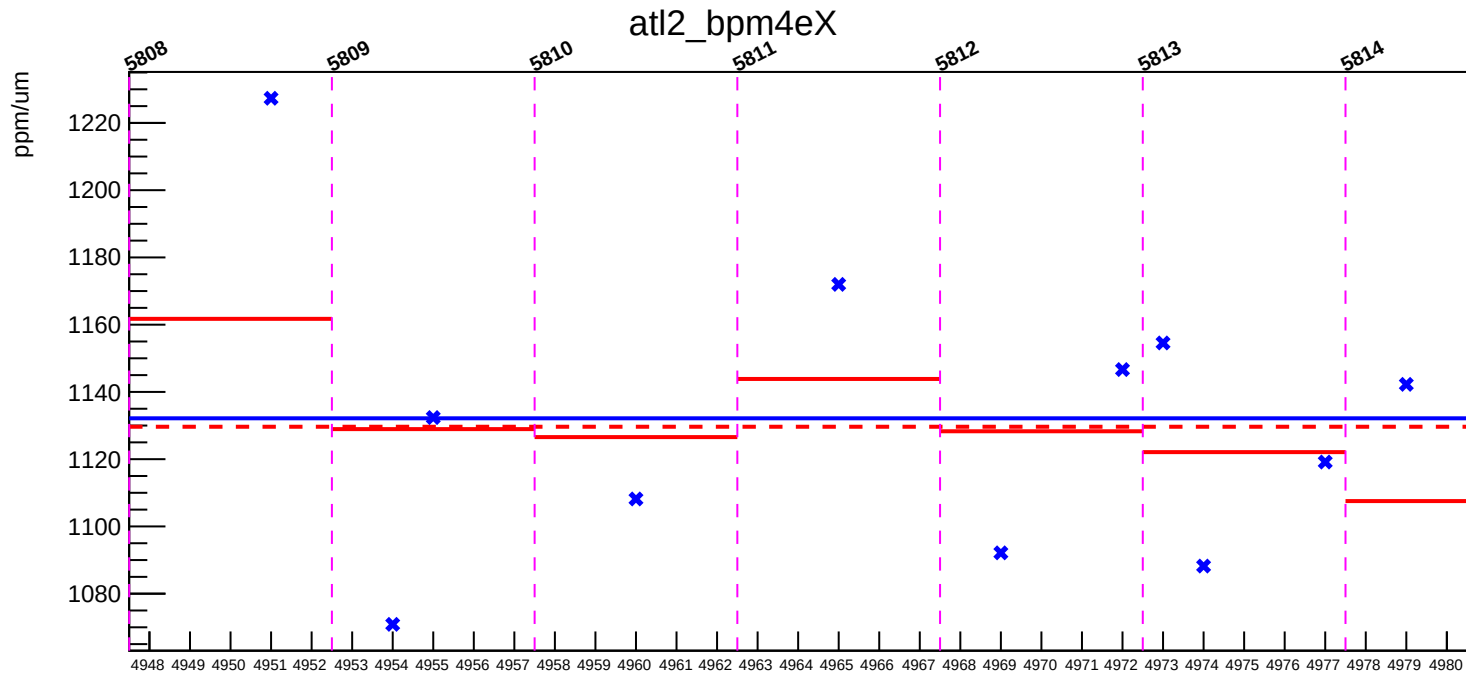


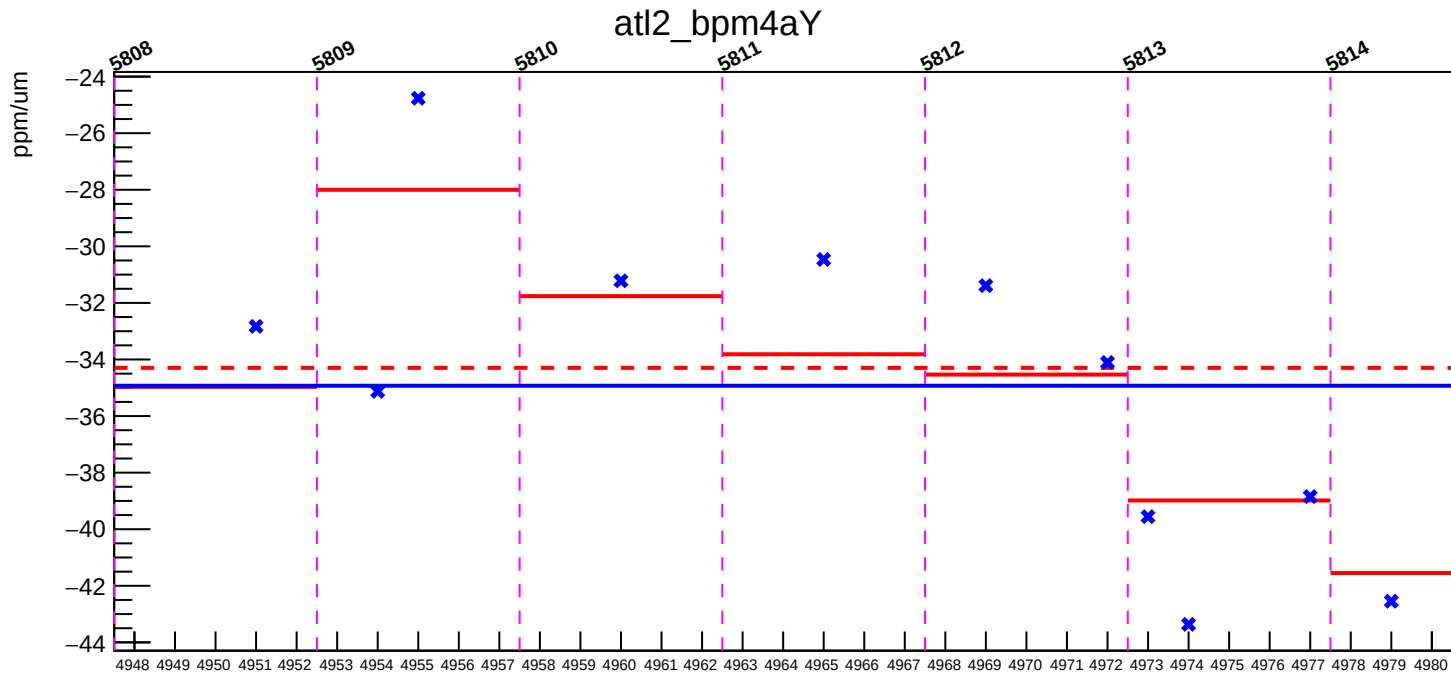


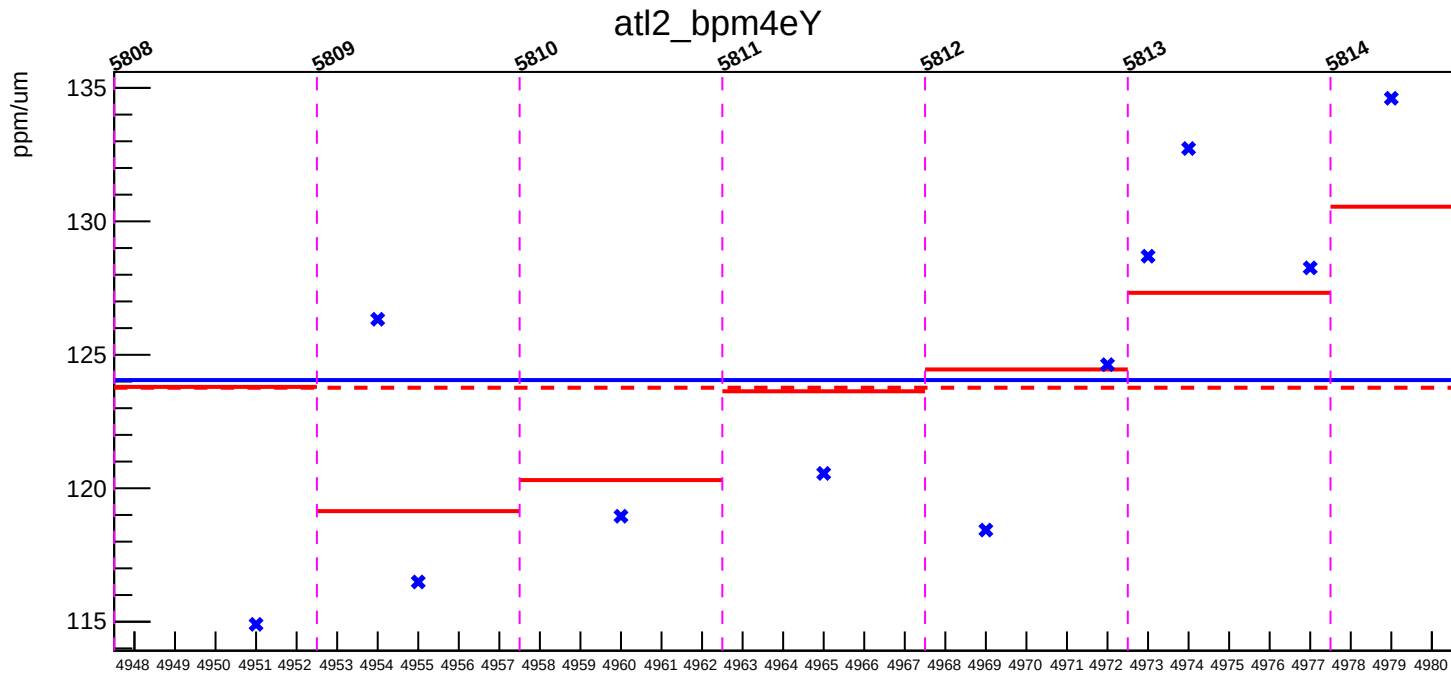


atl2_bpm4aX

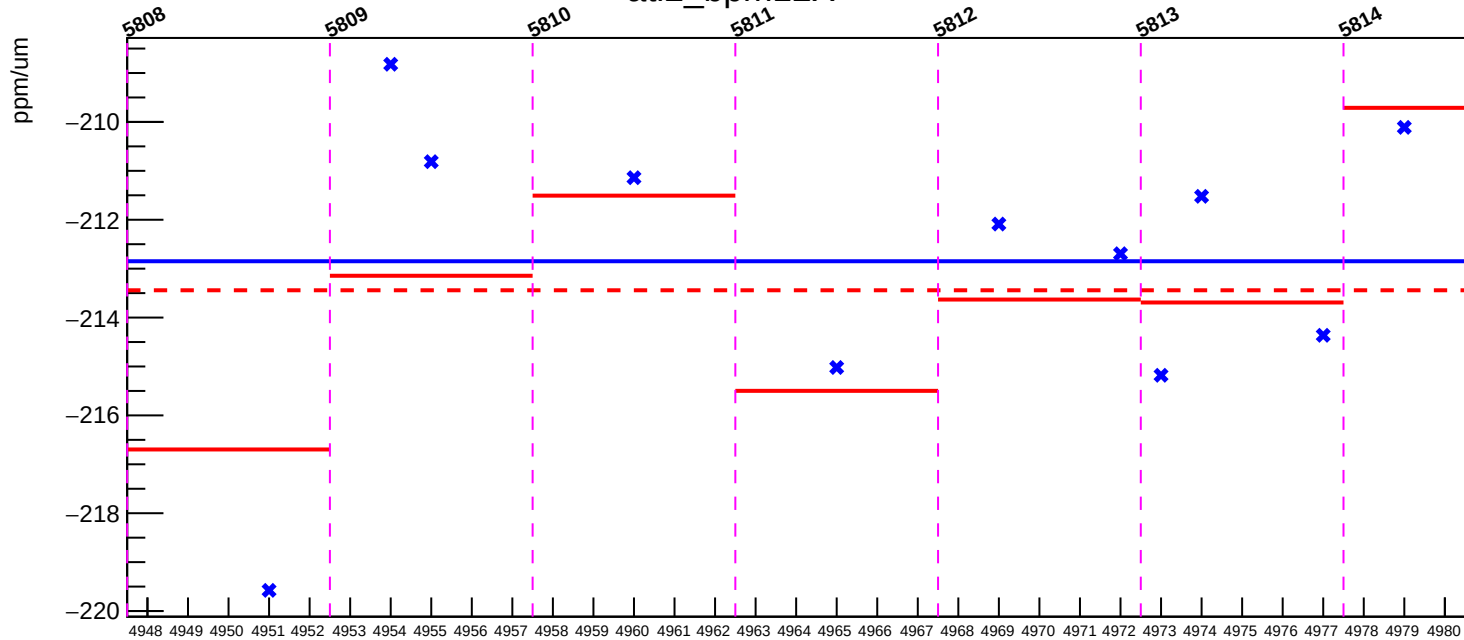




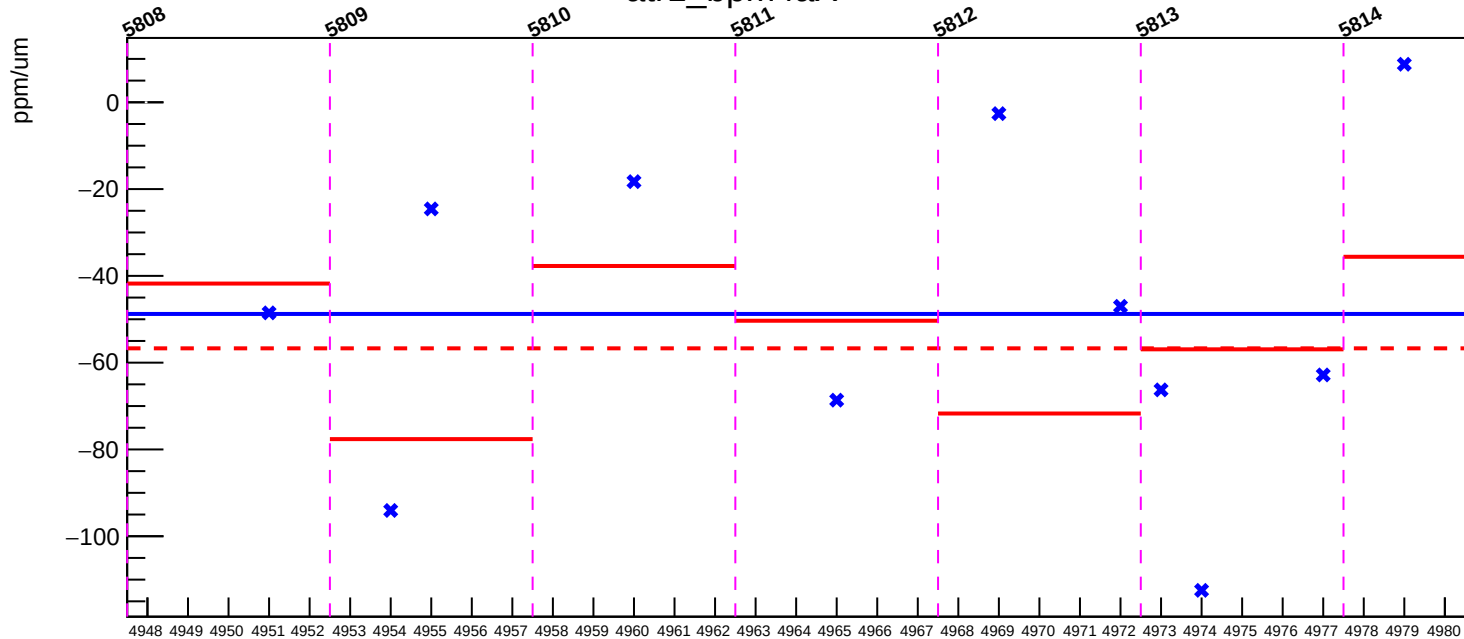


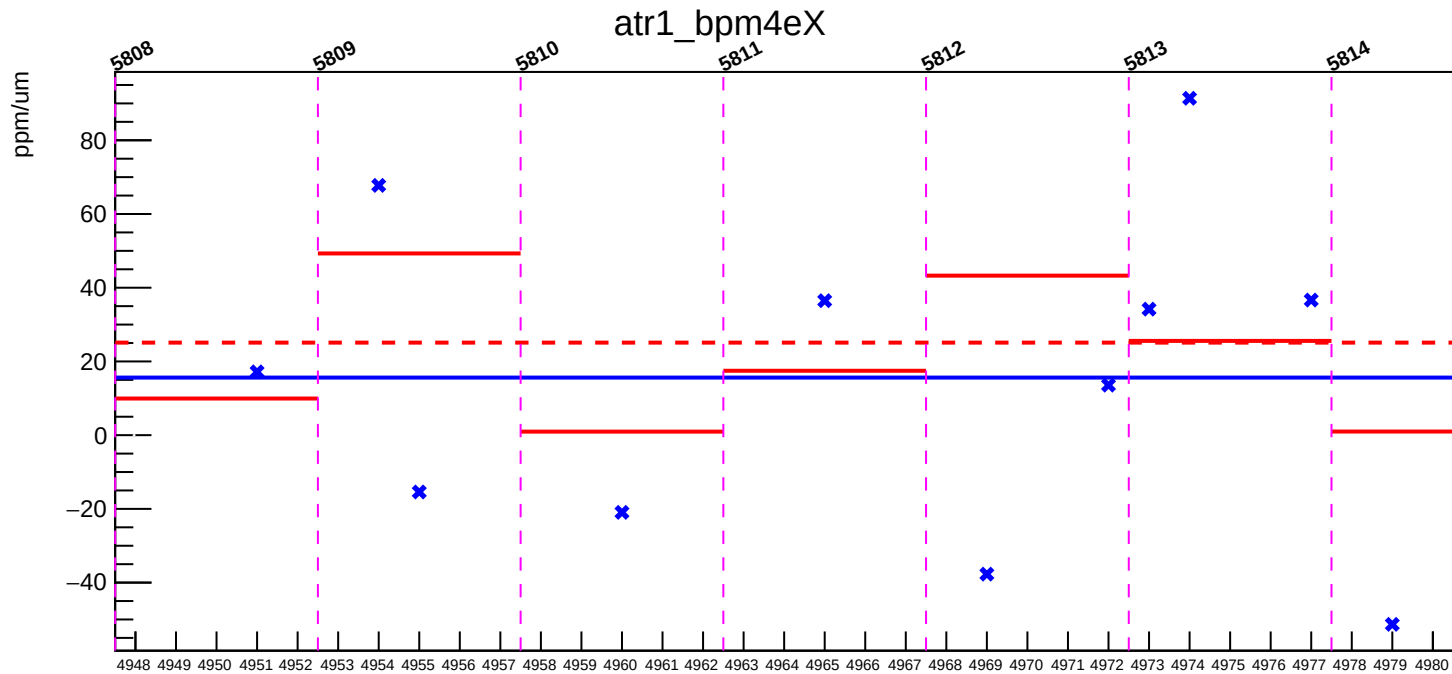


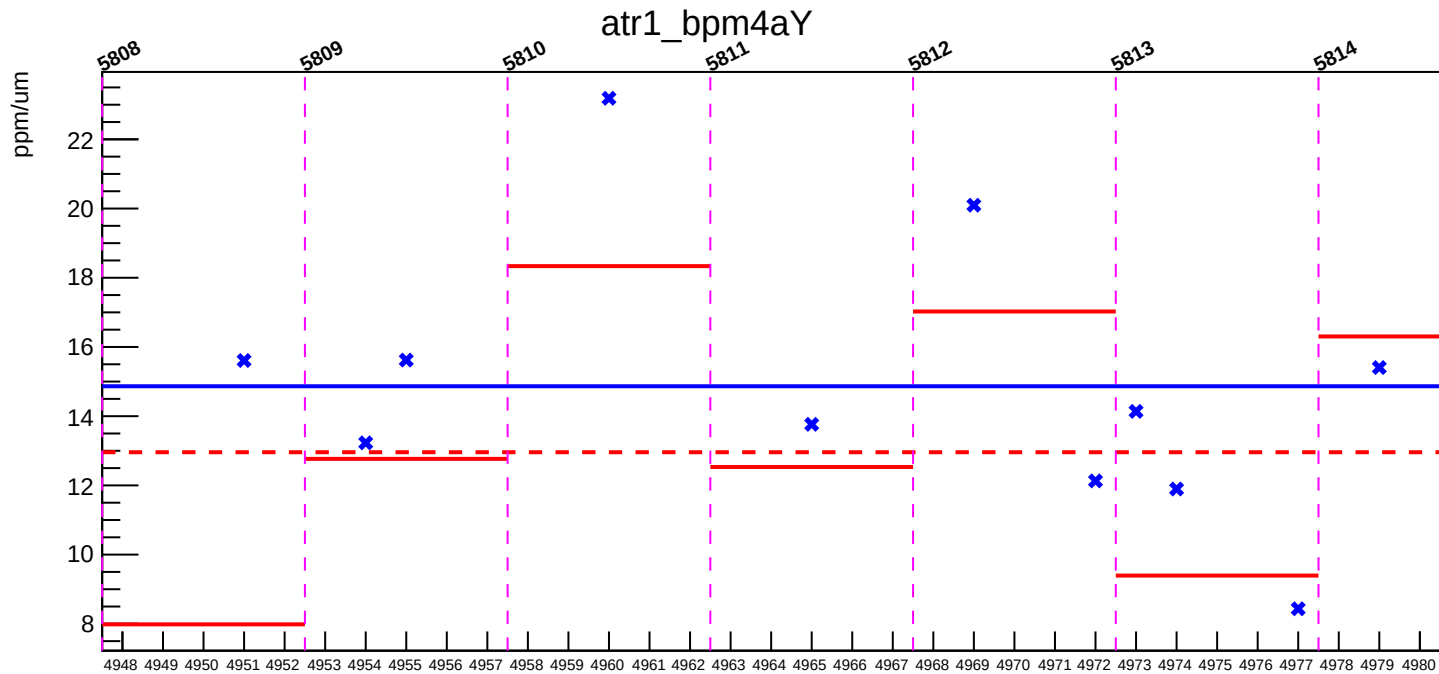
atl2_bpm12X

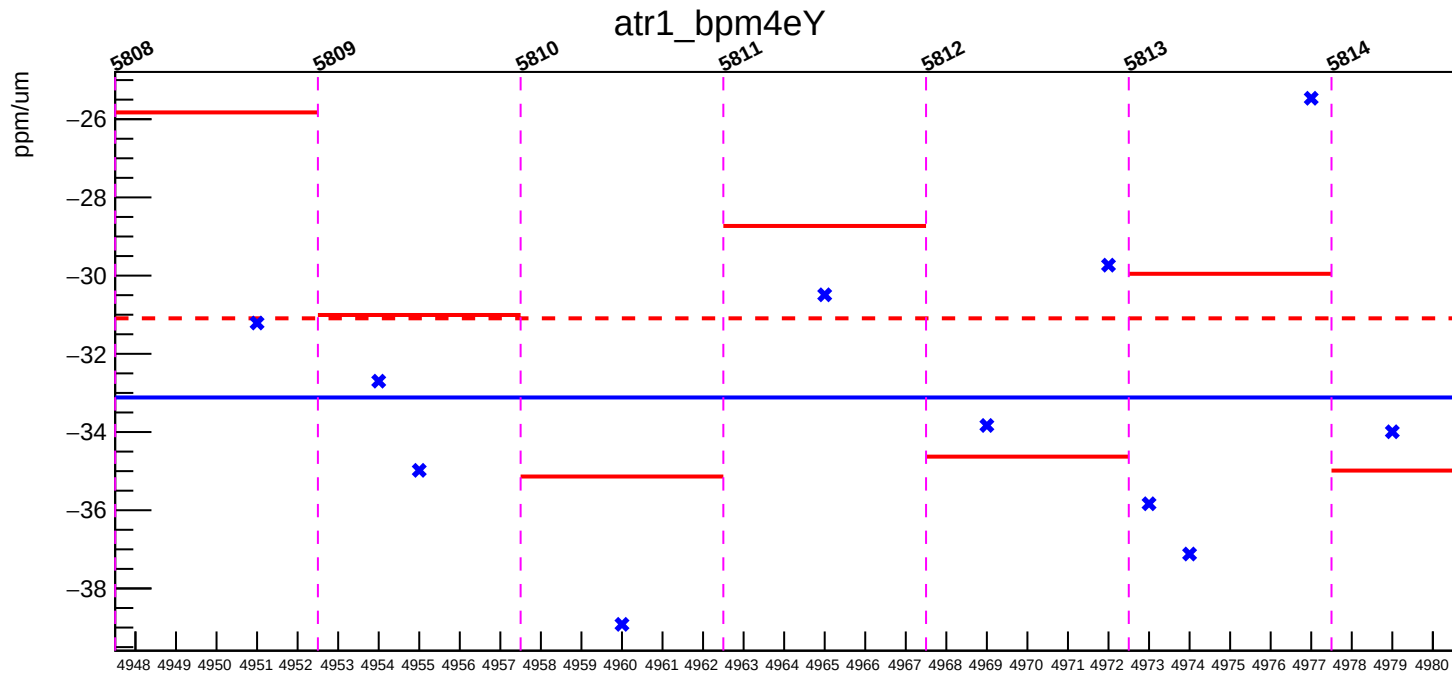


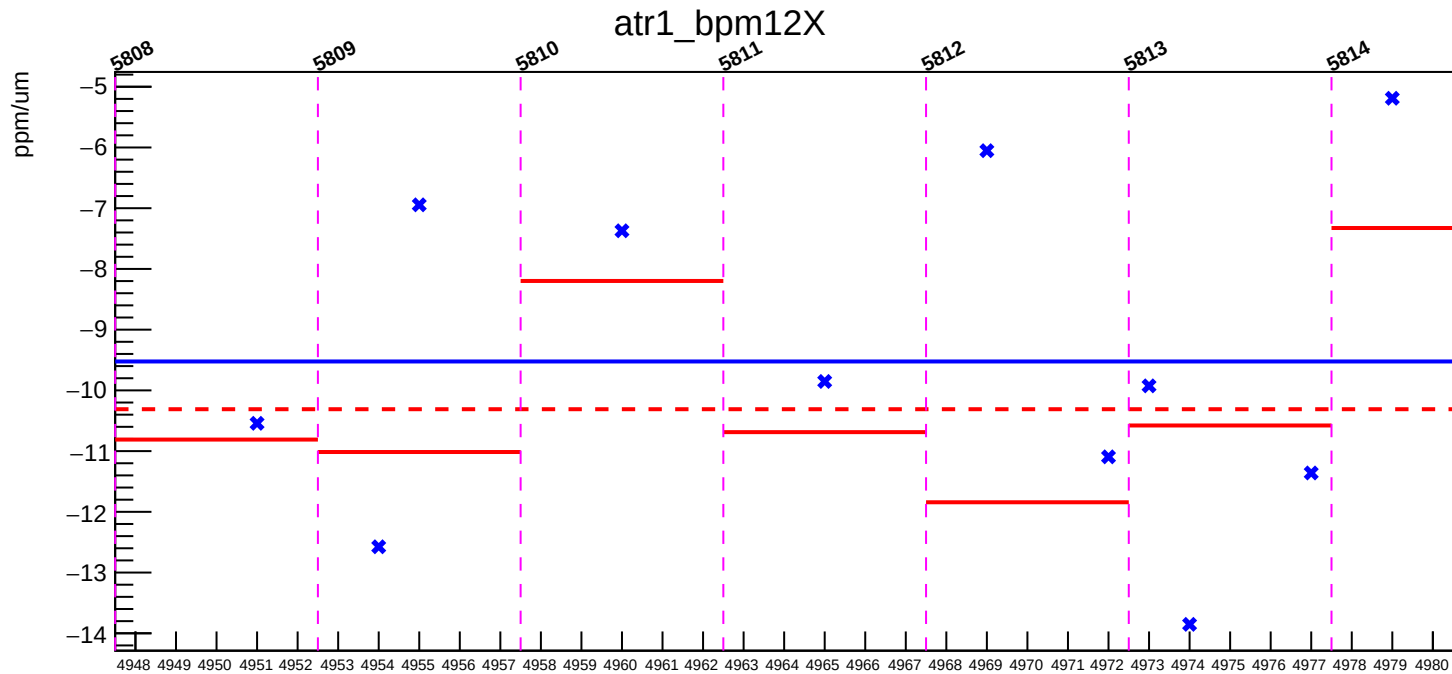
atr1_bpm4aX



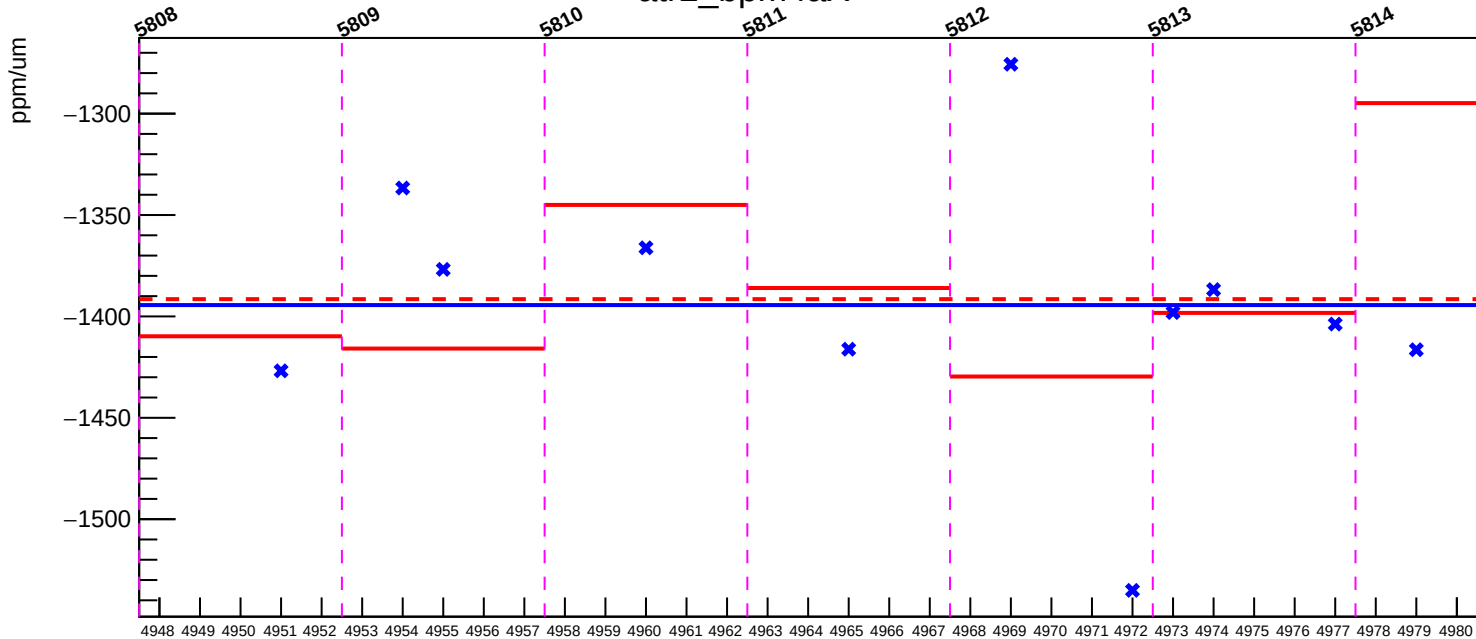




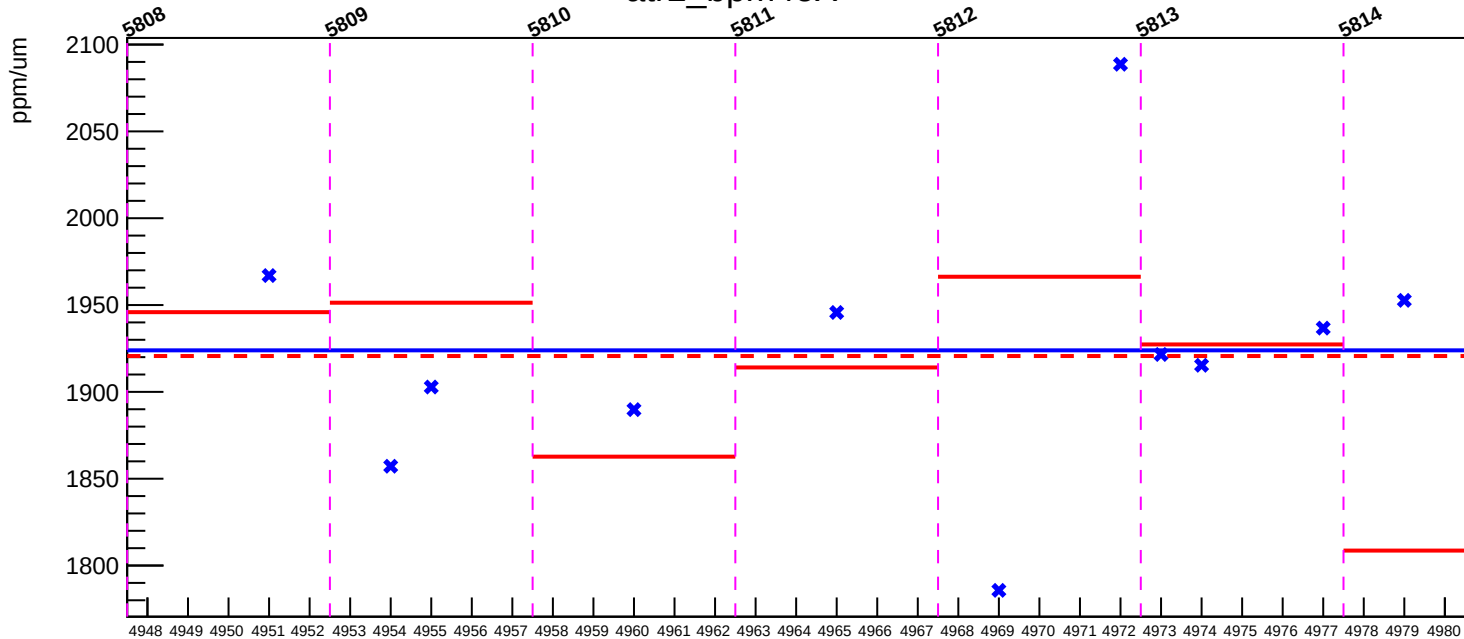


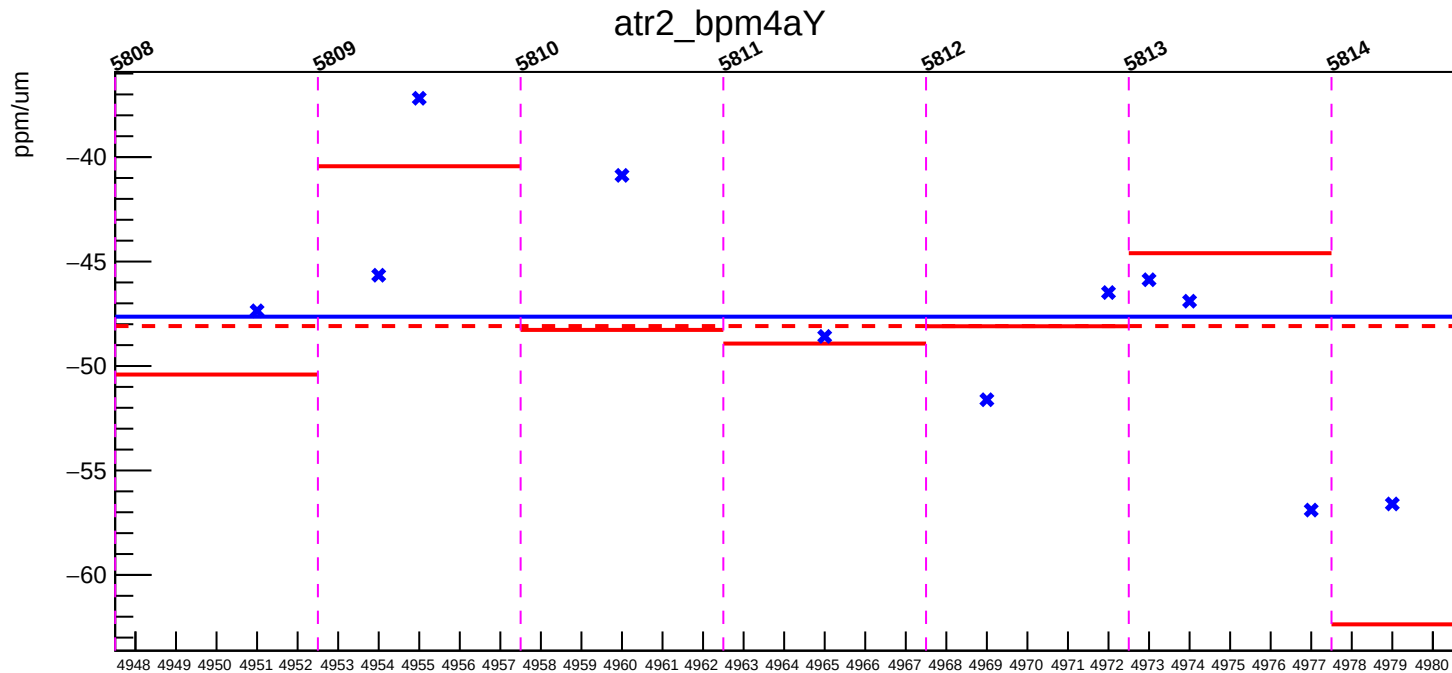


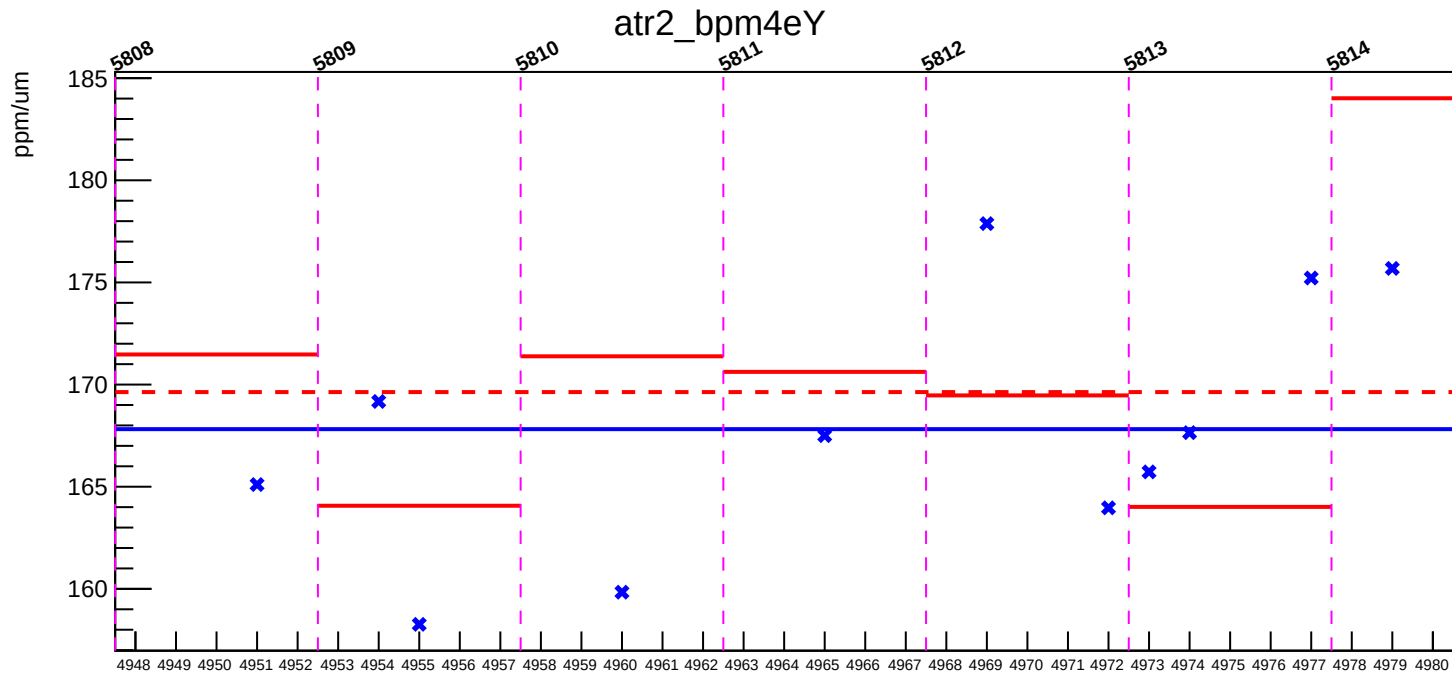
atr2_bpm4aX

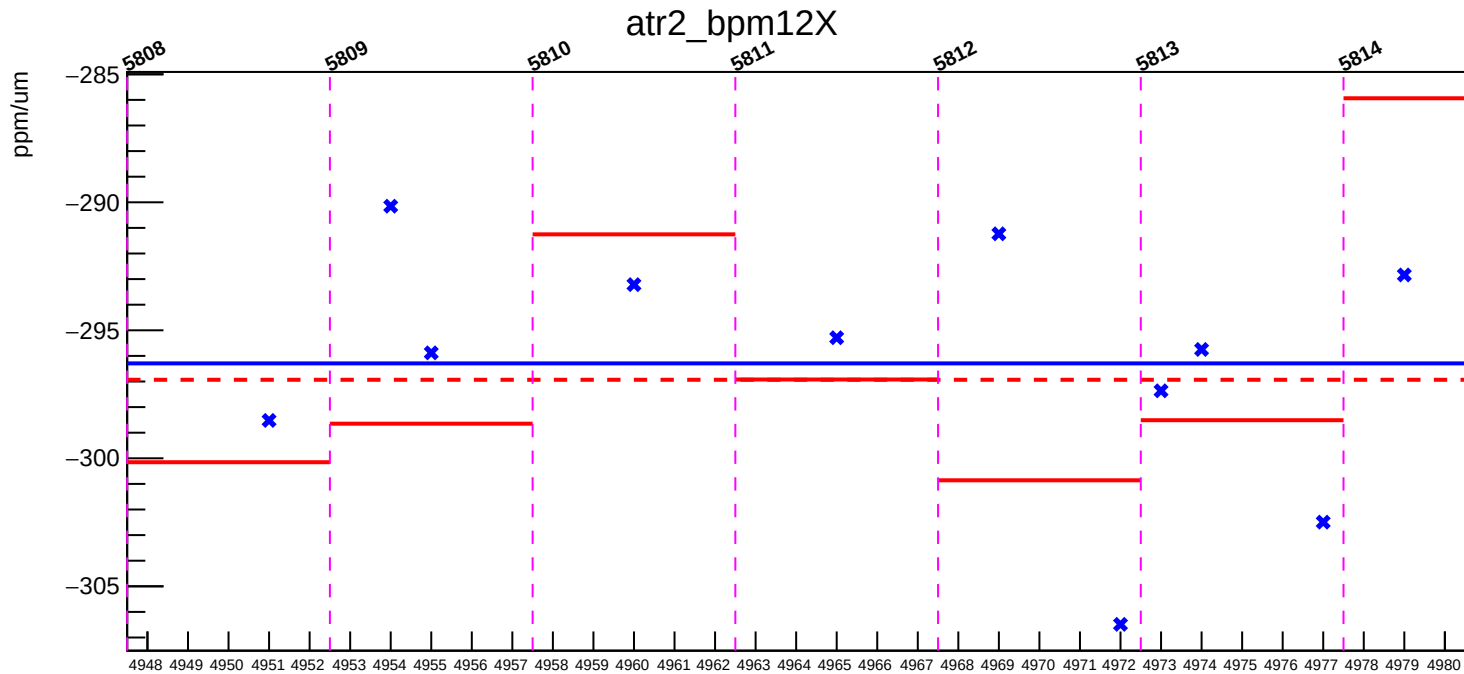


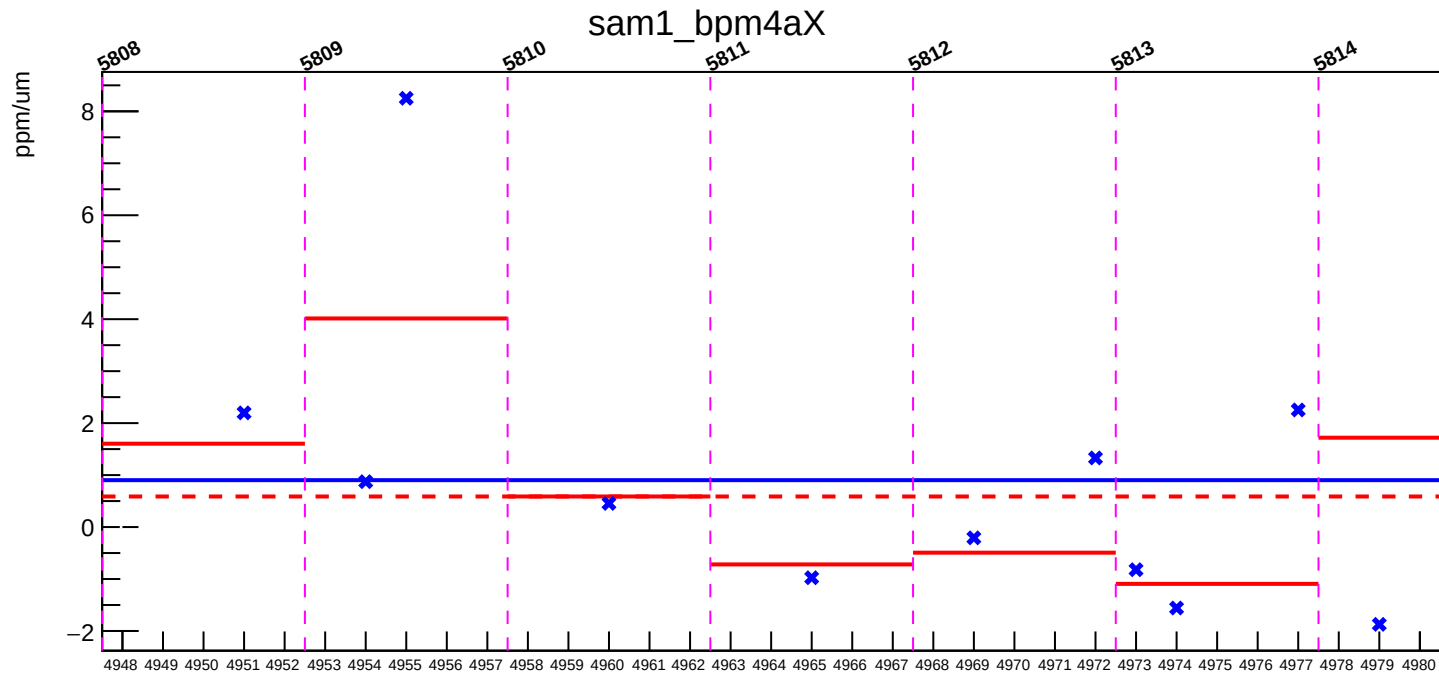
atr2_bpm4eX

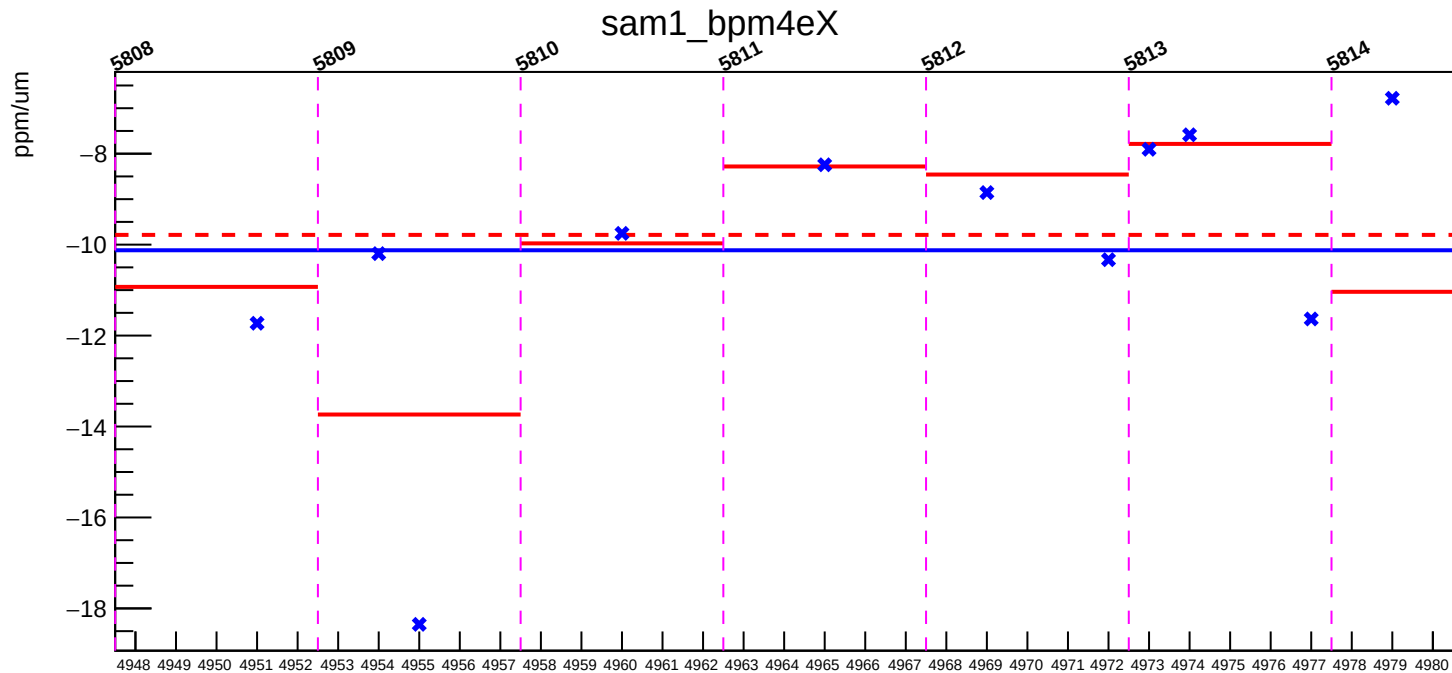


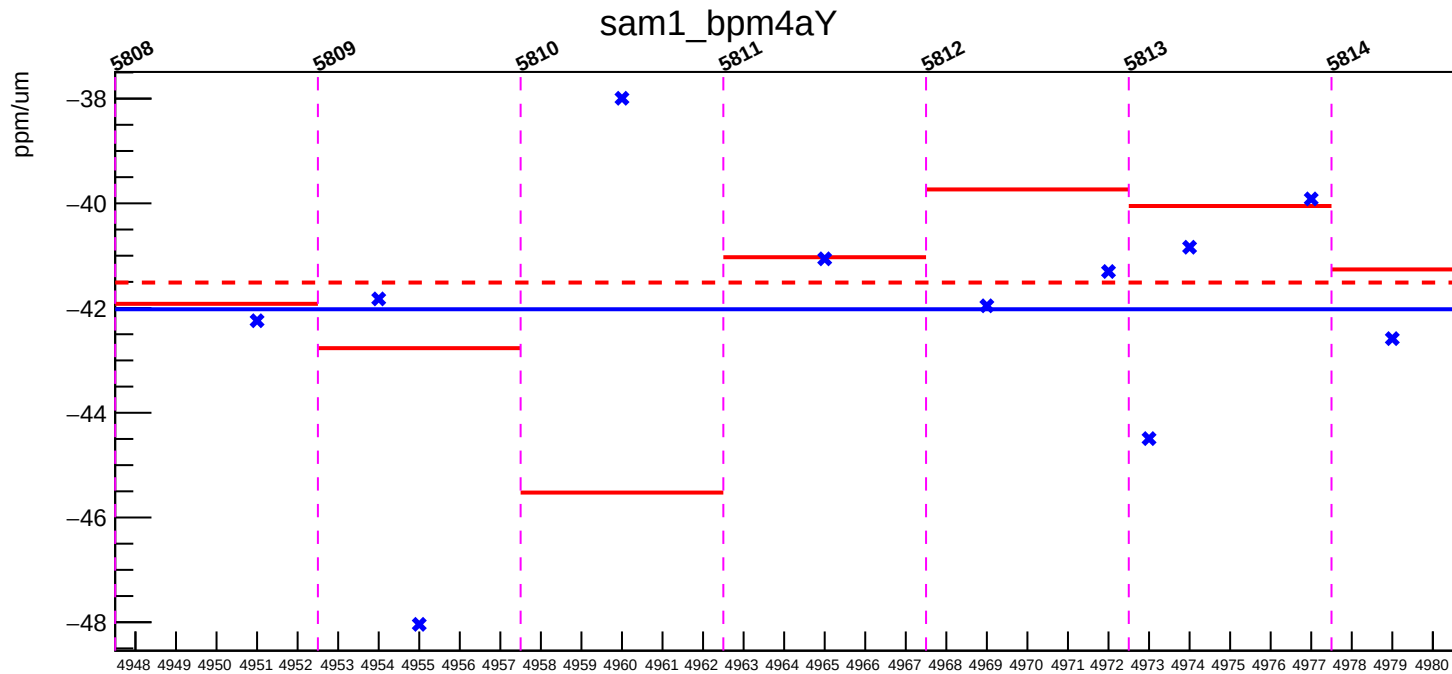


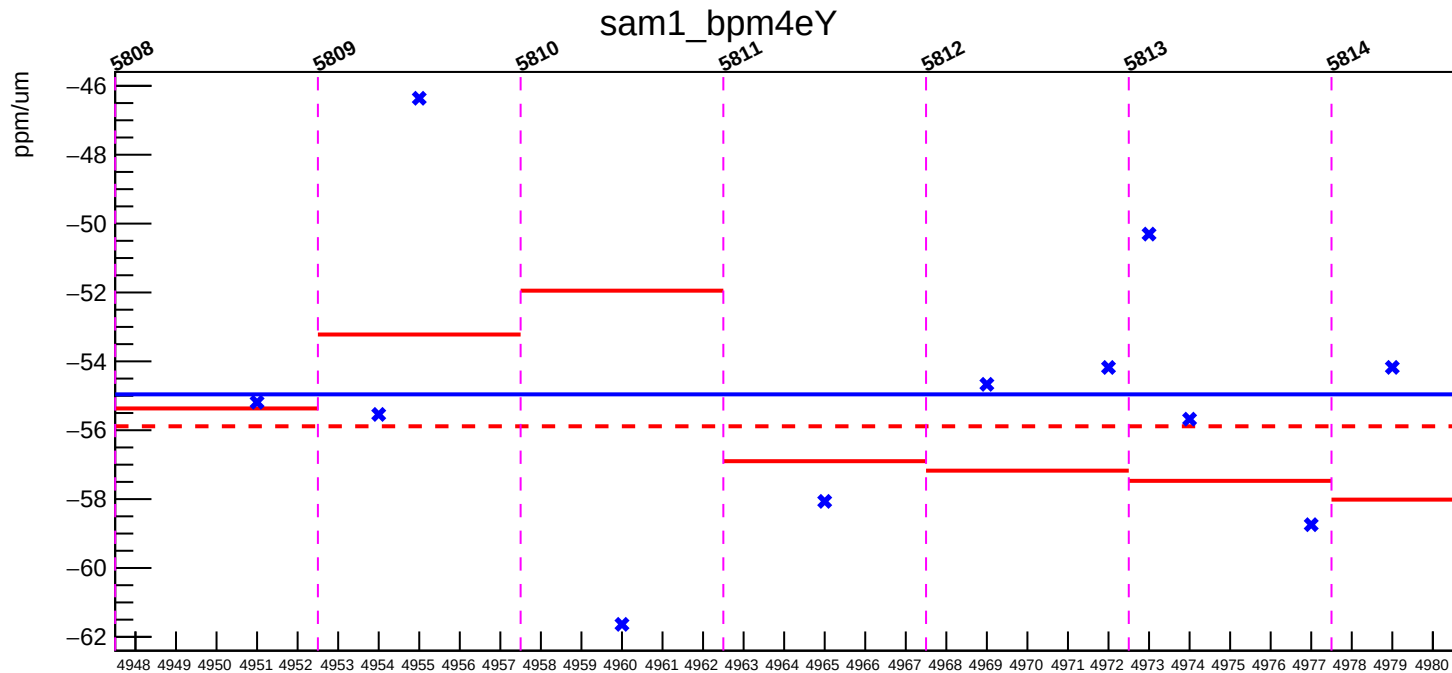


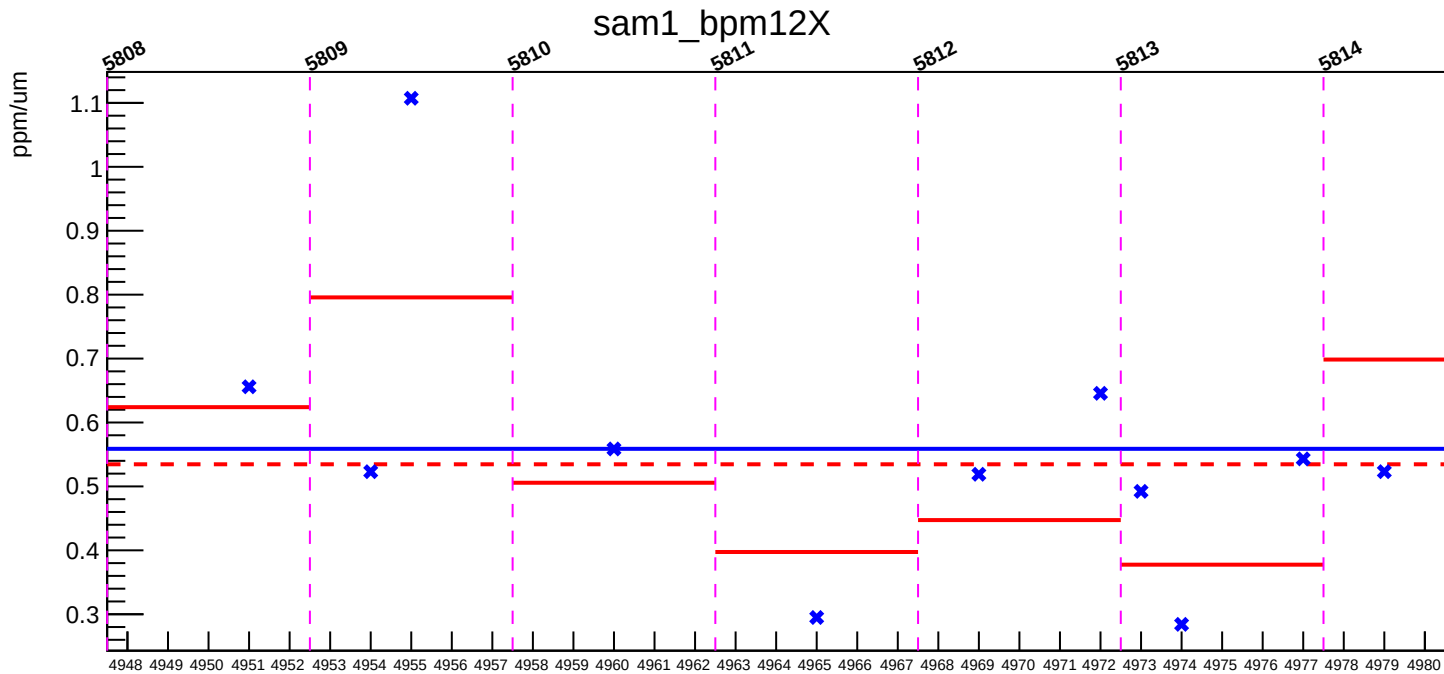


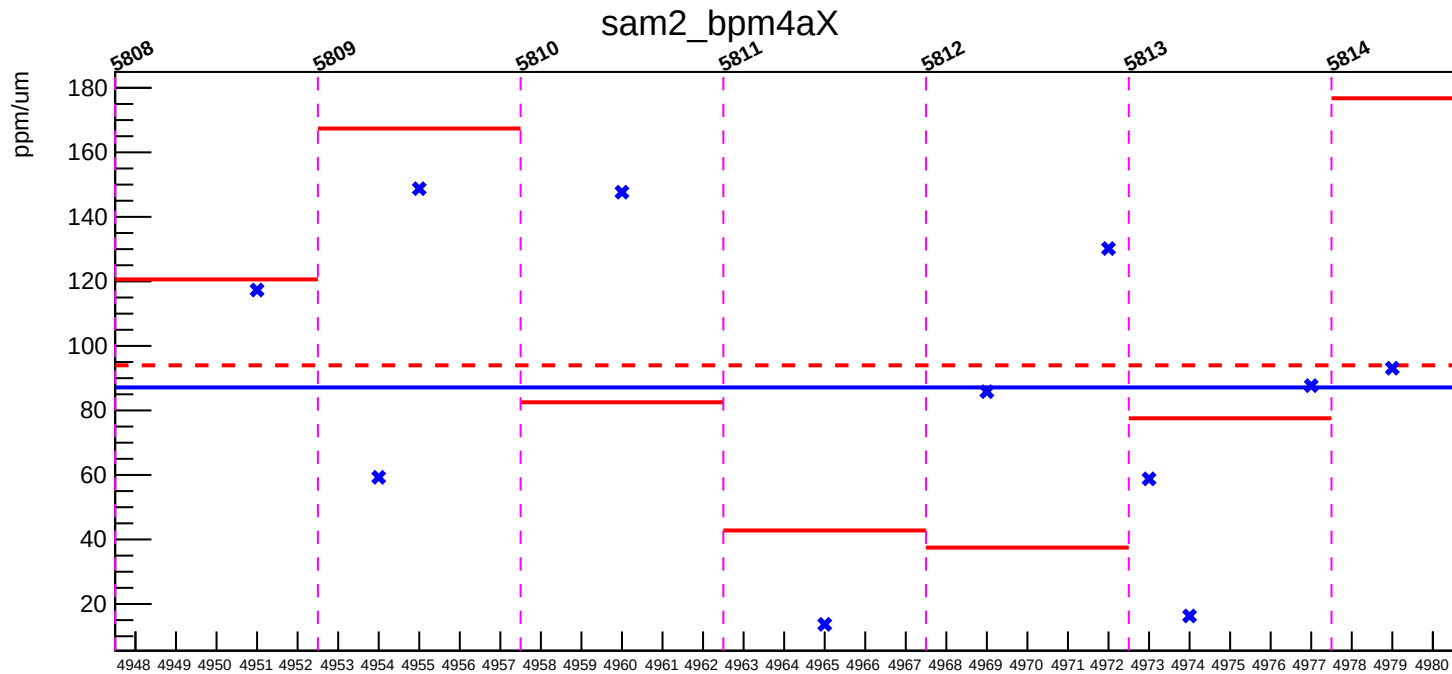




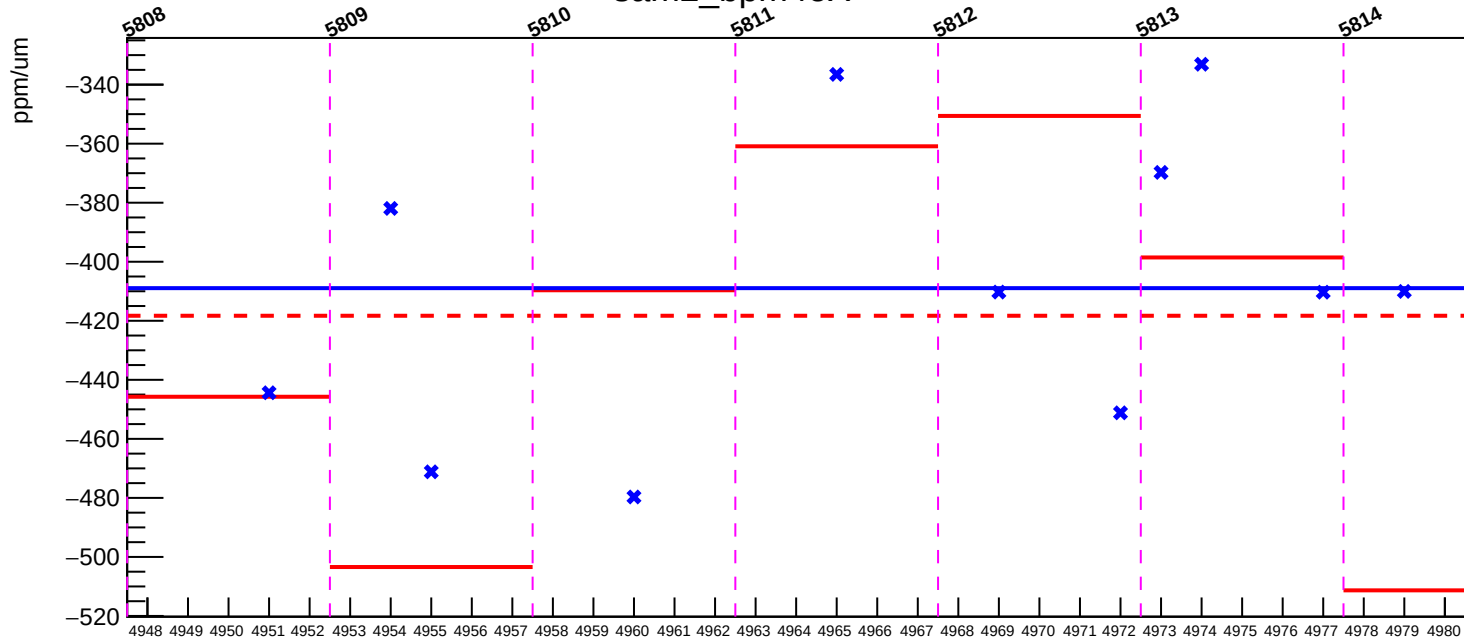


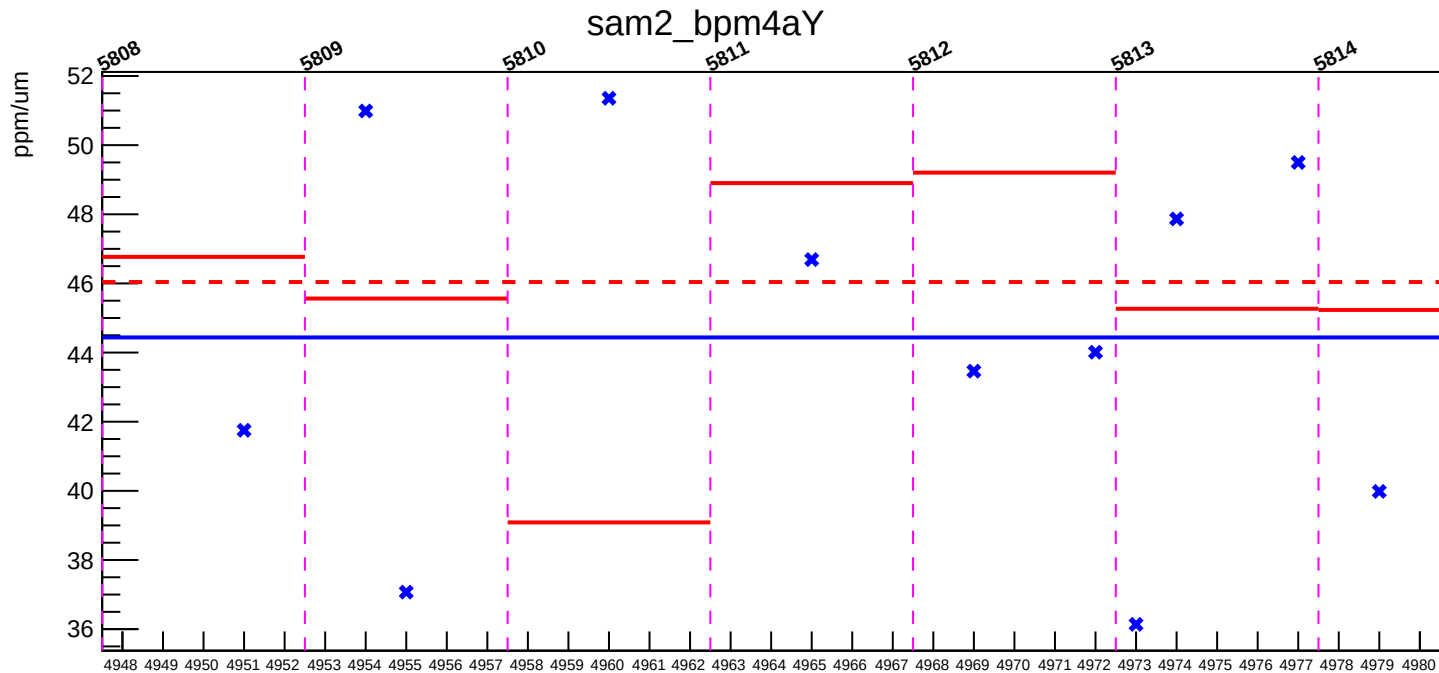




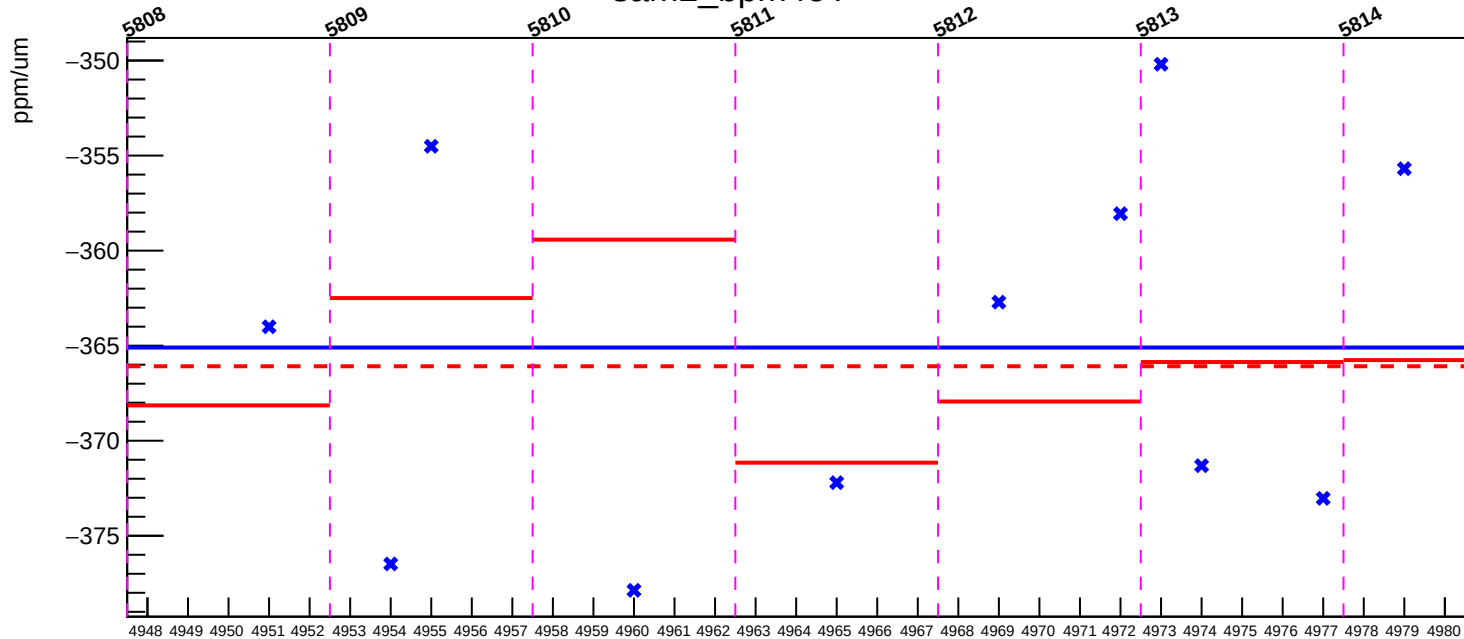


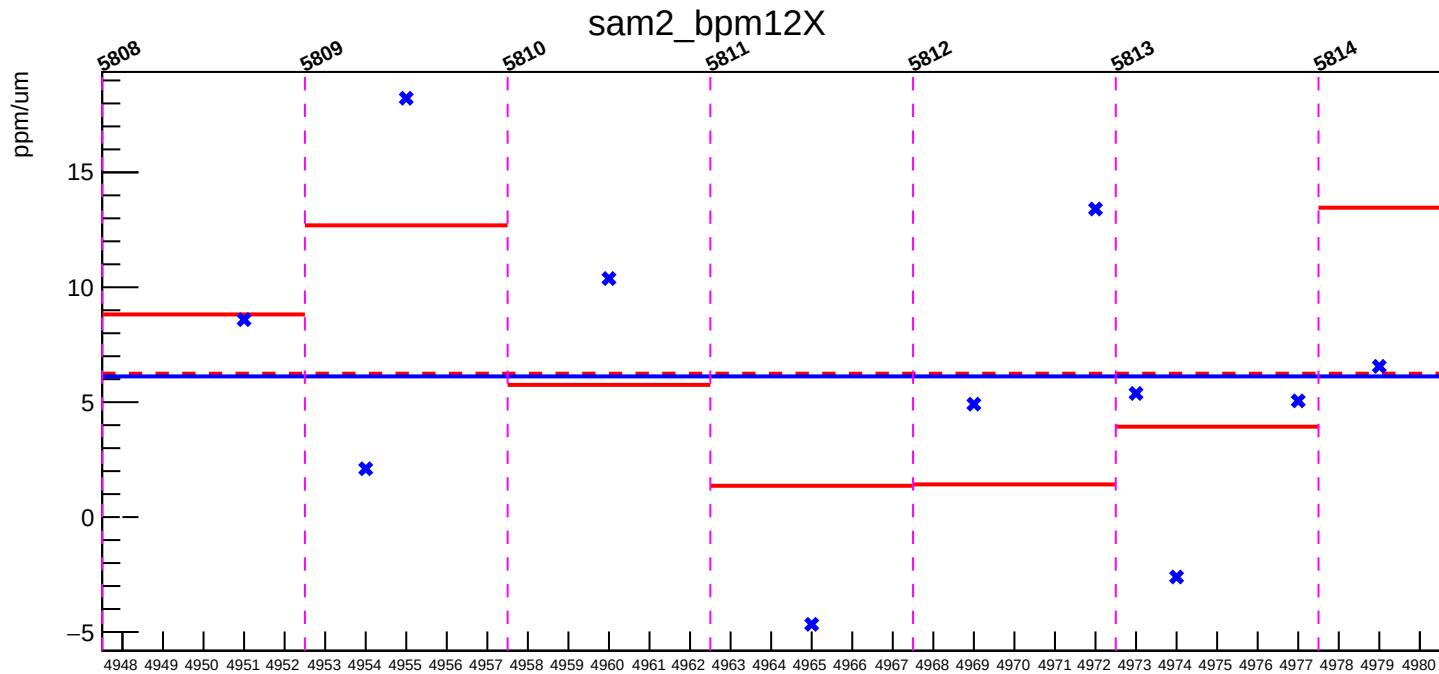
sam2_bpm4eX

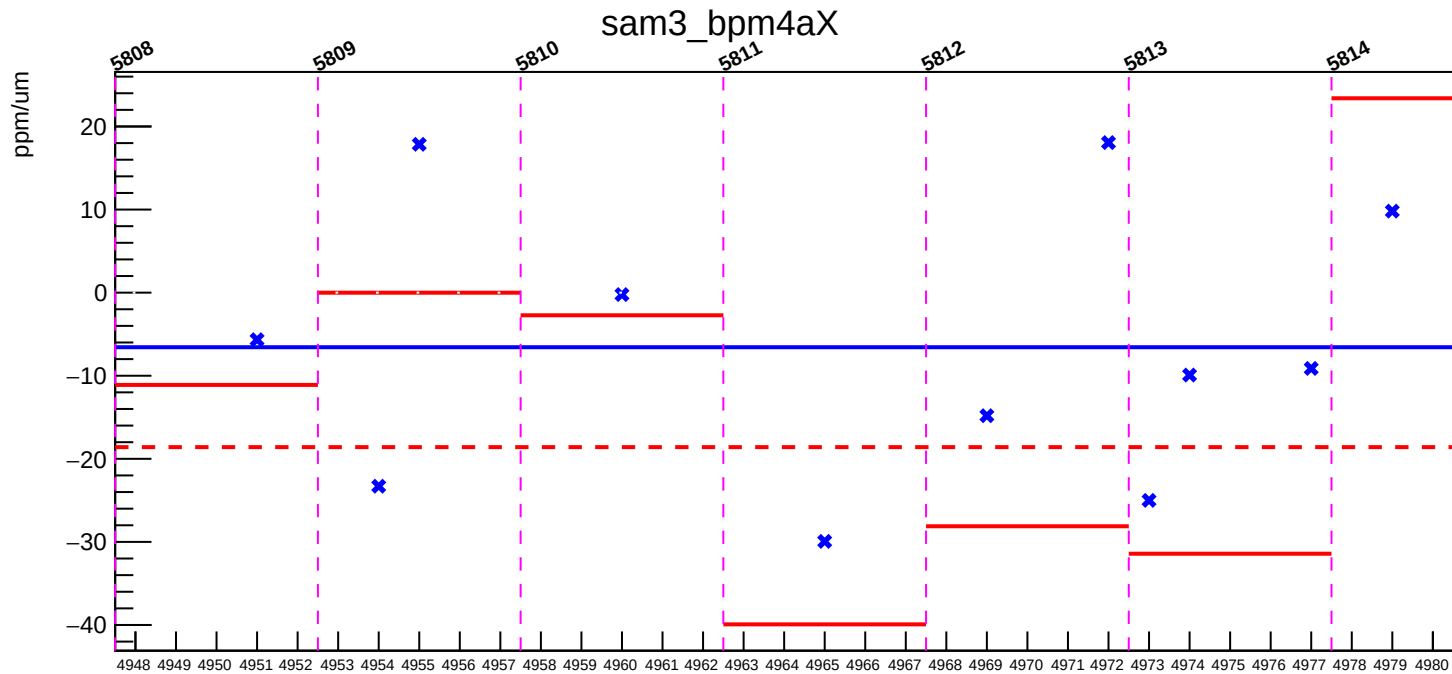




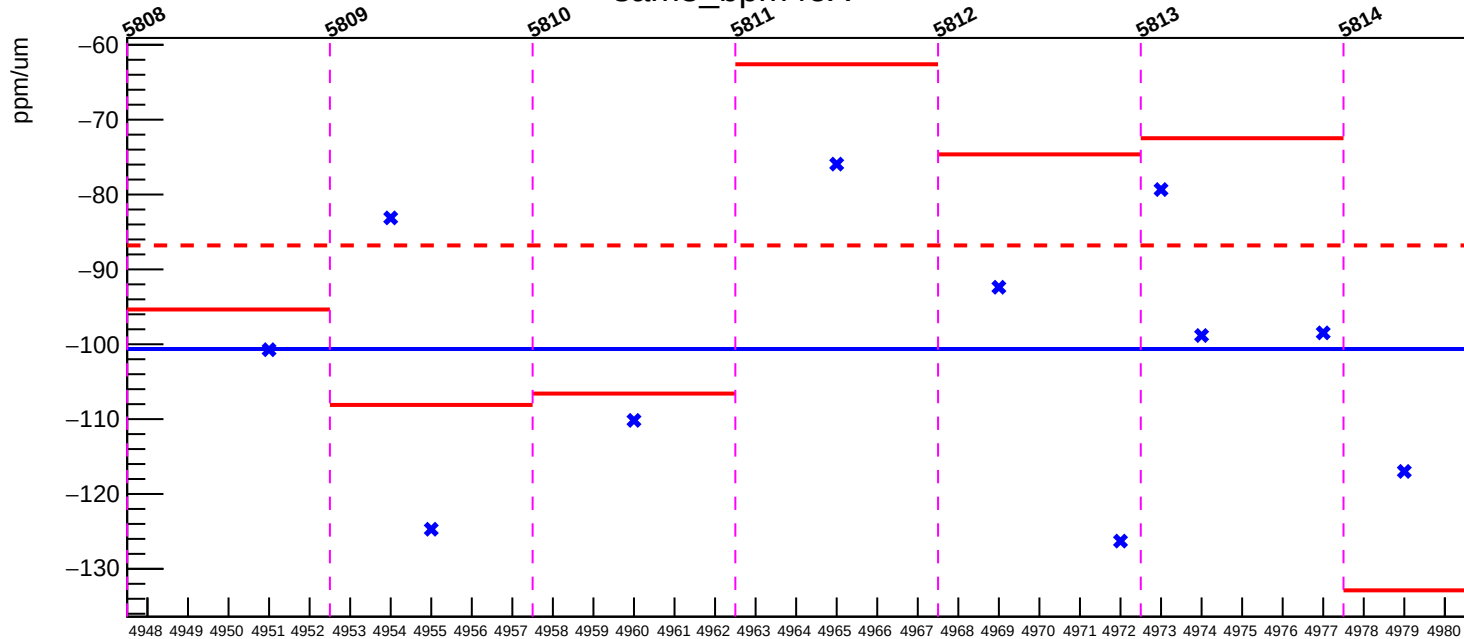
sam2_bpm4eY

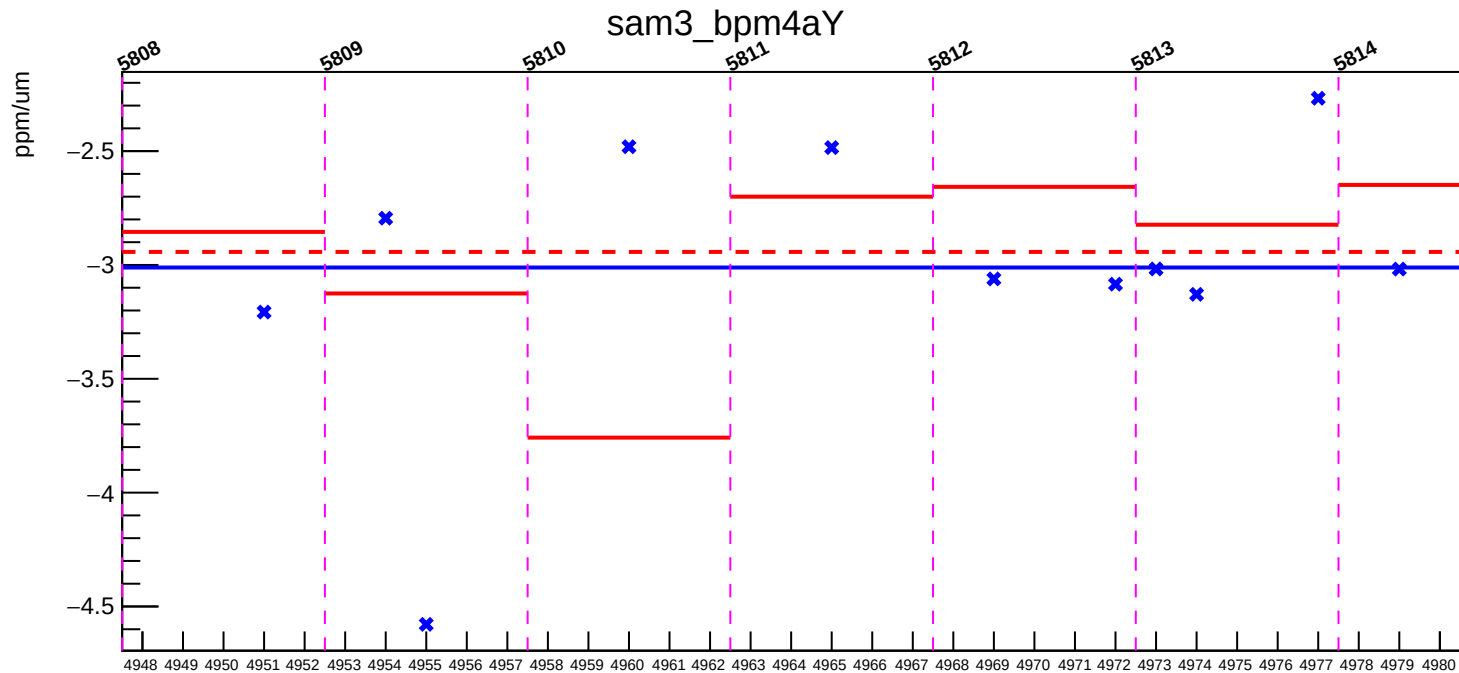




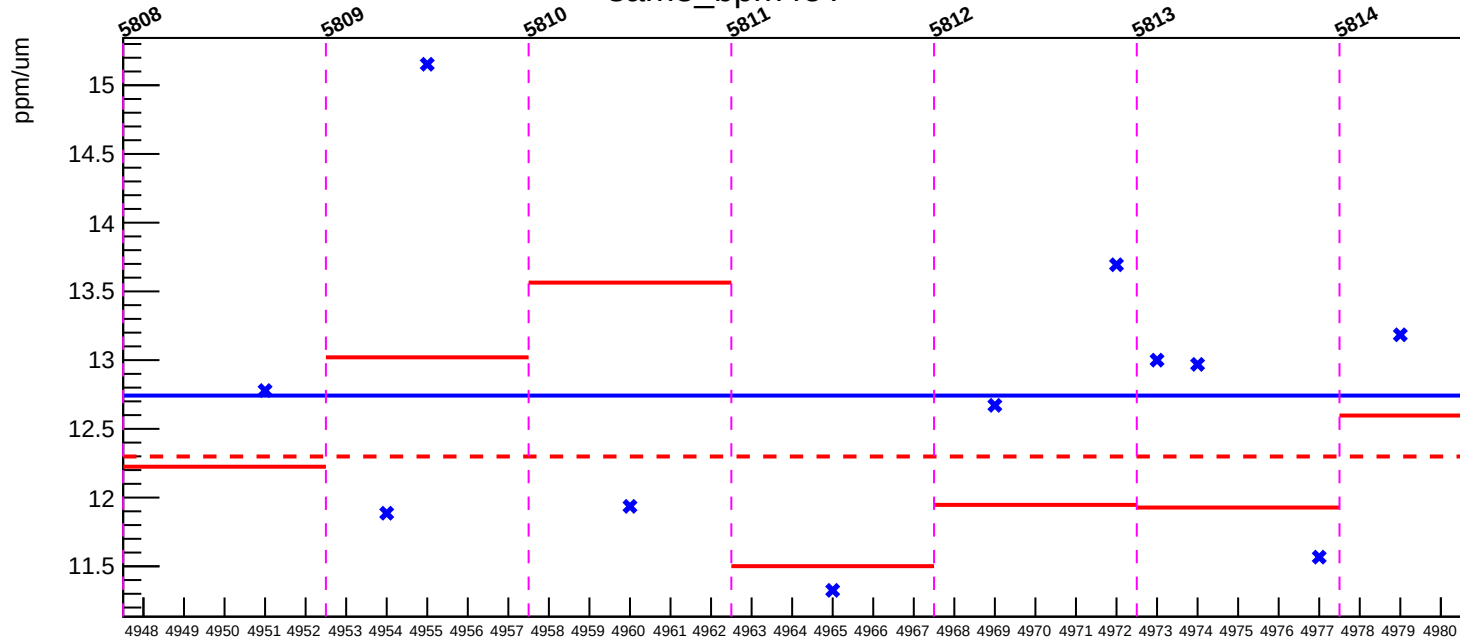


sam3_bpm4eX

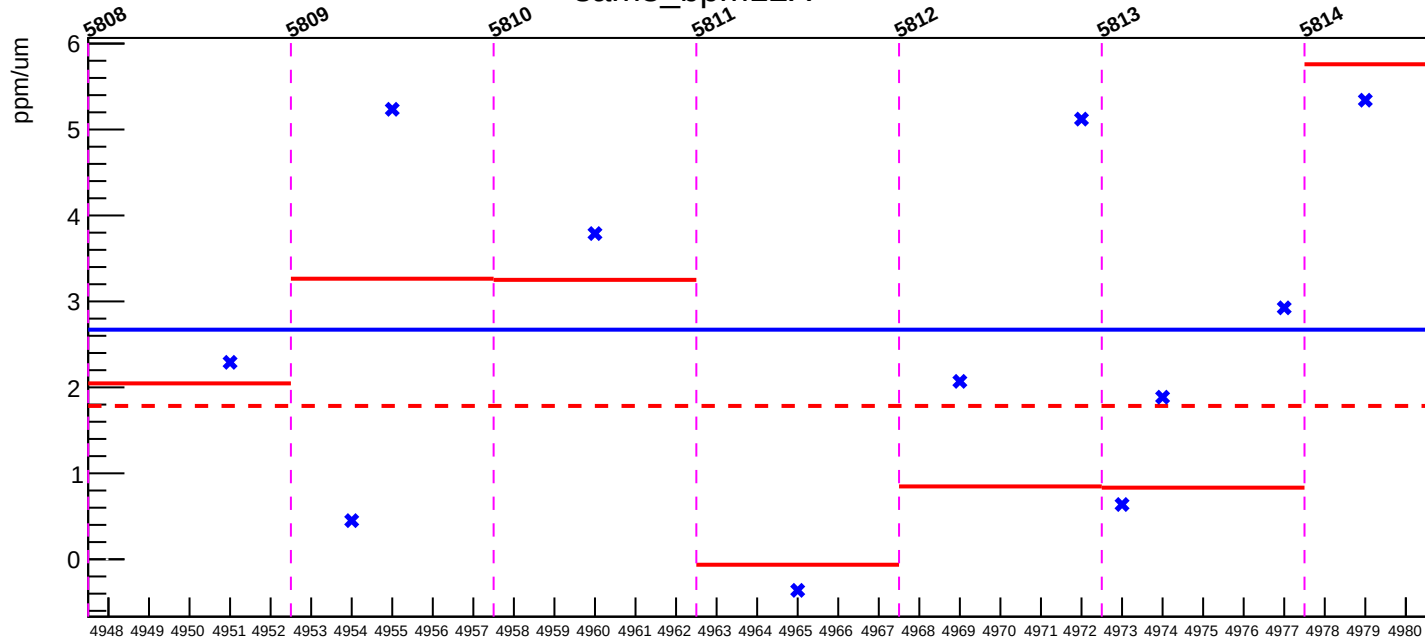


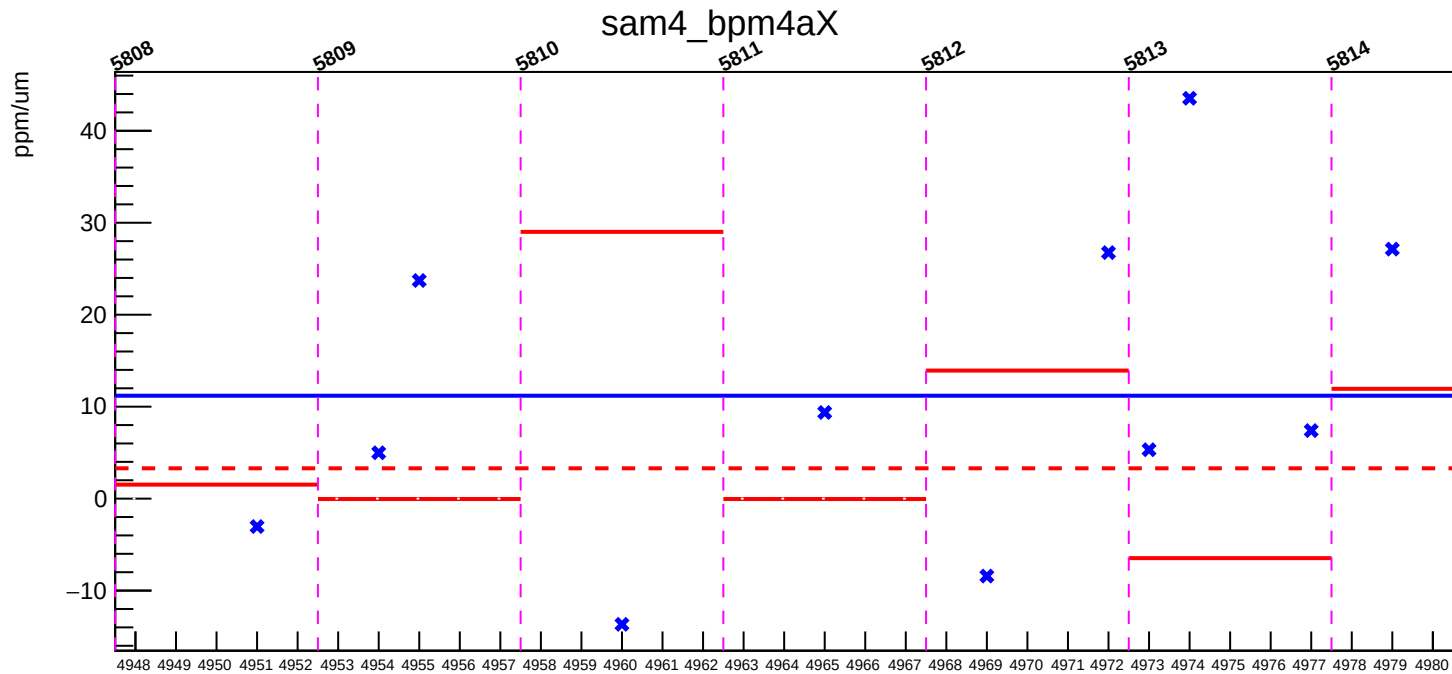


sam3_bpm4eY

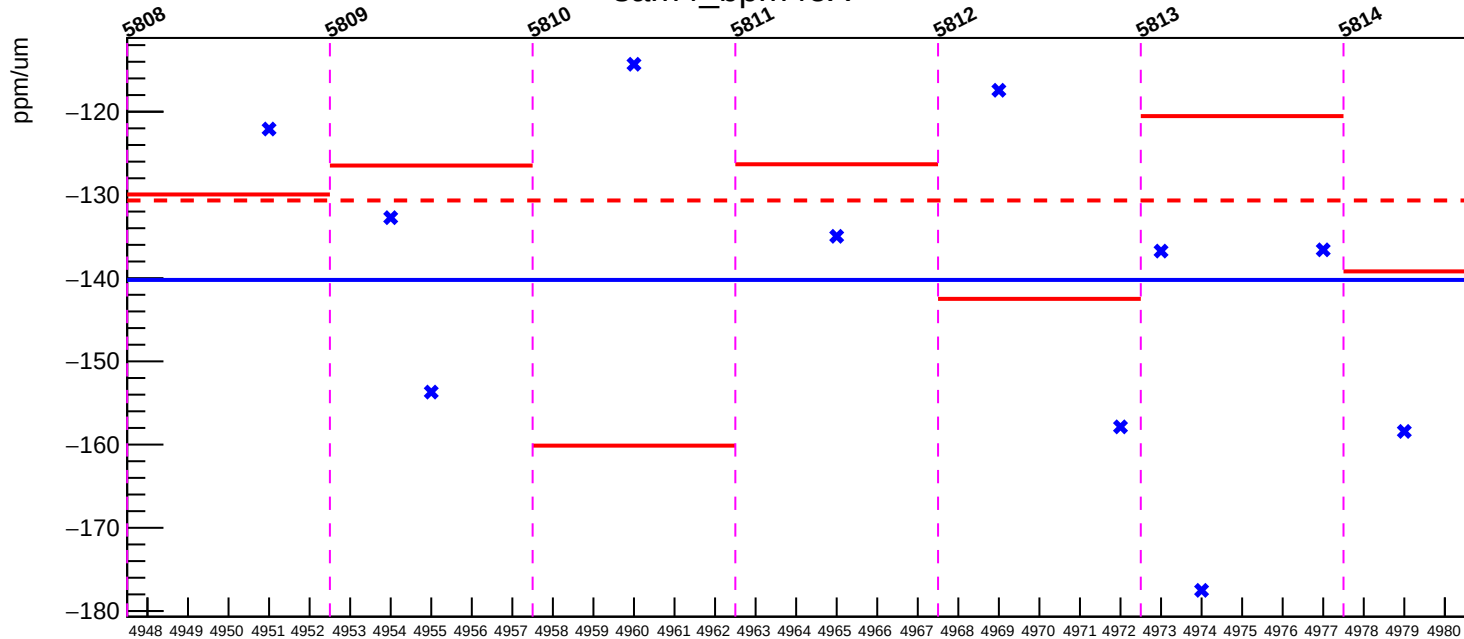


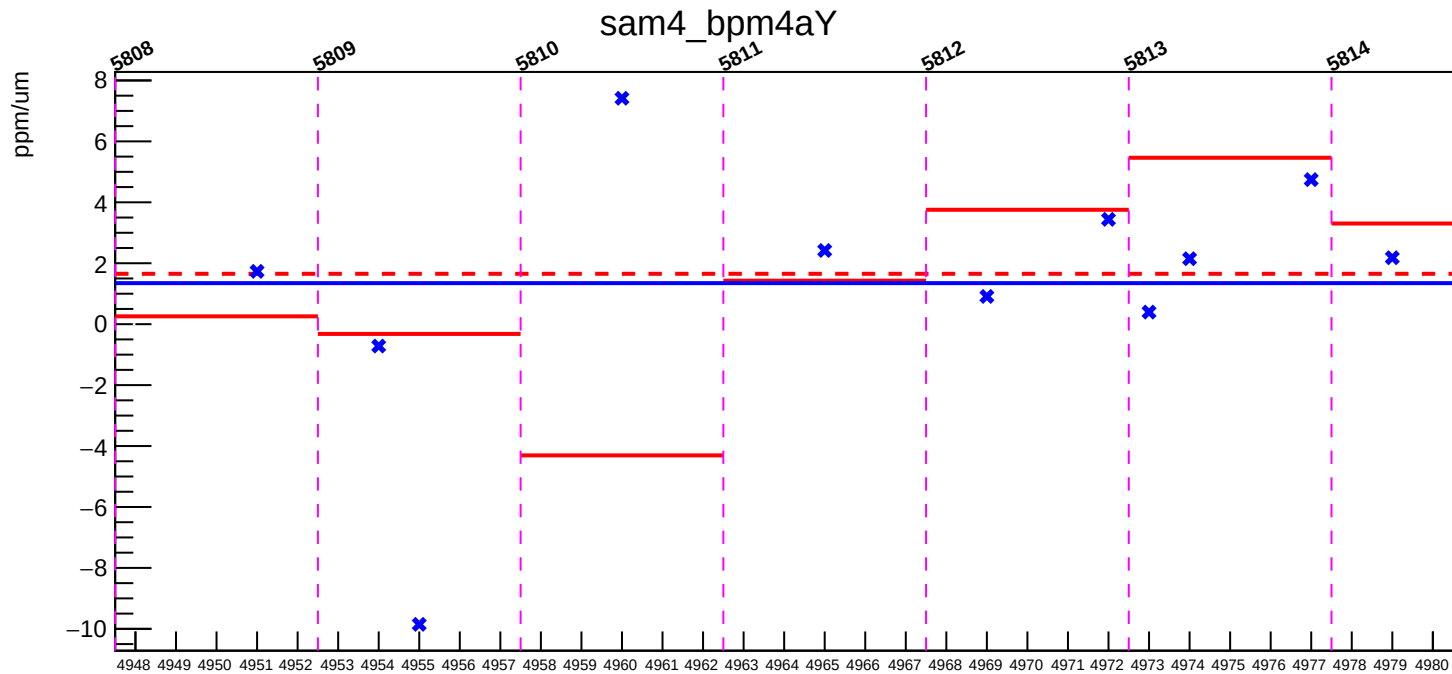
sam3_bpm12X

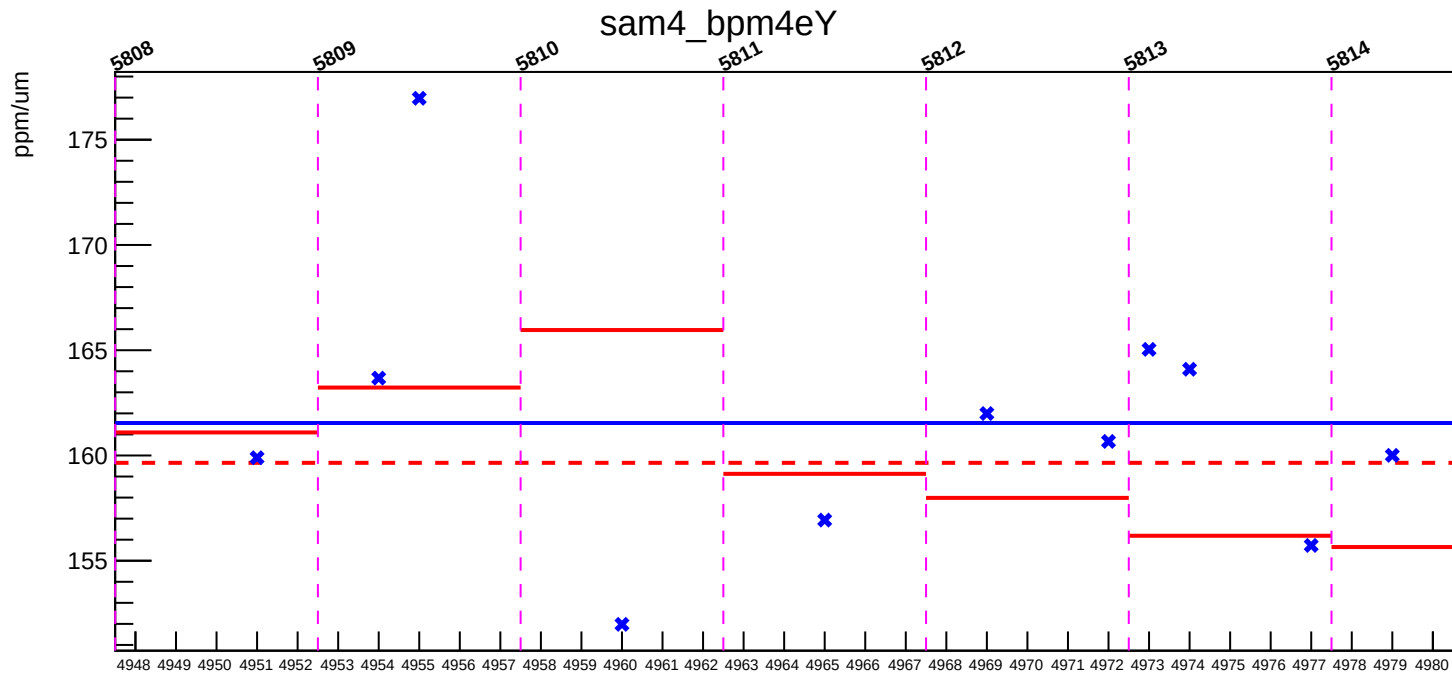




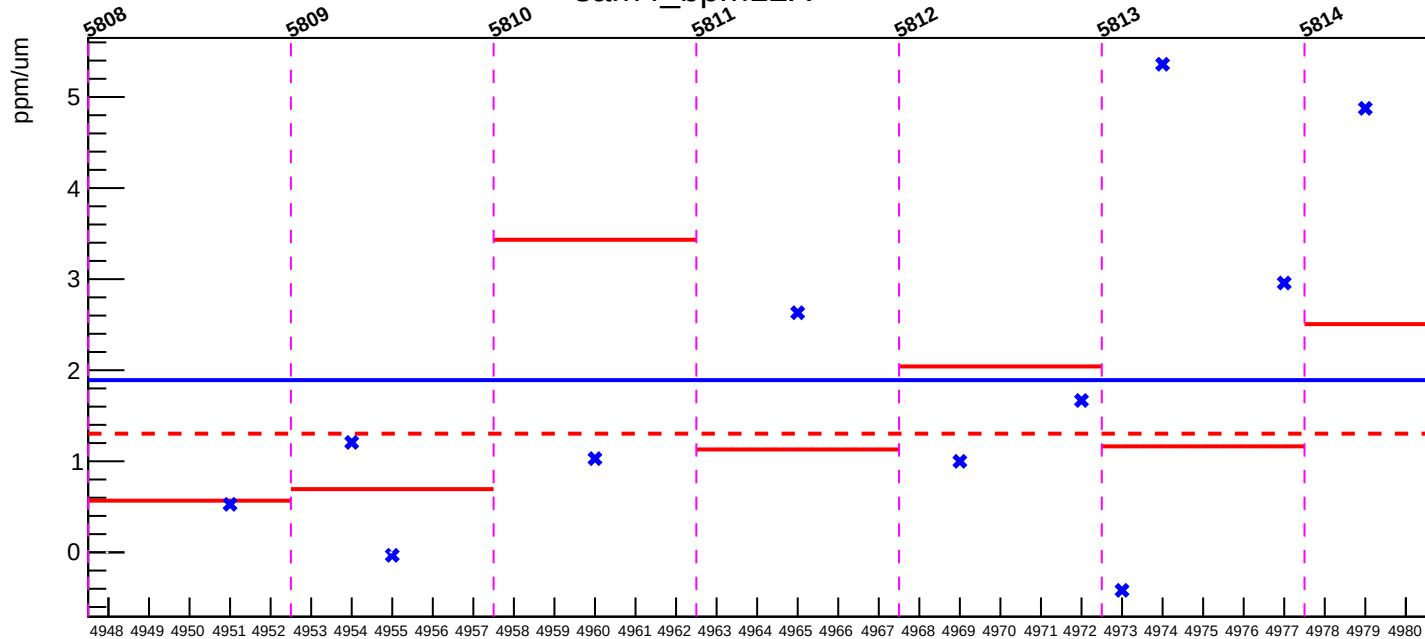
sam4_bpm4eX

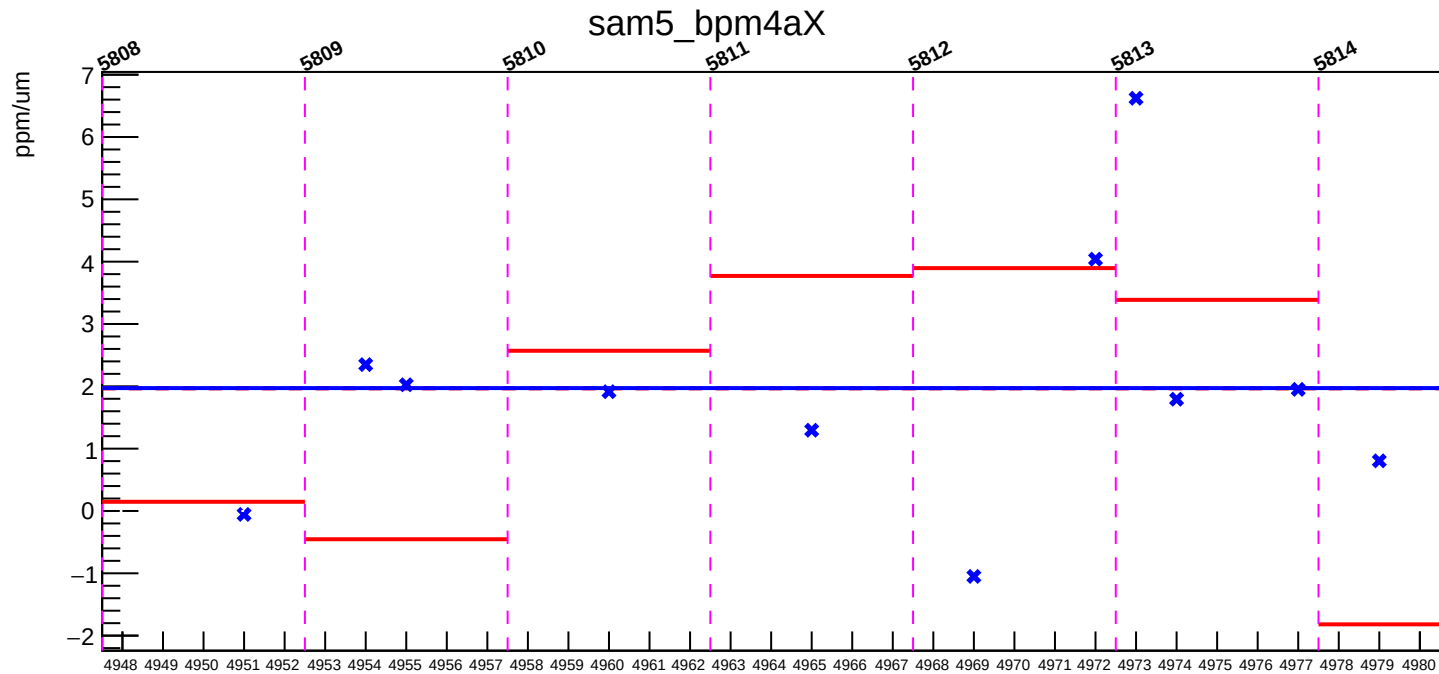


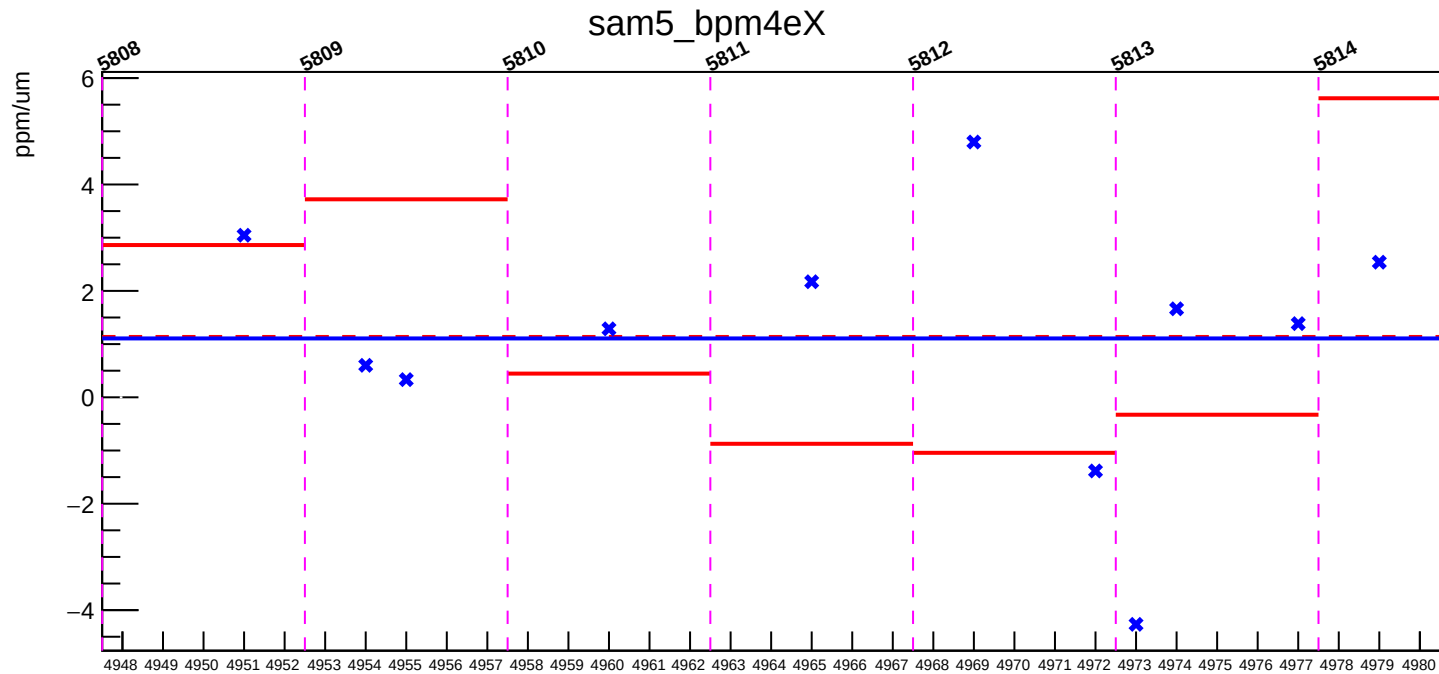


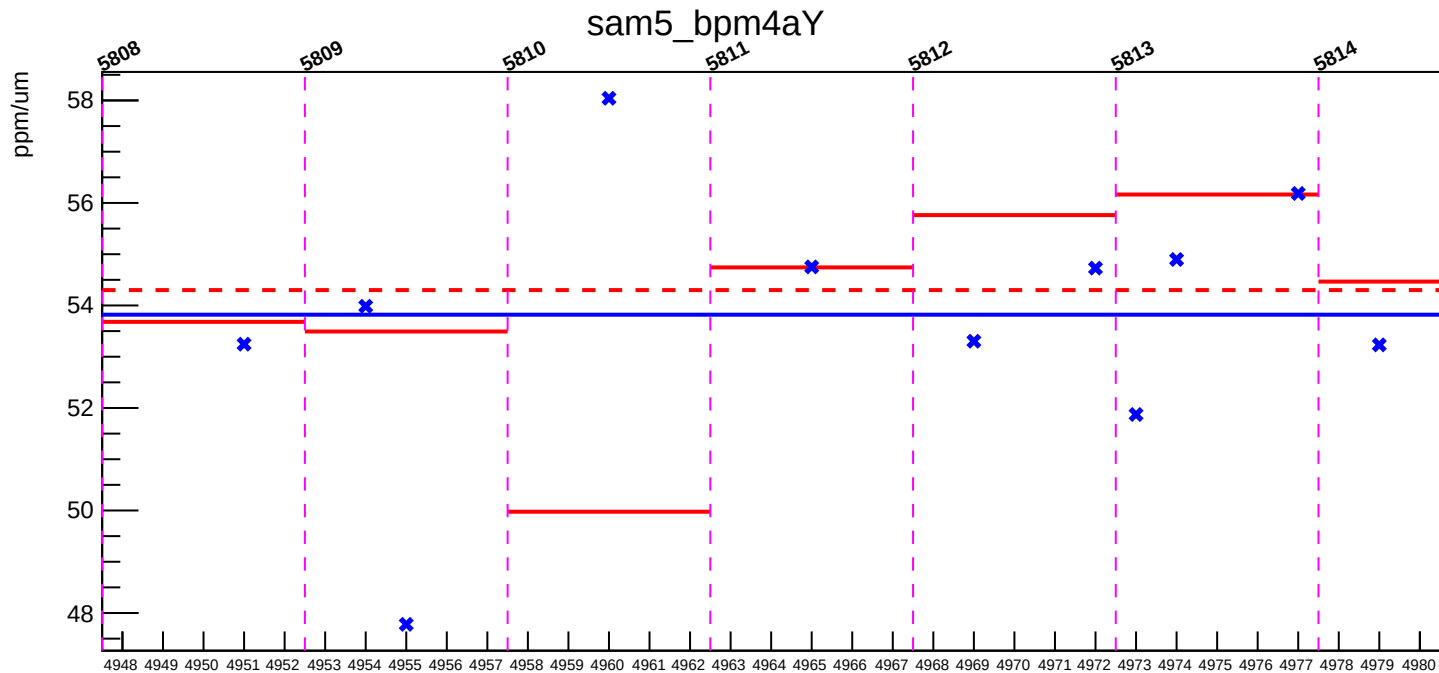


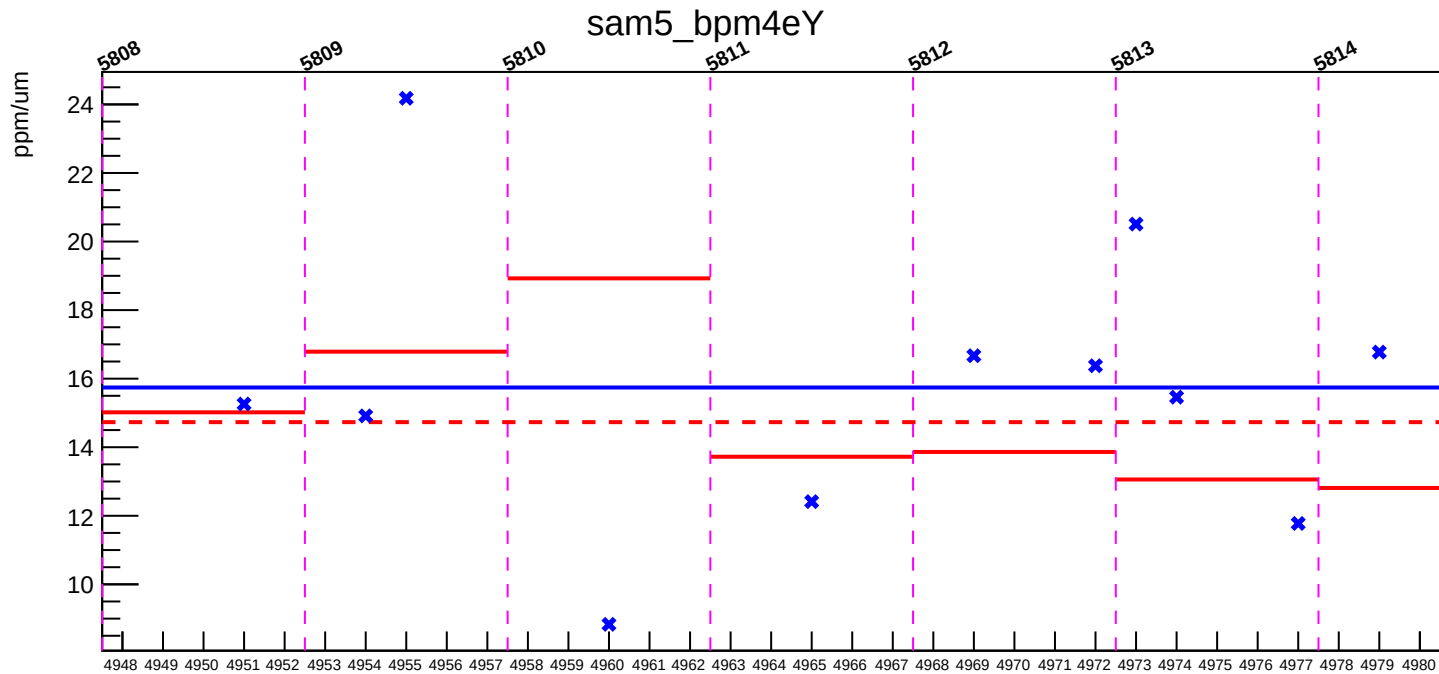
sam4_bpm12X

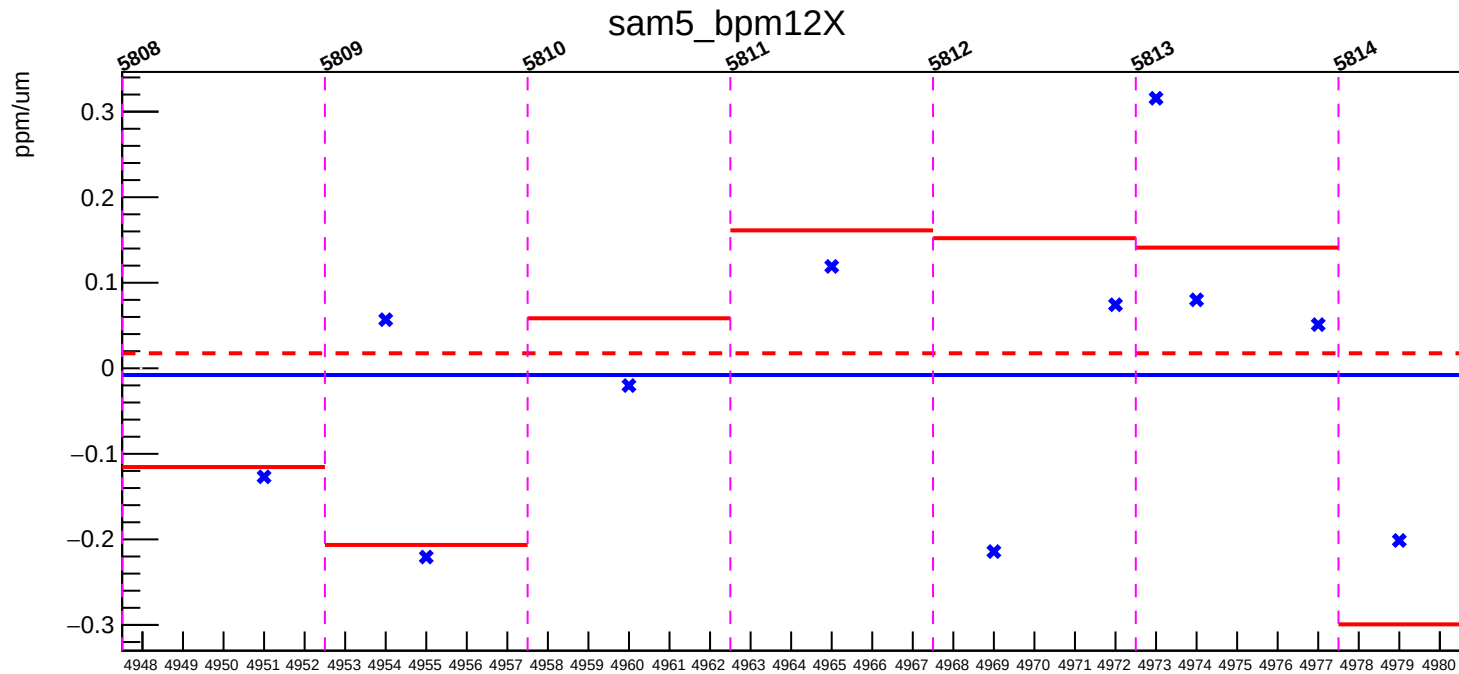


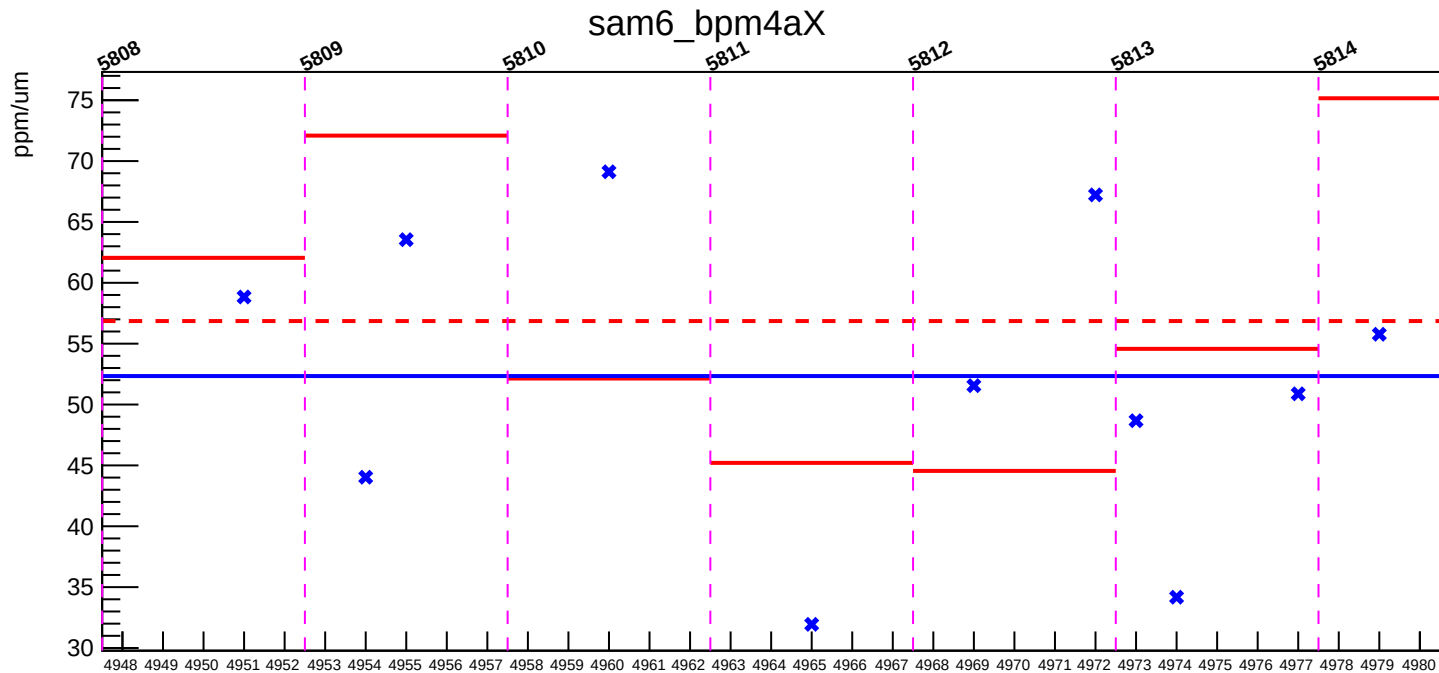


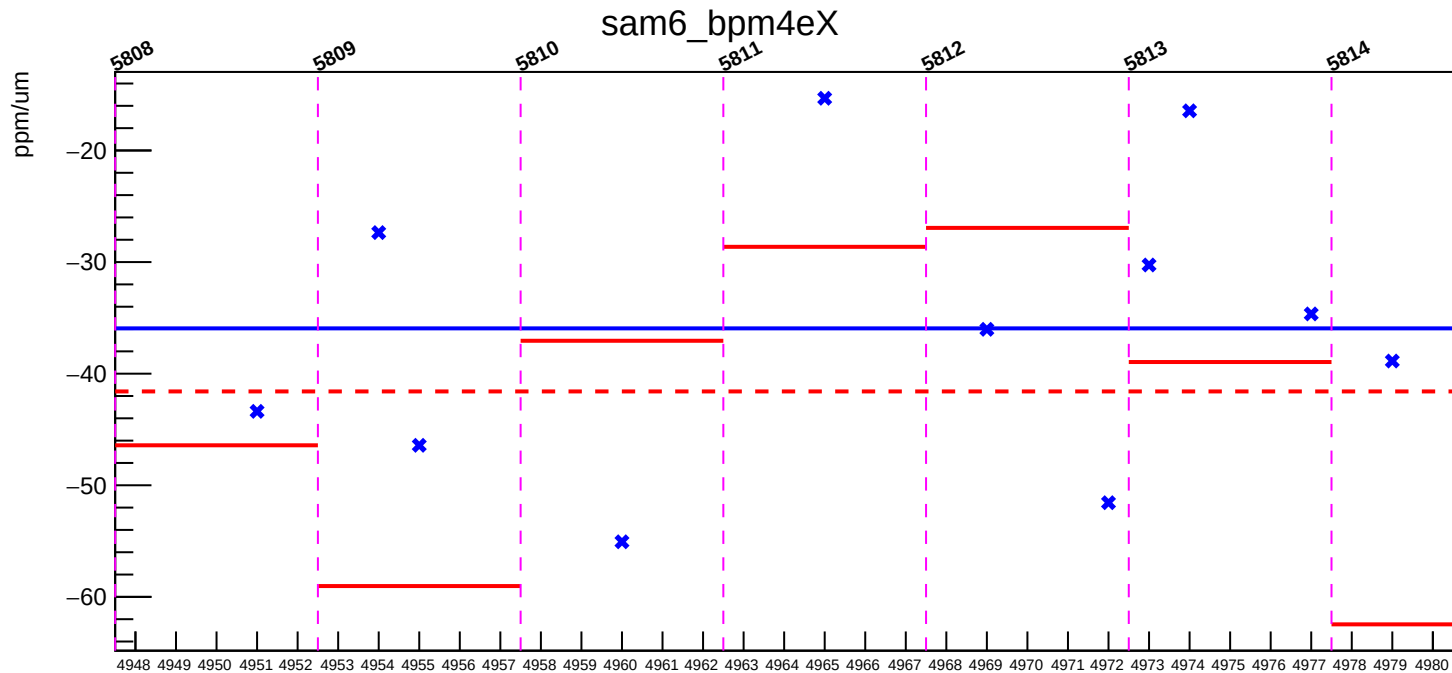


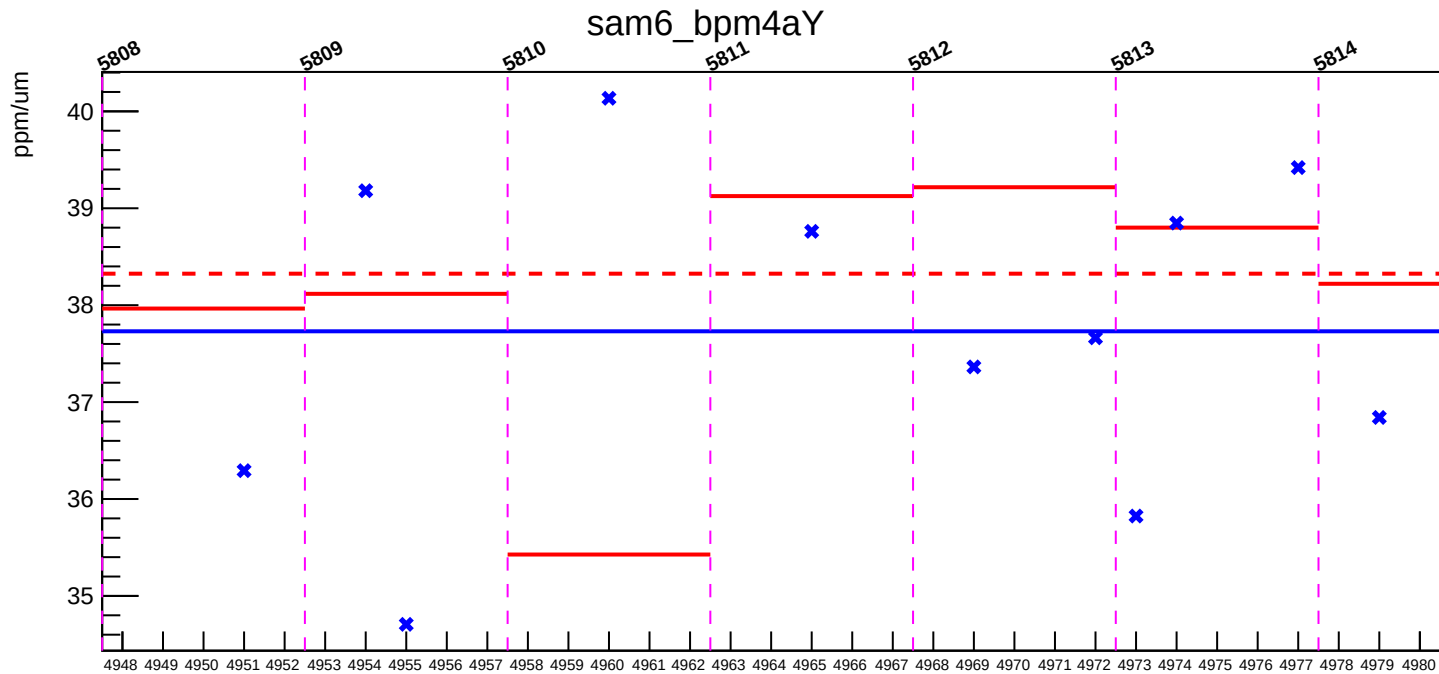


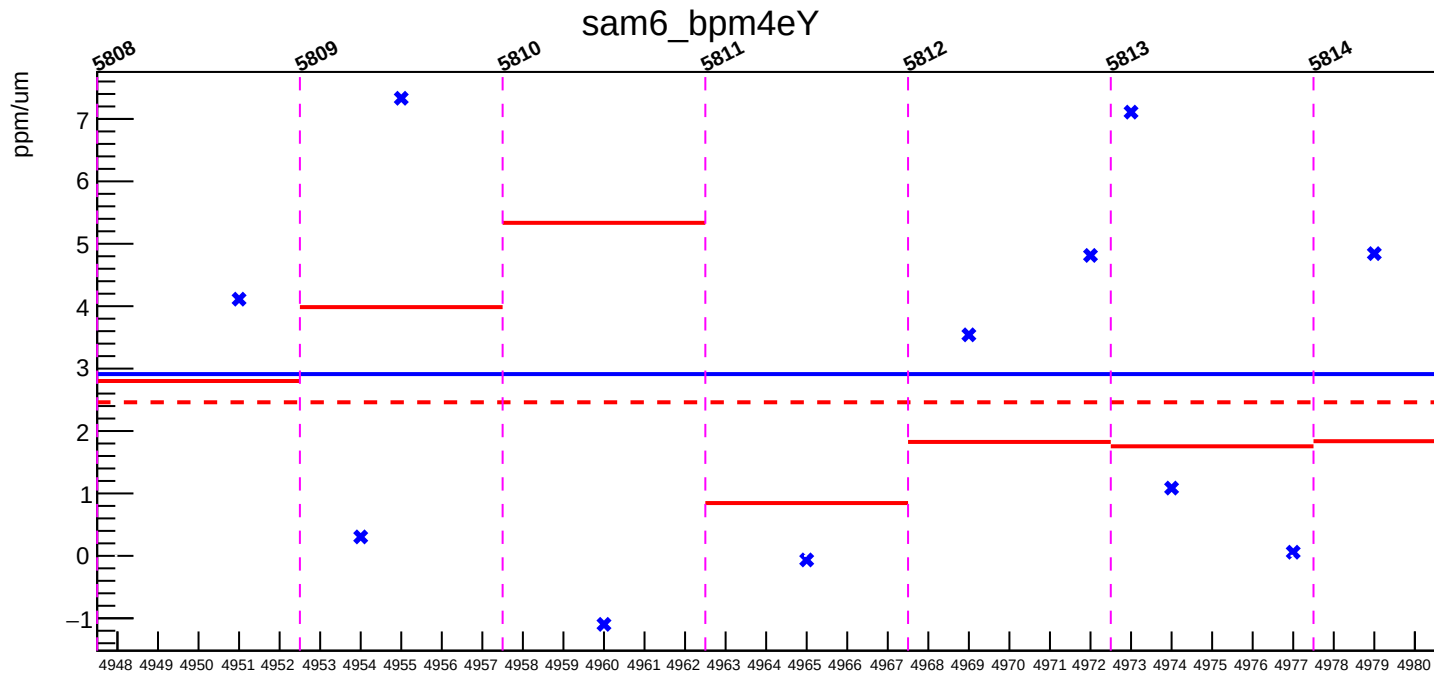


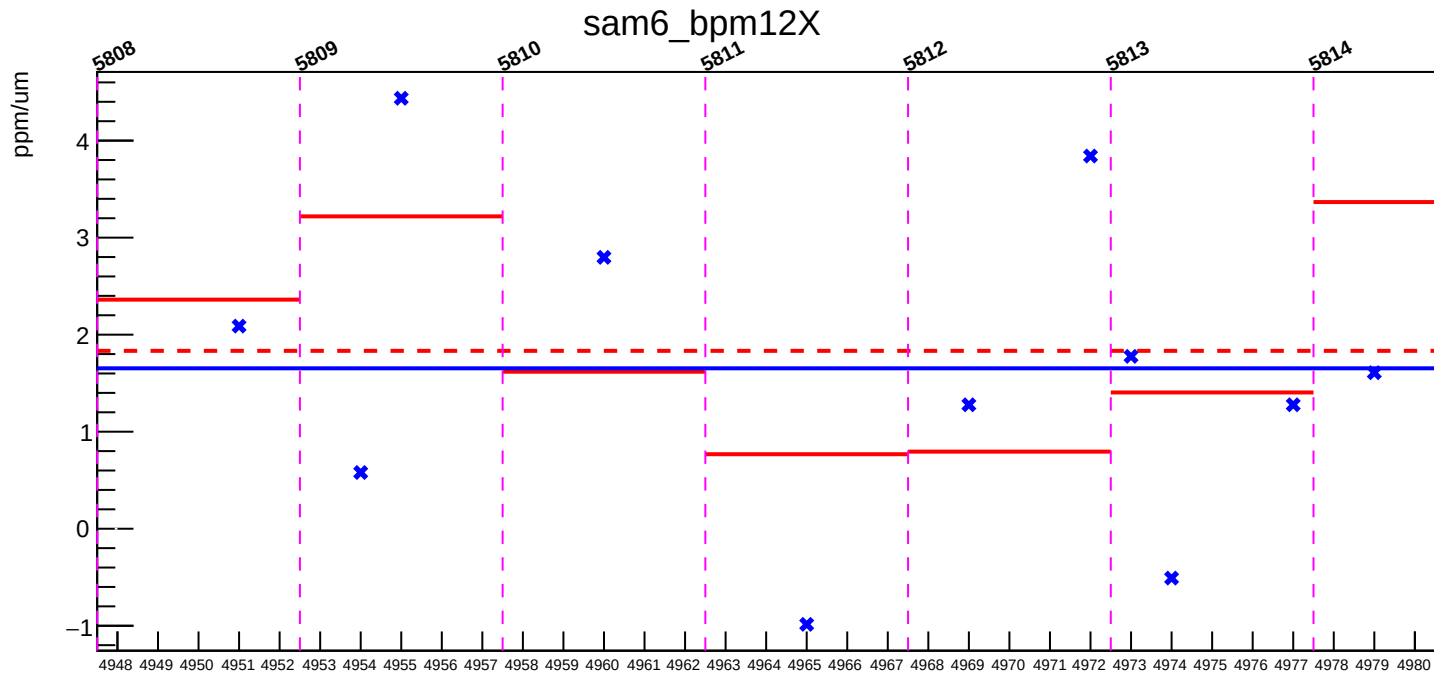


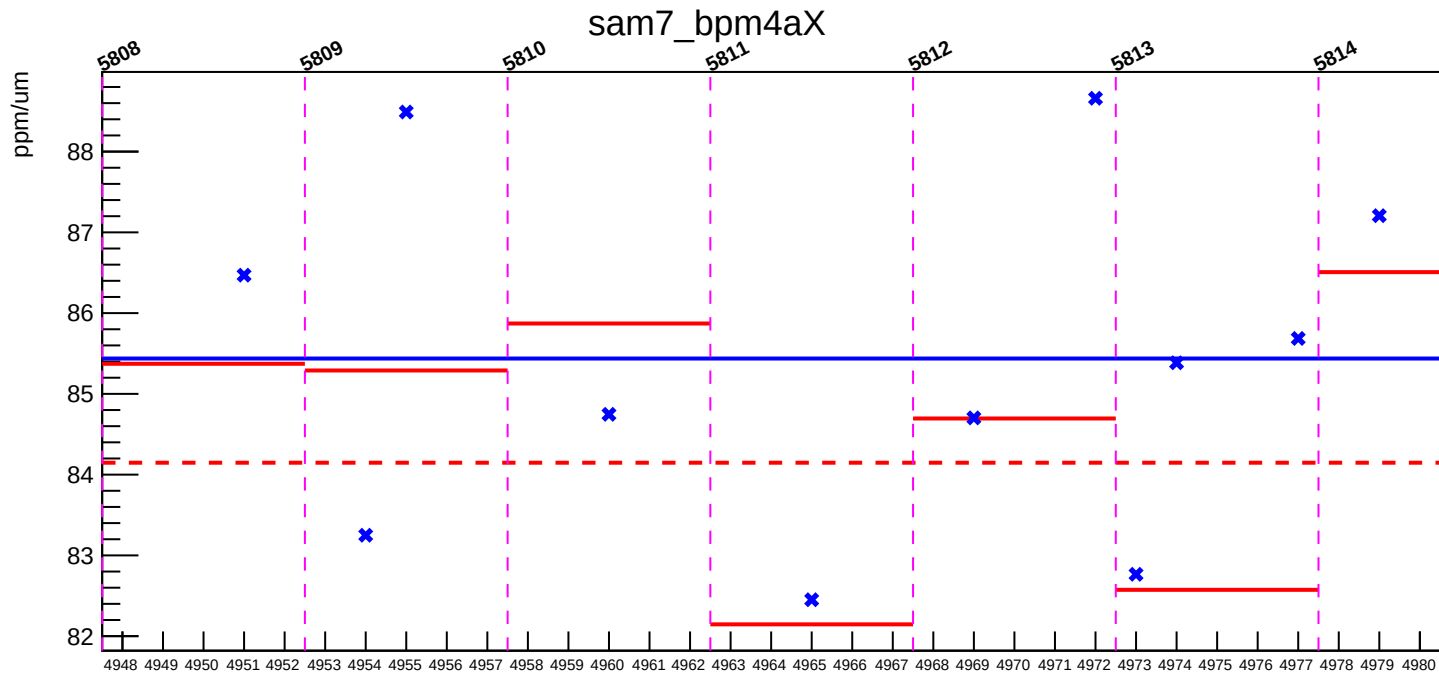




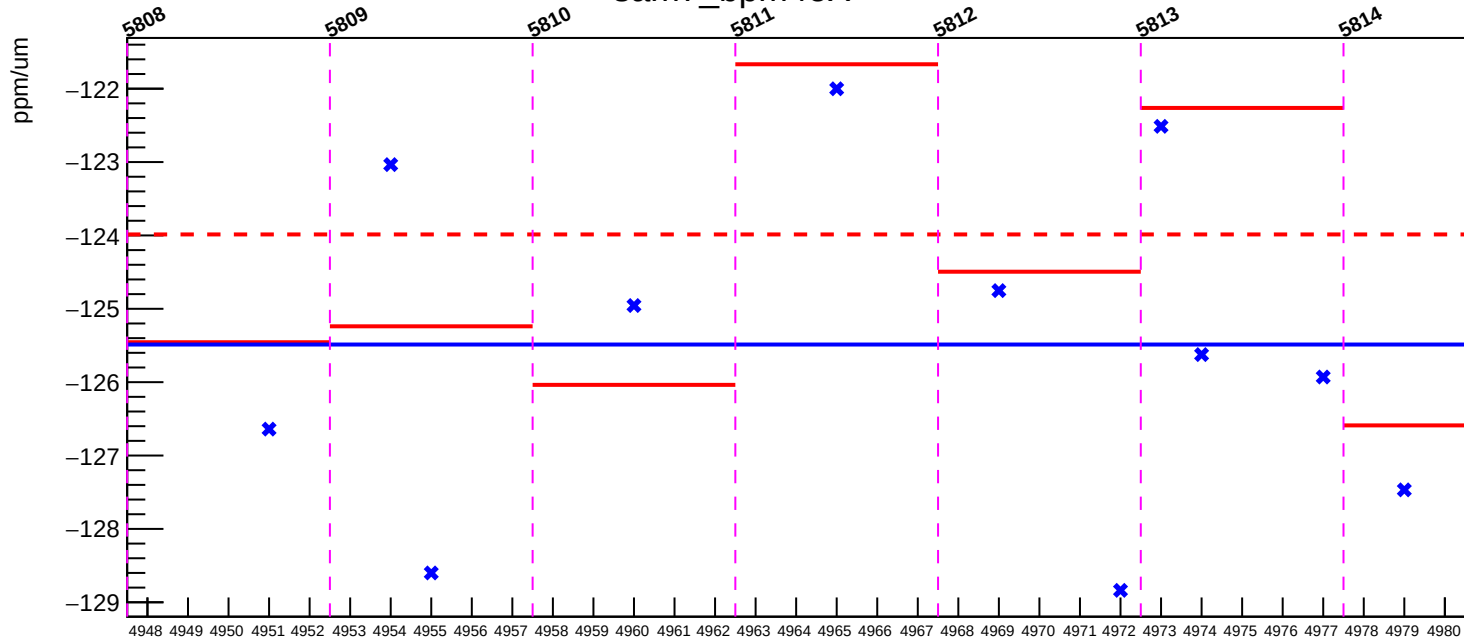


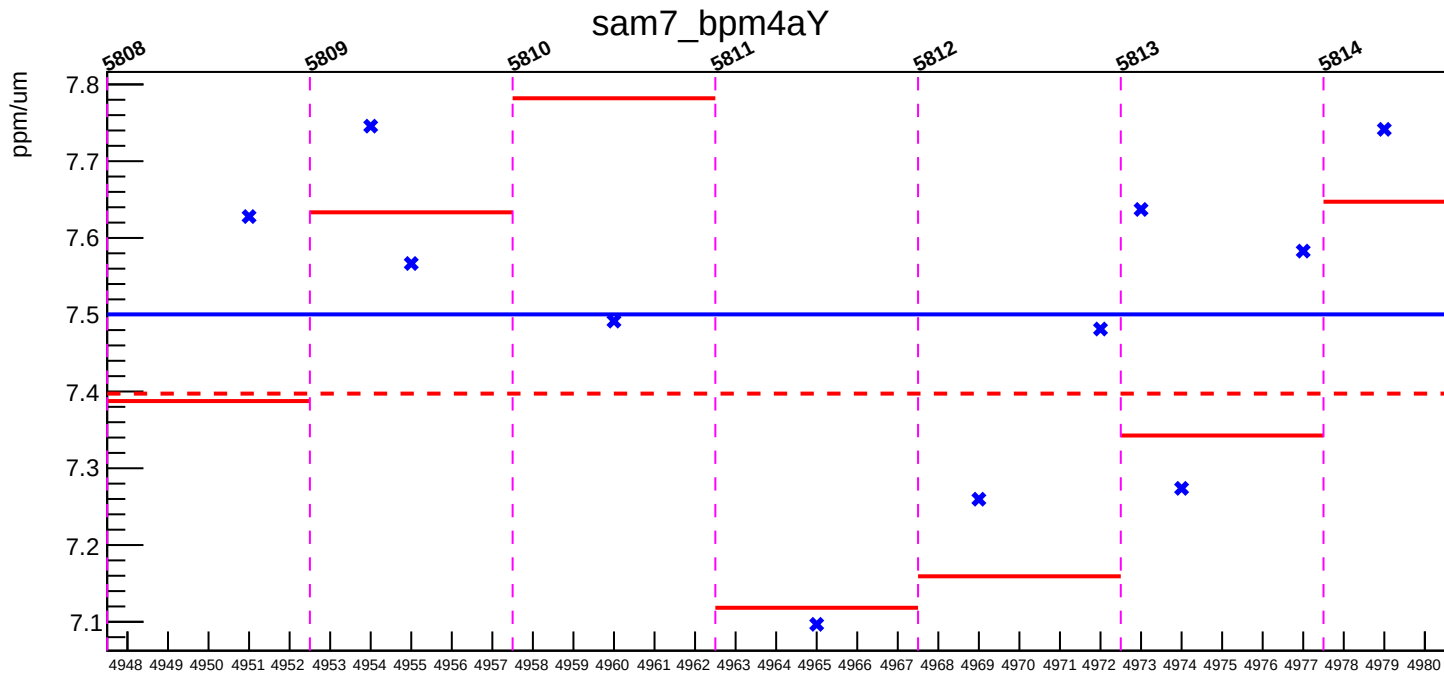






sam7_bpm4eX





sam7_bpm4eY

