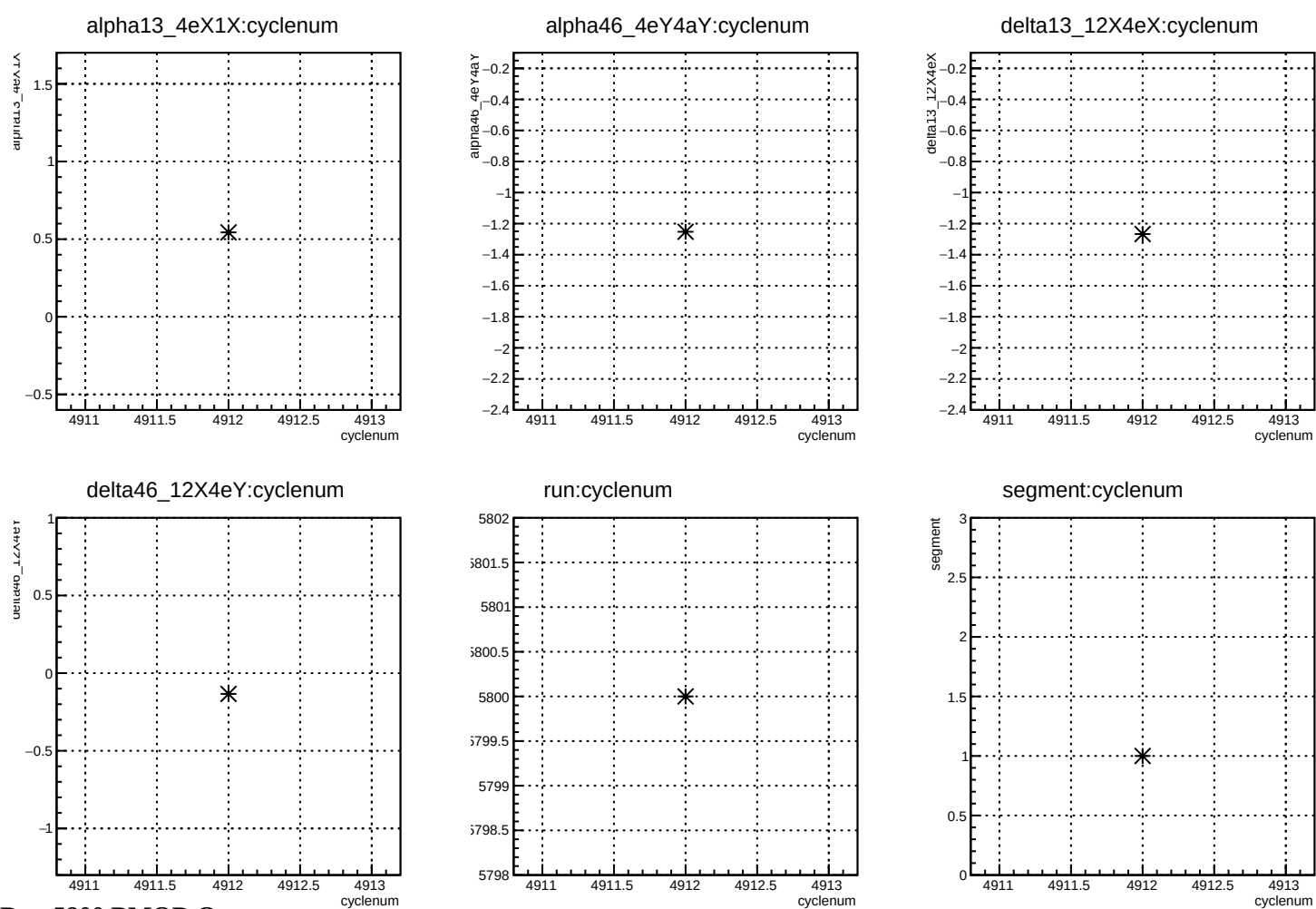
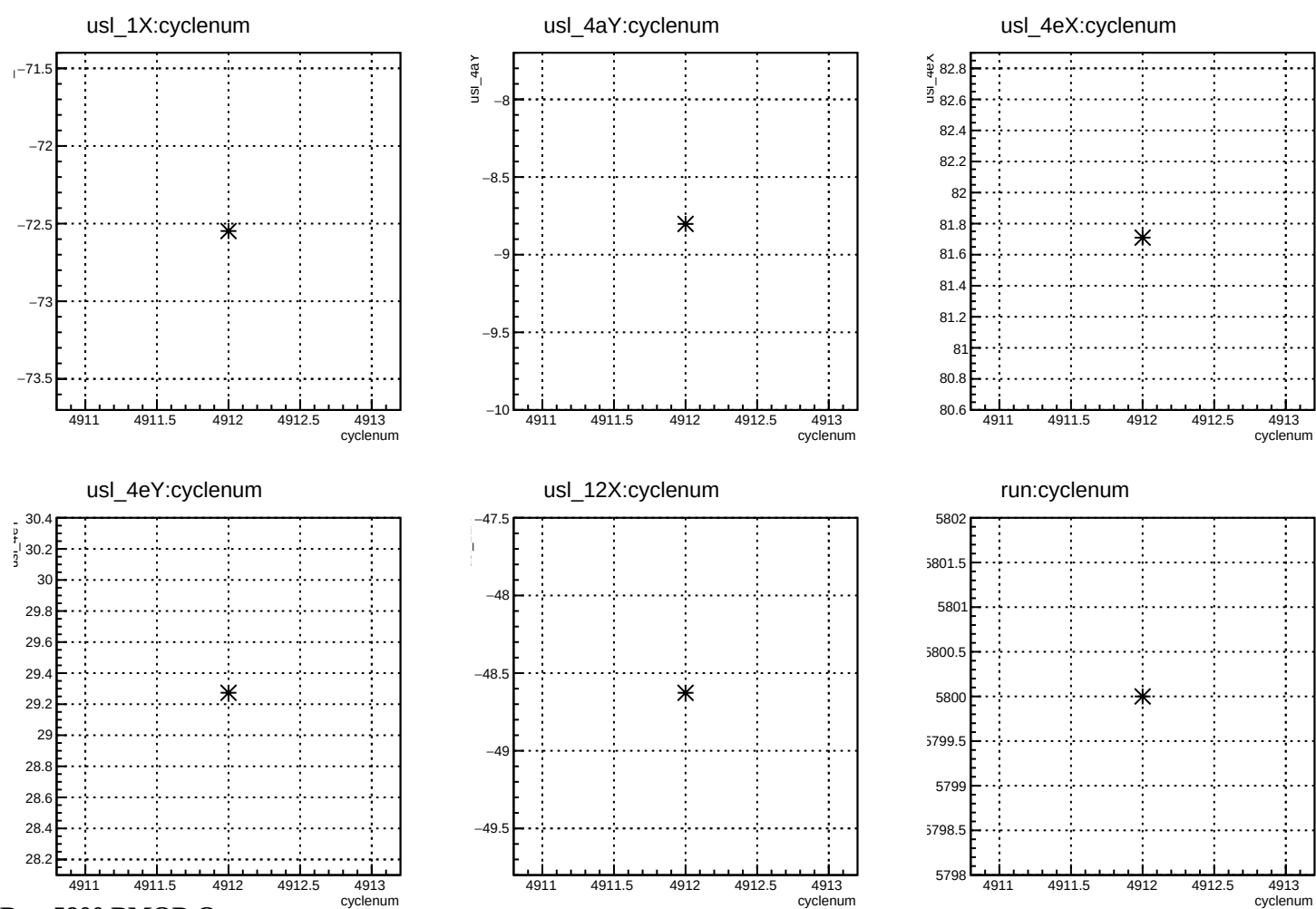


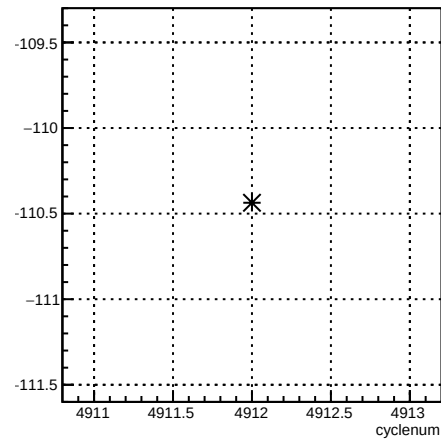


Run 5800 BMOD Outputs

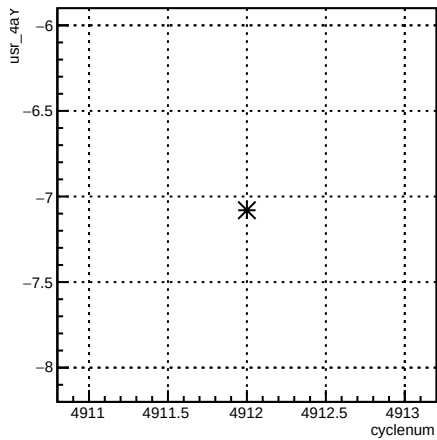


Run 5800 BMOD Outputs

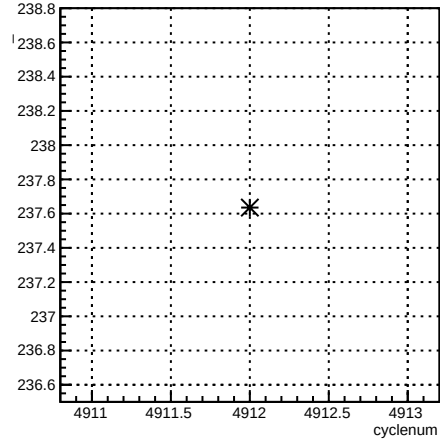
usr_1X:cyclenum



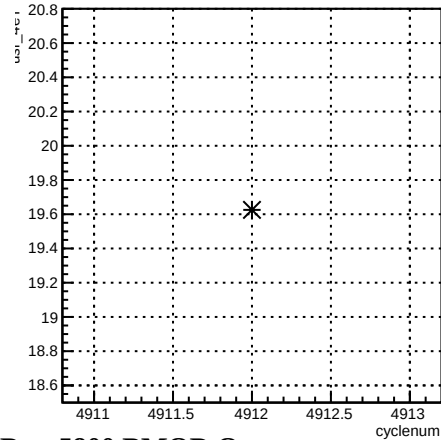
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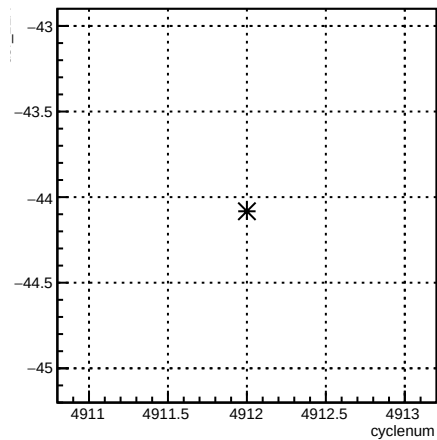
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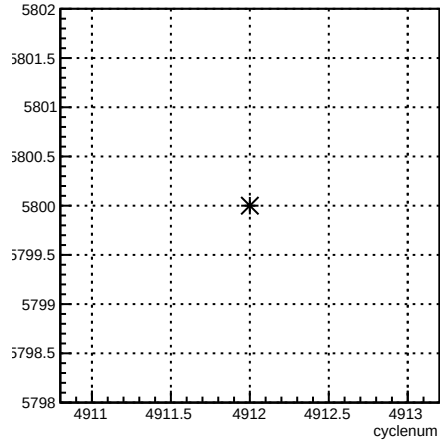
usr_4eY:cyclenum



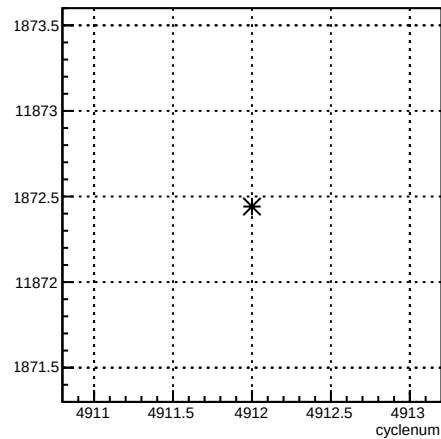
usr_12X:cyclenum



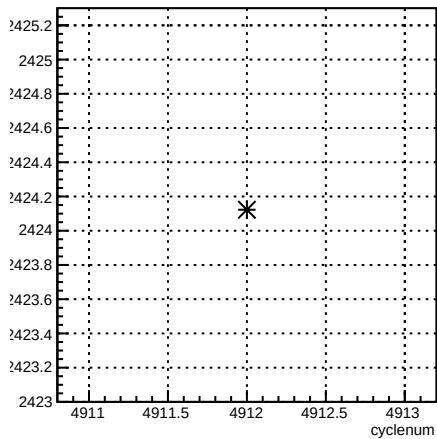
run:cyclenum



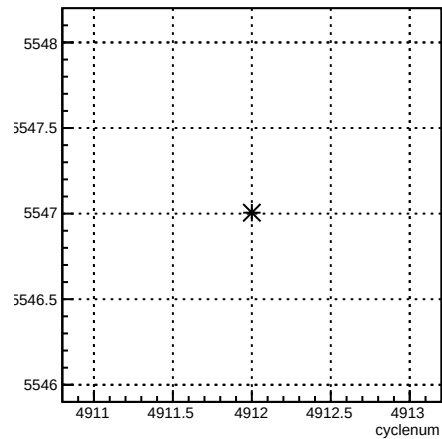
usl_coil1:cyclenum



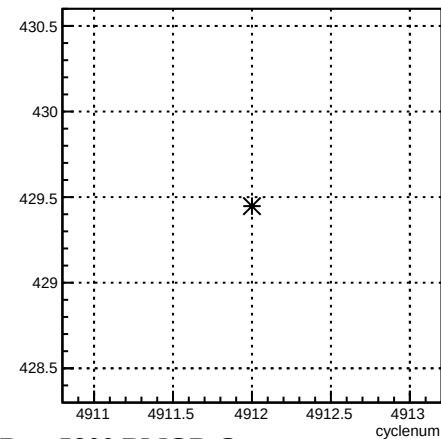
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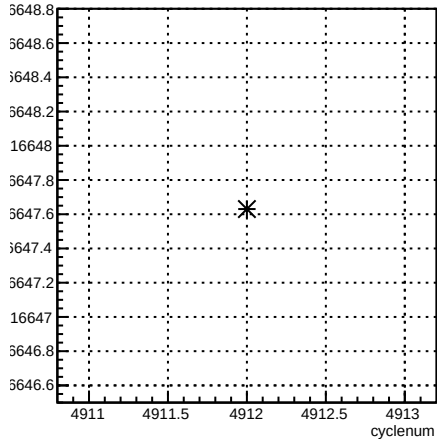
usl_coil4:cyclenum



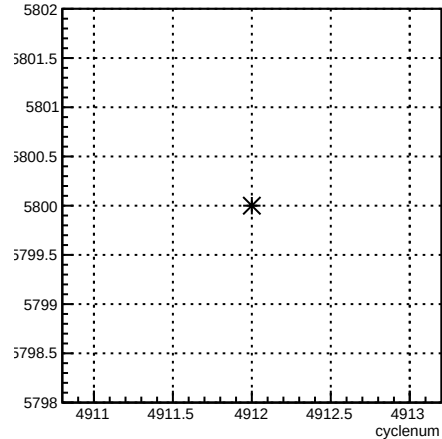
usl_coil6:cyclenum



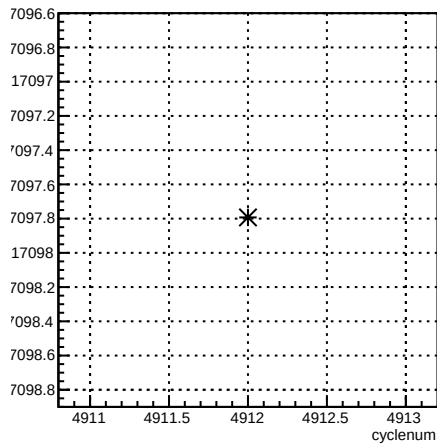
usl_coil7:cyclenum



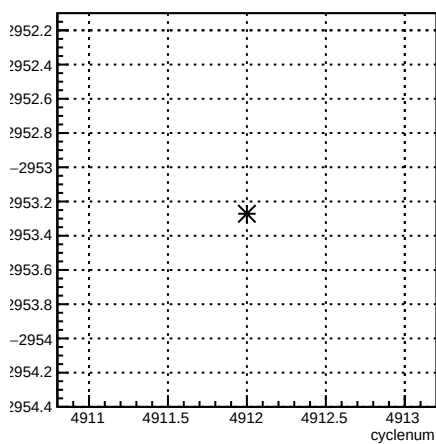
run:cyclenum



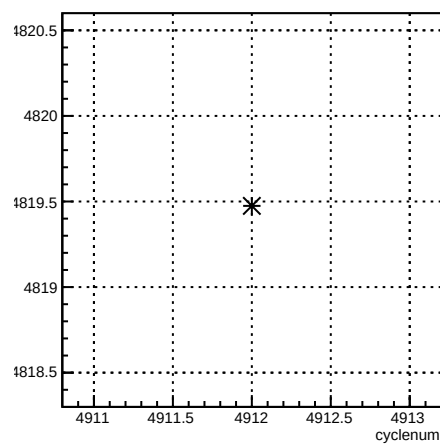
usr_coil1:cyclenum



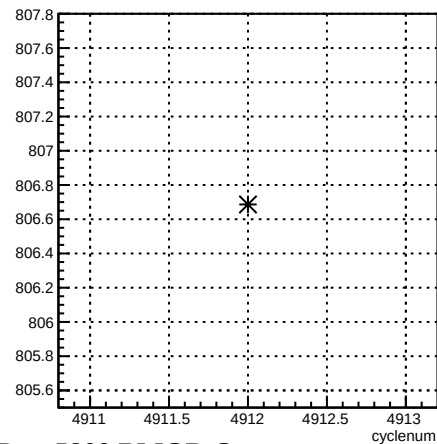
usr_coil3:cyclenum



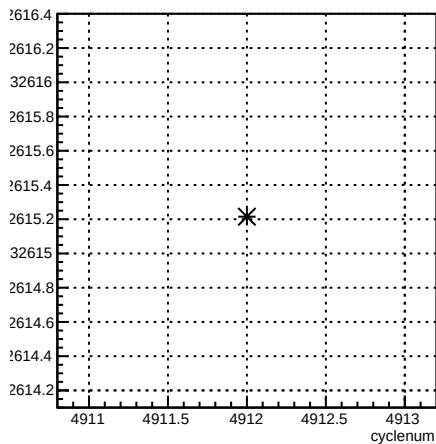
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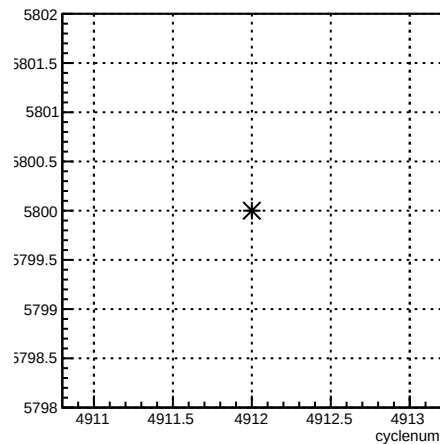
usr_coil6:cyclenum



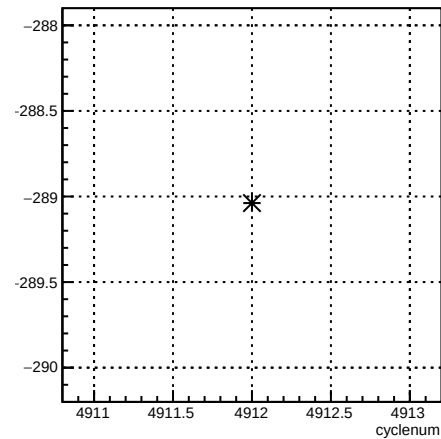
usr_coil7:cyclenum



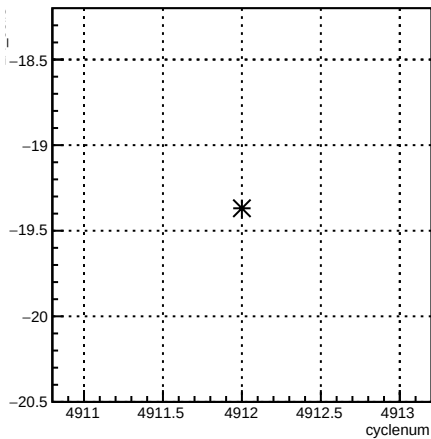
run:cyclenum



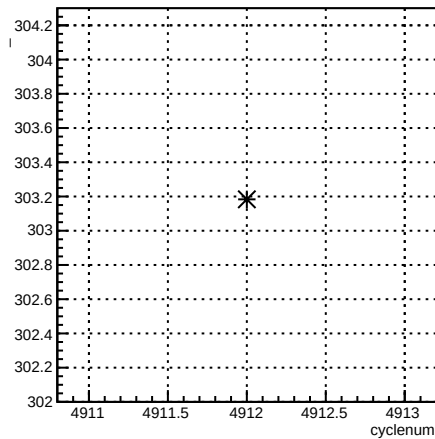
1X_coil1:cyclenum



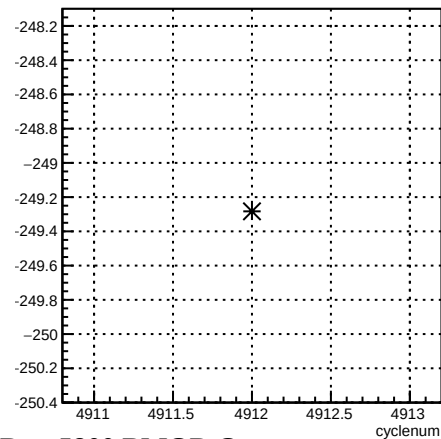
1X_coil3:cyclenum



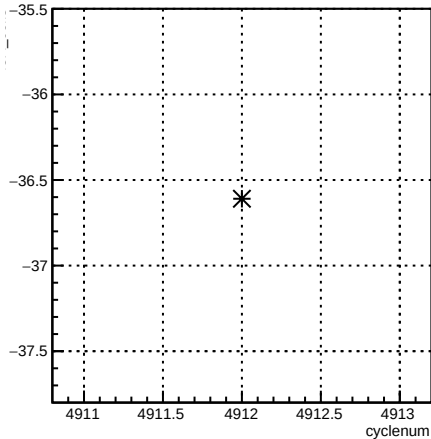
1X_coil7:cyclenum



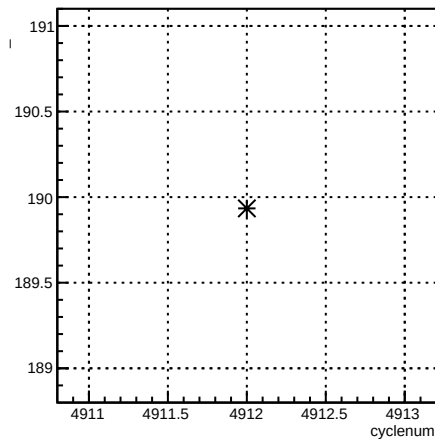
4eX_coil1:cyclenum



4eX_coil3:cyclenum

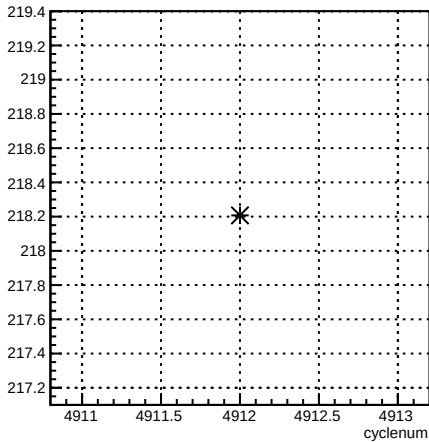


4eX_coil7:cyclenum

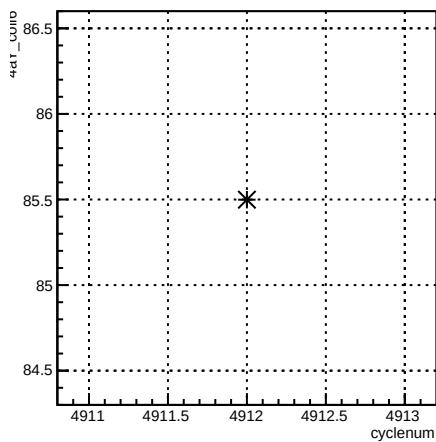


Run 5800 BMOD Outputs

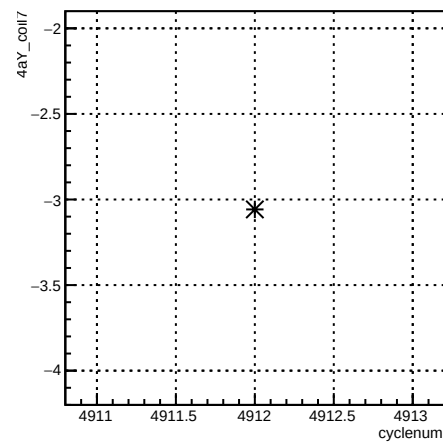
4aY_coil4:cyclenum



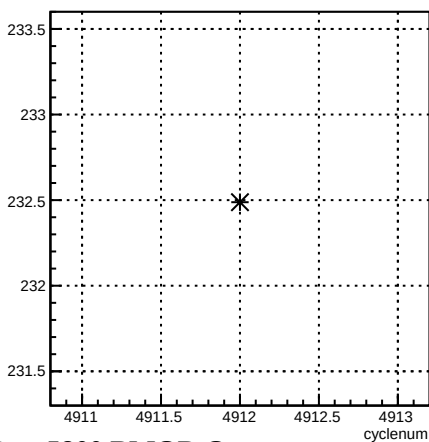
4aY_coil6:cyclenum



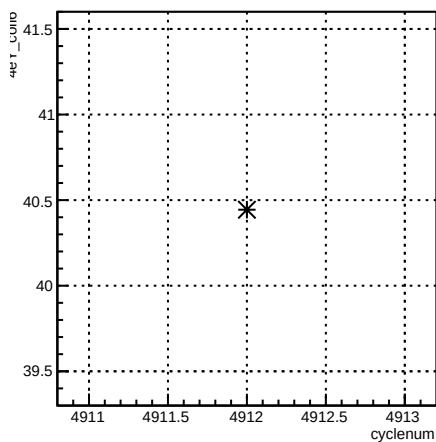
4aY_coil7:cyclenum



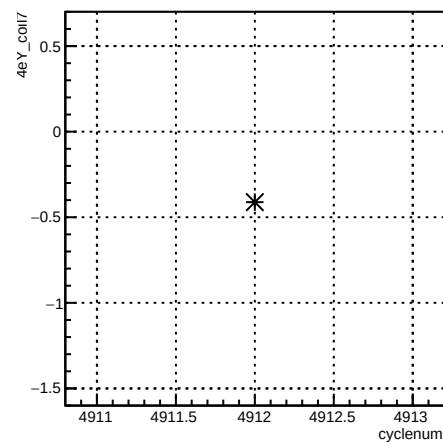
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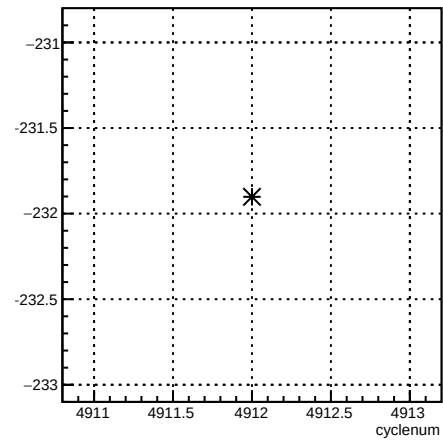
4eY_coil6:cyclenum



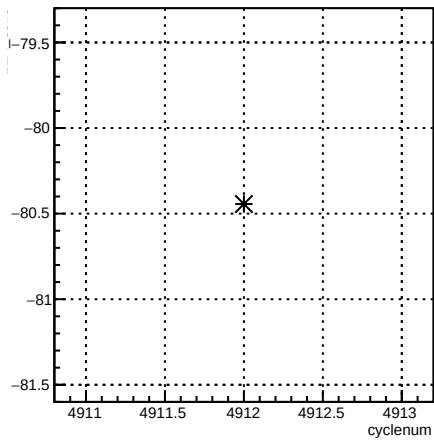
4eY_coil7:cyclenum



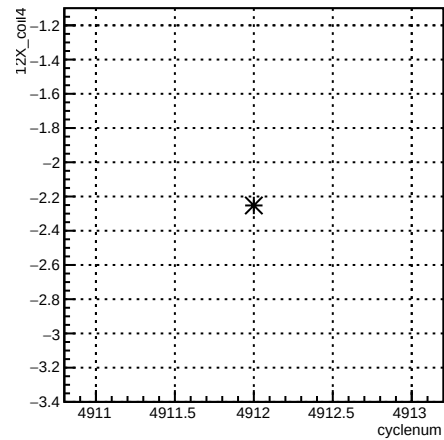
12X_coil1:cyclenum



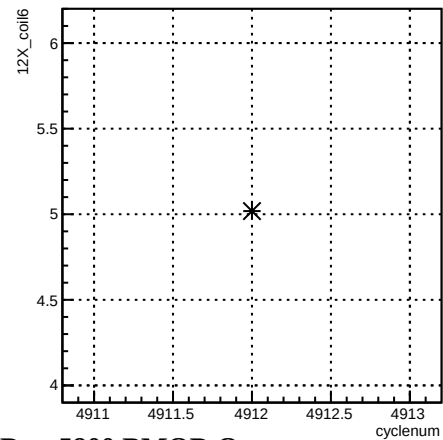
12X_coil3:cyclenum



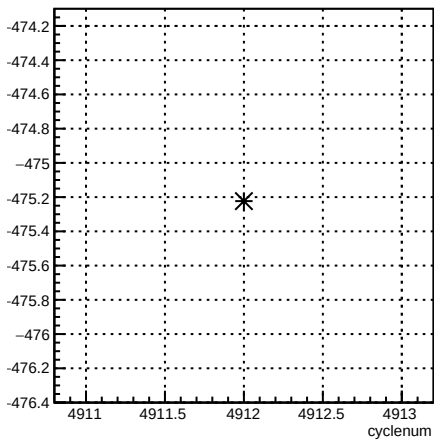
12X_coil4:cyclenum



12X_coil6:cyclenum



12X_coil7:cyclenum



run:cyclenum

