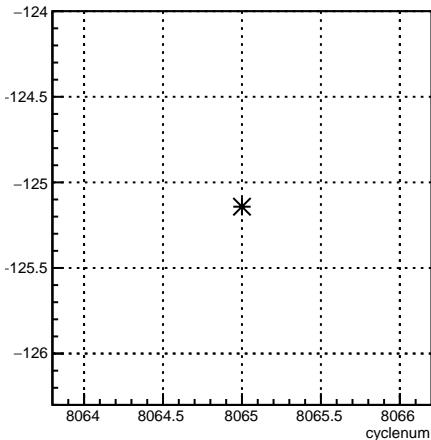
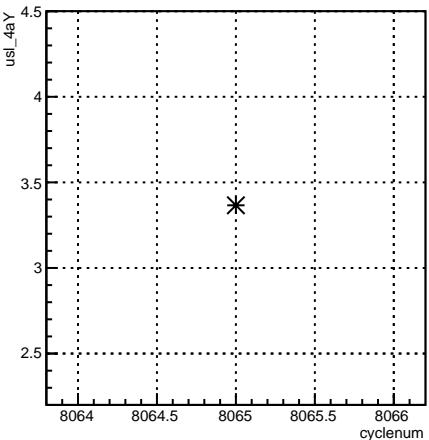


Run 7120 BMOD Outputs

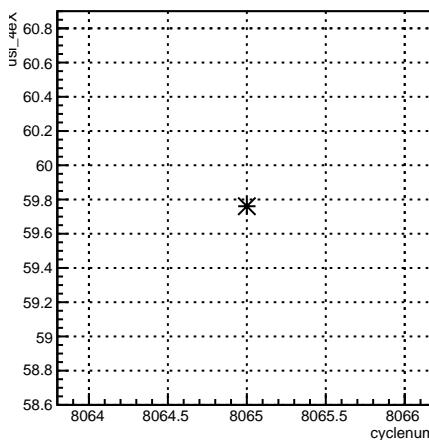
usl_1X:cyclenum



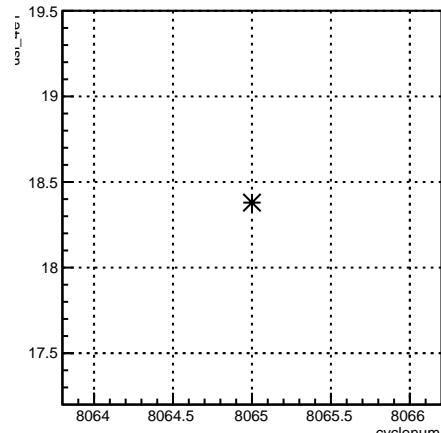
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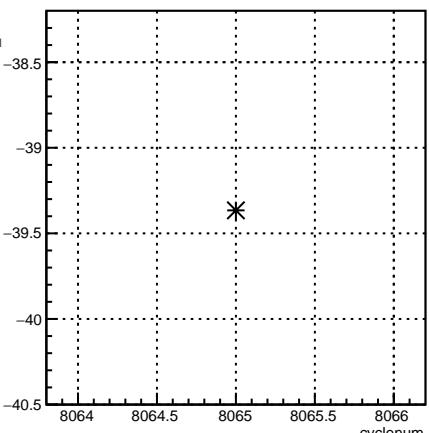
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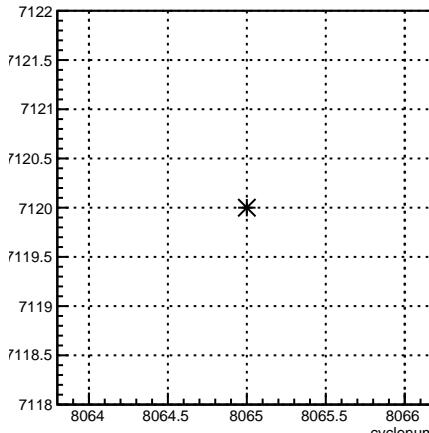
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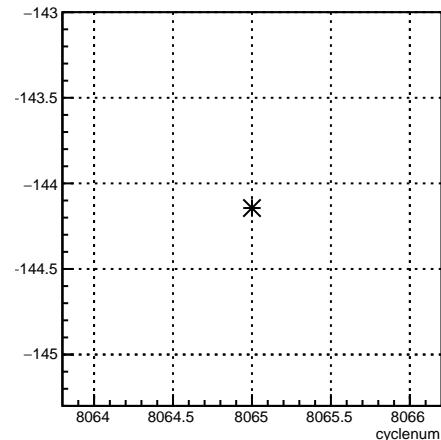
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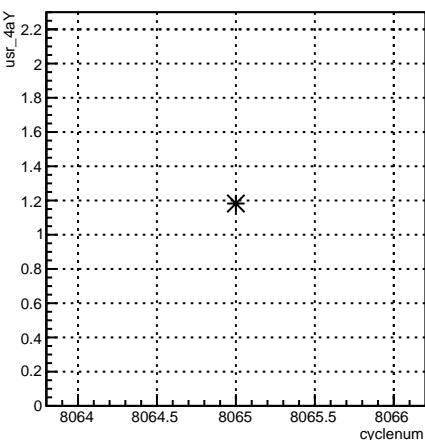
run:cyclenum

**Run 7120 BMOD Outputs**

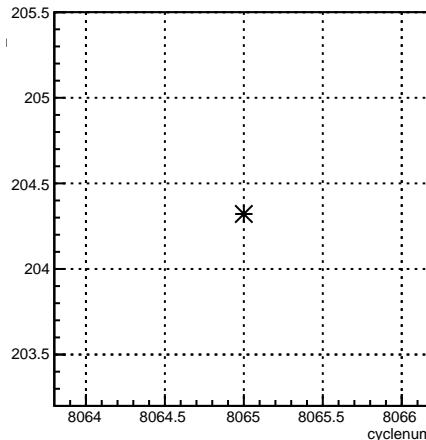
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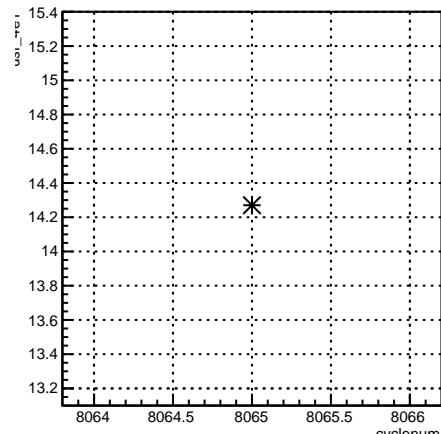
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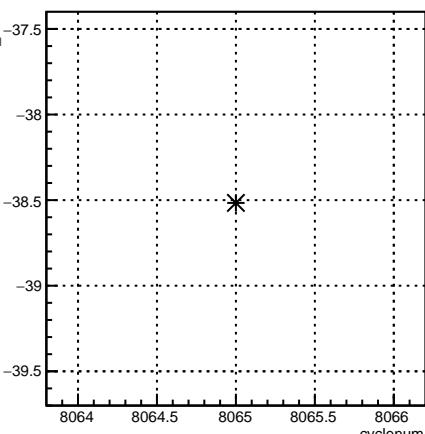
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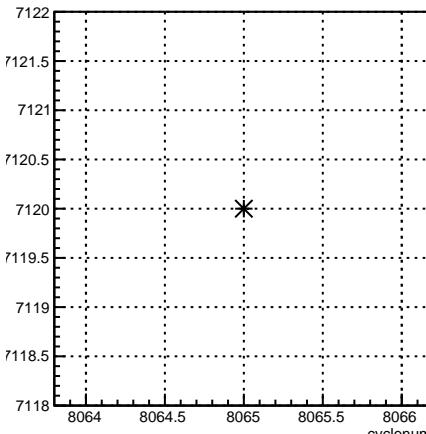
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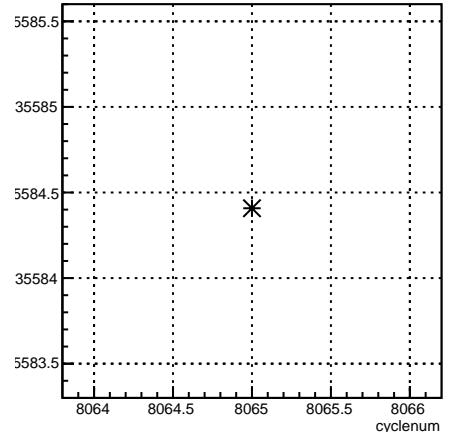
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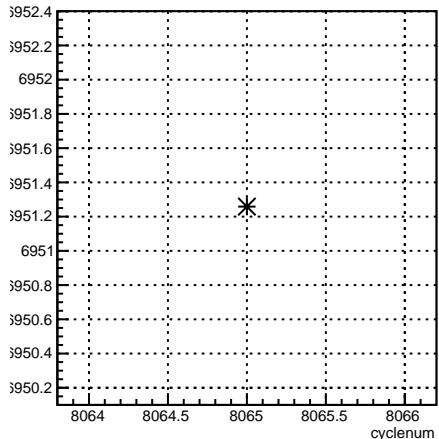
run:cyclenum

**Run 7120 BMOD Outputs**

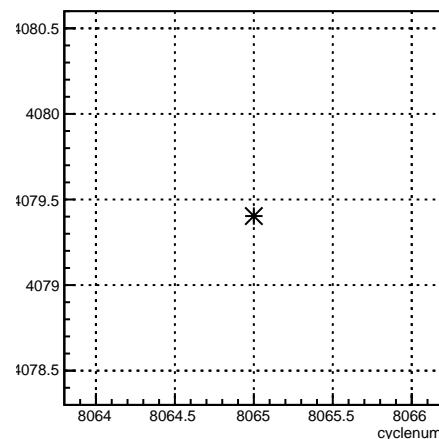
usl_coil1:cyclenum



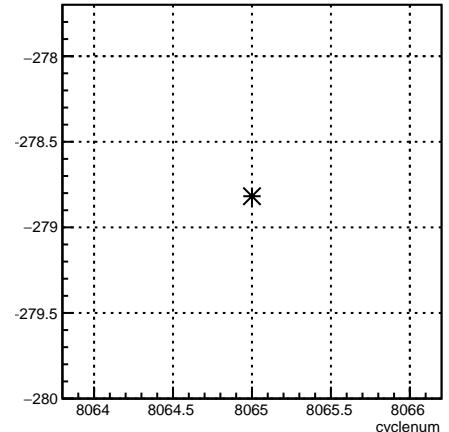
usl_coil3:cyclenum



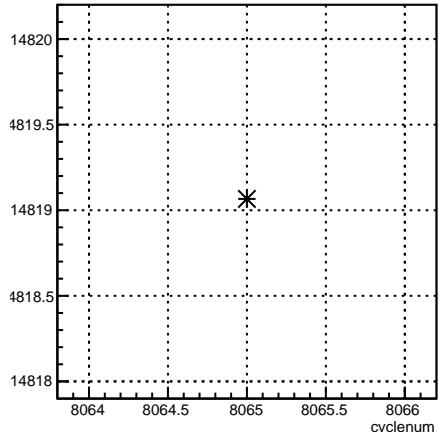
usl_coil4:cyclenum



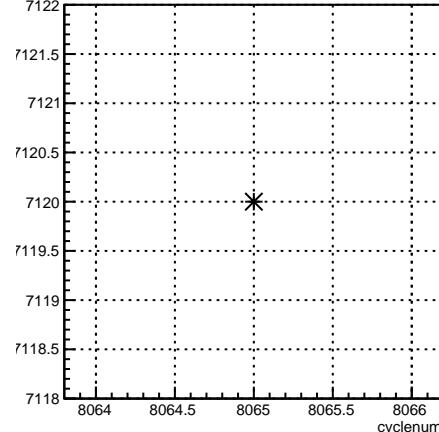
usl_coil6:cyclenum



usl_coil7:cyclenum

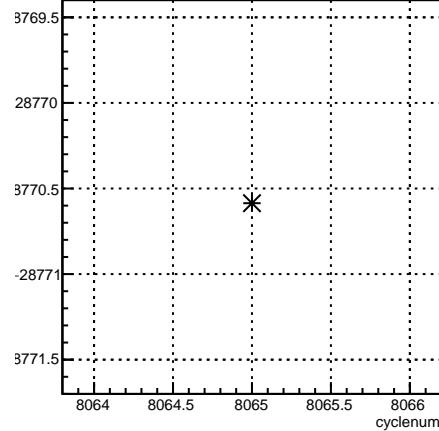


run:cyclenum

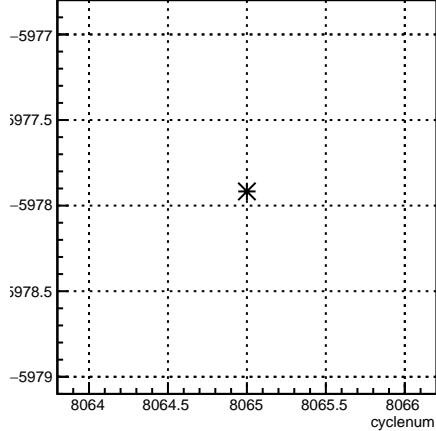


Run 7120 BMOD Outputs

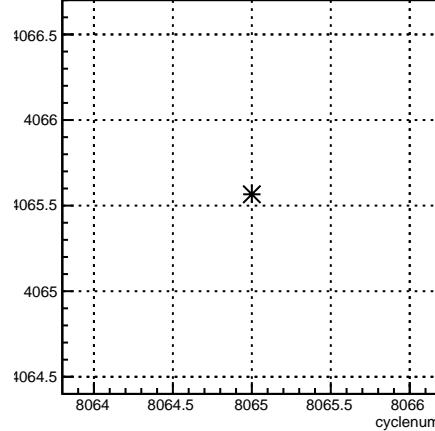
usr_coil1:cyclenum



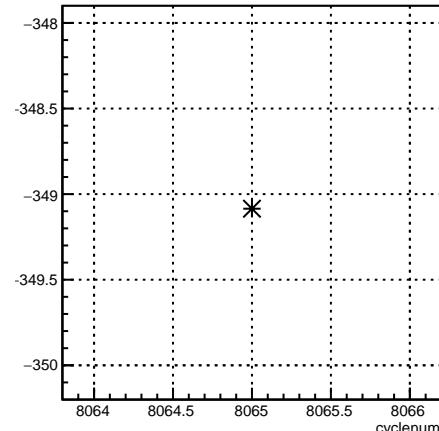
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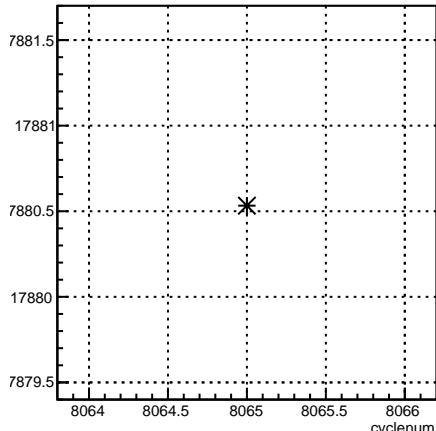
usr_coil4:cyclenum



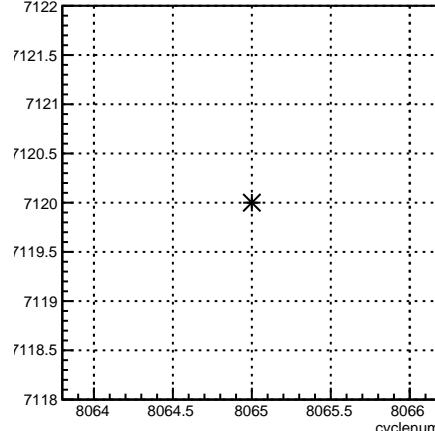
usr_coil6:cyclenum



usr_coil7:cyclenum

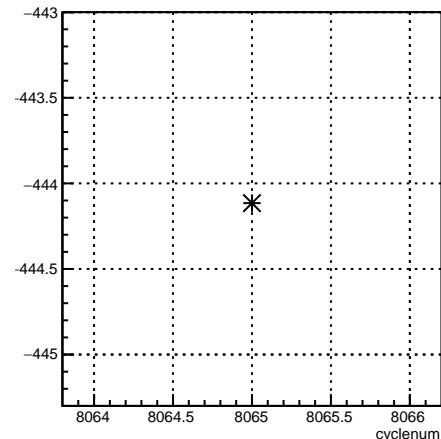


run:cyclenum

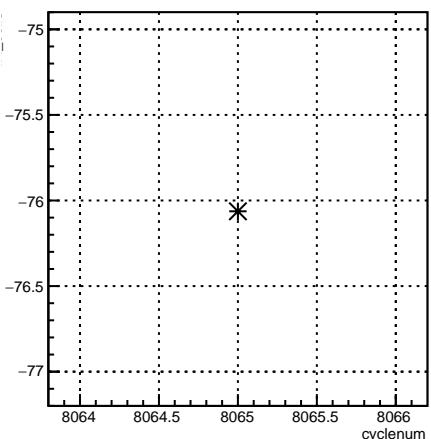


Run 7120 BMOD Outputs

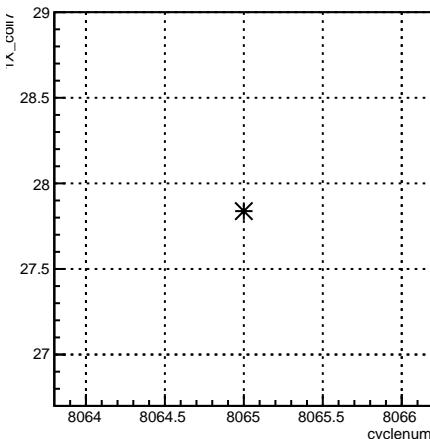
1X_coil1:cyclenum



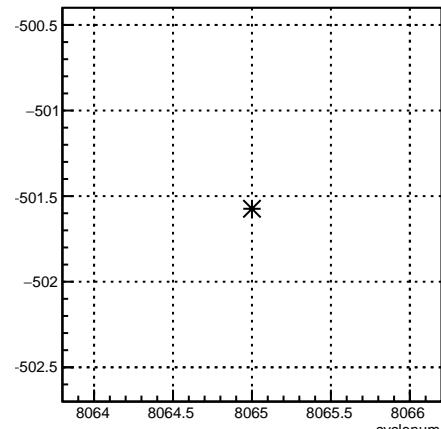
1X_coil3:cyclenum



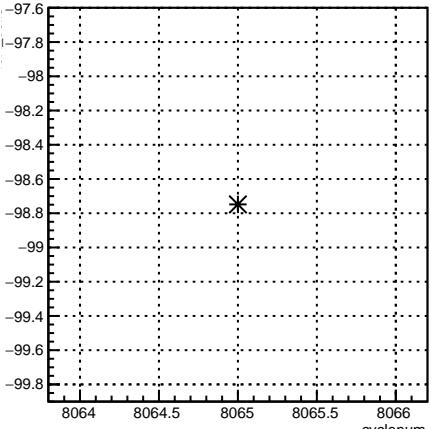
1X_coil7:cyclenum



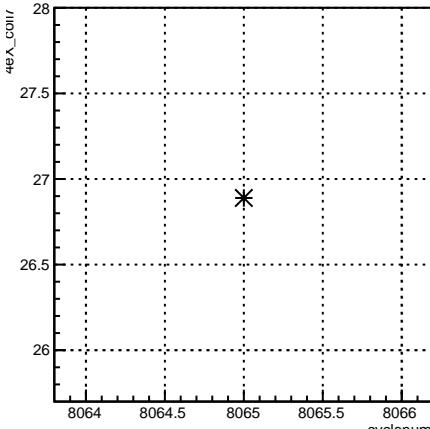
4eX_coil1:cyclenum



4eX_coil3:cyclenum

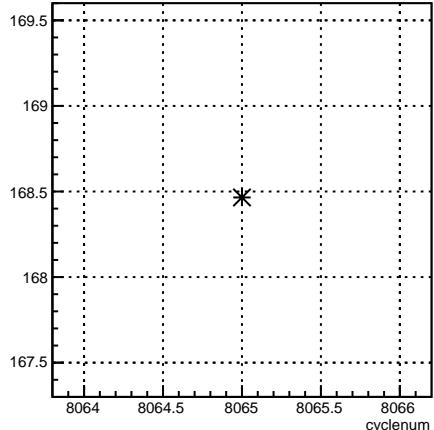


4eX_coil7:cyclenum

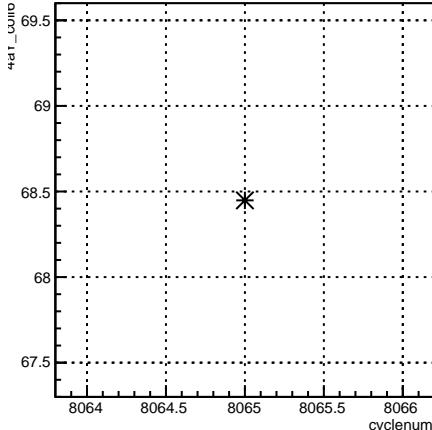


Run 7120 BMOD Outputs

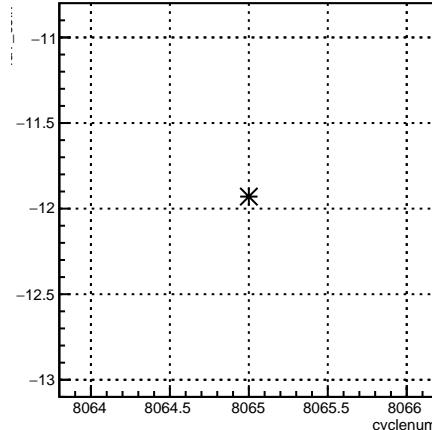
4aY_coil4:cyclenum



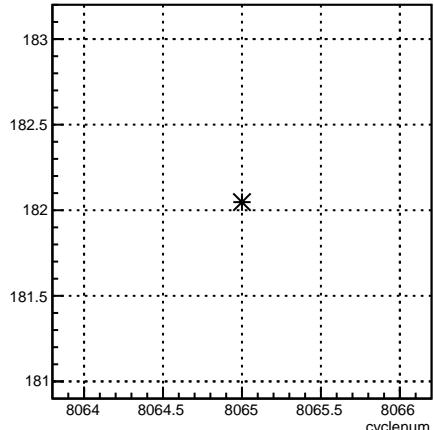
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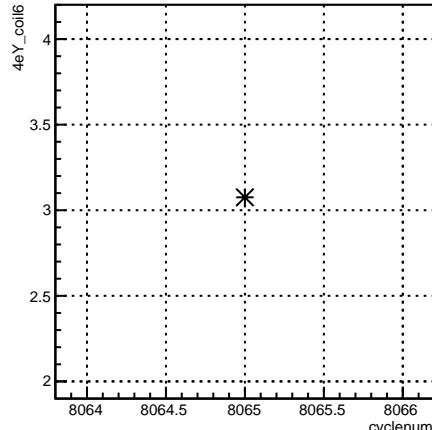
4aY_coil7:cyclenum



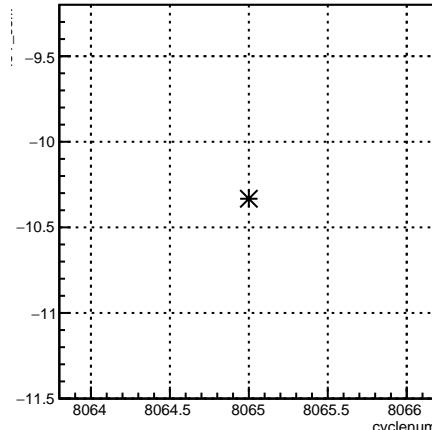
4eY_coil4:cyclenum



4eY_coil6:cyclenum

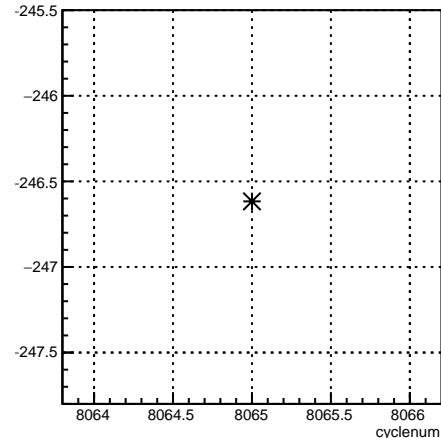


4eY_coil7:cyclenum

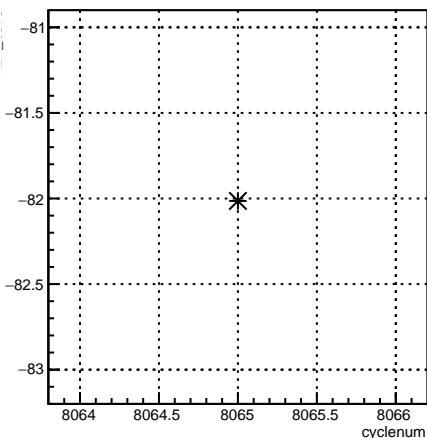


Run 7120 BMOD Outputs

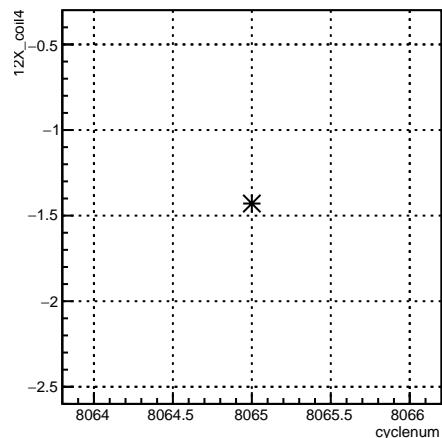
12X_coil1:cyclenum



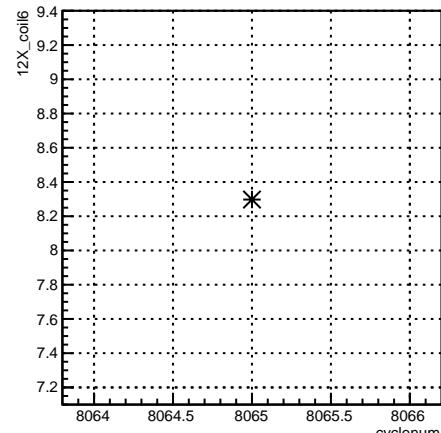
12X_coil3:cyclenum



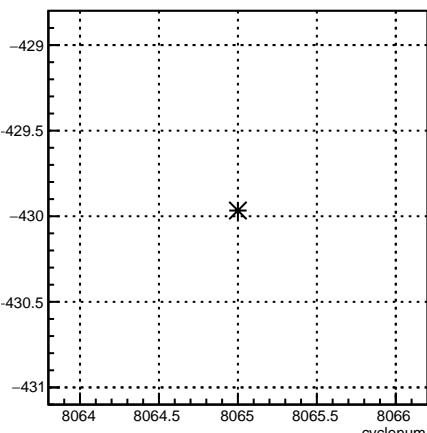
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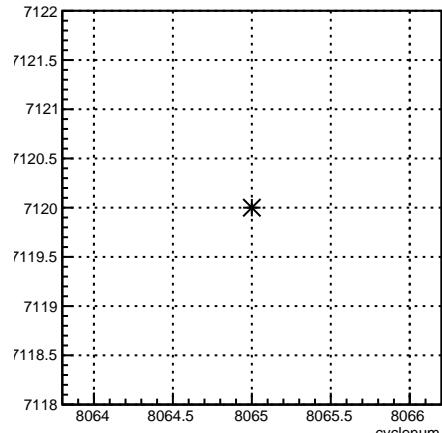
12X_coil6:cyclenum



12X_coil7:cyclenum



run:cyclenum

**Run 7120 BMOD Outputs**