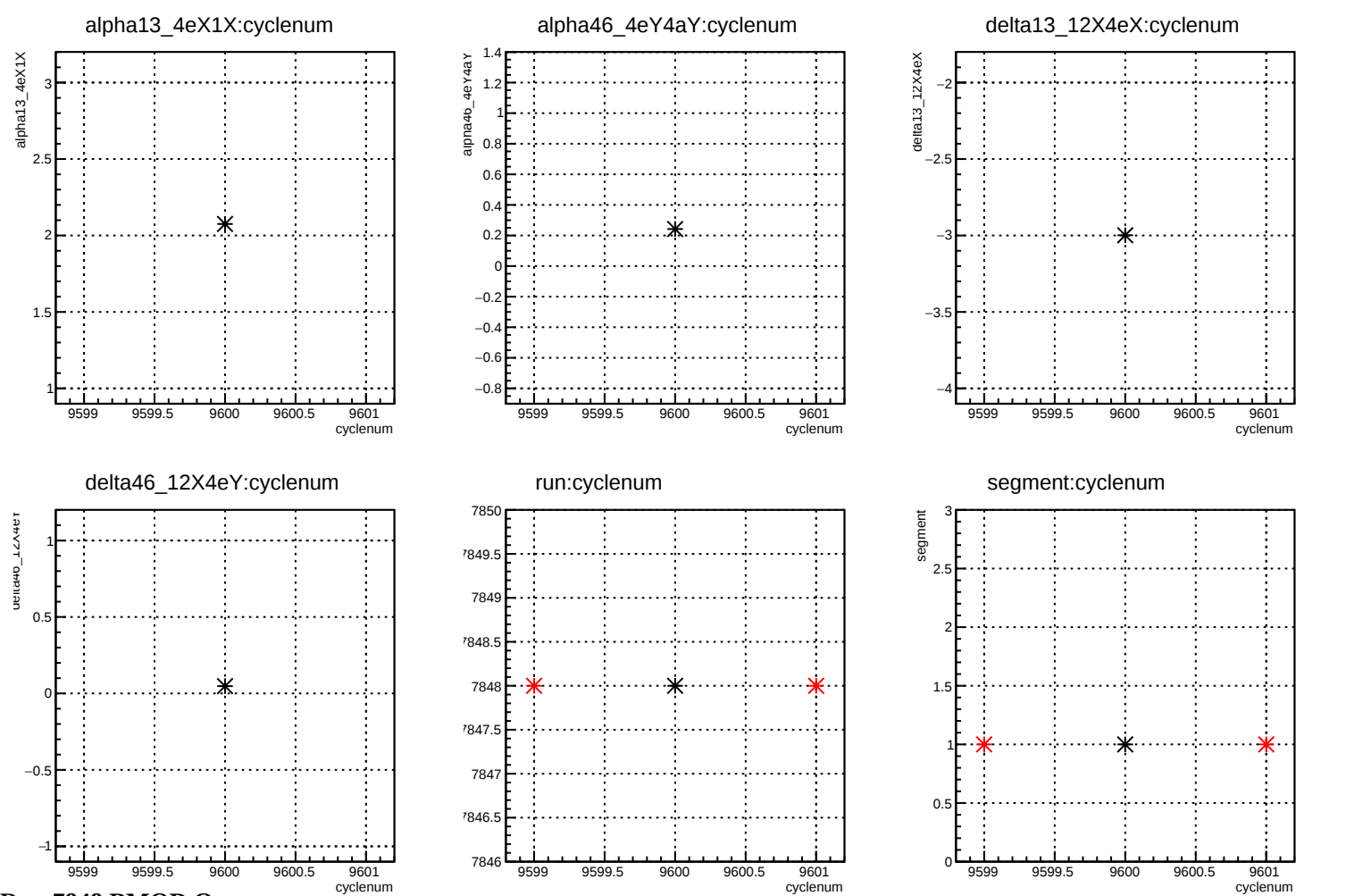
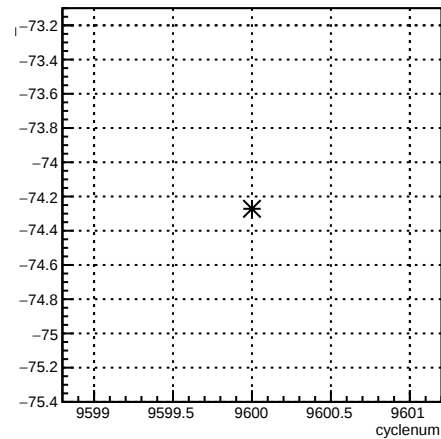


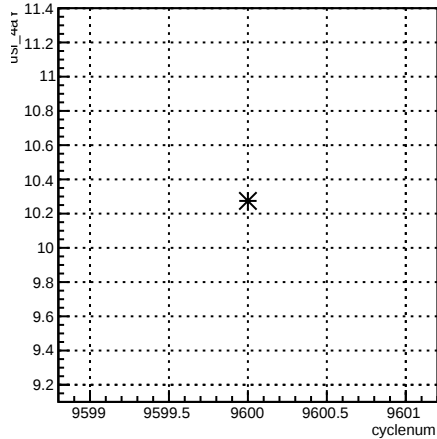


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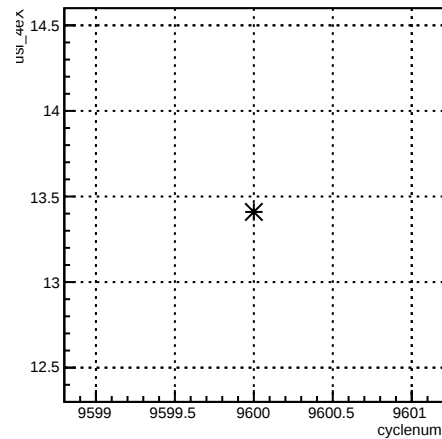
usl_1X:cyclenum



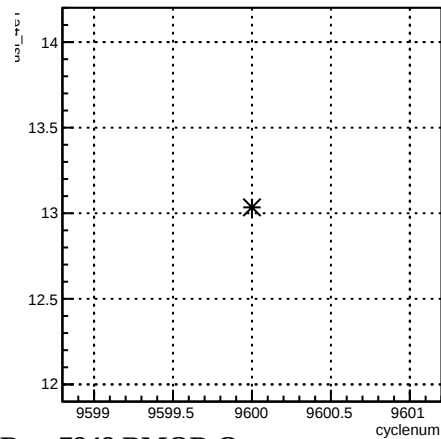
usl_4aY:cyclenum



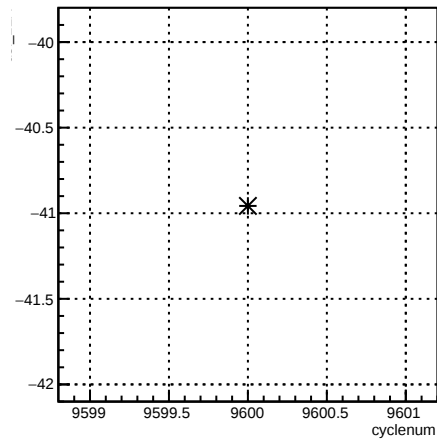
usl_4eX:cyclenum



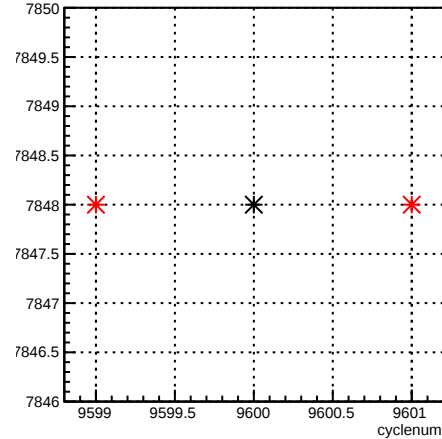
usl_4eY:cyclenum



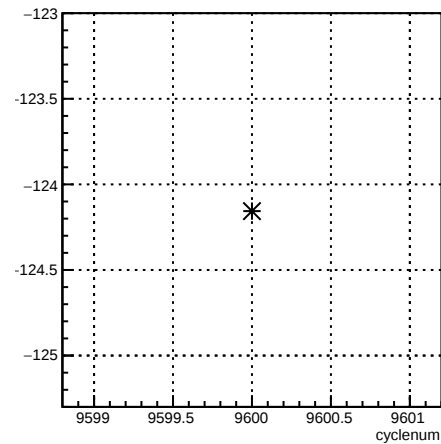
usl_12X:cyclenum



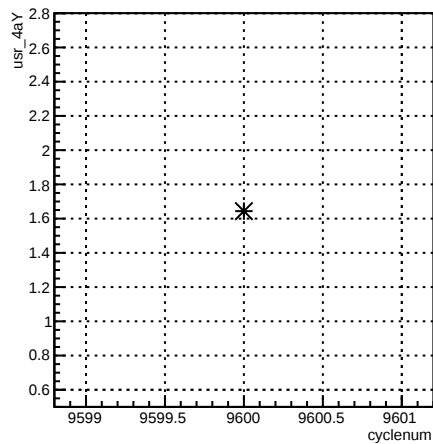
run:cyclenum



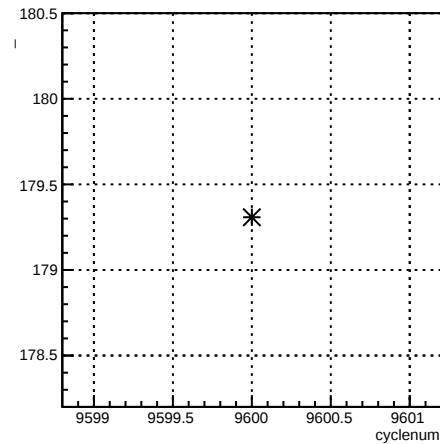
usr_1X:cyclenum



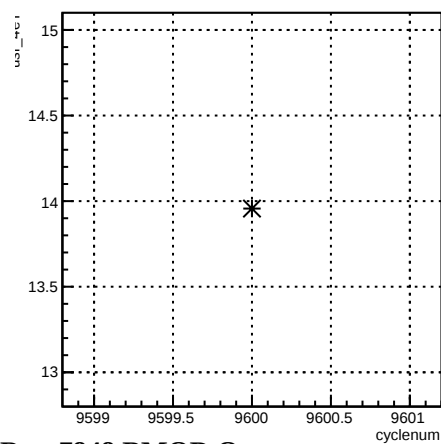
usr_4aY:cyclenum



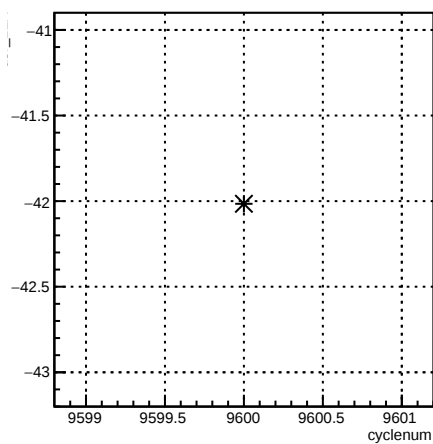
usr_4eX:cyclenum



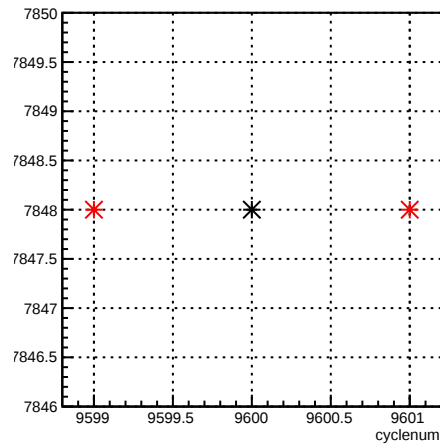
usr_4eY:cyclenum



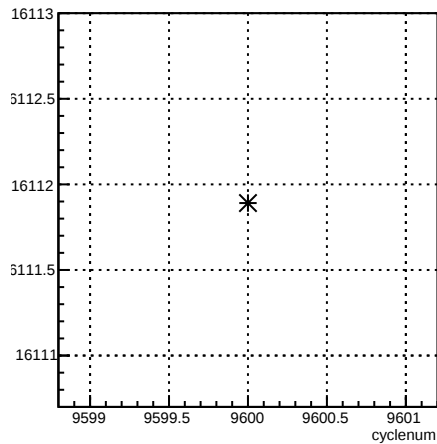
usr_12X:cyclenum



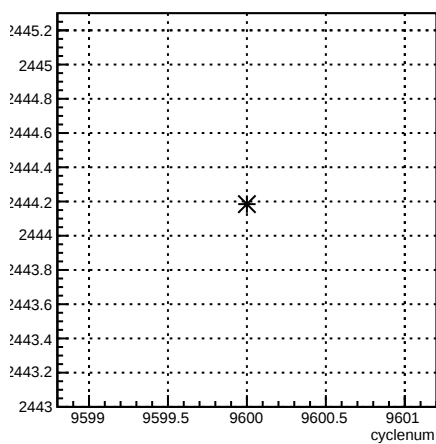
run:cyclenum



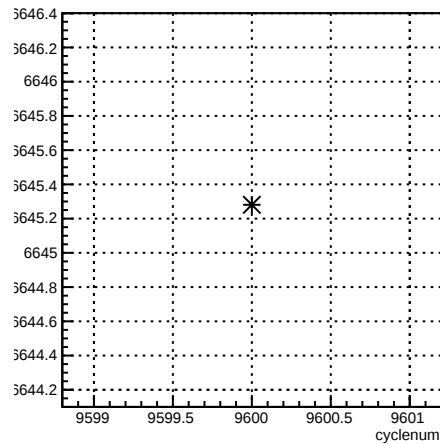
usl_coil1:cyclenum



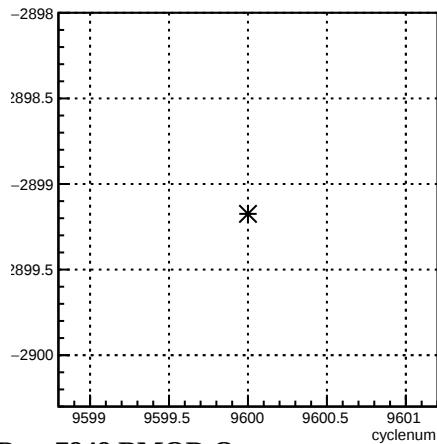
usl_coil3:cyclenum



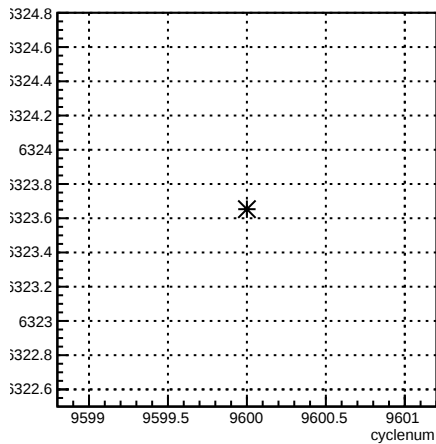
usl_coil4:cyclenum



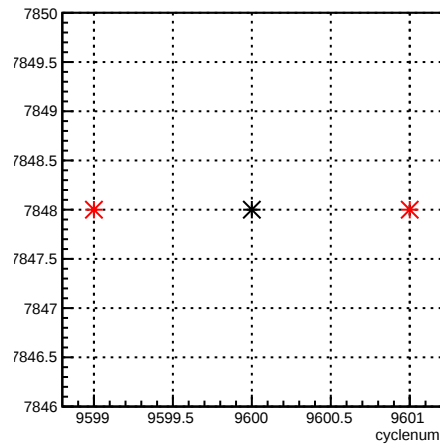
usl_coil6:cyclenum



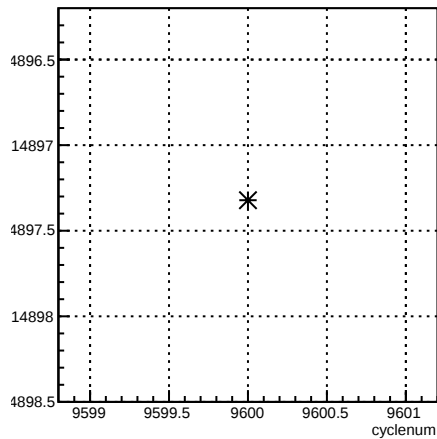
usl_coil7:cyclenum



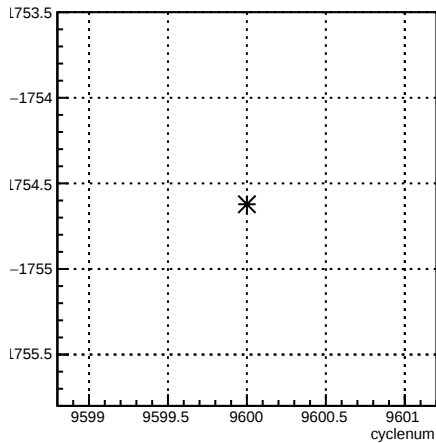
run:cyclenum



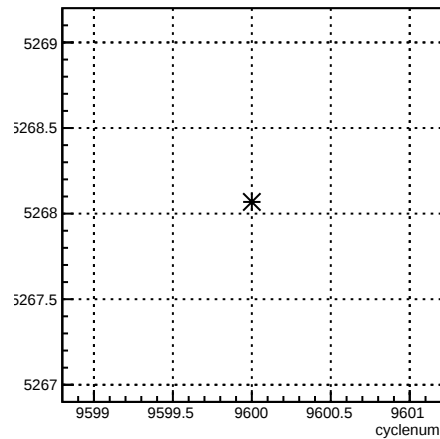
usr_coil1:cyclenum



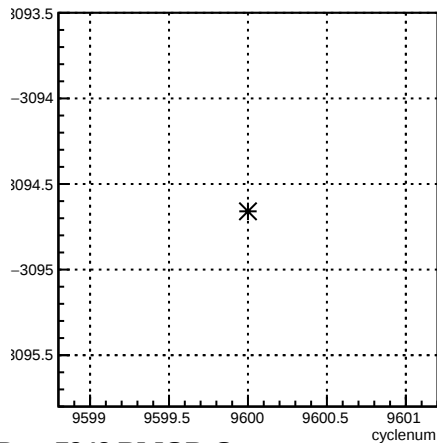
usr_coil3:cyclenum



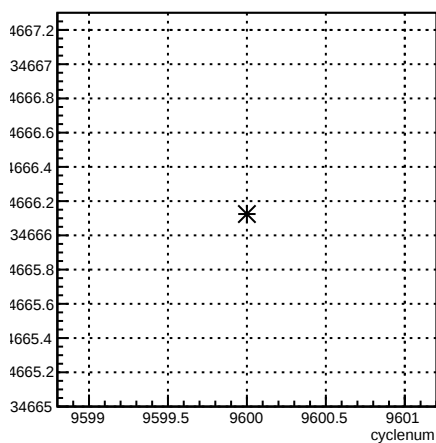
usr_coil4:cyclenum



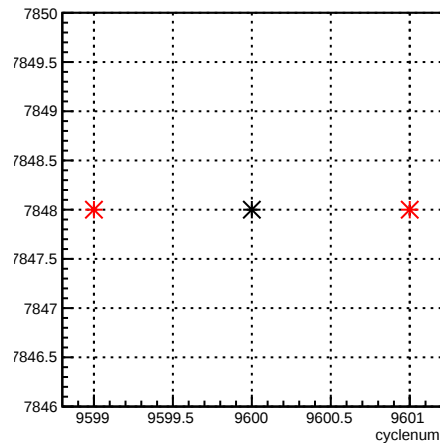
usr_coil6:cyclenum



usr_coil7:cyclenum

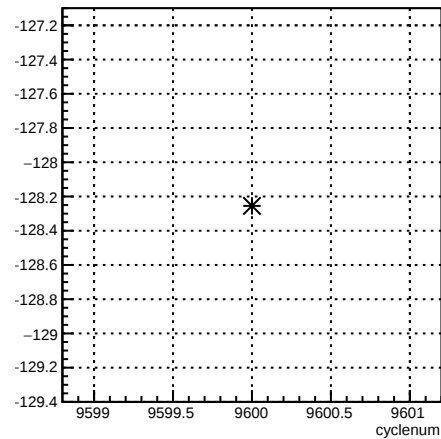


run:cyclenum

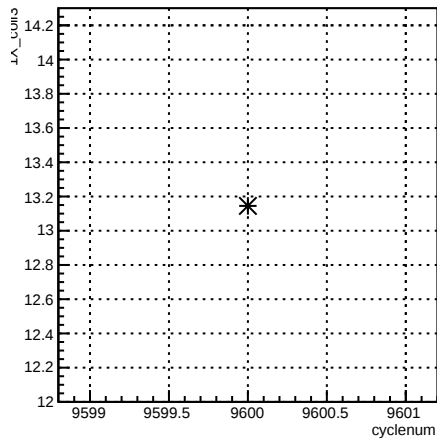


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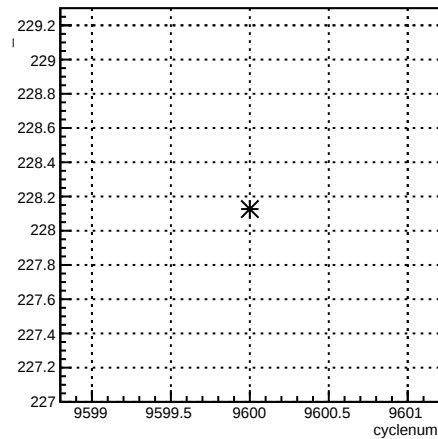
1X_coil1:cyclenum



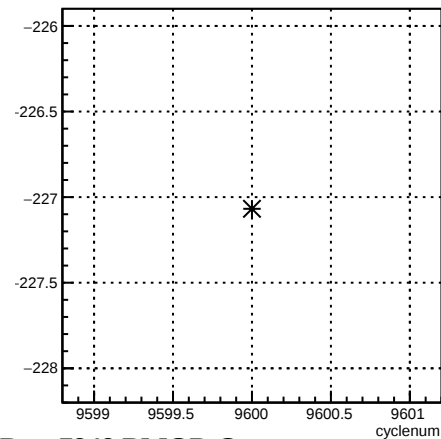
1X_coil3:cyclenum



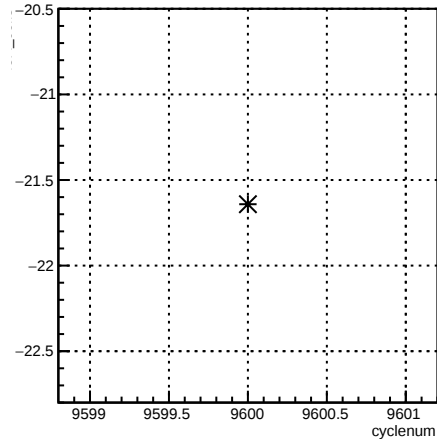
1X_coil7:cyclenum



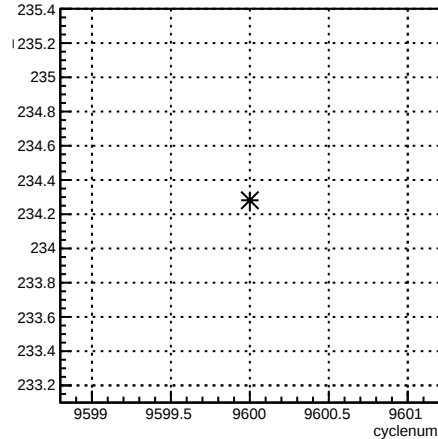
4eX_coil1:cyclenum



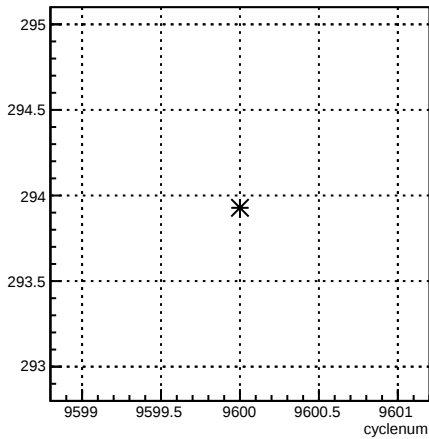
4eX_coil3:cyclenum



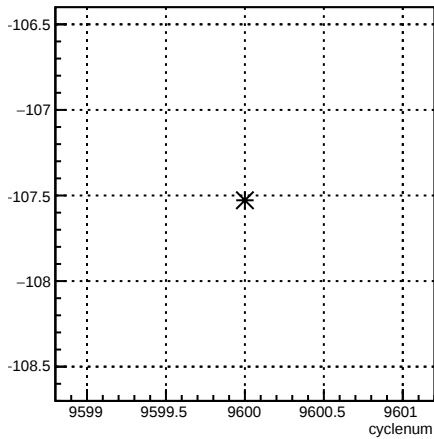
4eX_coil7:cyclenum



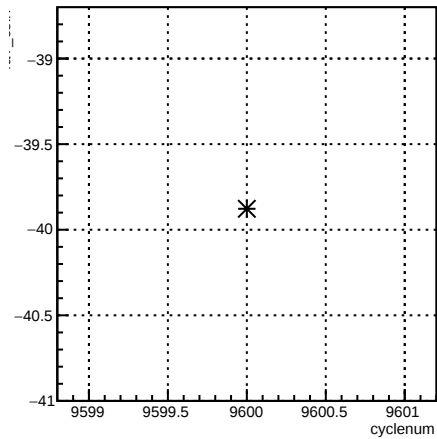
4aY_coil4:cyclenum



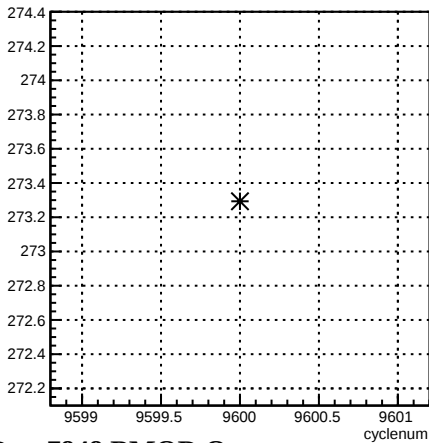
4aY_coil6:cyclenum



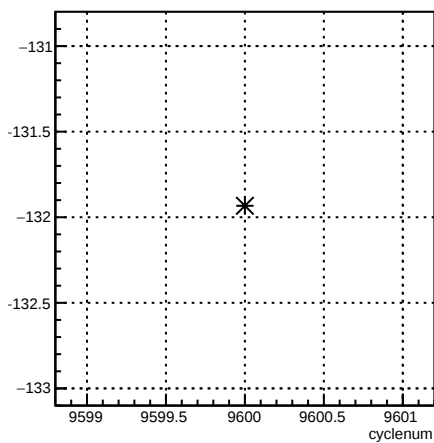
4aY_coil7:cyclenum



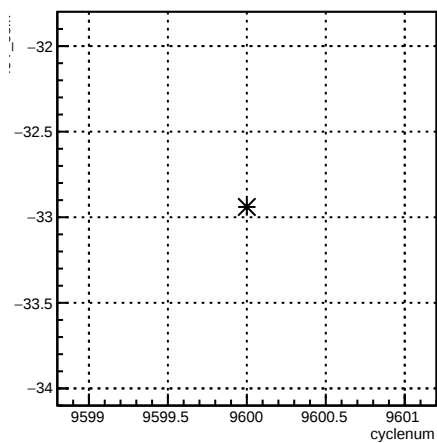
4eY_coil4:cyclenum



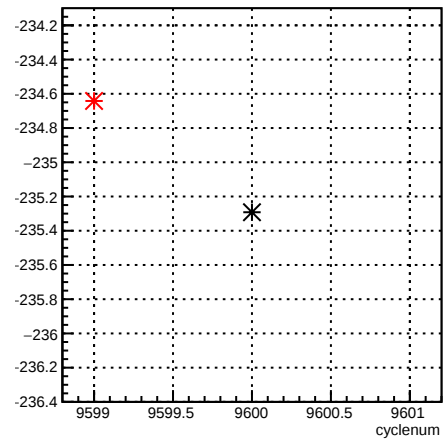
4eY_coil6:cyclenum



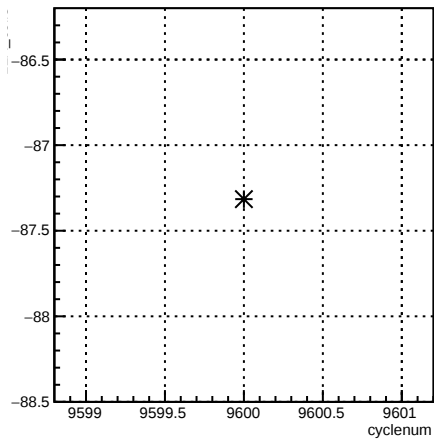
4eY_coil7:cyclenum



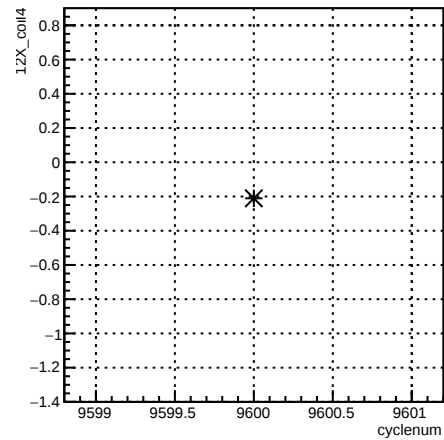
12X_coil1:cyclenum



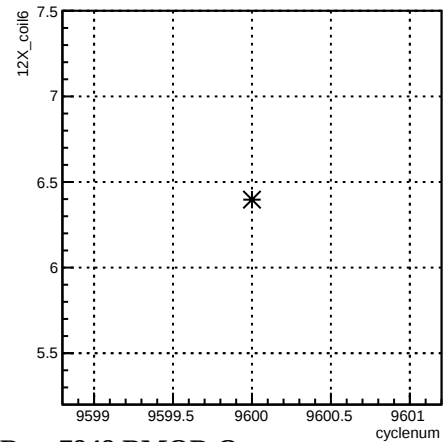
12X_coil3:cyclenum



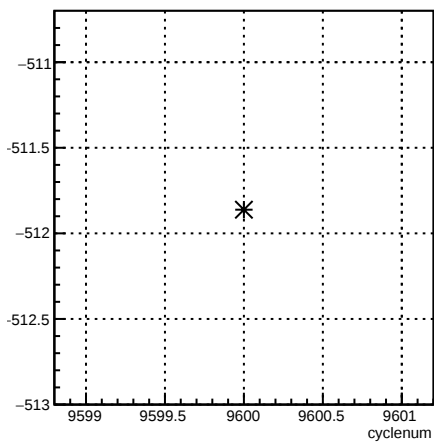
12X_coil4:cyclenum



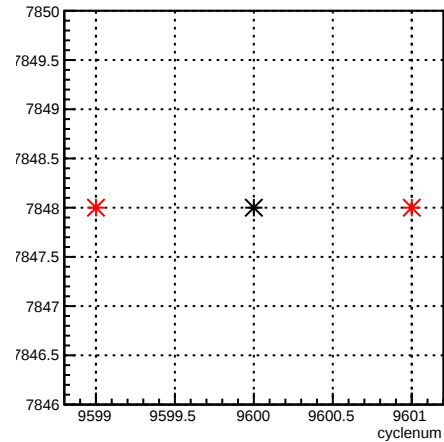
12X_coil6:cyclenum



12X_coil7:cyclenum



run:cyclenum



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