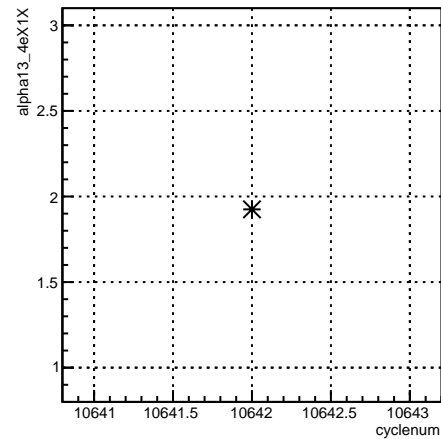
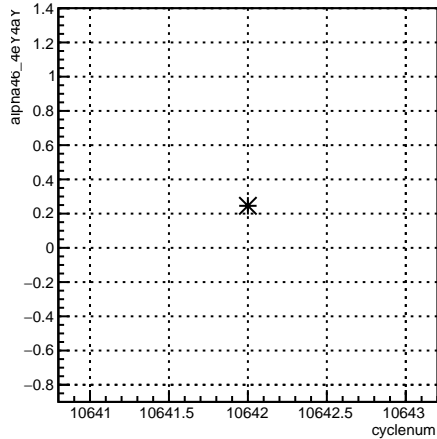


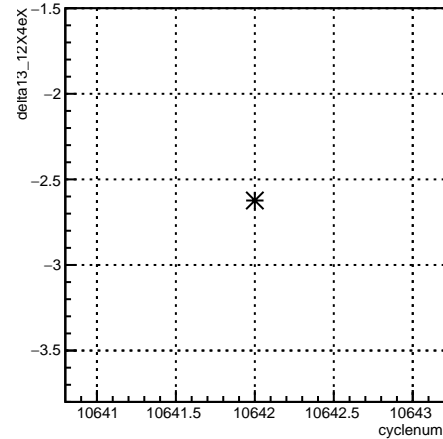
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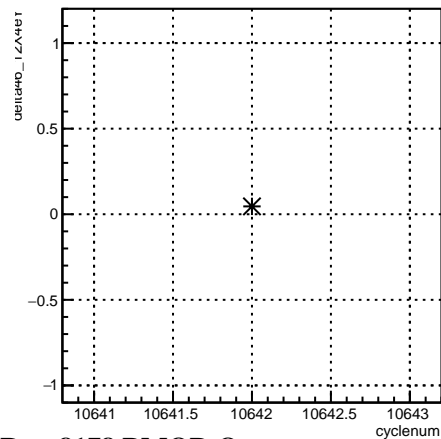
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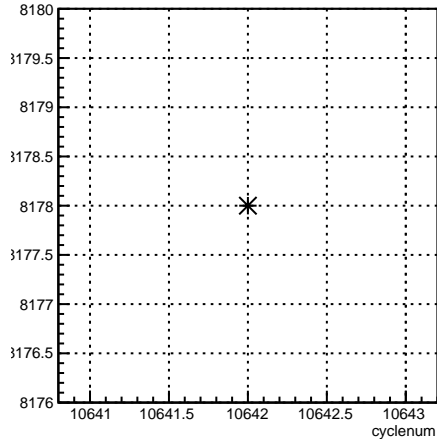
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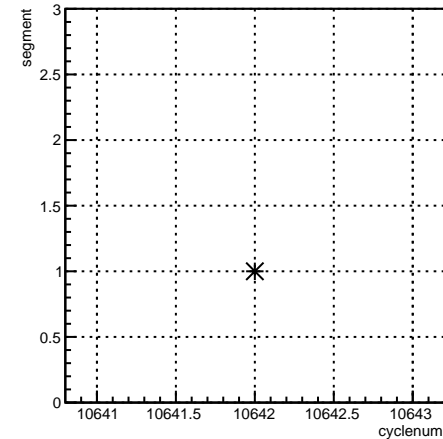
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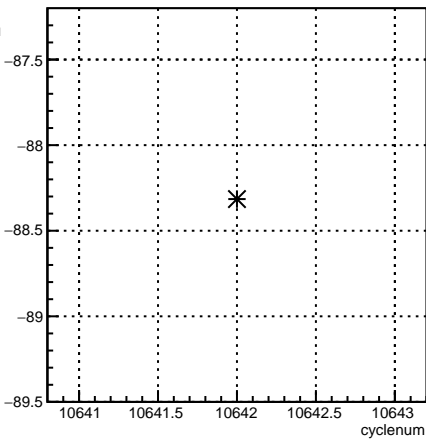
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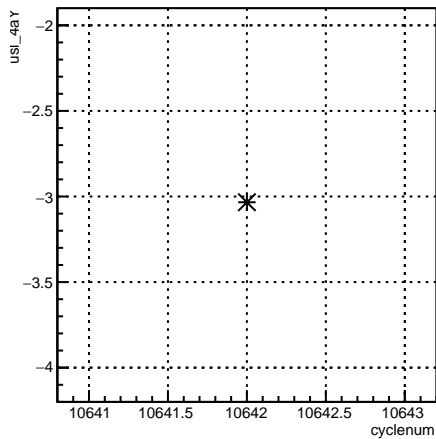
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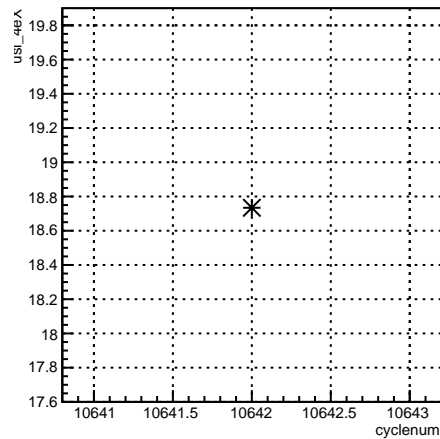
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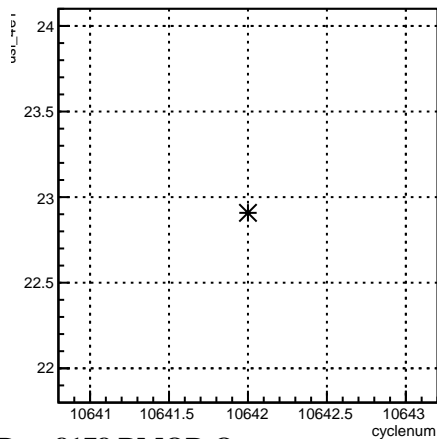
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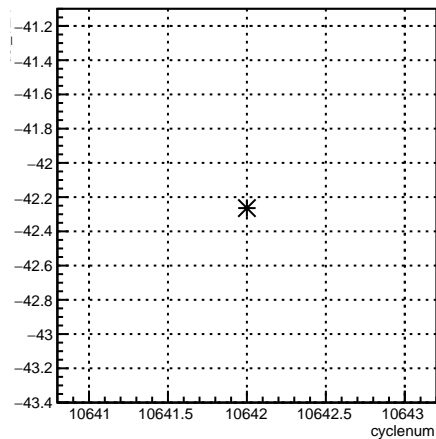
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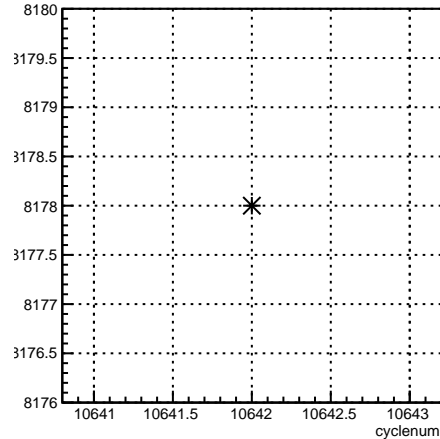
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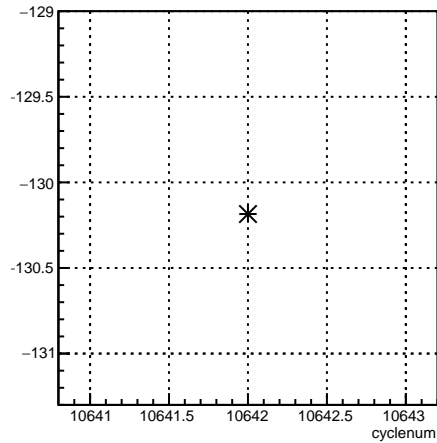
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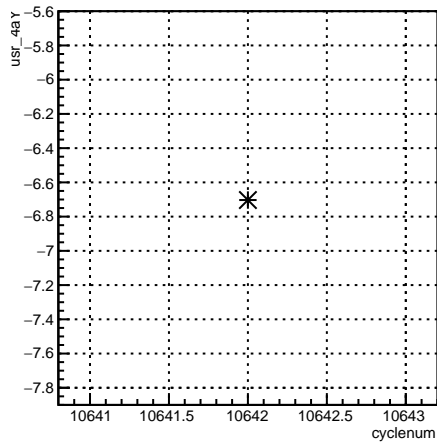
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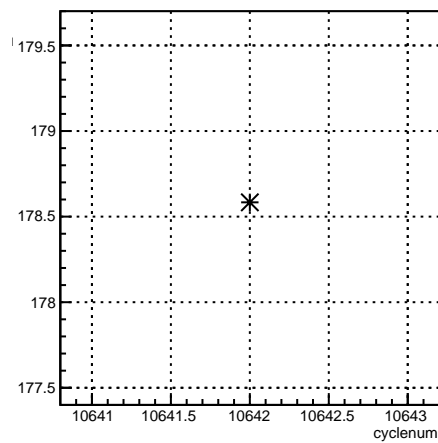
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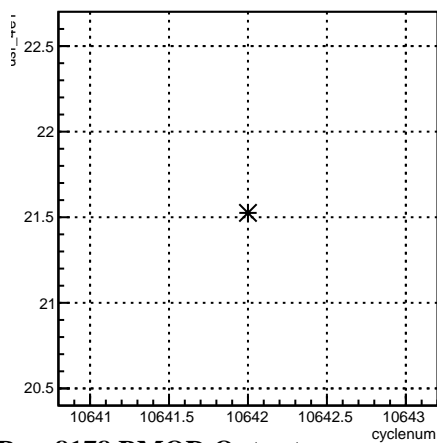
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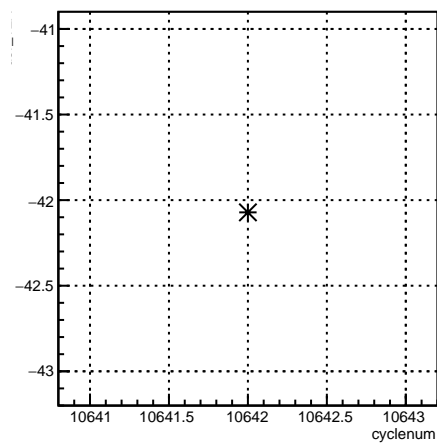
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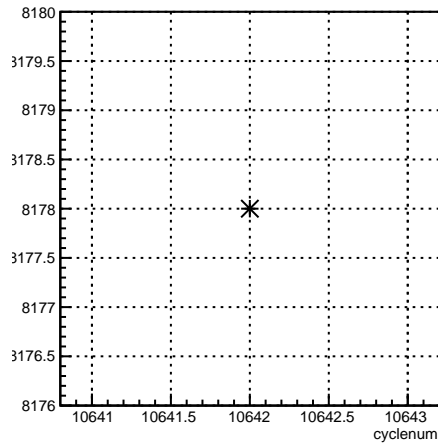
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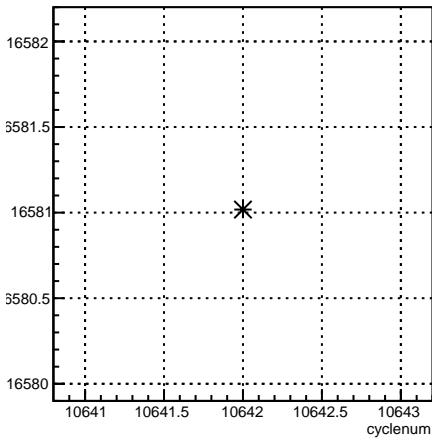
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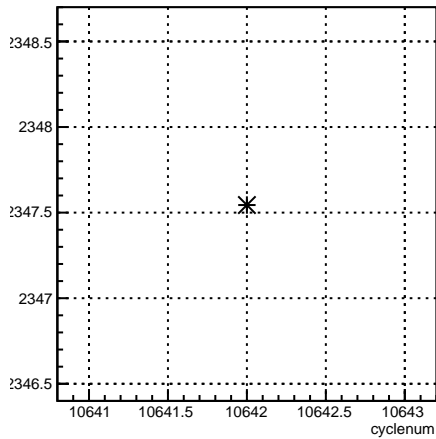
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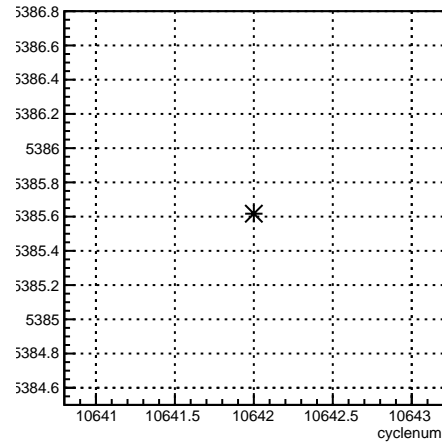
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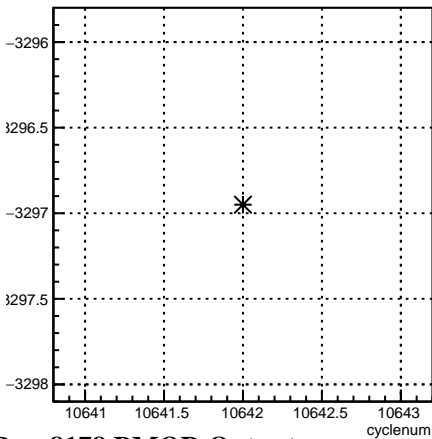
usl\_coil3:cyclenum



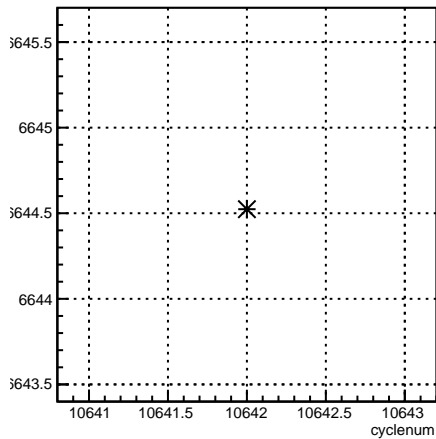
usl\_coil4:cyclenum



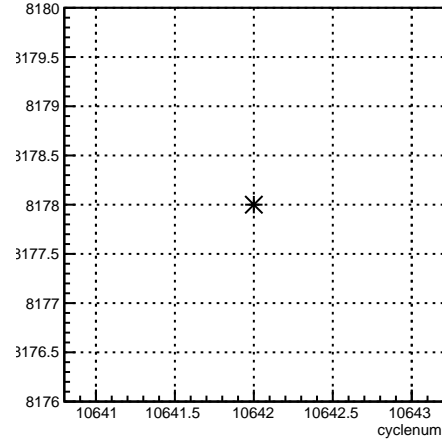
usl\_coil6:cyclenum



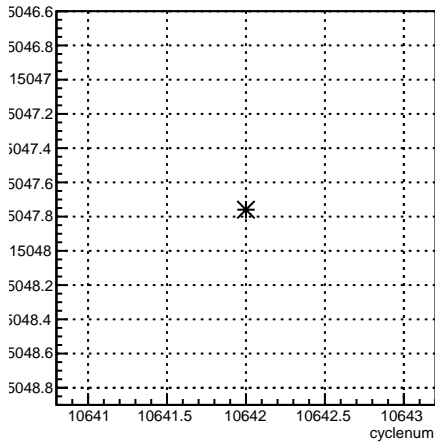
usl\_coil7:cyclenum



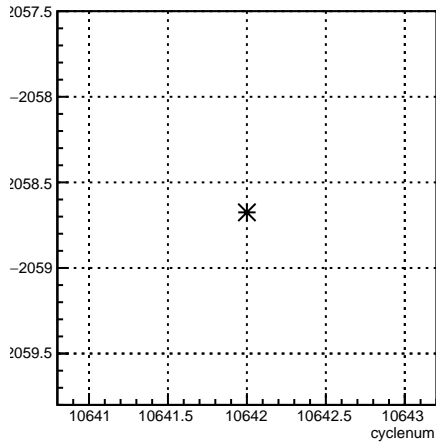
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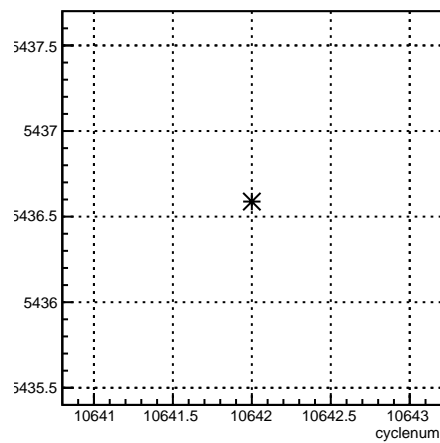
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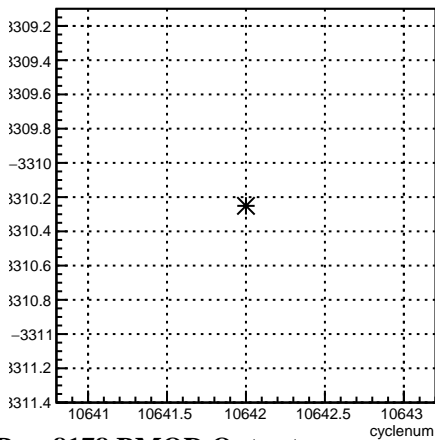
usr\_coil3:cyclenum



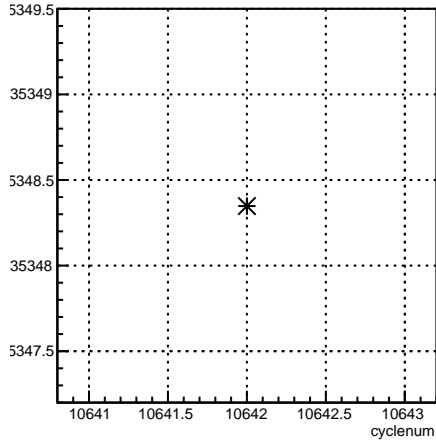
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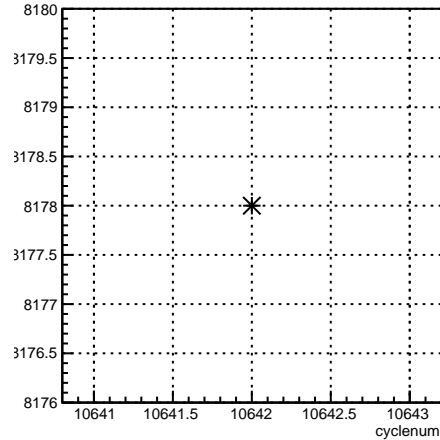
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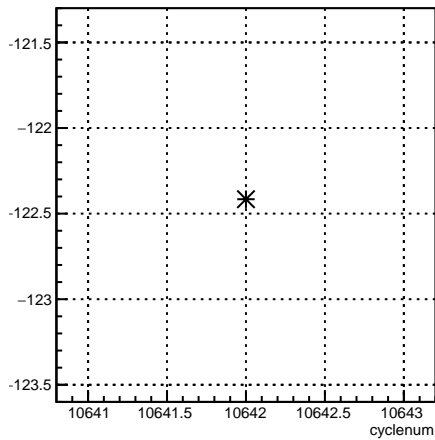
usr\_coil7:cyclenum



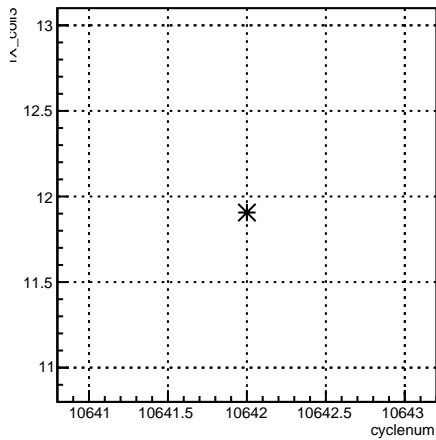
run:cyclenum



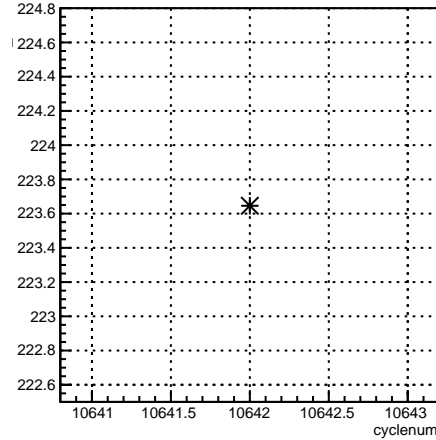
1X\_coil1:cyclenum



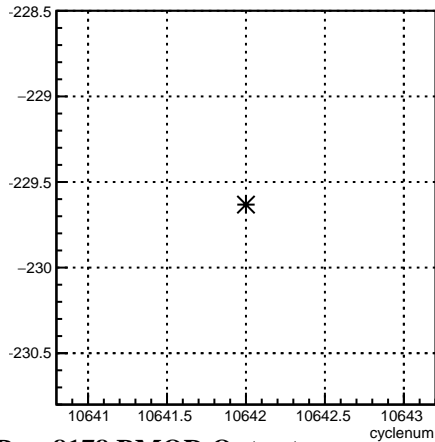
1X\_coil3:cyclenum



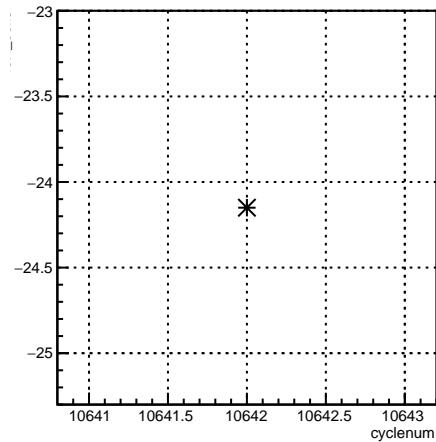
1X\_coil7:cyclenum



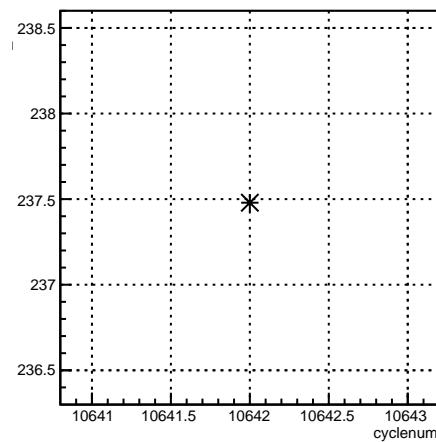
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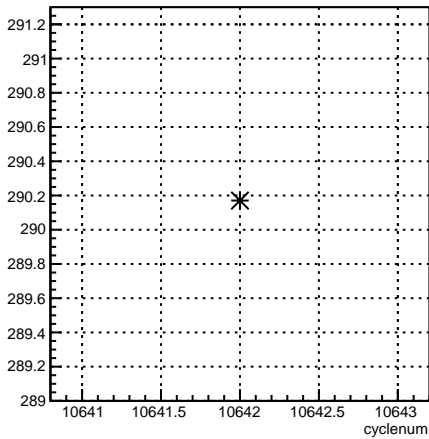
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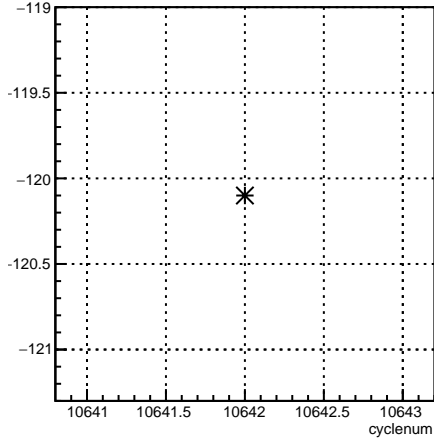
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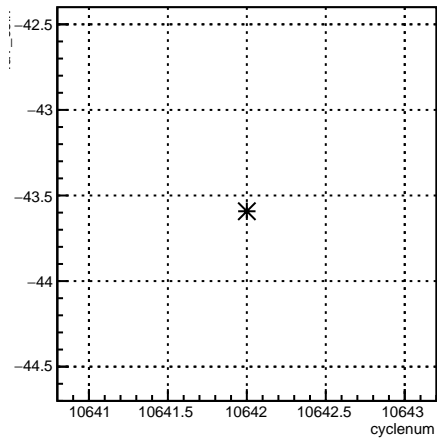
4aY\_coil4:cyclenum



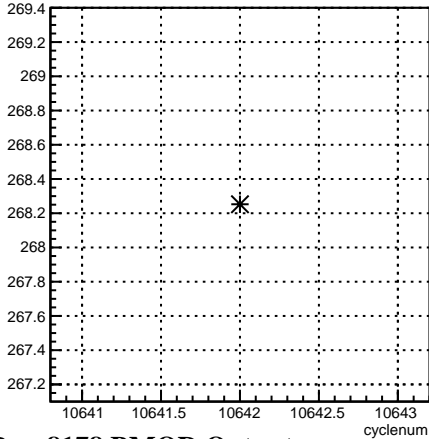
4aY\_coil6:cyclenum



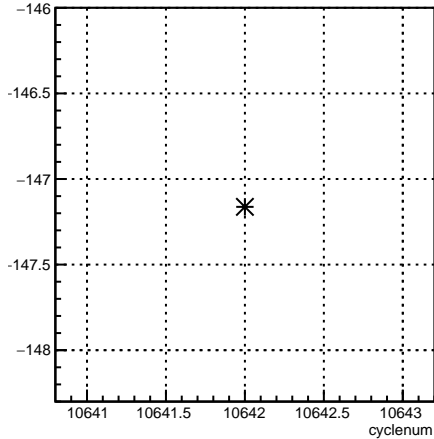
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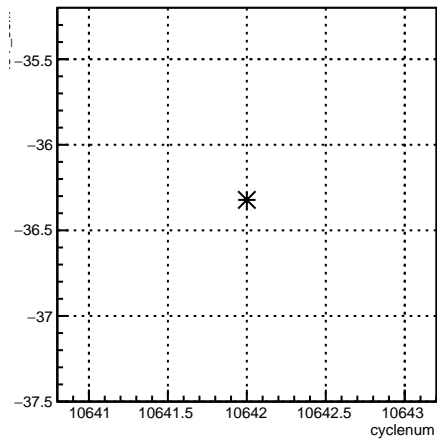
4eY\_coil4:cyclenum



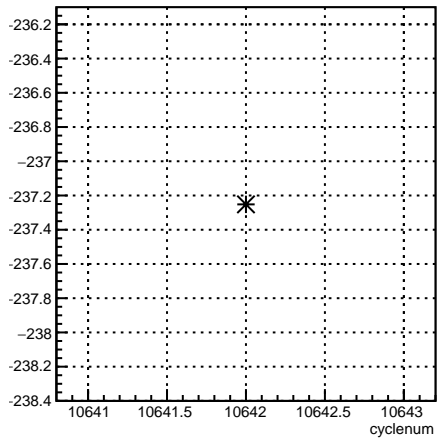
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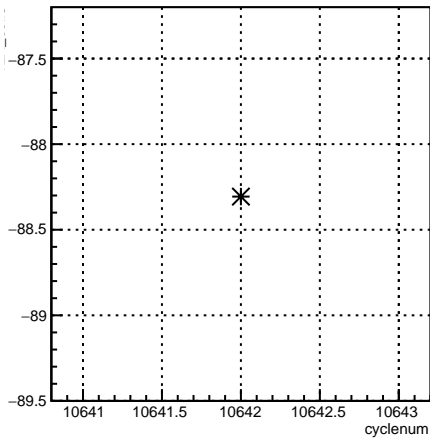
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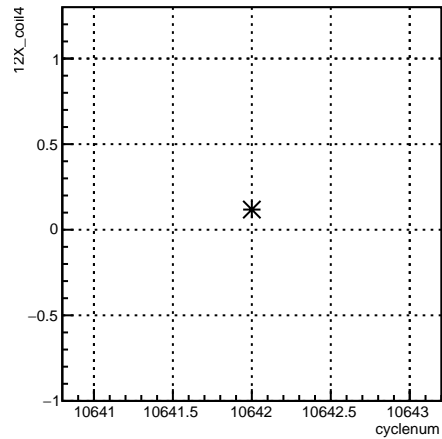
12X\_coil1:cyclenum



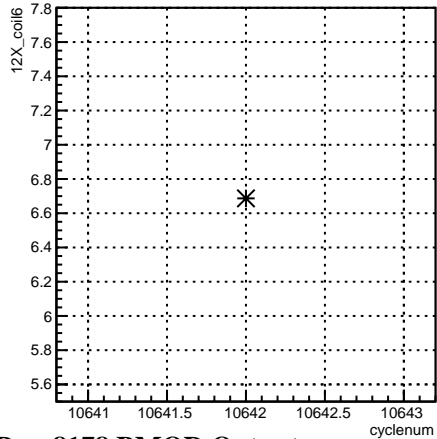
12X\_coil3:cyclenum



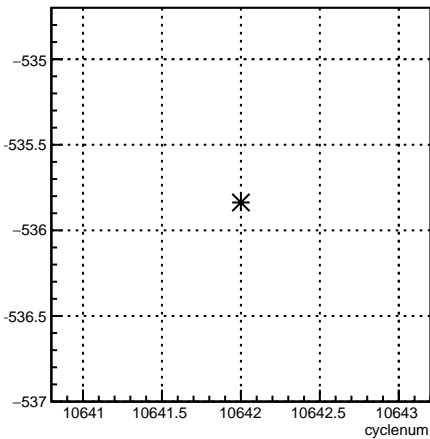
12X\_coil4:cyclenum



12X\_coil6:cyclenum



12X\_coil7:cyclenum



run:cyclenum

