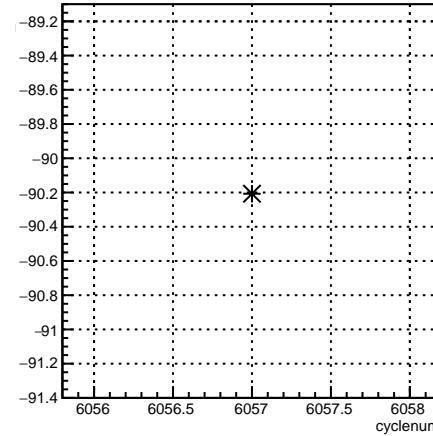
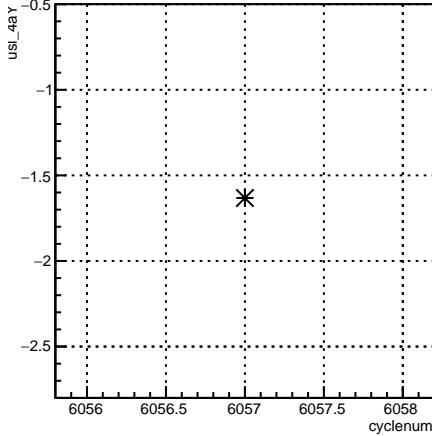


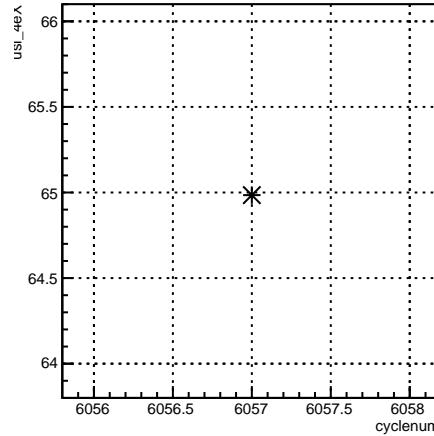
usl\_1X:cyclenum



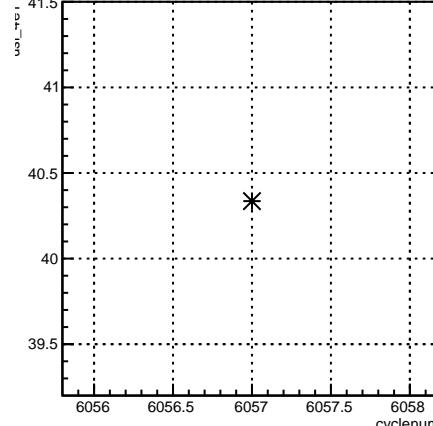
usl\_4aY:cyclenum



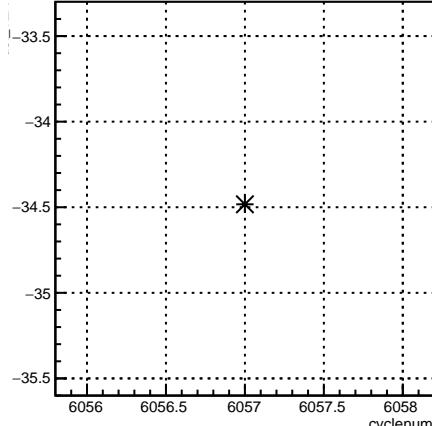
usl\_4eX:cyclenum



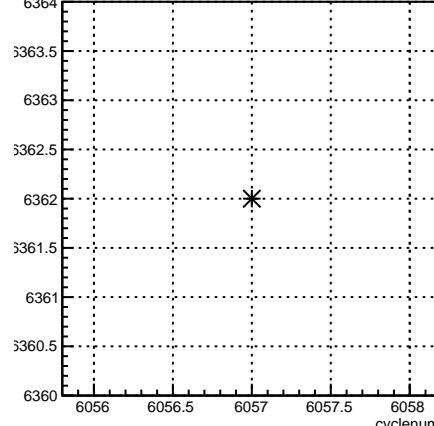
usl\_4eY:cyclenum



usl\_12X:cyclenum

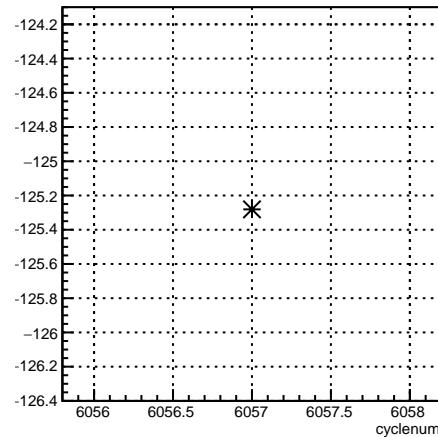


run:cyclenum

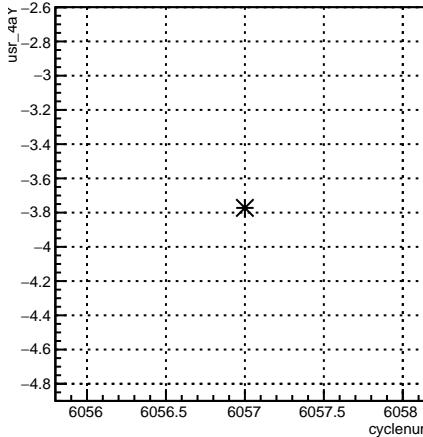


Run 6362 BMOD Outputs

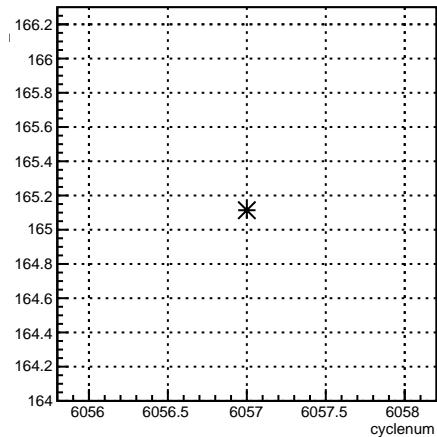
usr\_1X:cyclenum



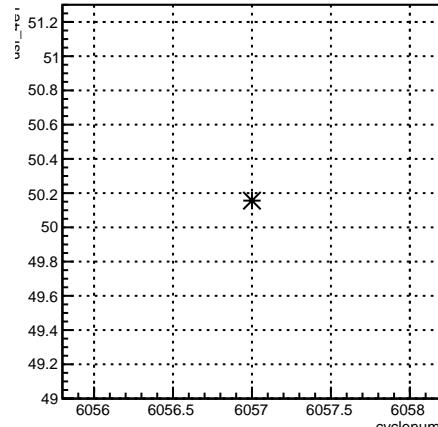
usr\_4aY:cyclenum



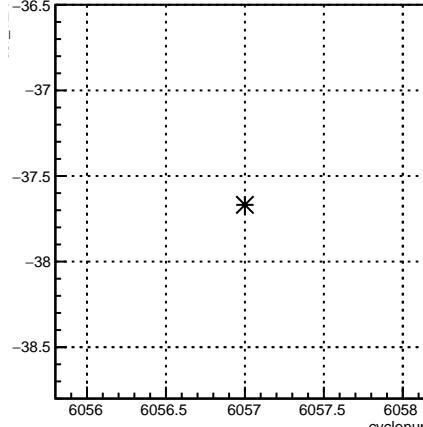
usr\_4eX:cyclenum



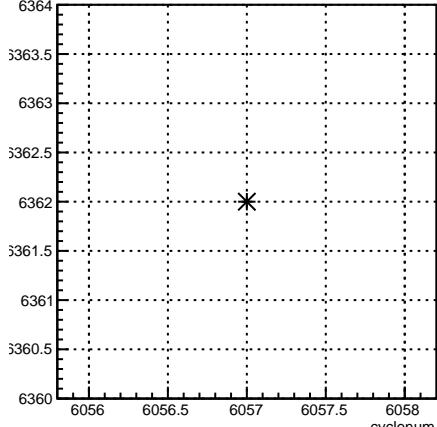
usr\_4eY:cyclenum



usr\_12X:cyclenum

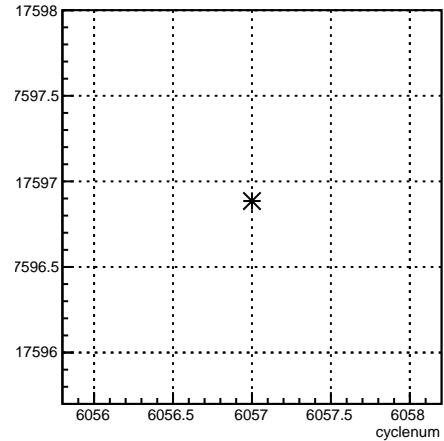


run:cyclenum

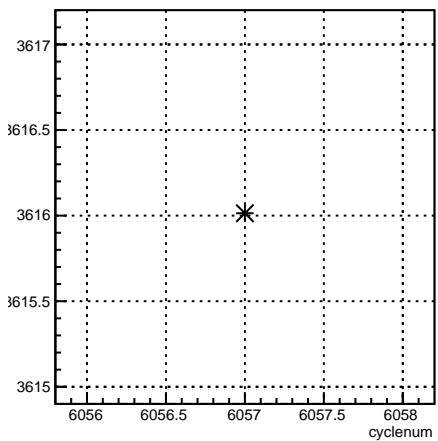


Run 6362 BMOD Outputs

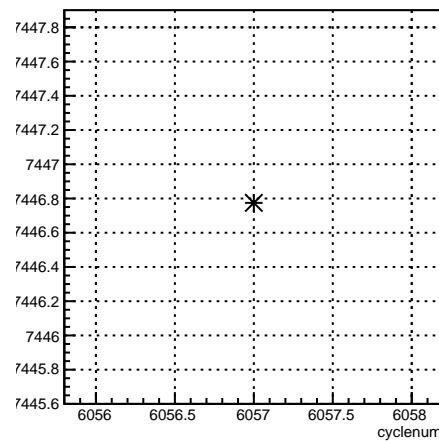
usl\_coil1:cyclenum



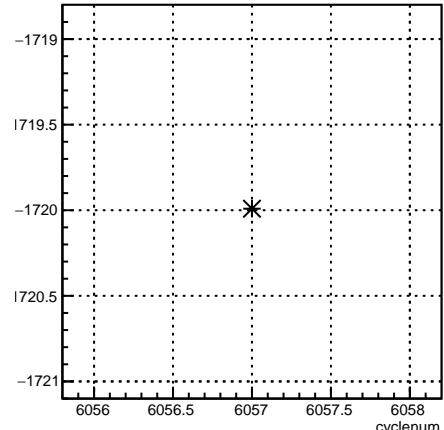
usl\_coil3:cyclenum



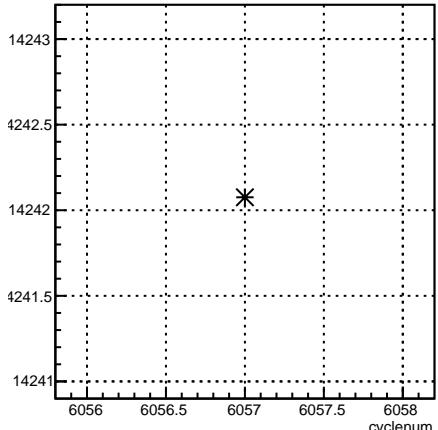
usl\_coil4:cyclenum



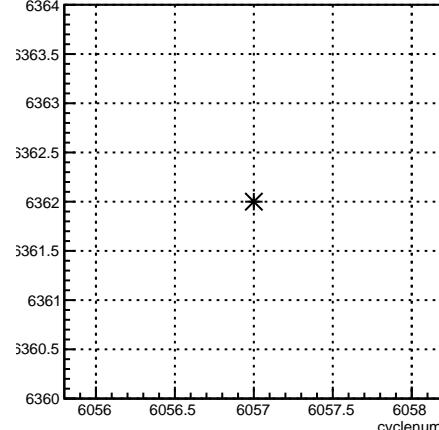
usl\_coil6:cyclenum



usl\_coil7:cyclenum

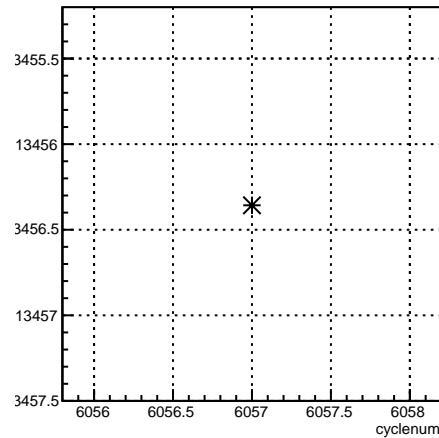


run:cyclenum

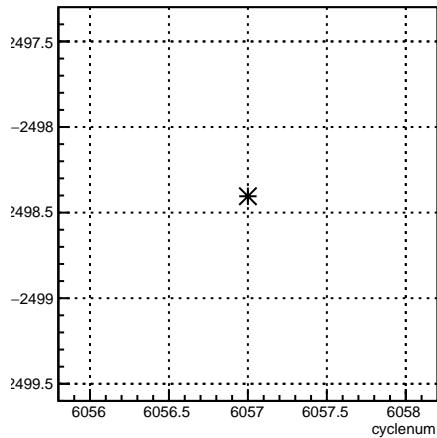


**Run 6362 BMOD Outputs**

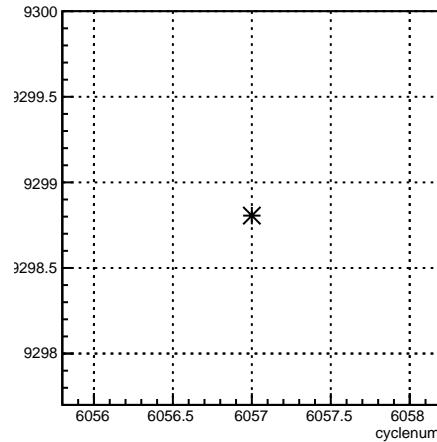
usr\_coil1:cyclenum



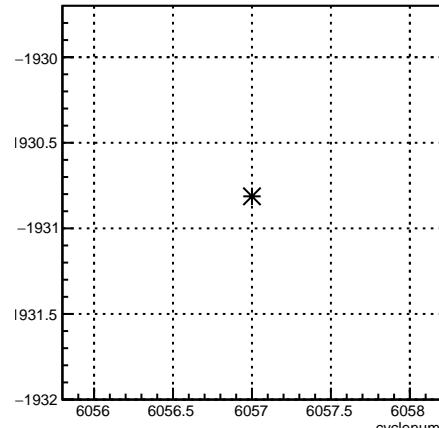
usr\_coil3:cyclenum



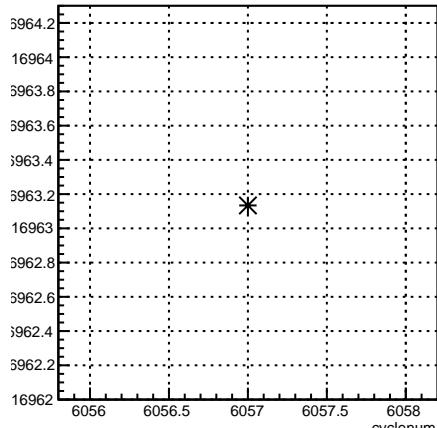
usr\_coil4:cyclenum



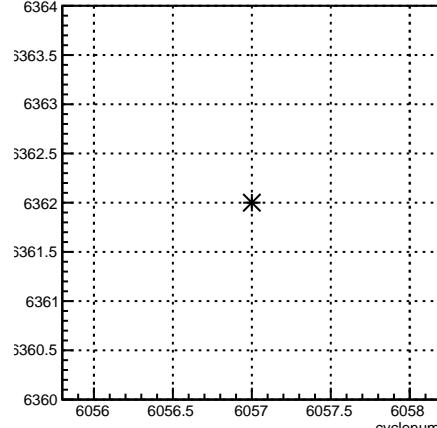
usr\_coil6:cyclenum



usr\_coil7:cyclenum

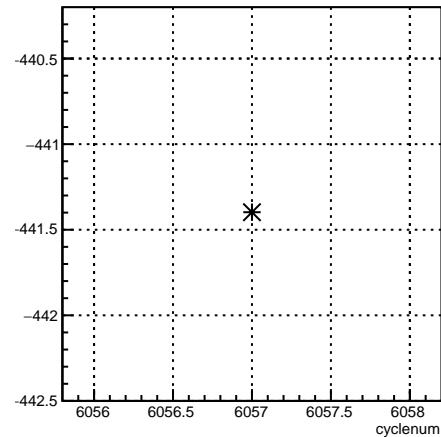


run:cyclenum

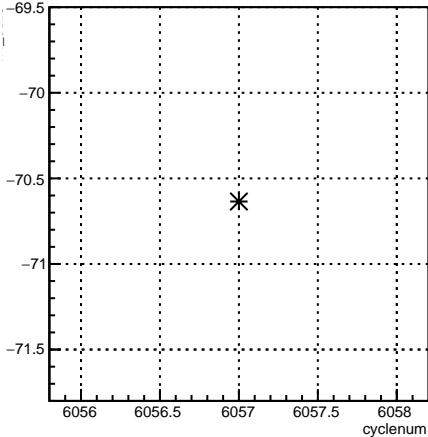


Run 6362 BMOD Outputs

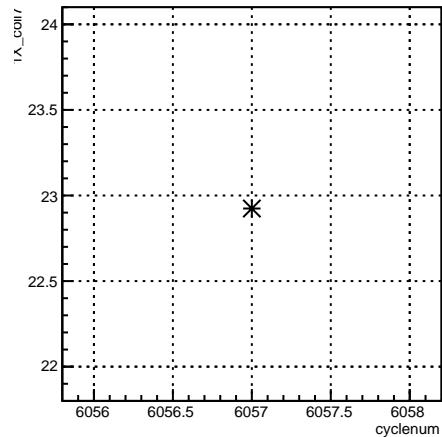
1X\_coil1:cyclenum



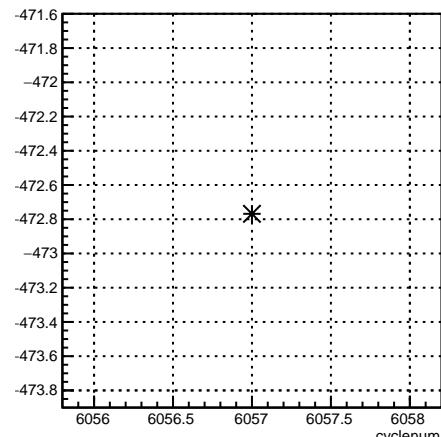
1X\_coil3:cyclenum



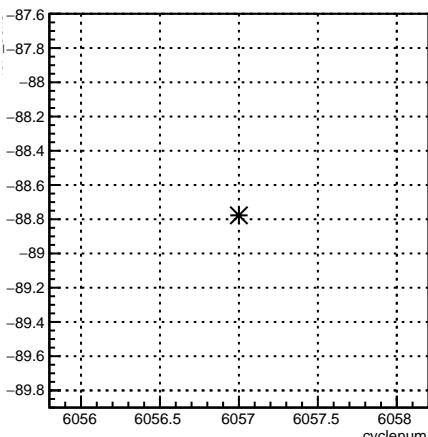
1X\_coil7:cyclenum



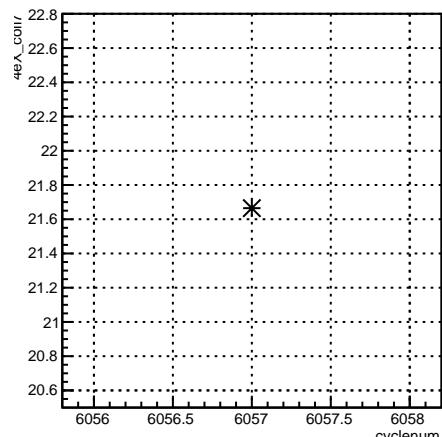
4eX\_coil1:cyclenum



4eX\_coil3:cyclenum

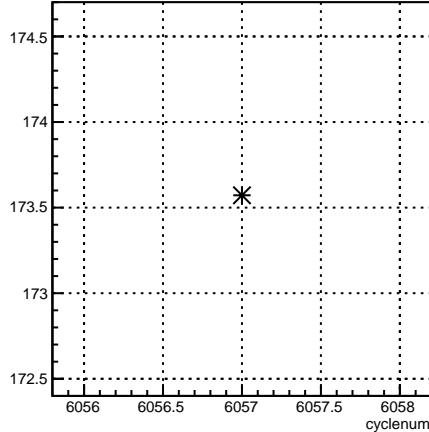


4eX\_coil7:cyclenum

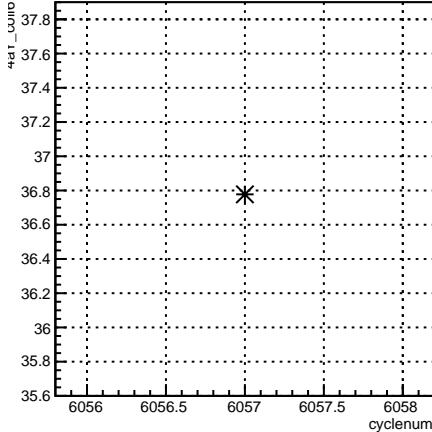


**Run 6362 BMOD Outputs**

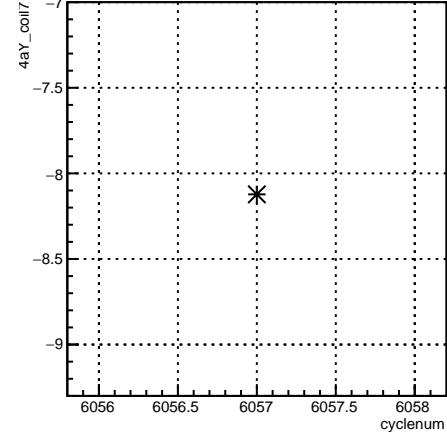
4aY\_coil4:cyclenum



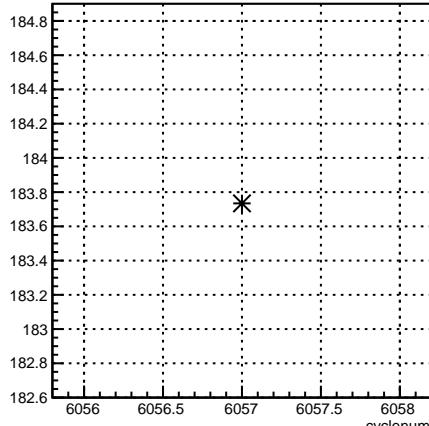
4aY\_coil6:cyclenum



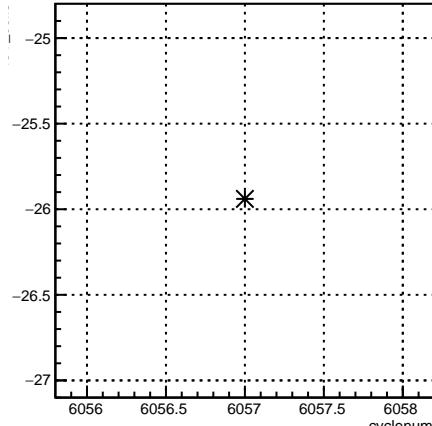
4aY\_coil7:cyclenum



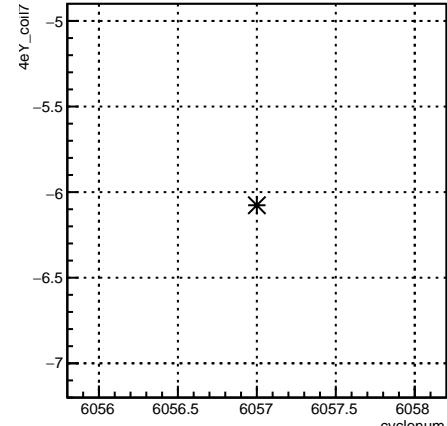
4eY\_coil4:cyclenum



4eY\_coil6:cyclenum

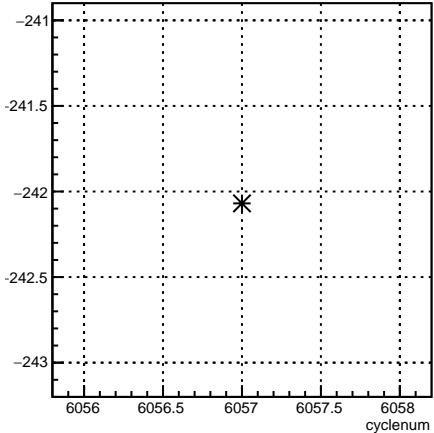


4eY\_coil7:cyclenum

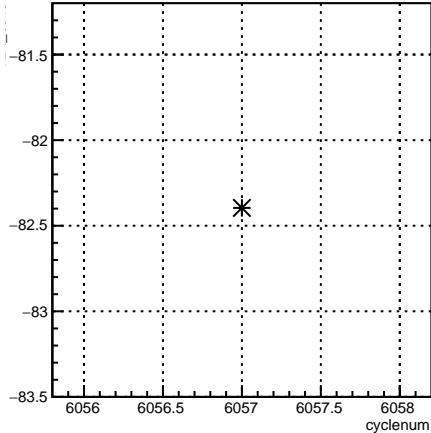


**Run 6362 BMOD Outputs**

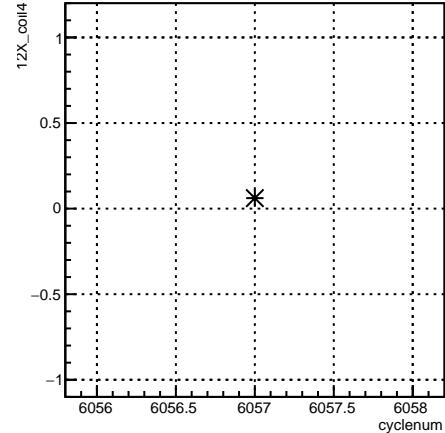
12X\_coil1:cyclenum



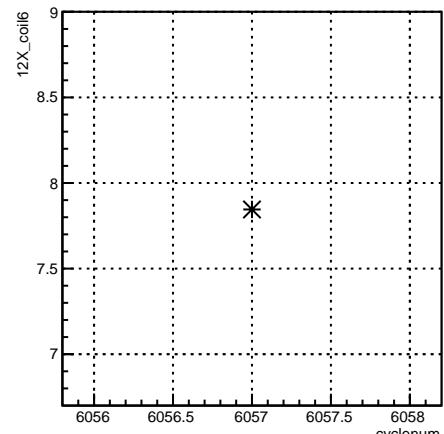
12X\_coil3:cyclenum



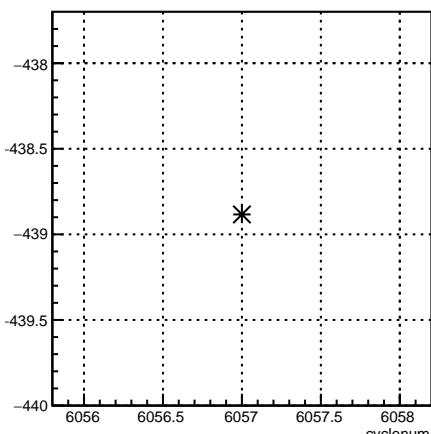
12X\_coil4:cyclenum



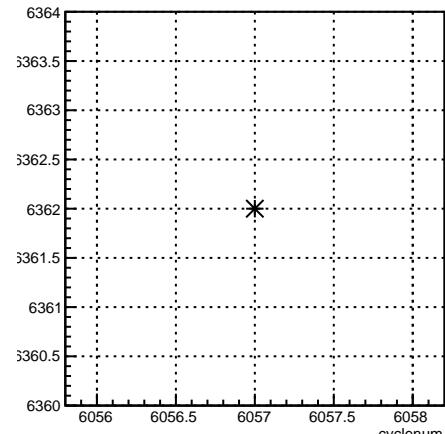
12X\_coil6:cyclenum



12X\_coil7:cyclenum



run:cyclenum



**Run 6362 BMOD Outputs**