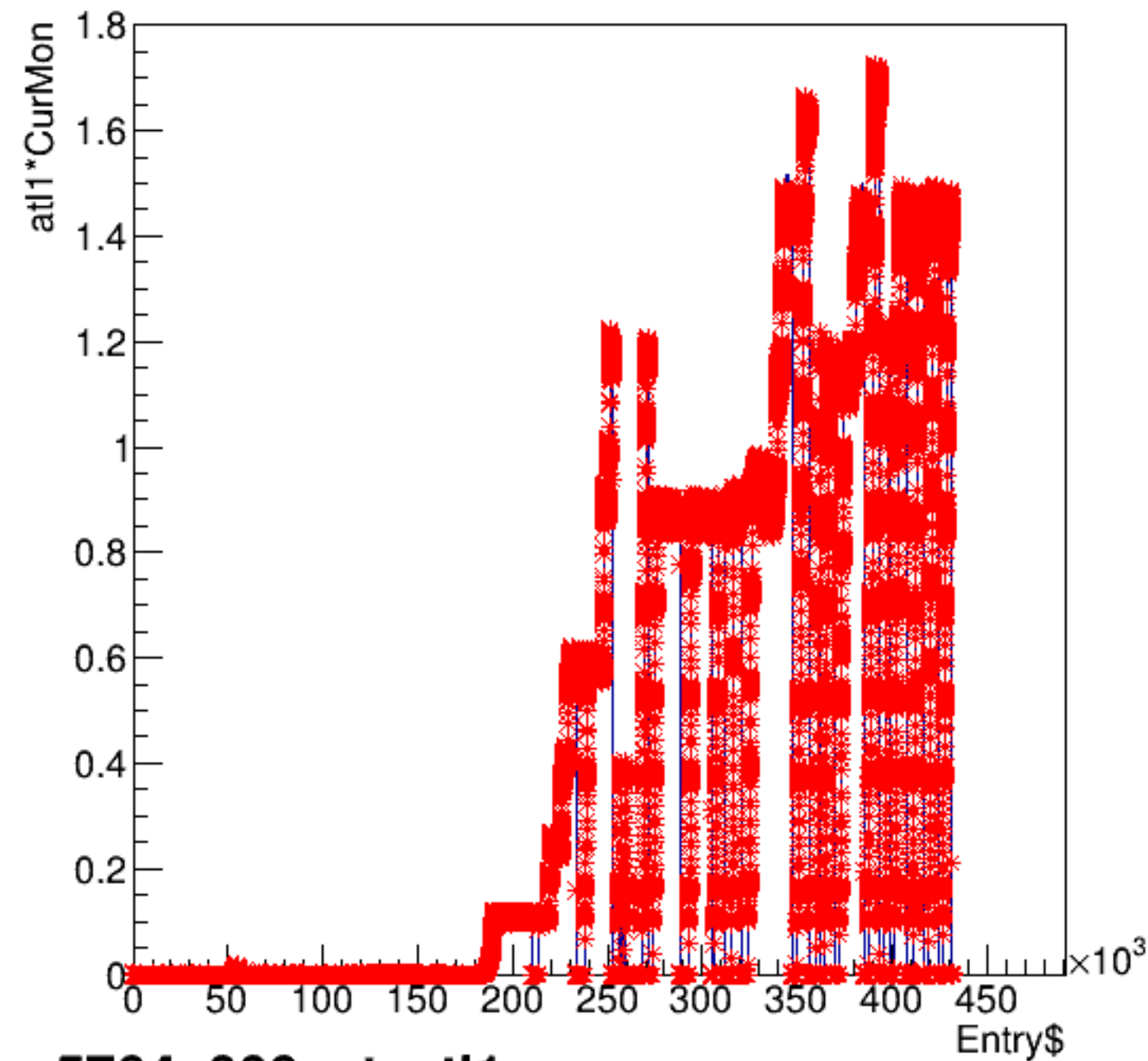
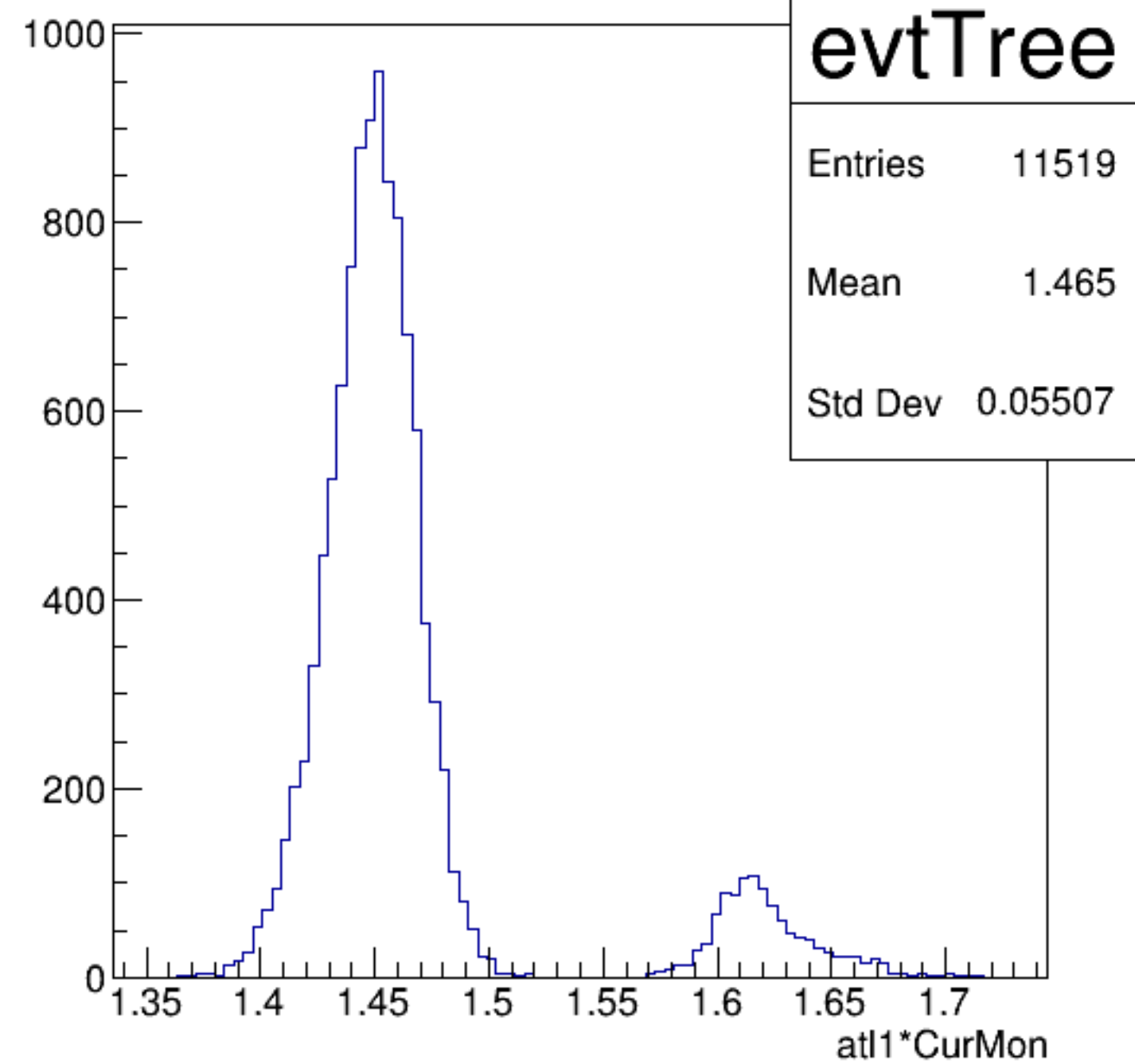


at1*CurMon:Entry\$



run5764_000_at_atl1.png

at1*CurMon {ErrorFlag==0}



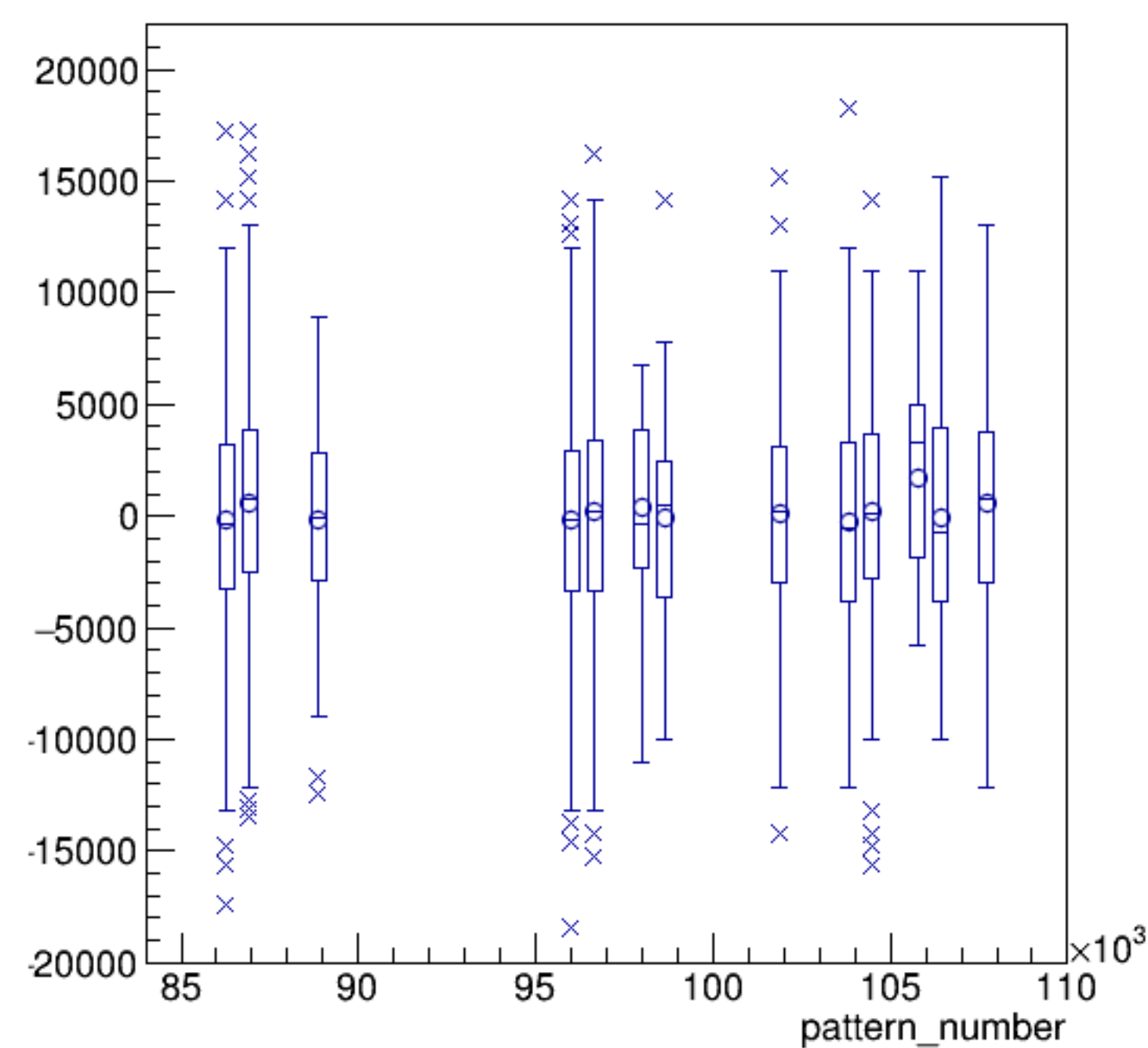
evtTree

Entries 11519

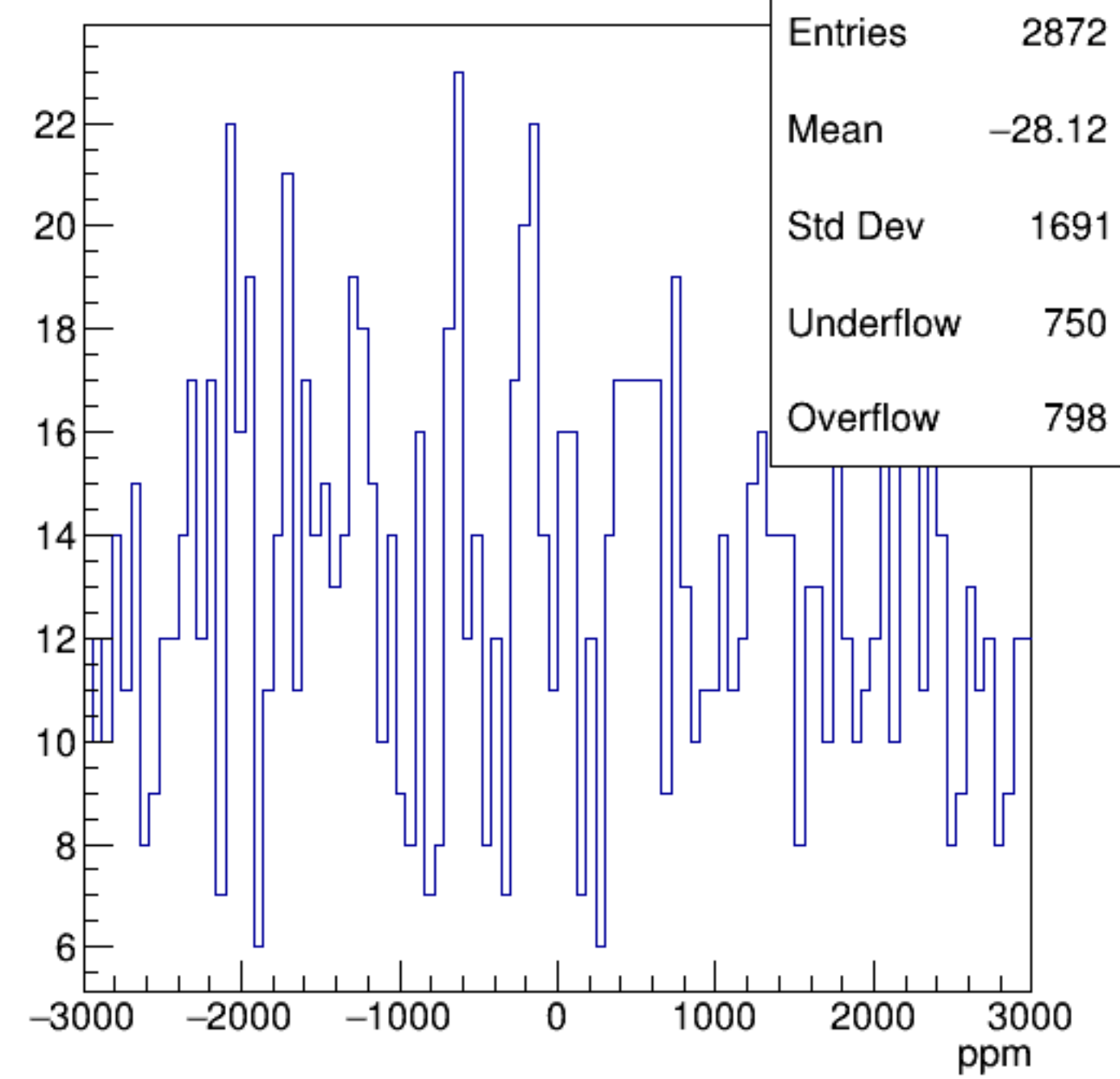
Mean 1.465

Std Dev 0.05507

asym_atl1/ppm:pattern_number {ErrorFlag==0}



asym_atl1



Entries 2872

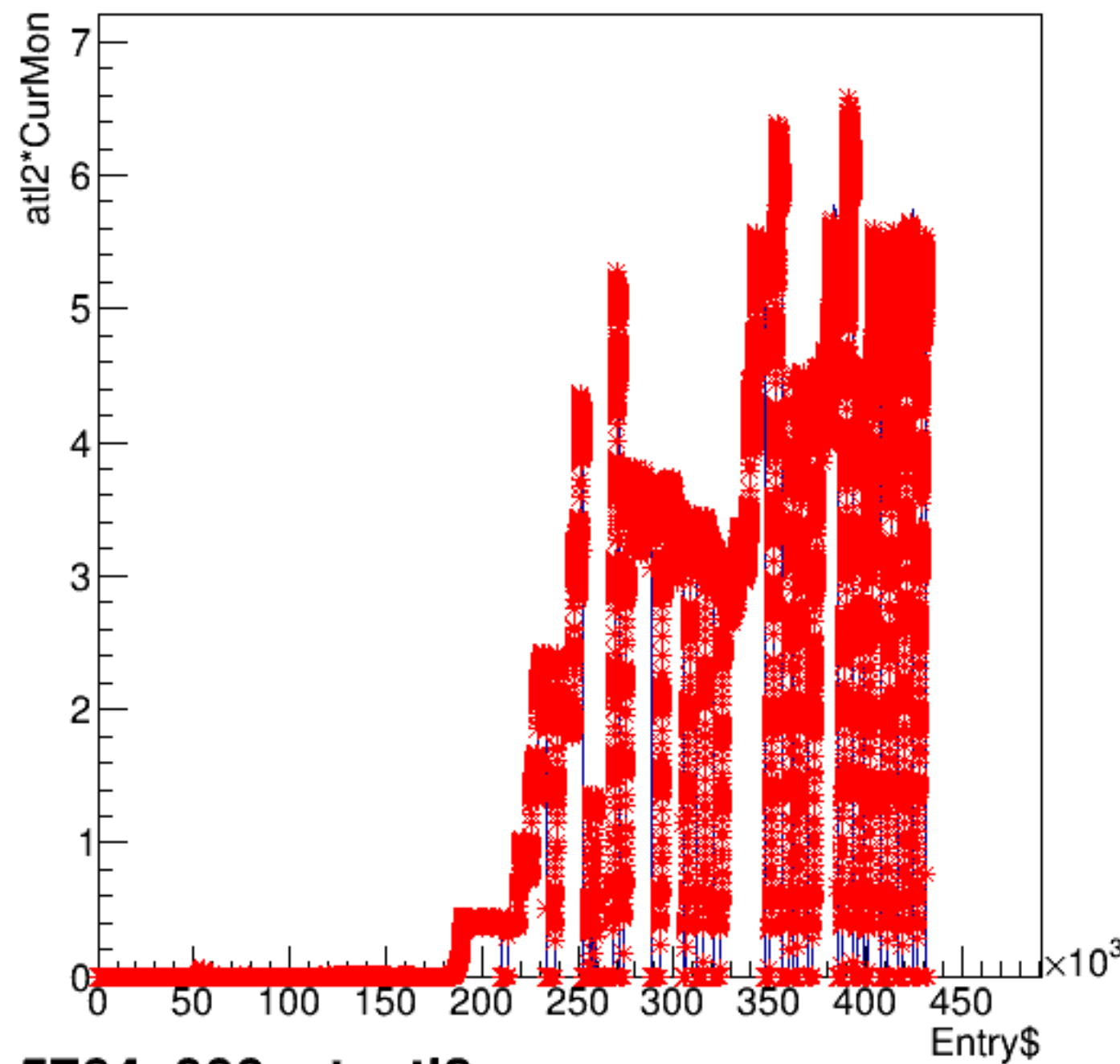
Mean -28.12

Std Dev 1691

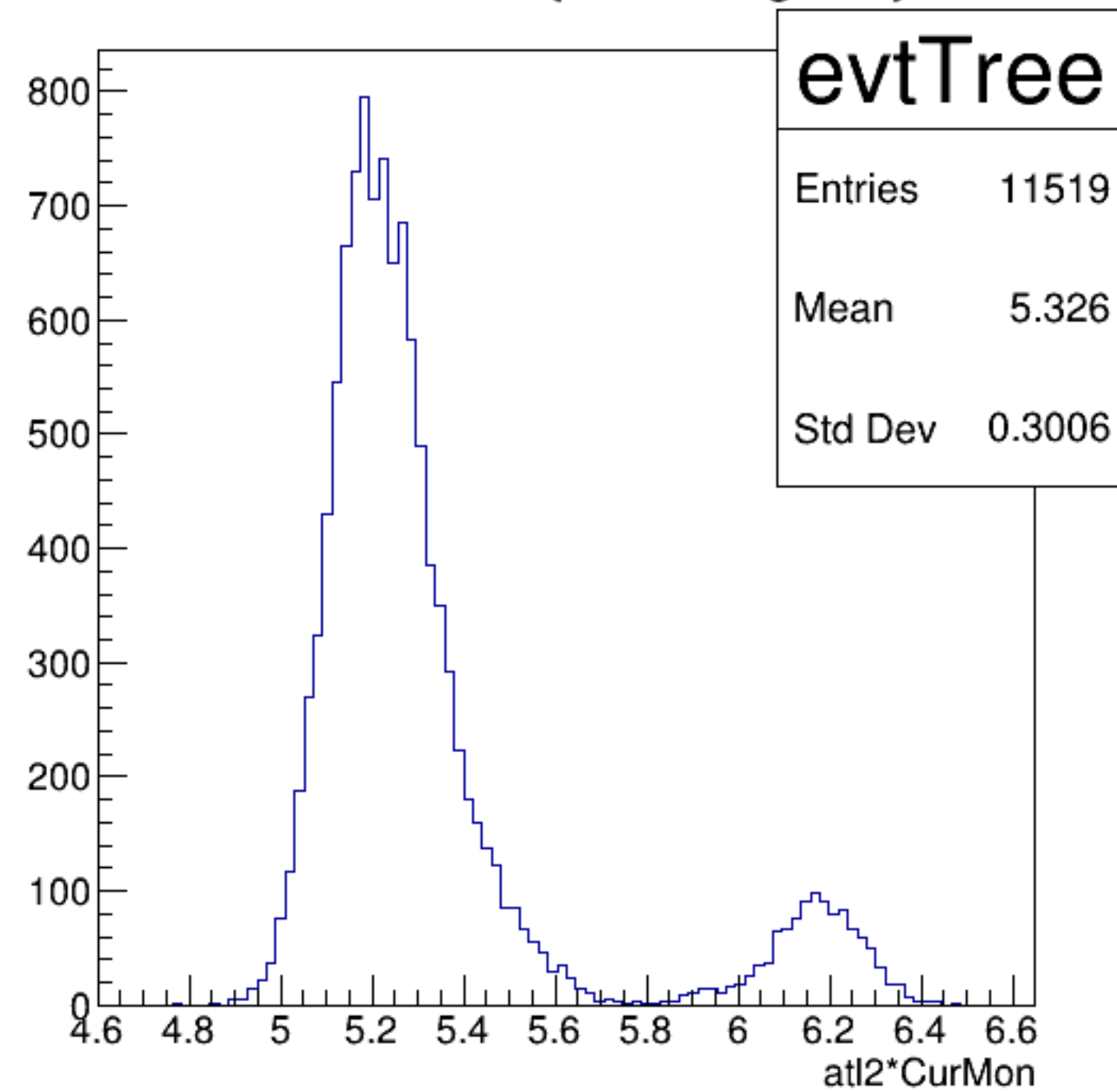
Underflow 750

Overflow 798

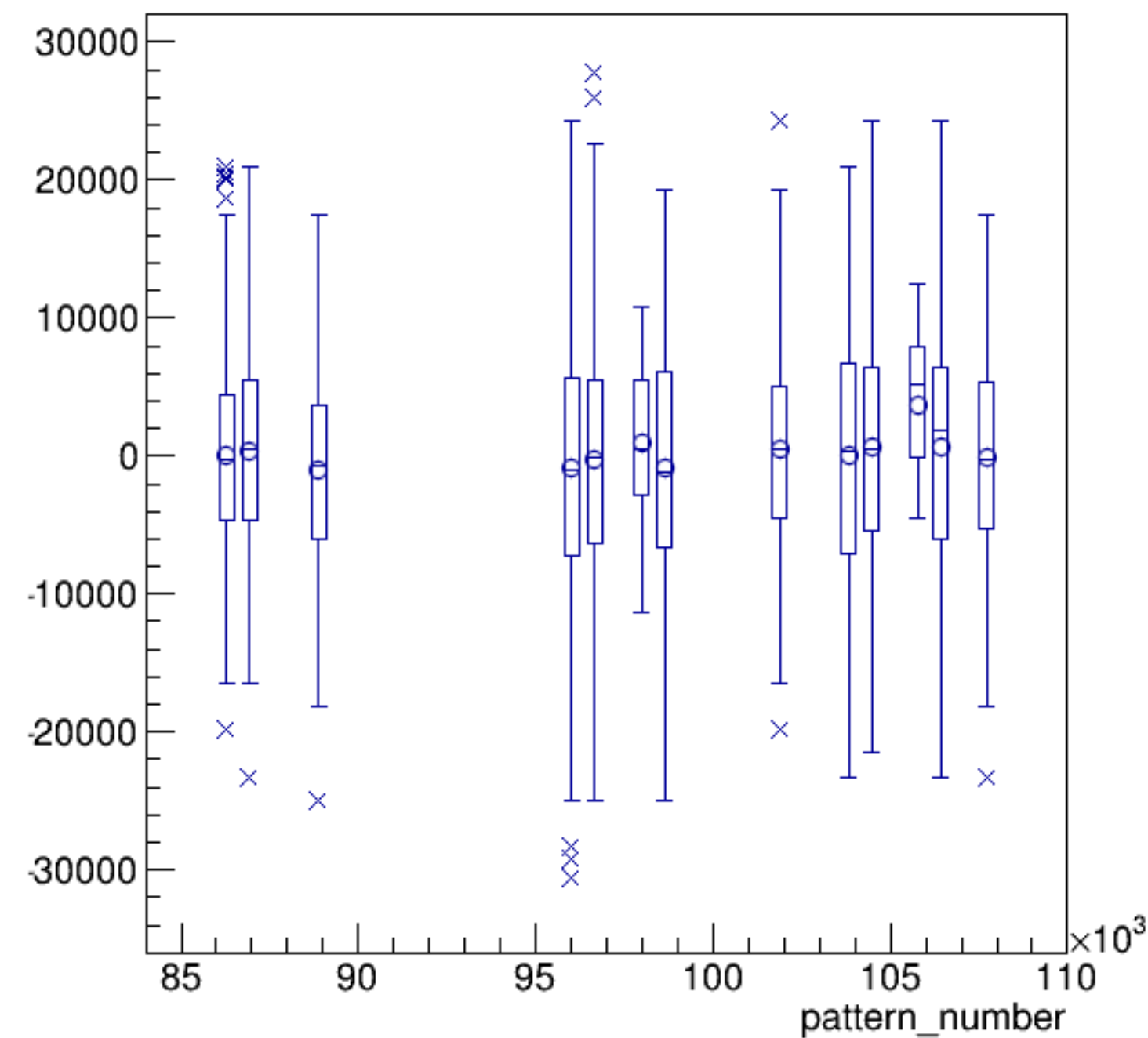
atl2*CurMon:Entry\$



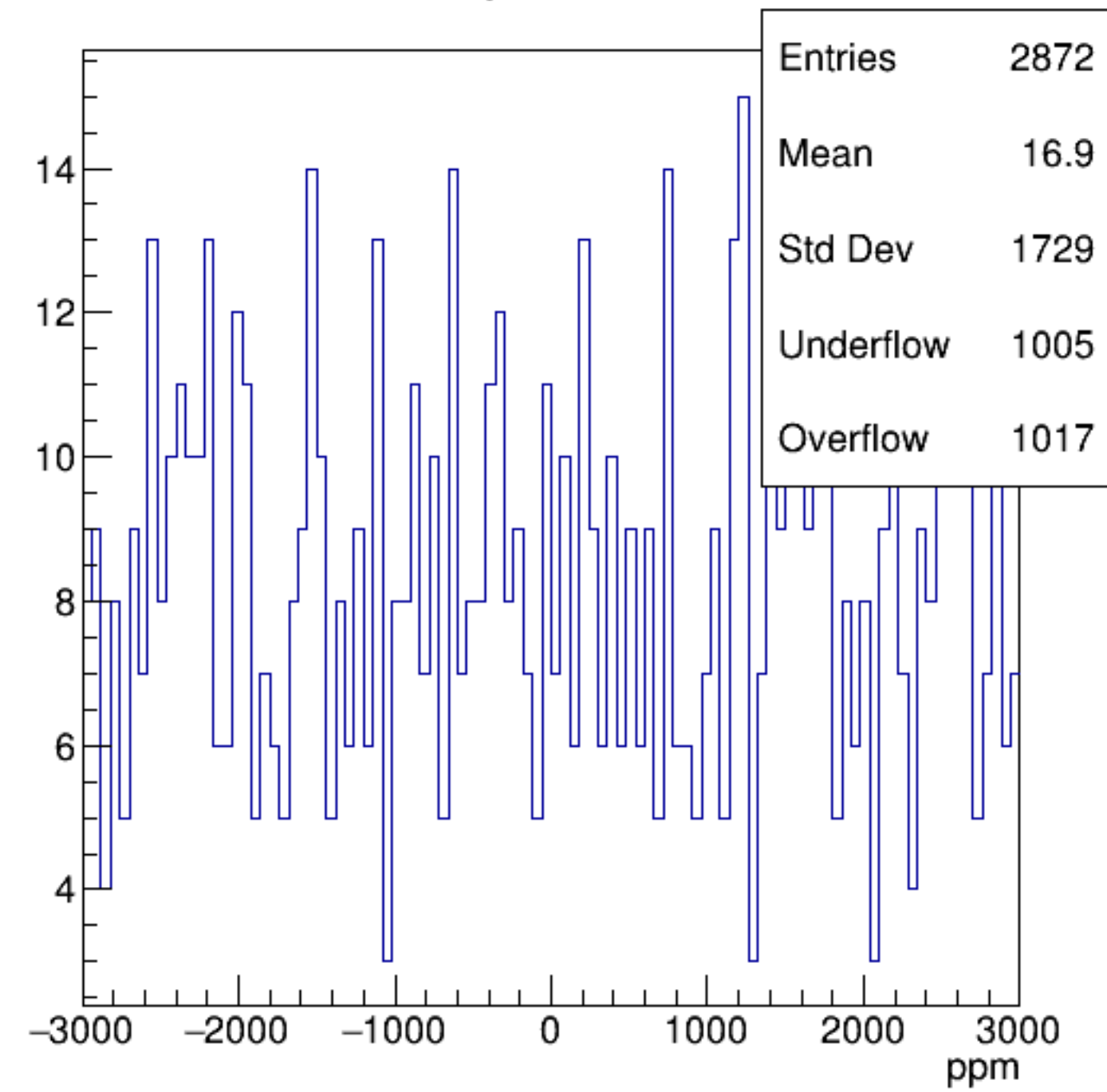
atl2*CurMon {ErrorFlag==0}



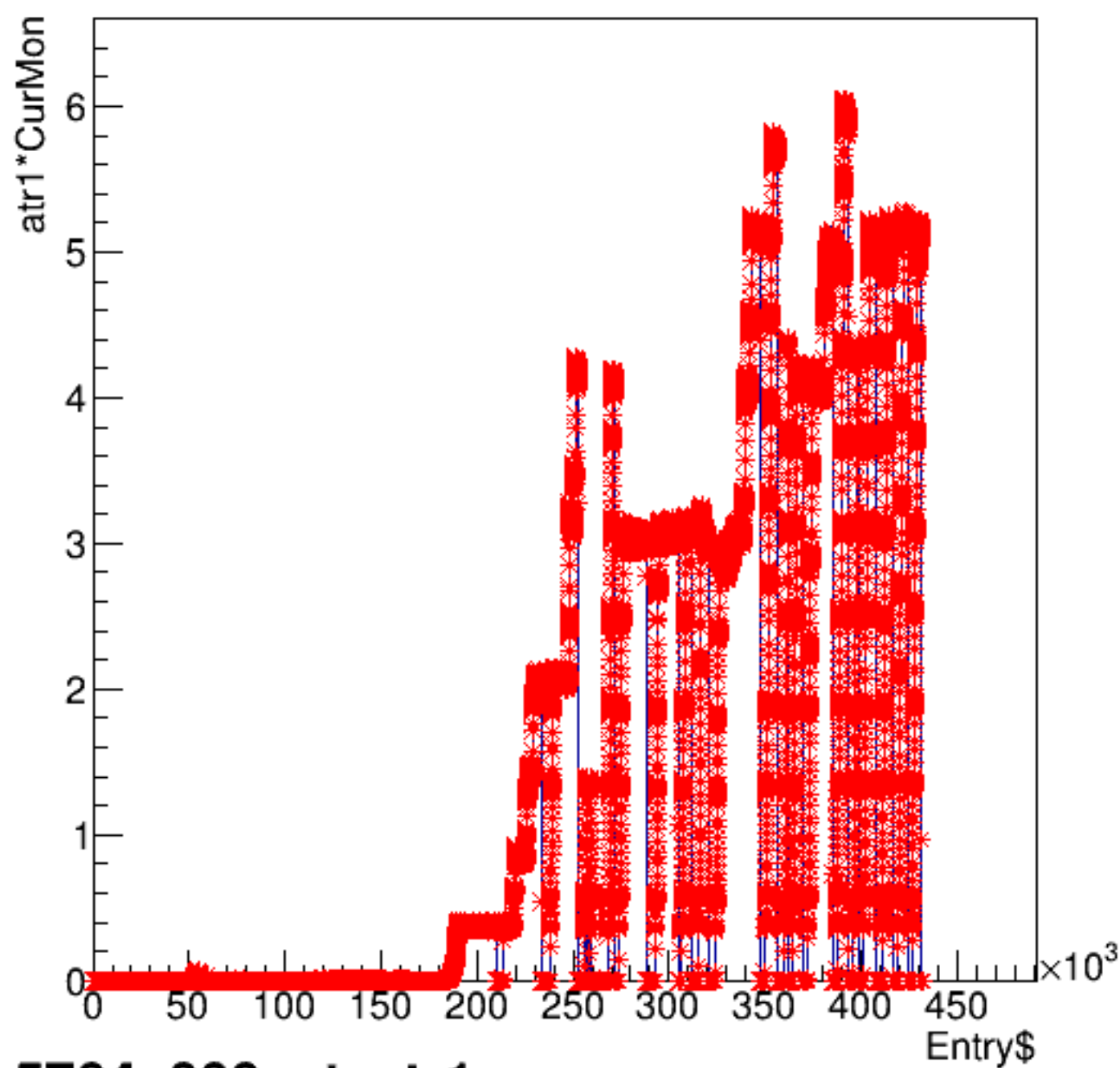
asym_atl2/ppm:pattern_number {ErrorFlag==0}



asym_atl2

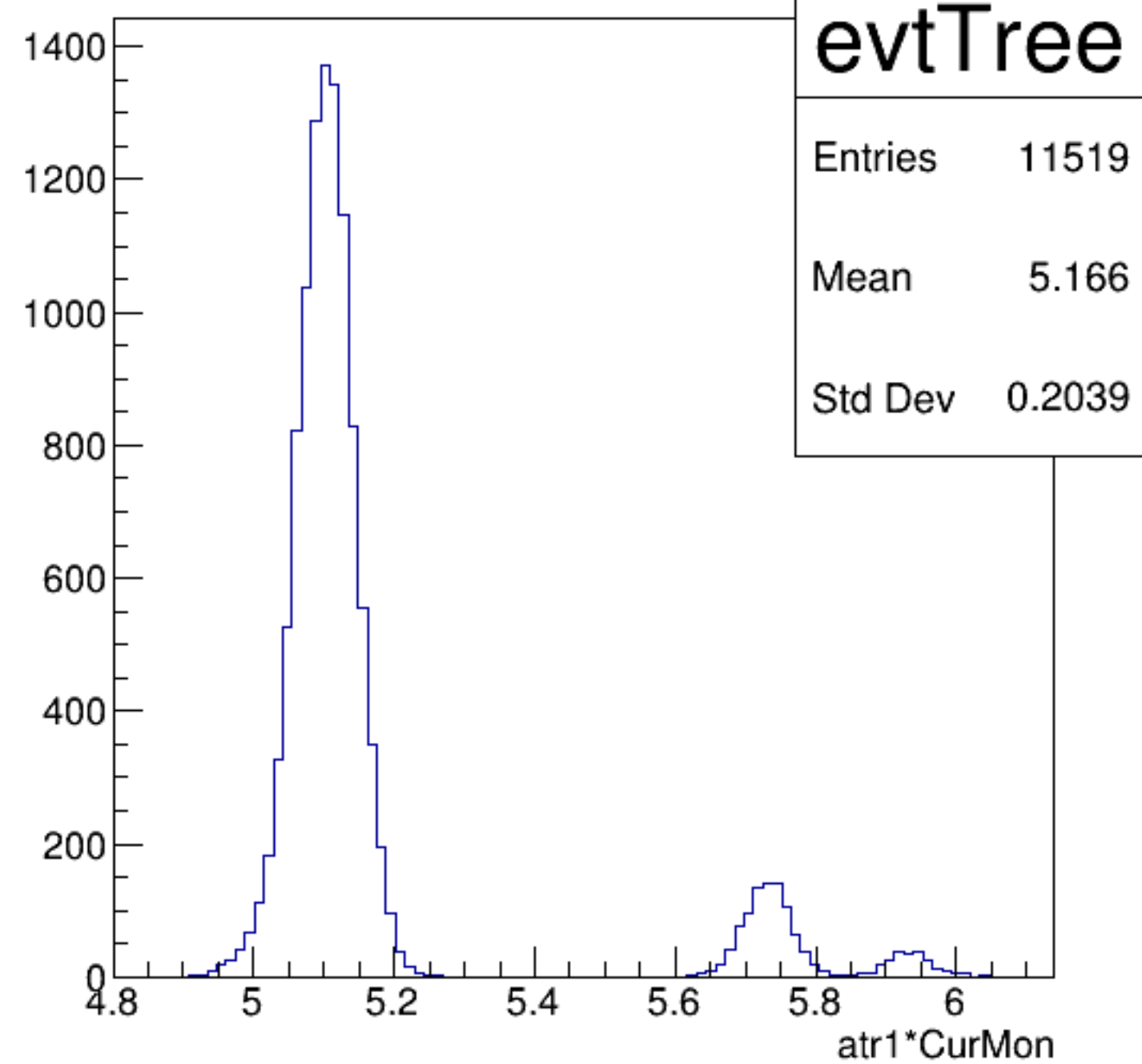


atr1*CurMon:Entry\$



run5764_000_at_atr1.png

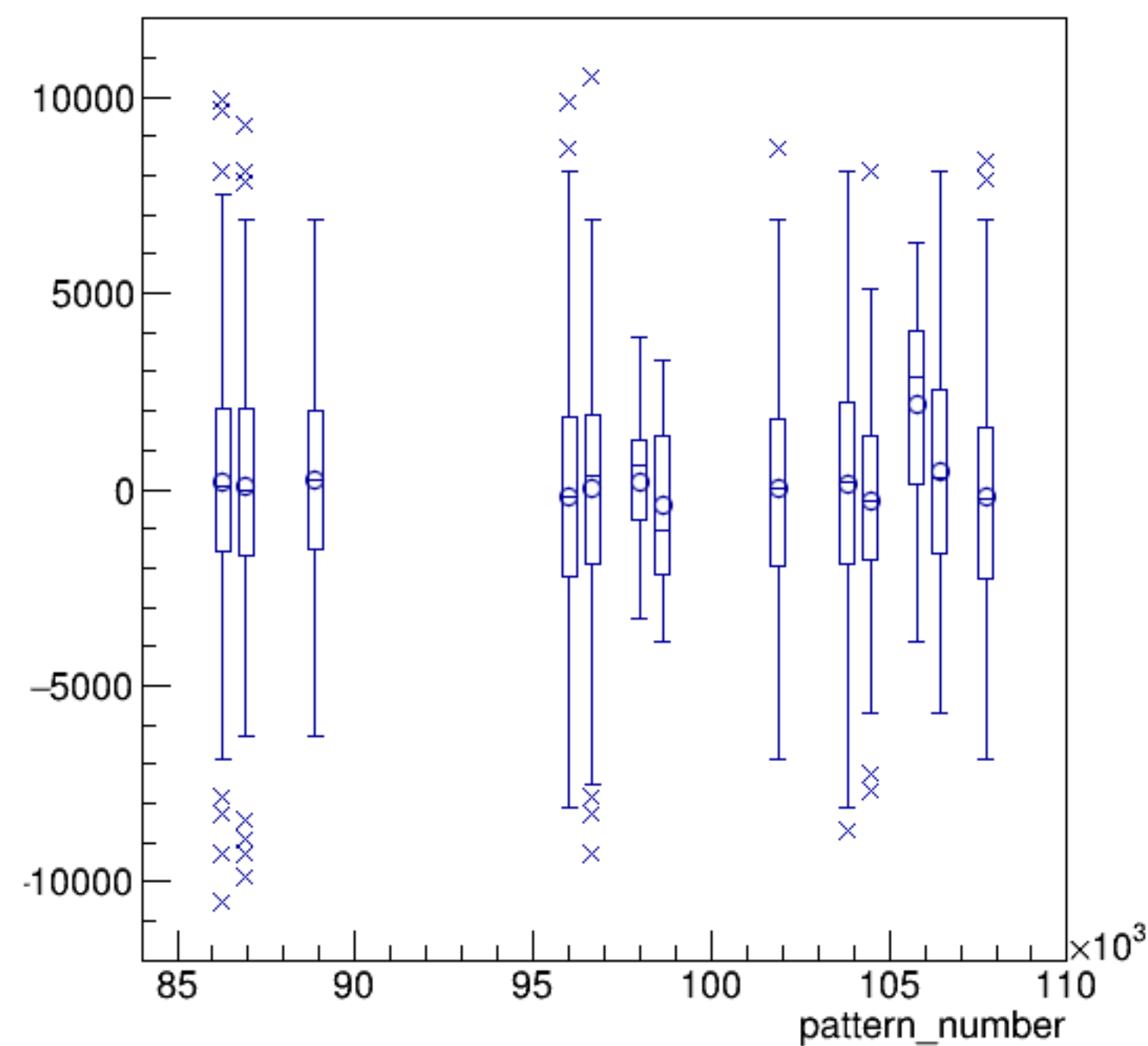
atr1*CurMon {ErrorFlag==0}



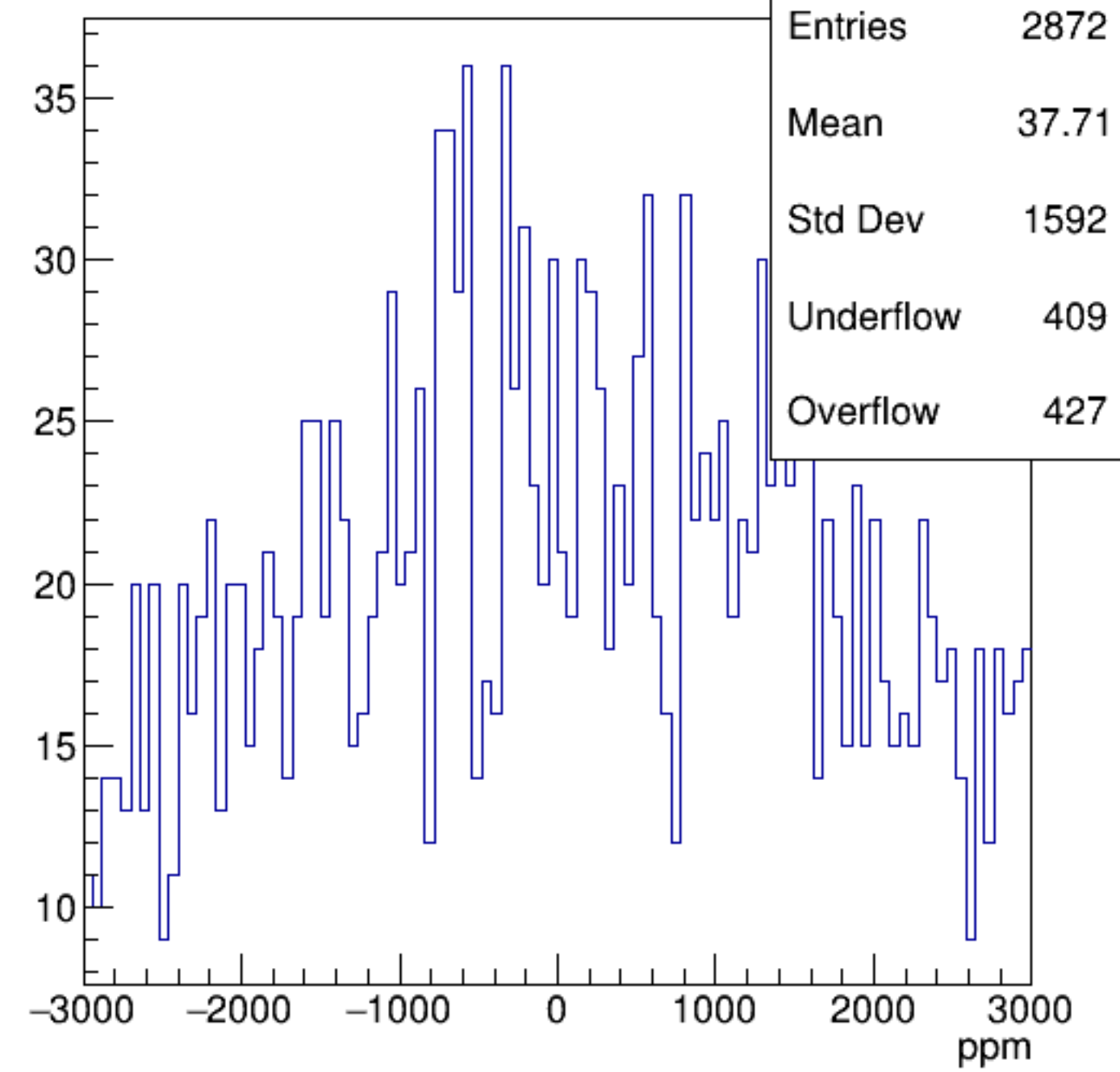
evtTree

Entries	11519
Mean	5.166
Std Dev	0.2039

asym_atr1/ppm:pattern_number {ErrorFlag==0}

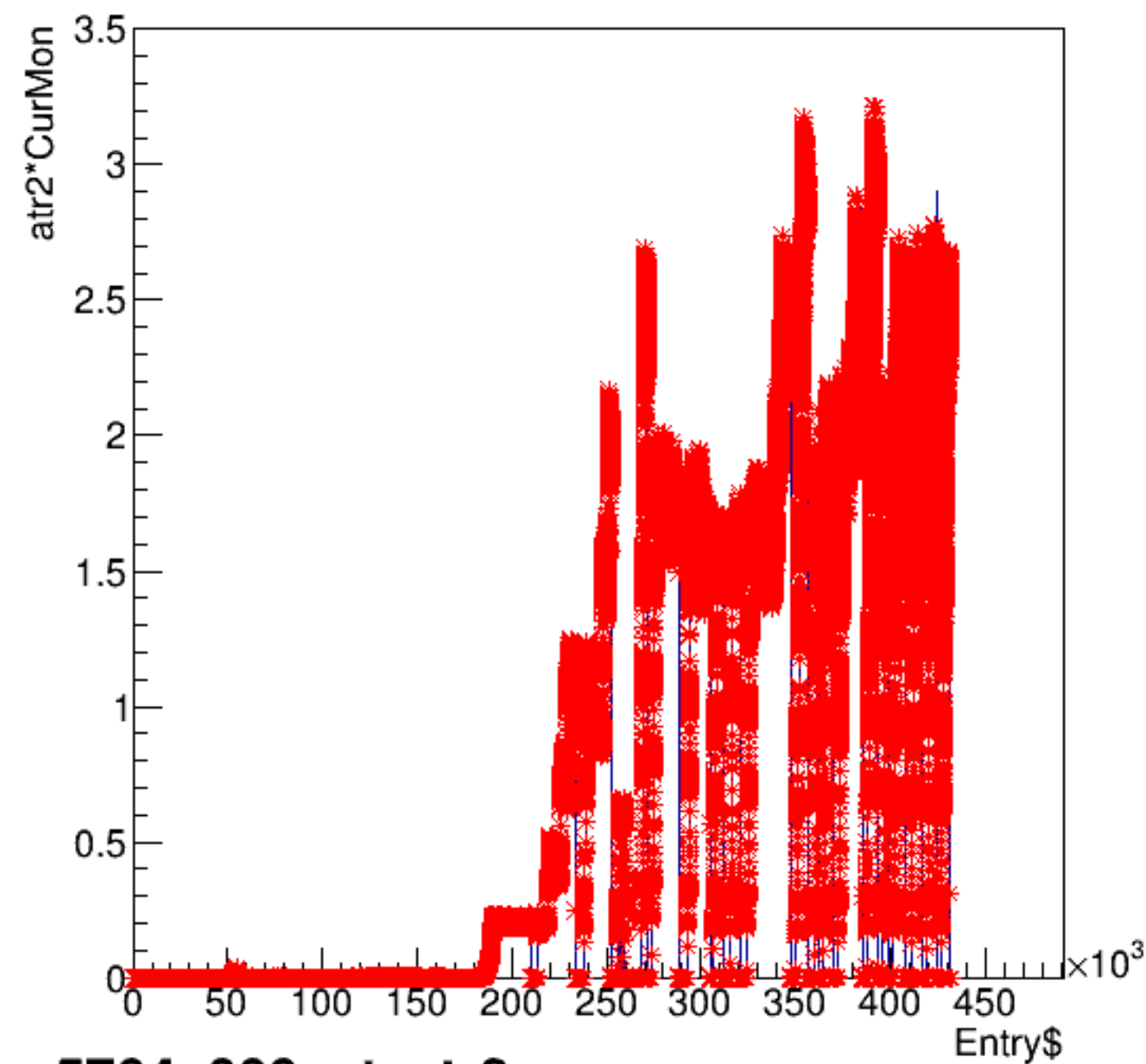


asym_atr1



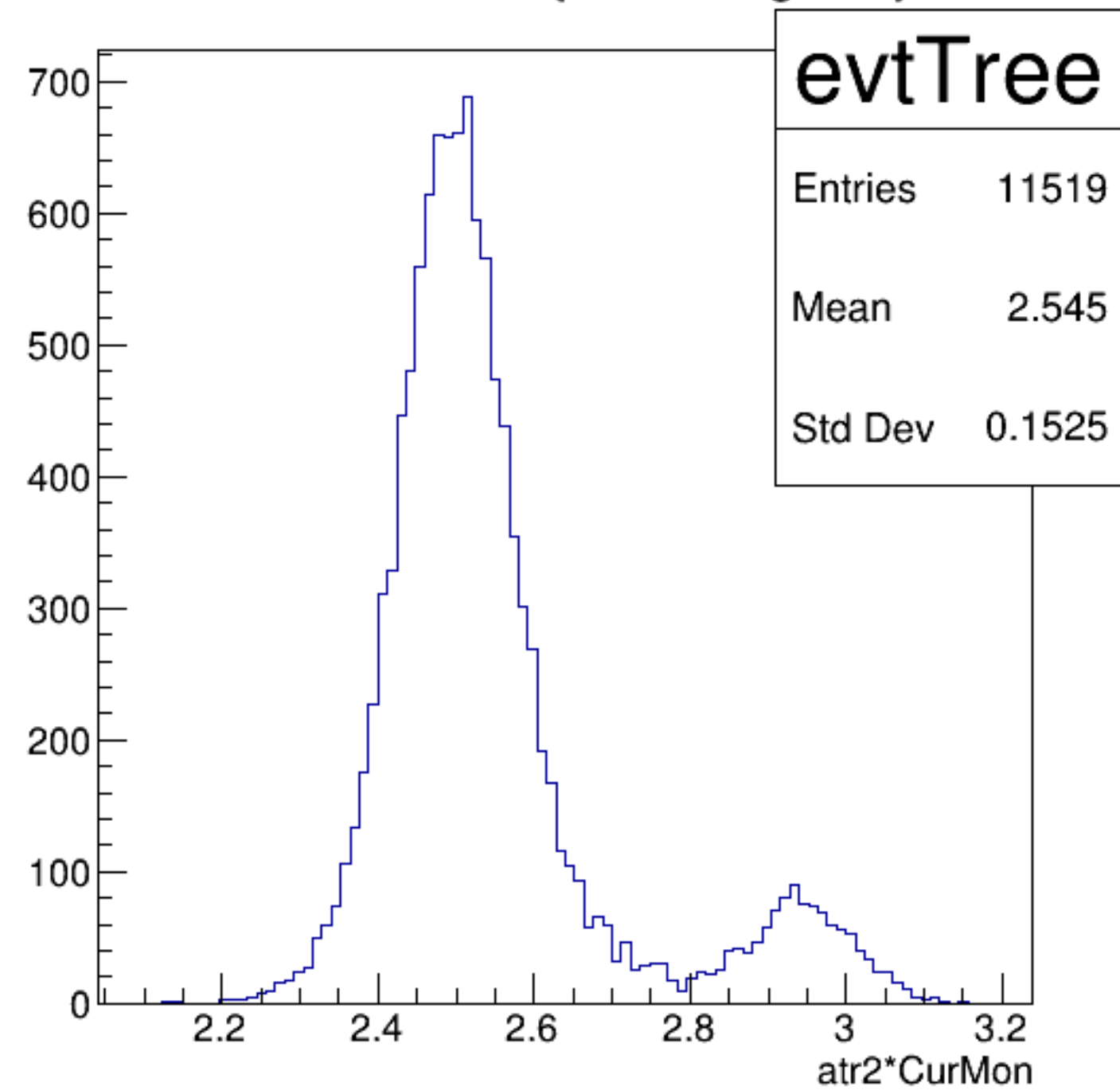
Entries	2872
Mean	37.71
Std Dev	1592
Underflow	409
Overflow	427

atr2*CurMon:Entry\$

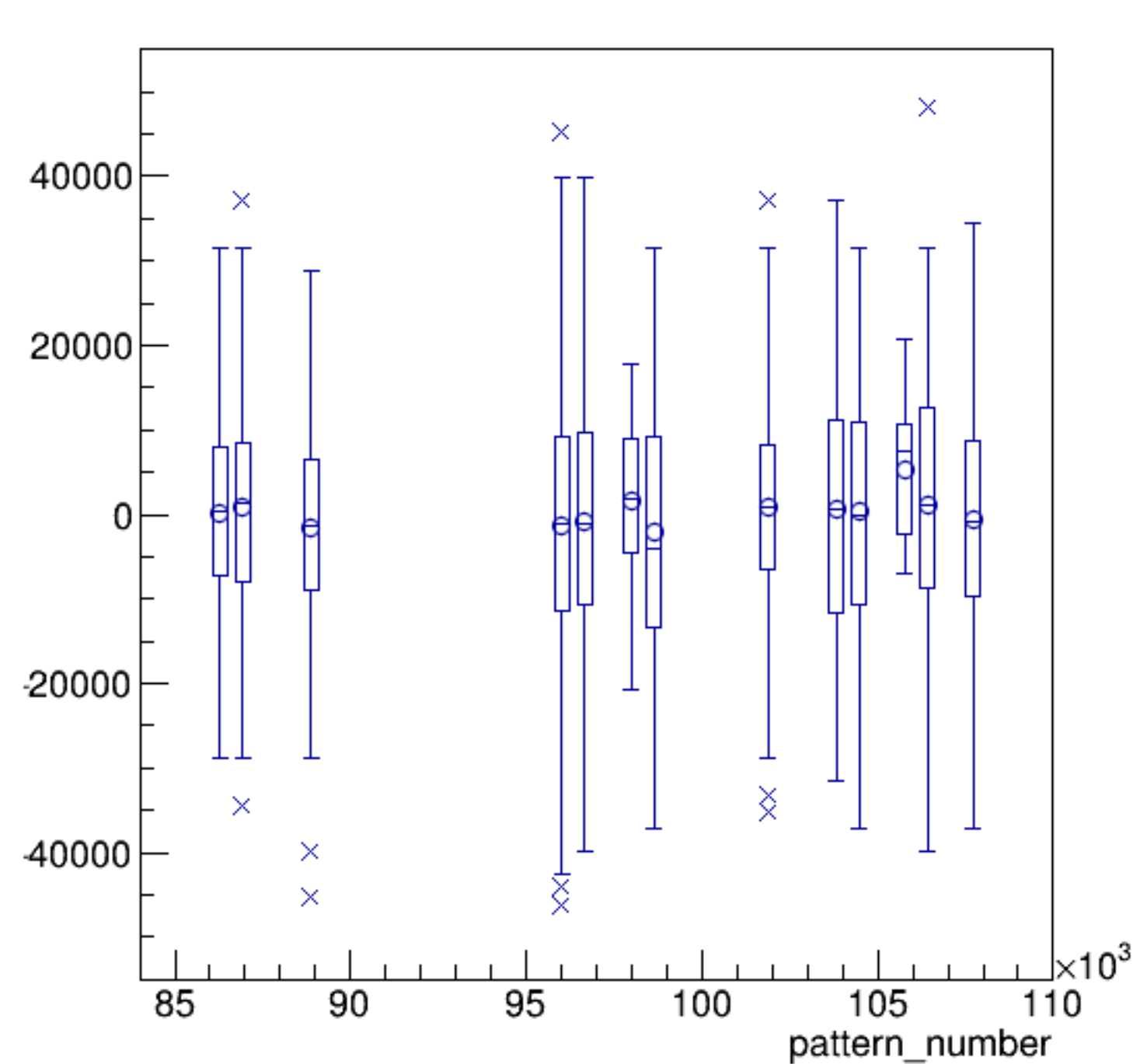


run5764_000_at_atr2.png

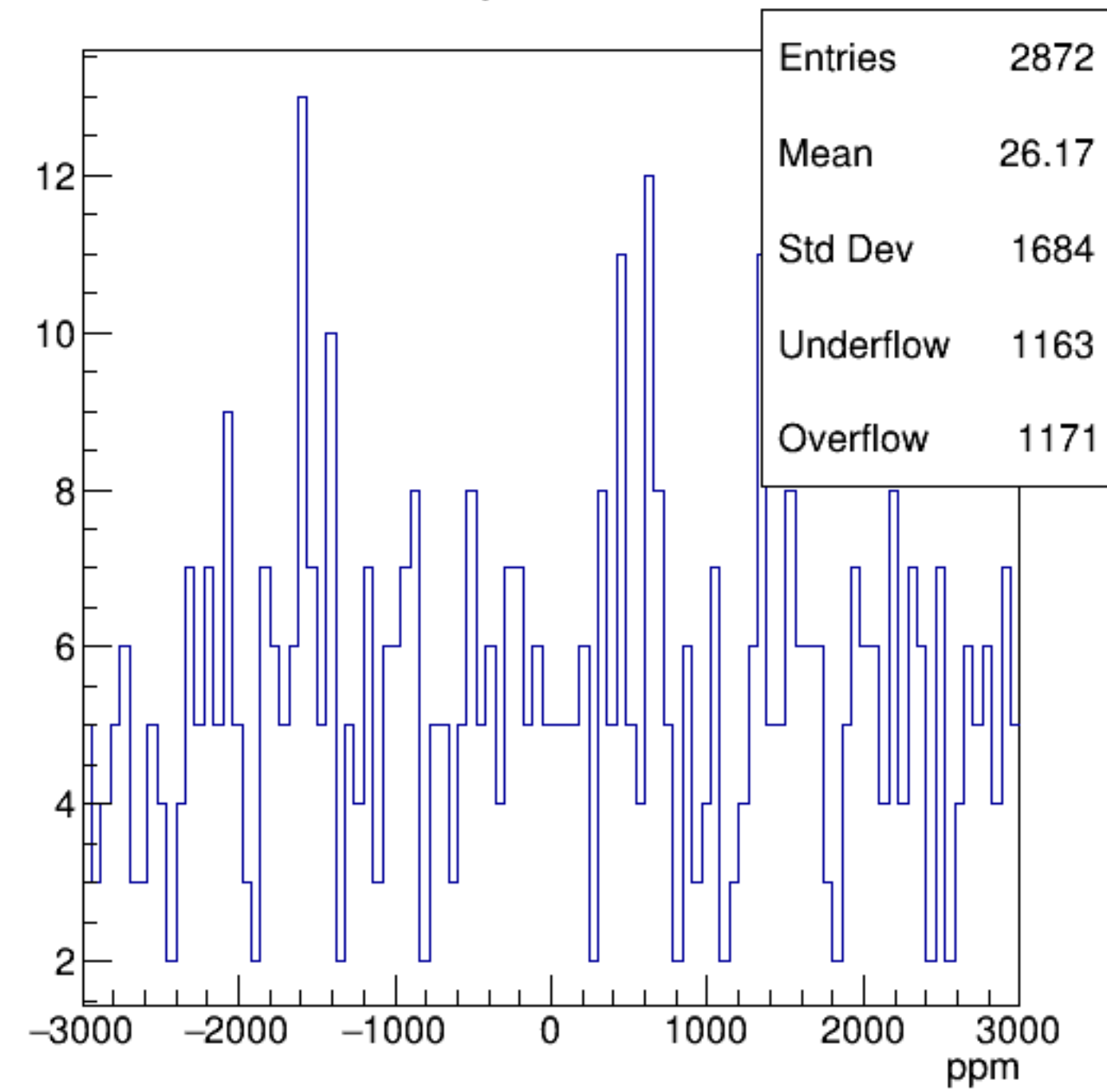
atr2*CurMon {ErrorFlag==0}



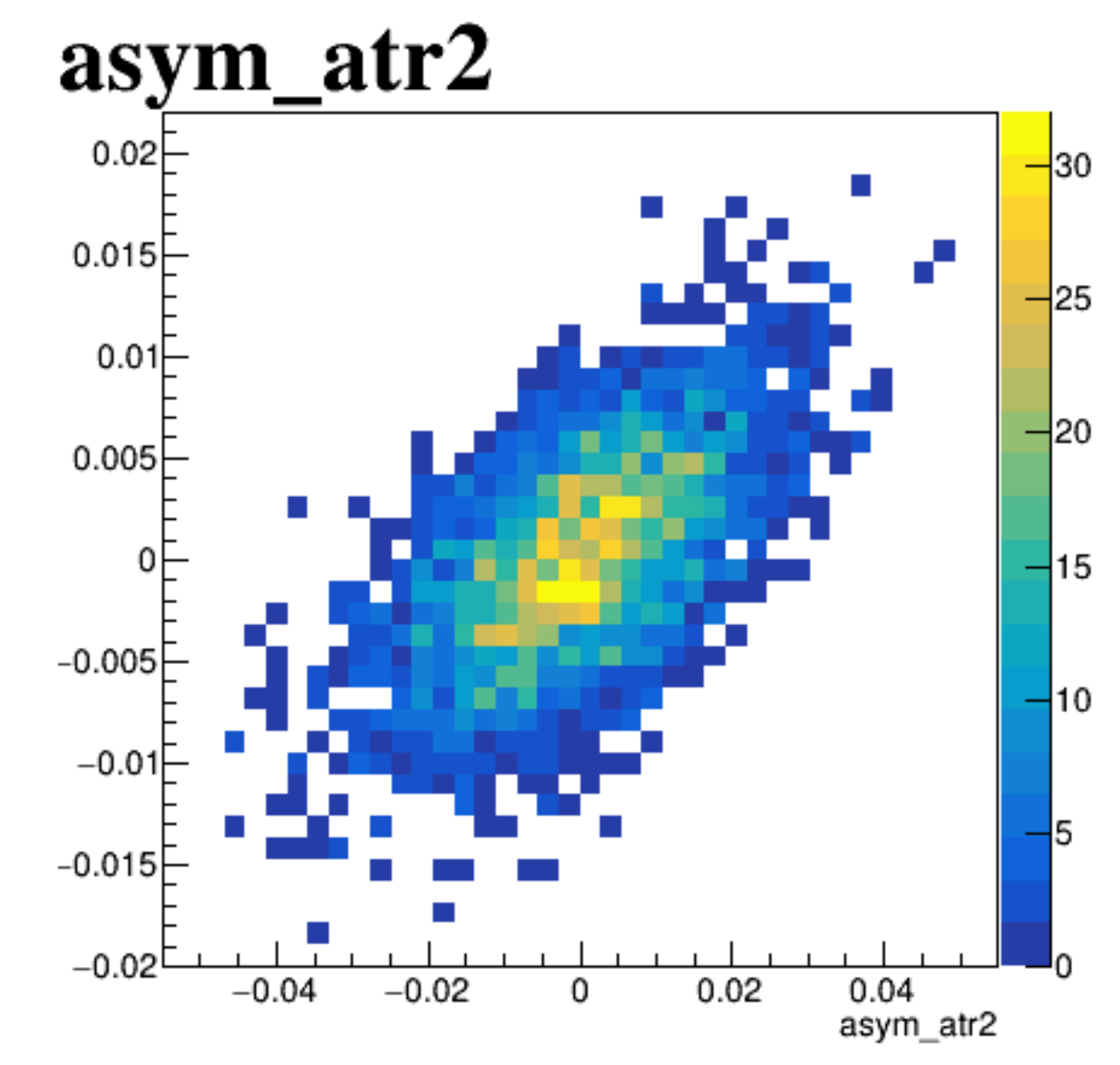
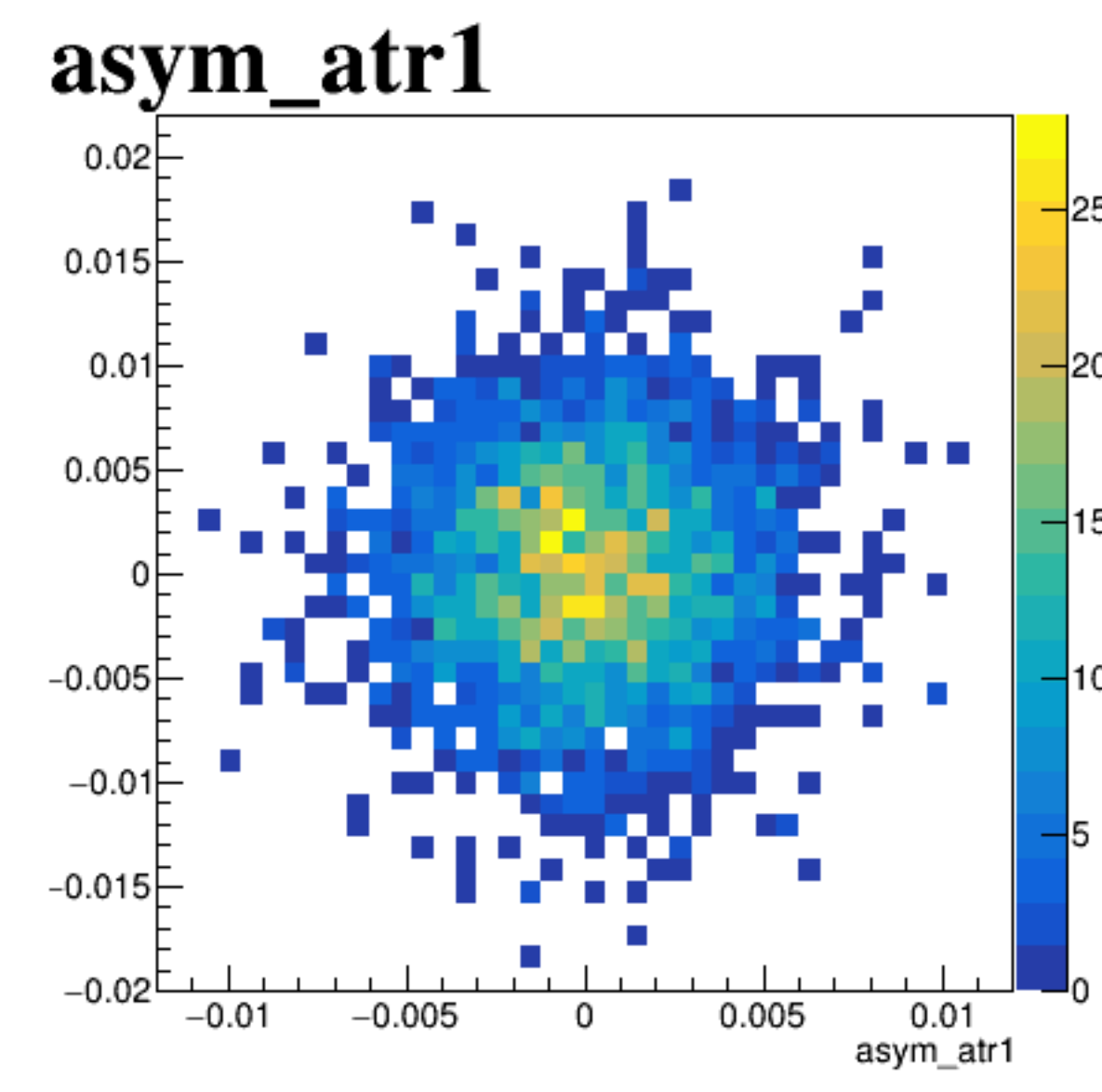
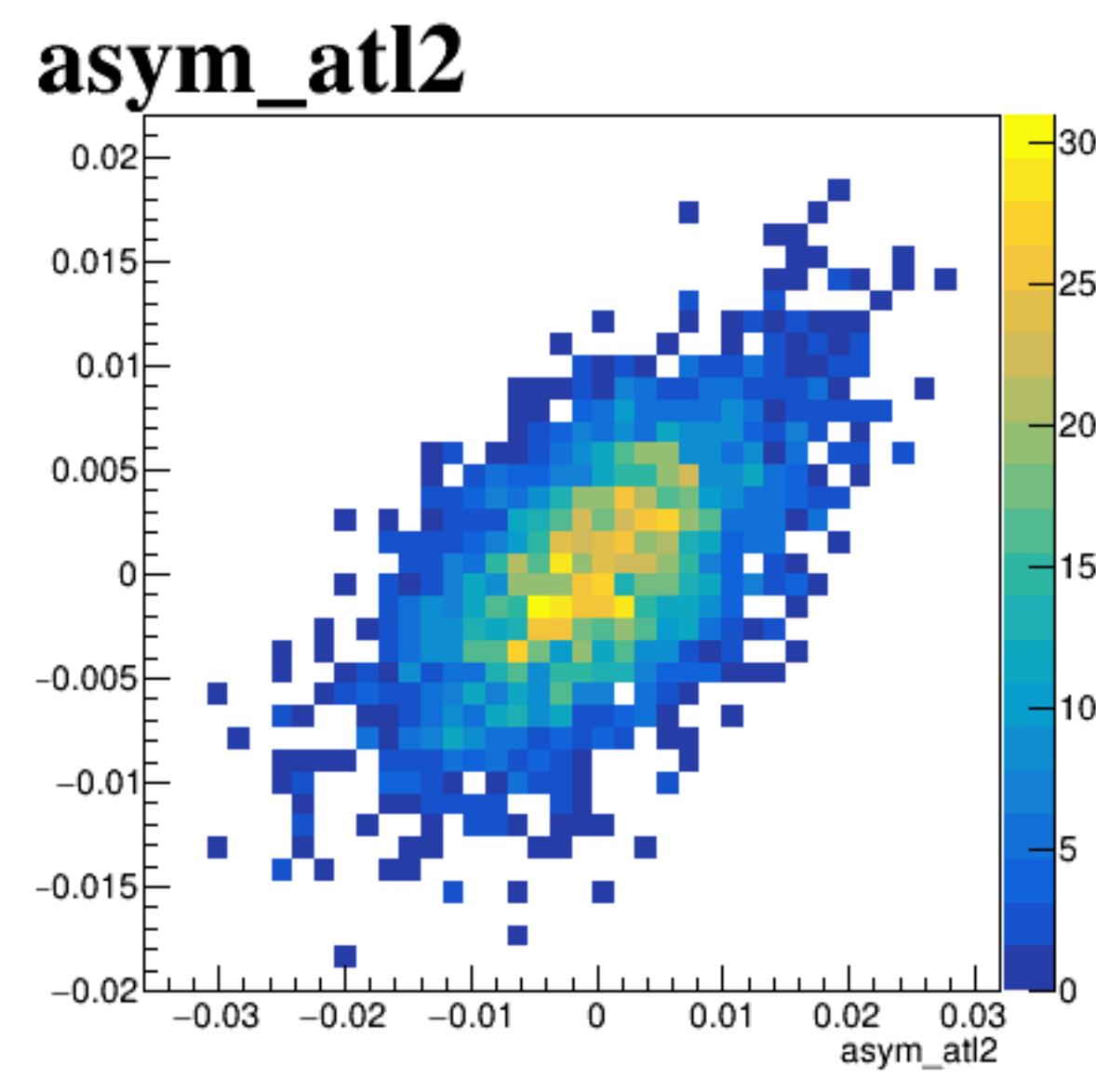
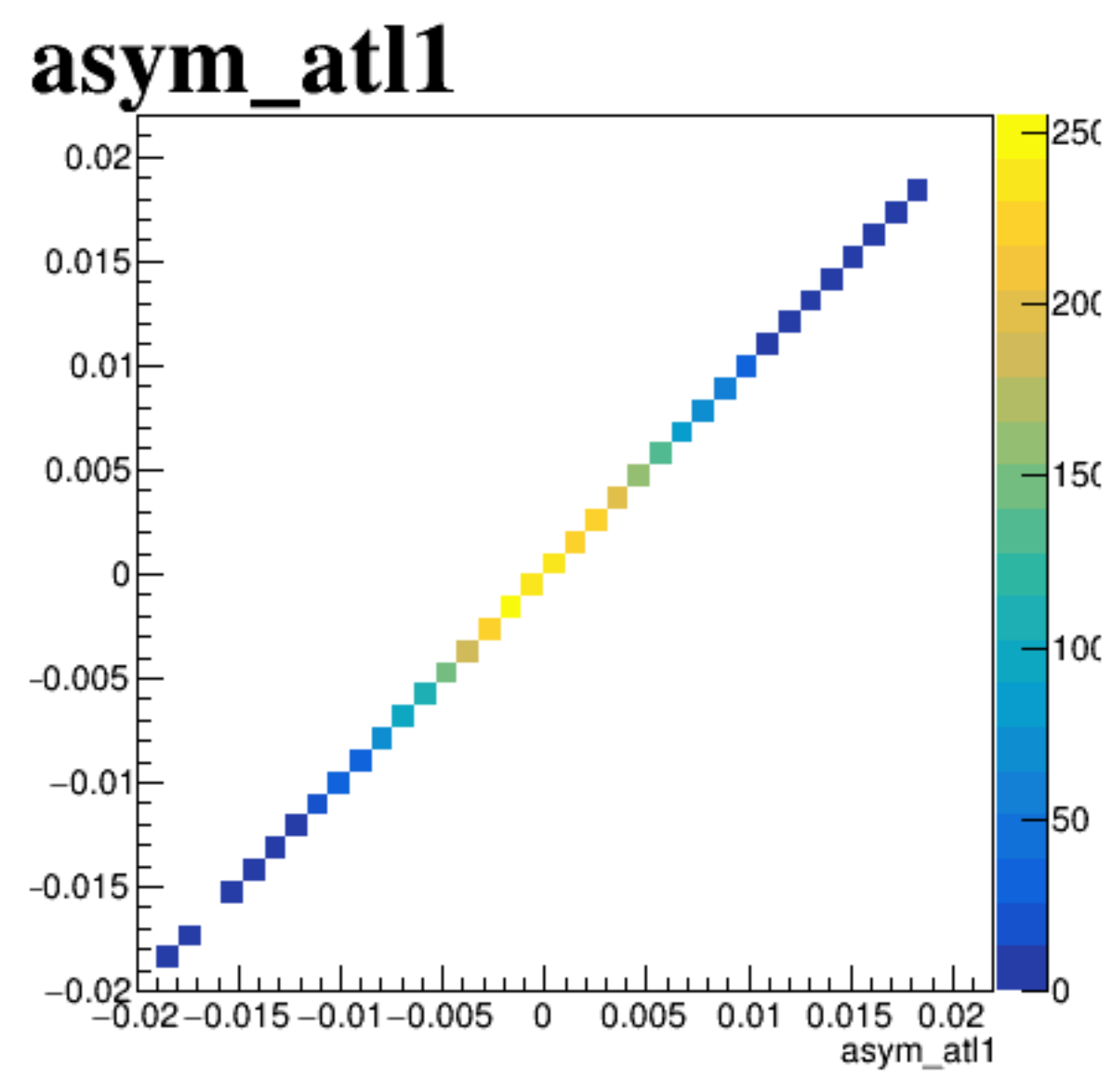
asym_atr2/ppm:pattern_number {ErrorFlag==0}



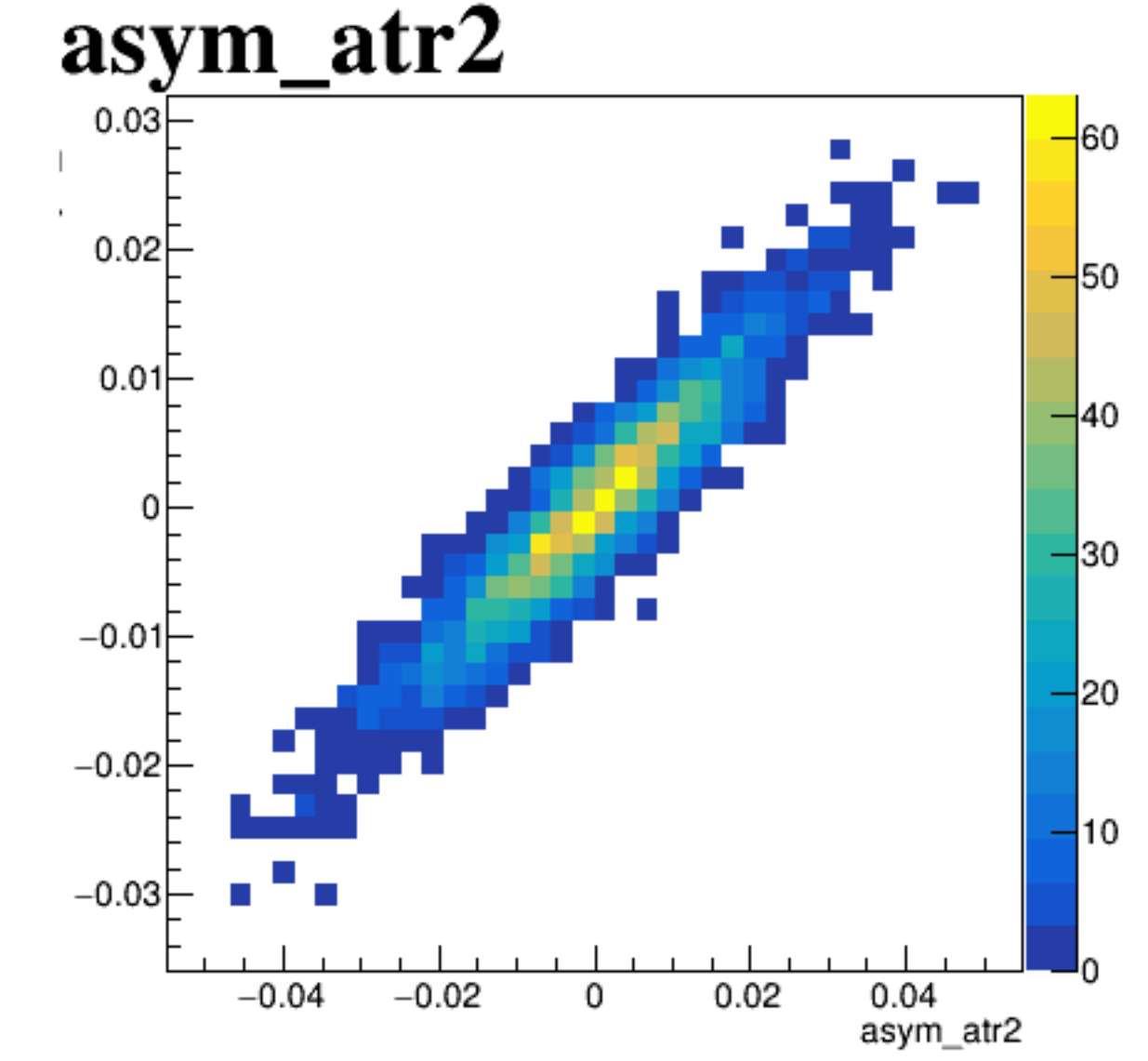
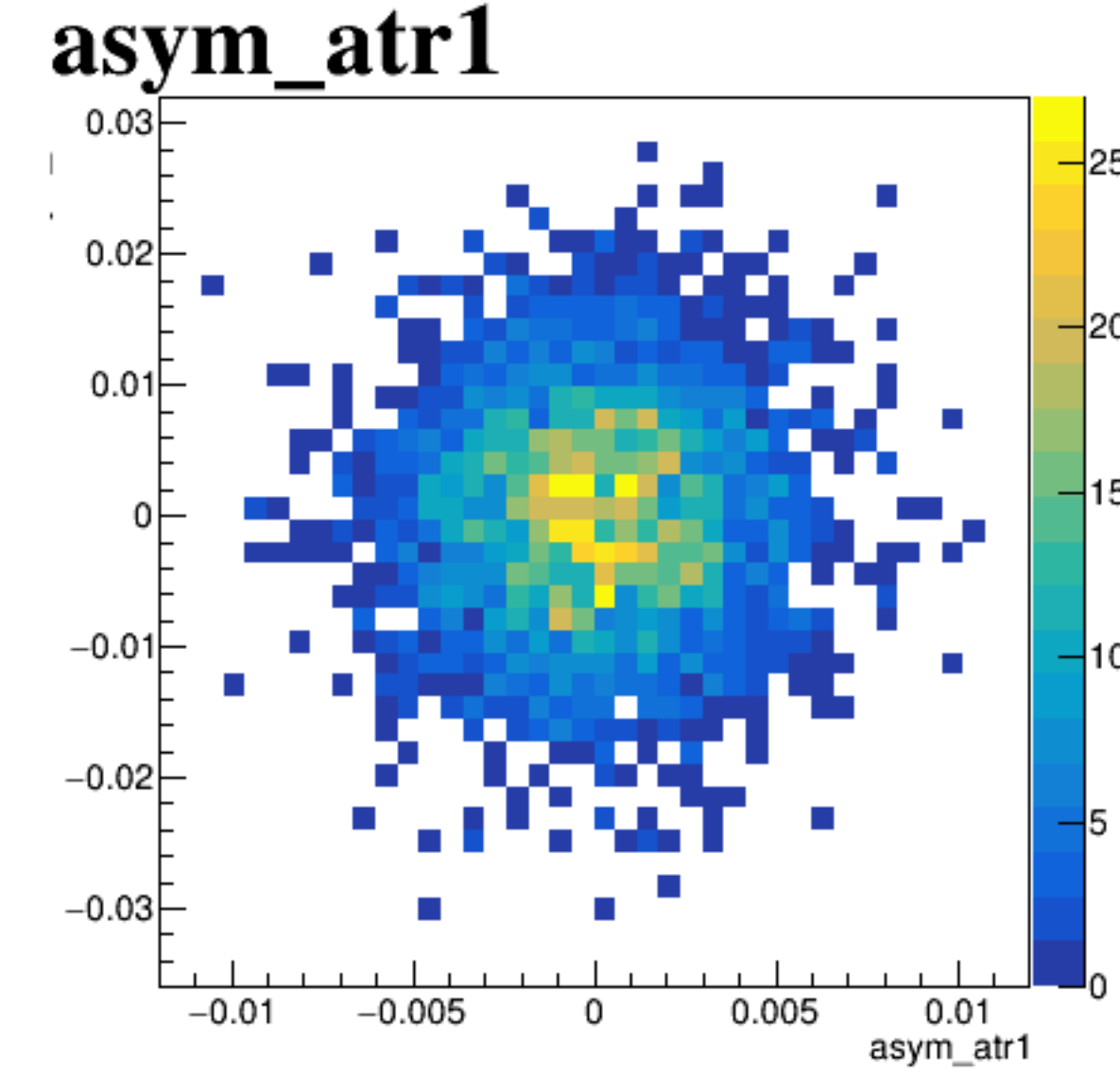
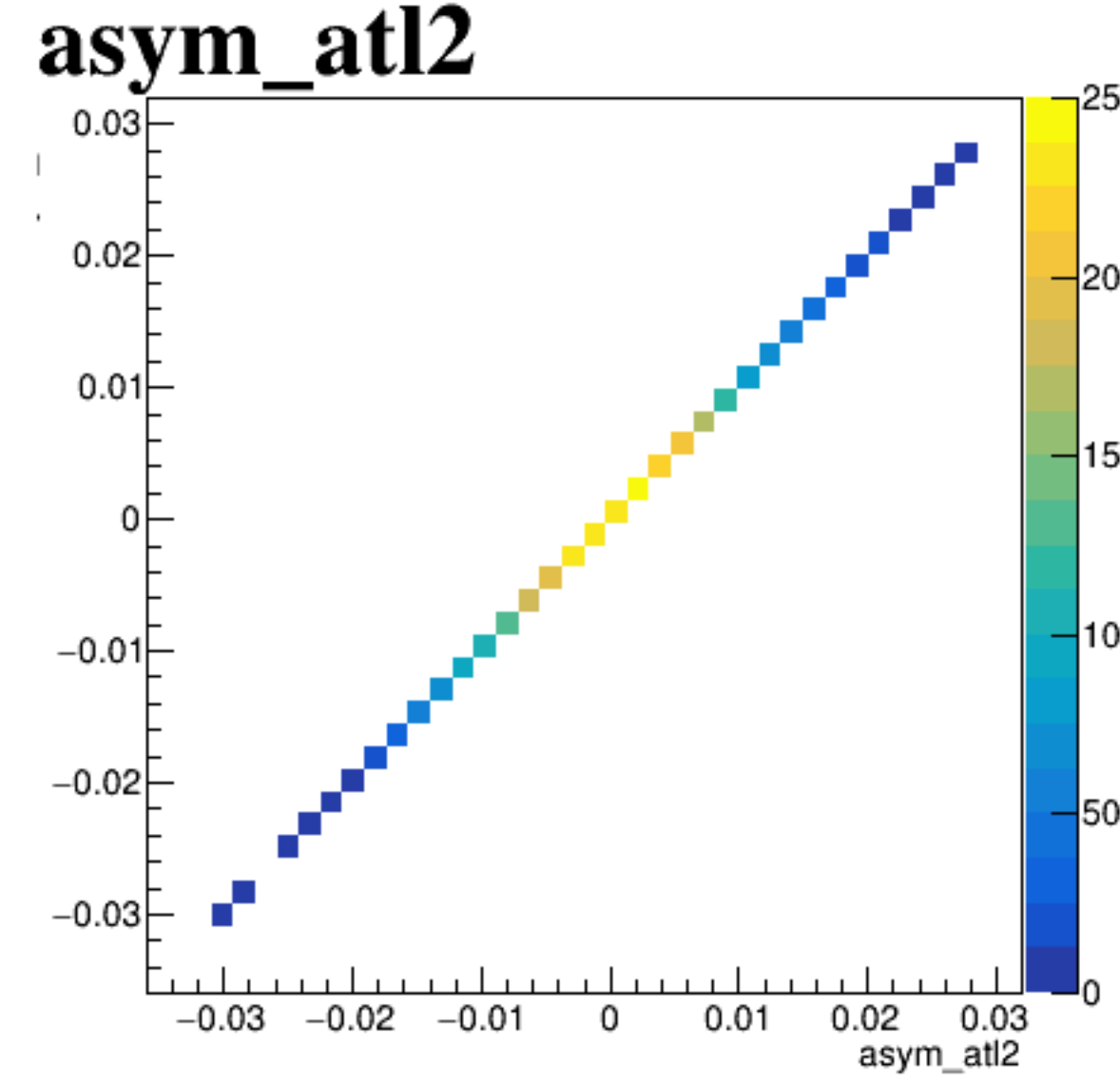
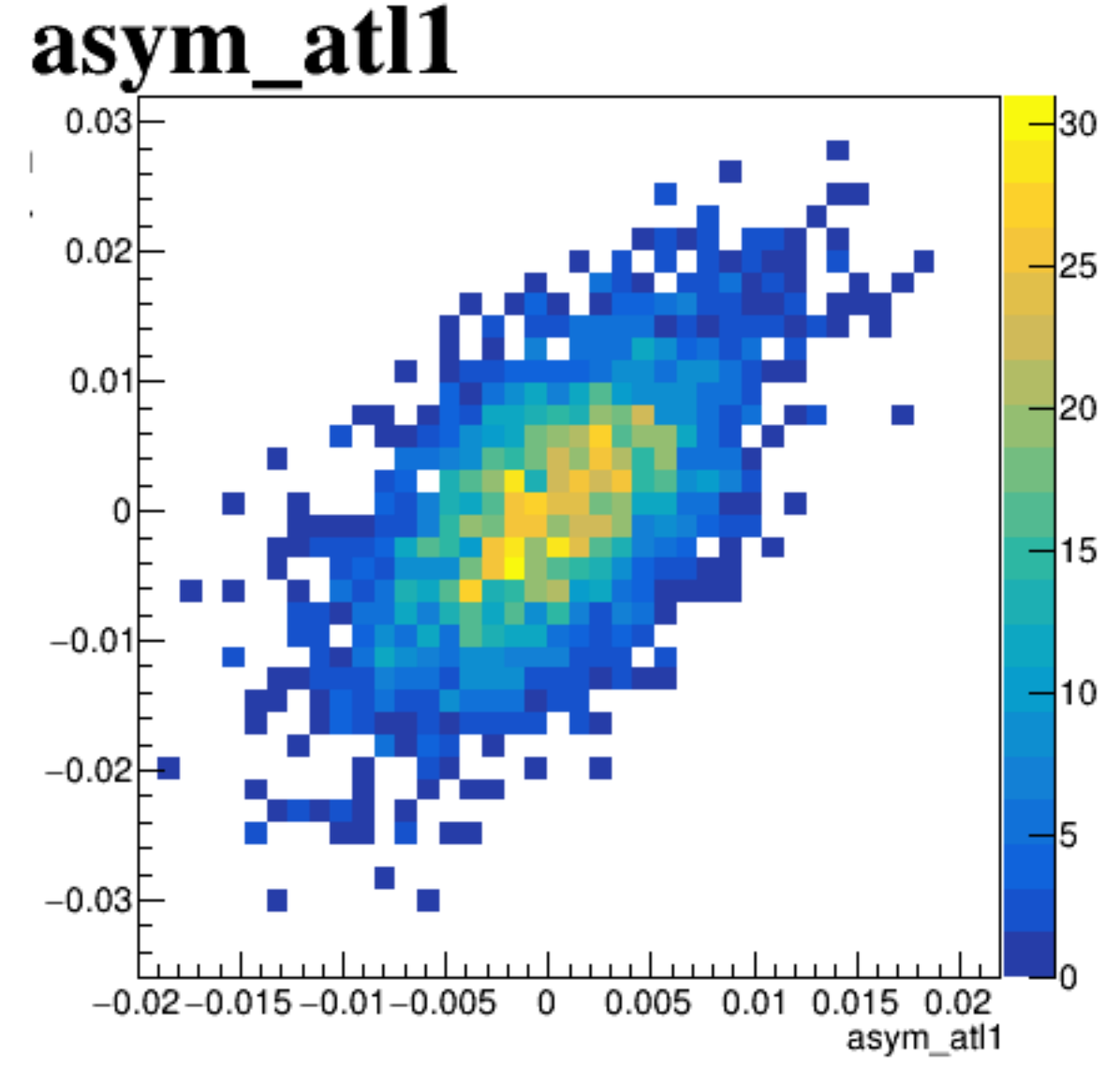
asym_atr2



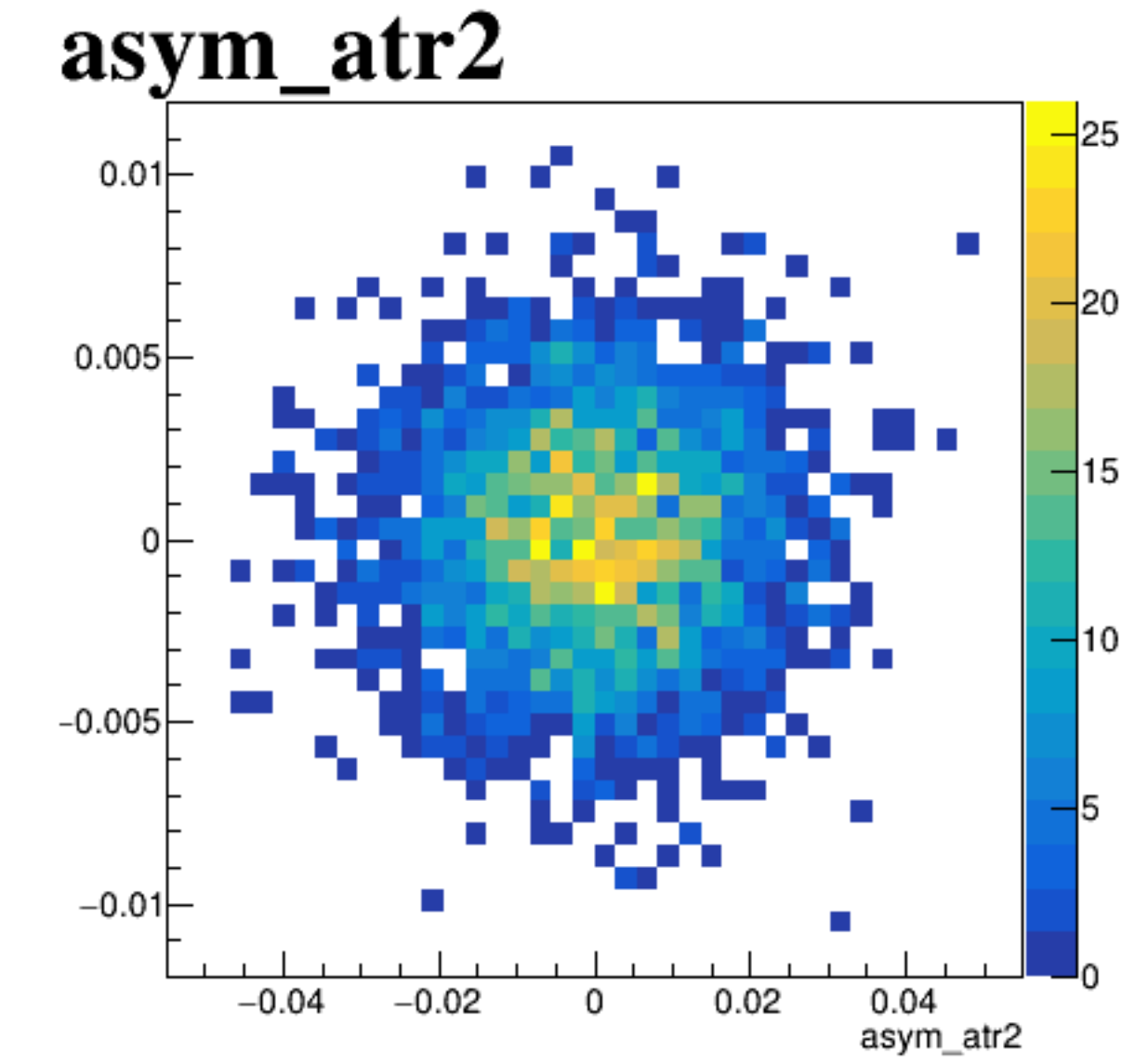
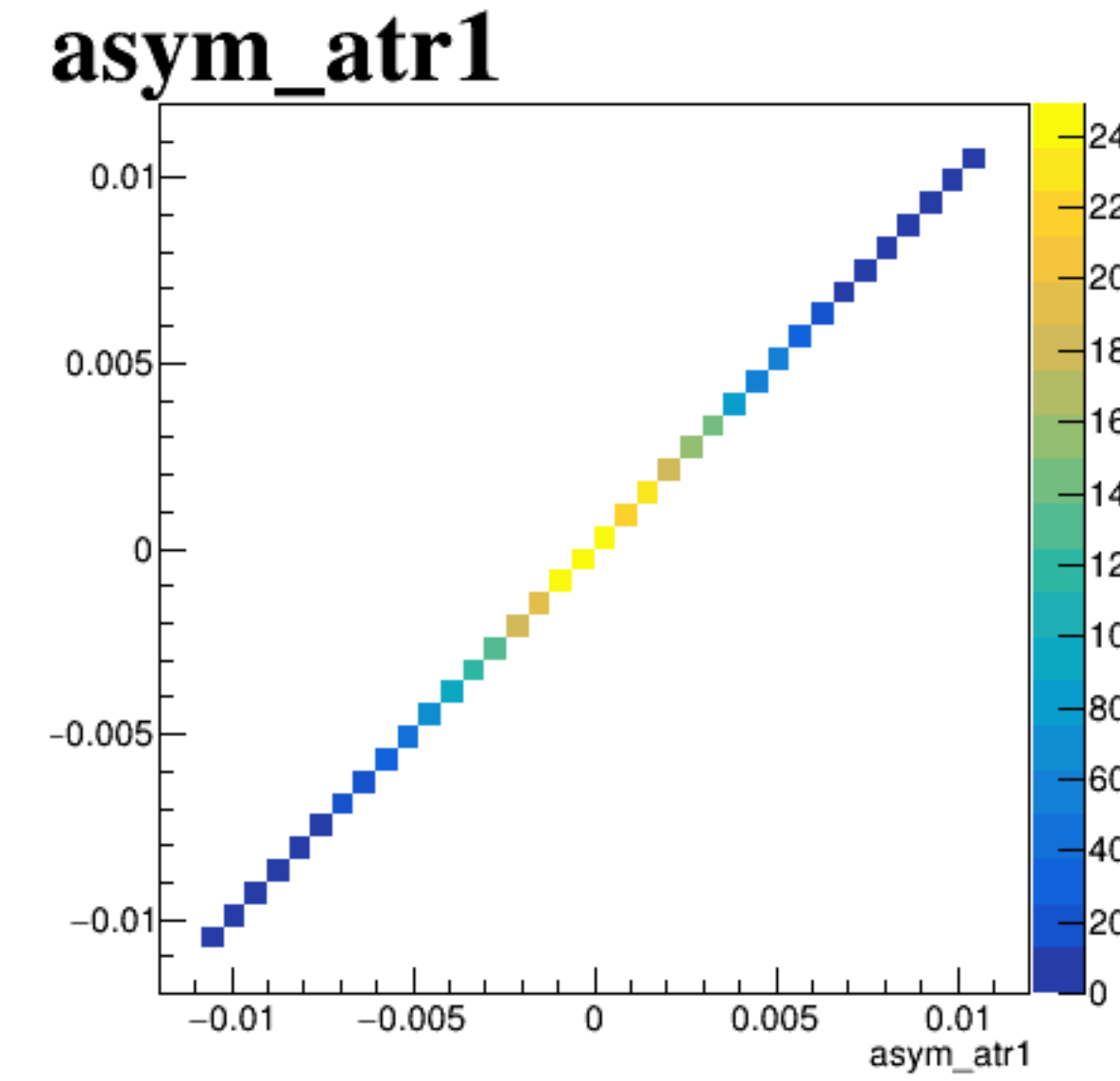
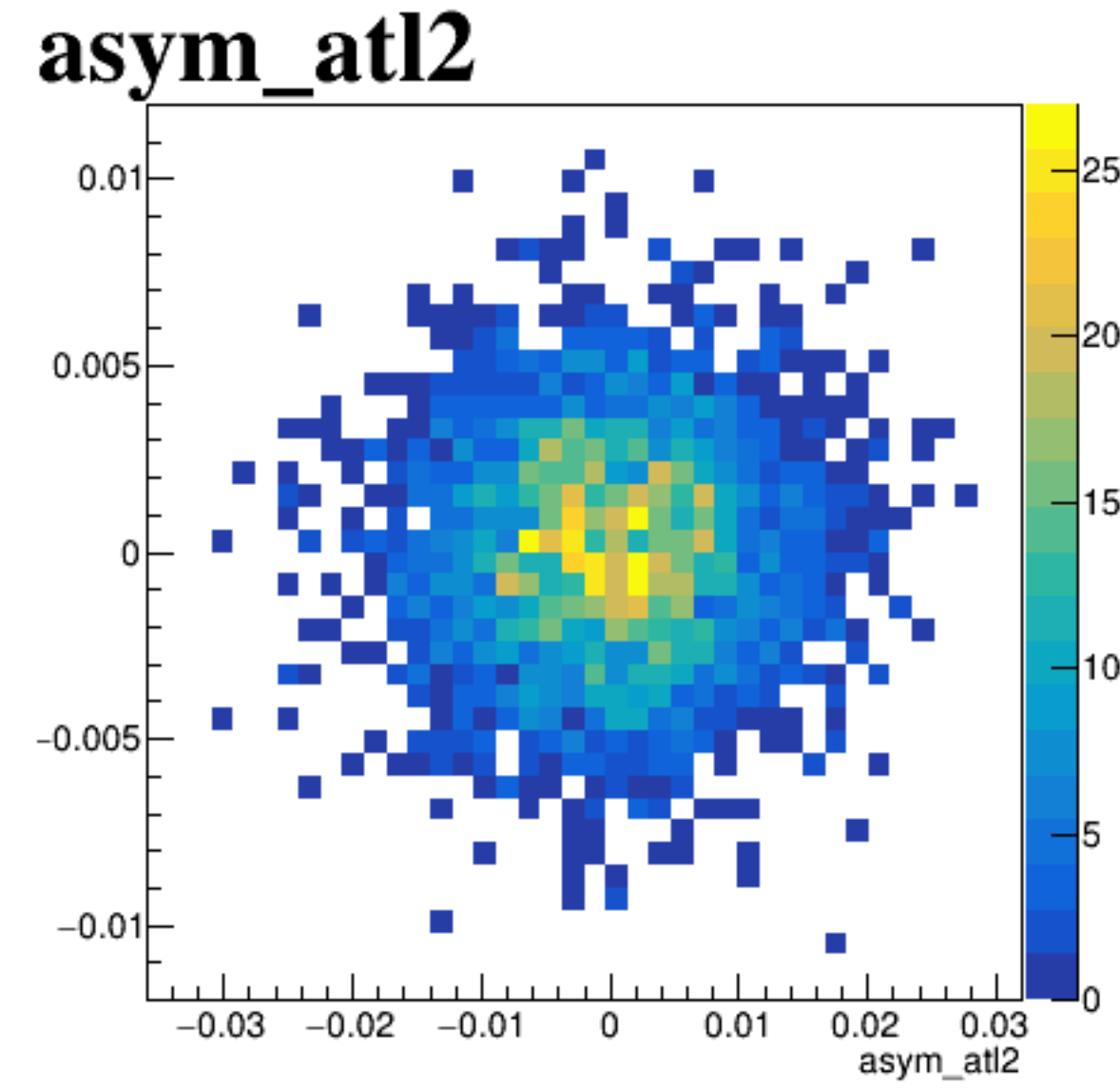
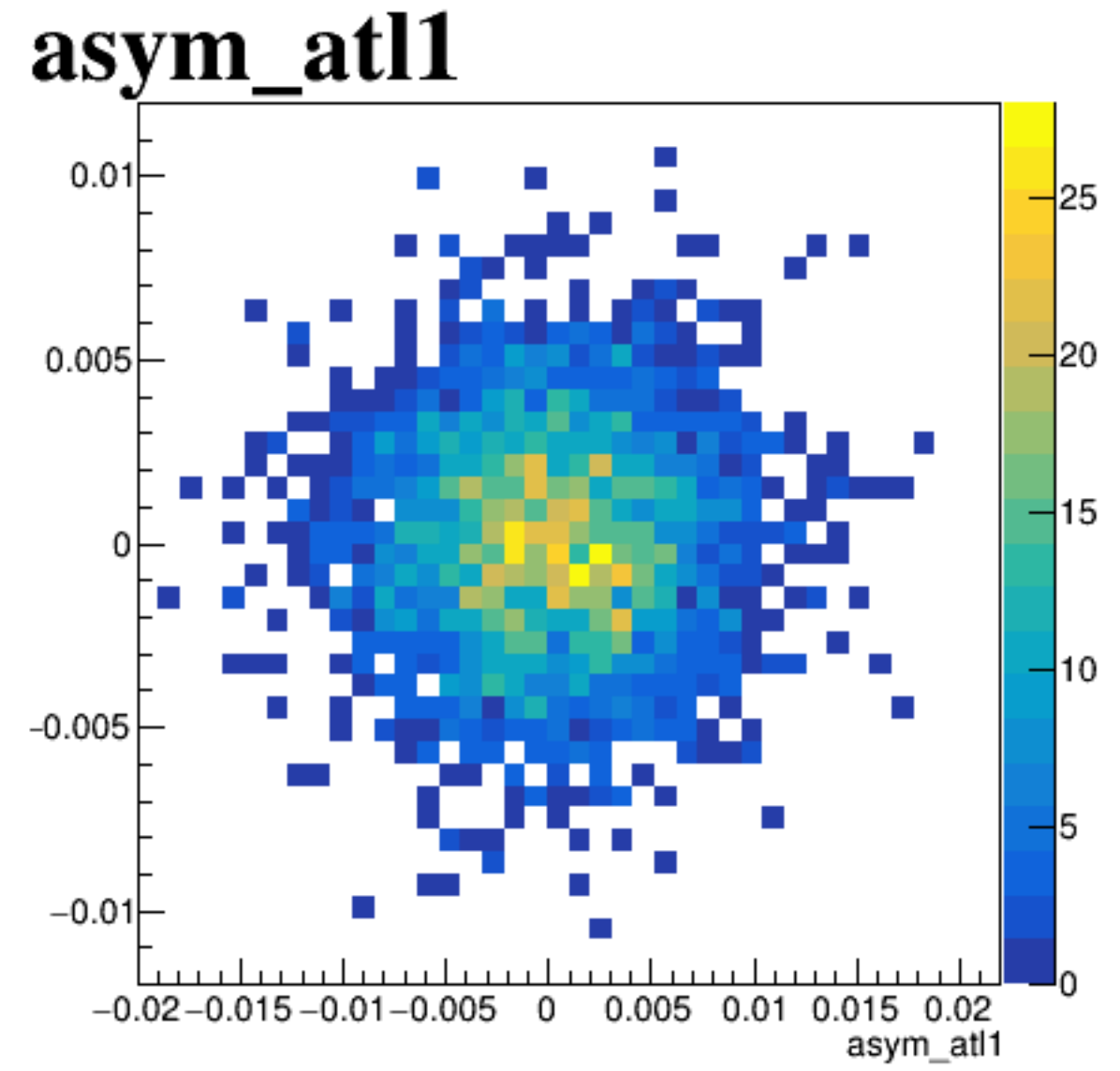
asym_atl1



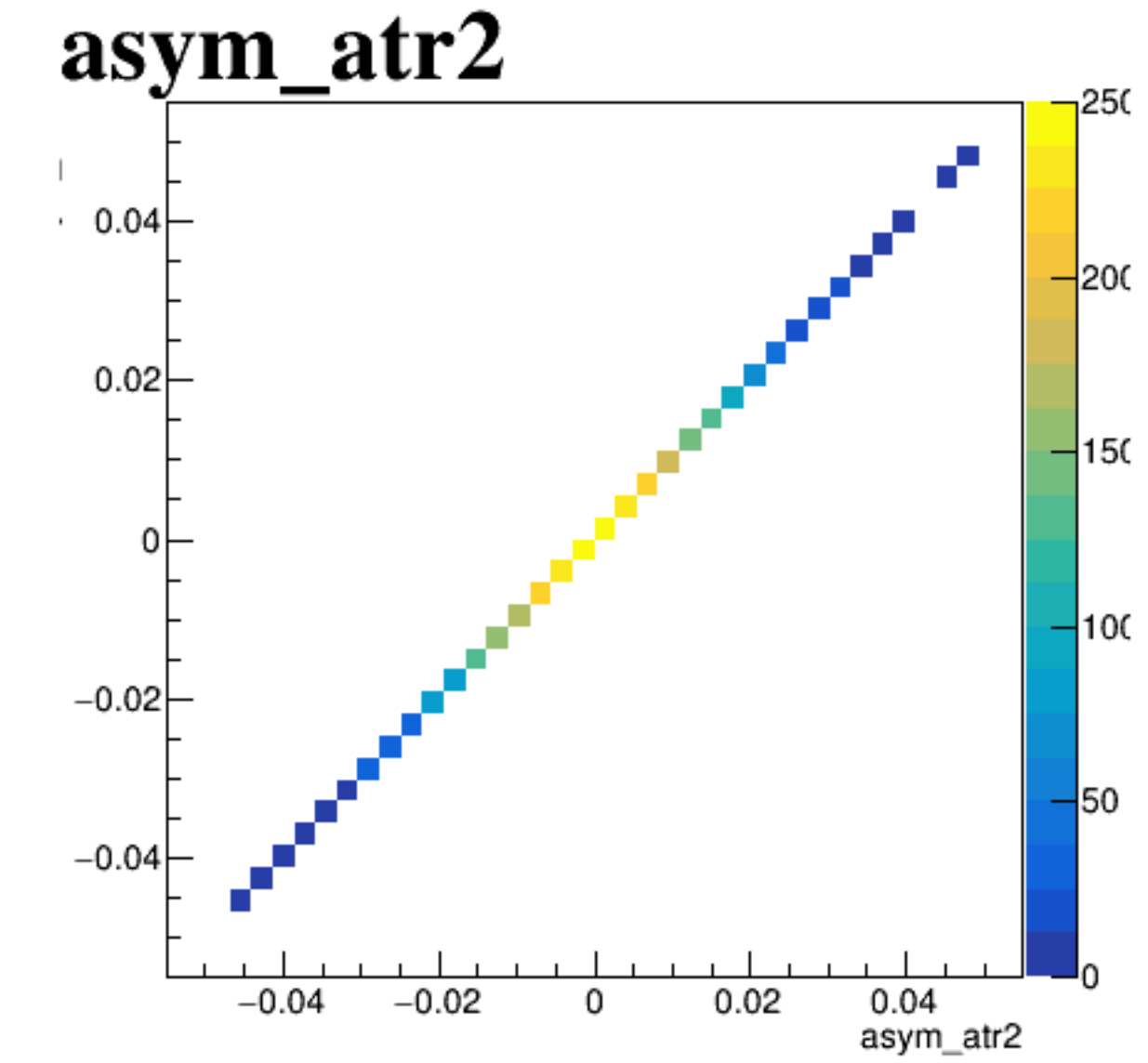
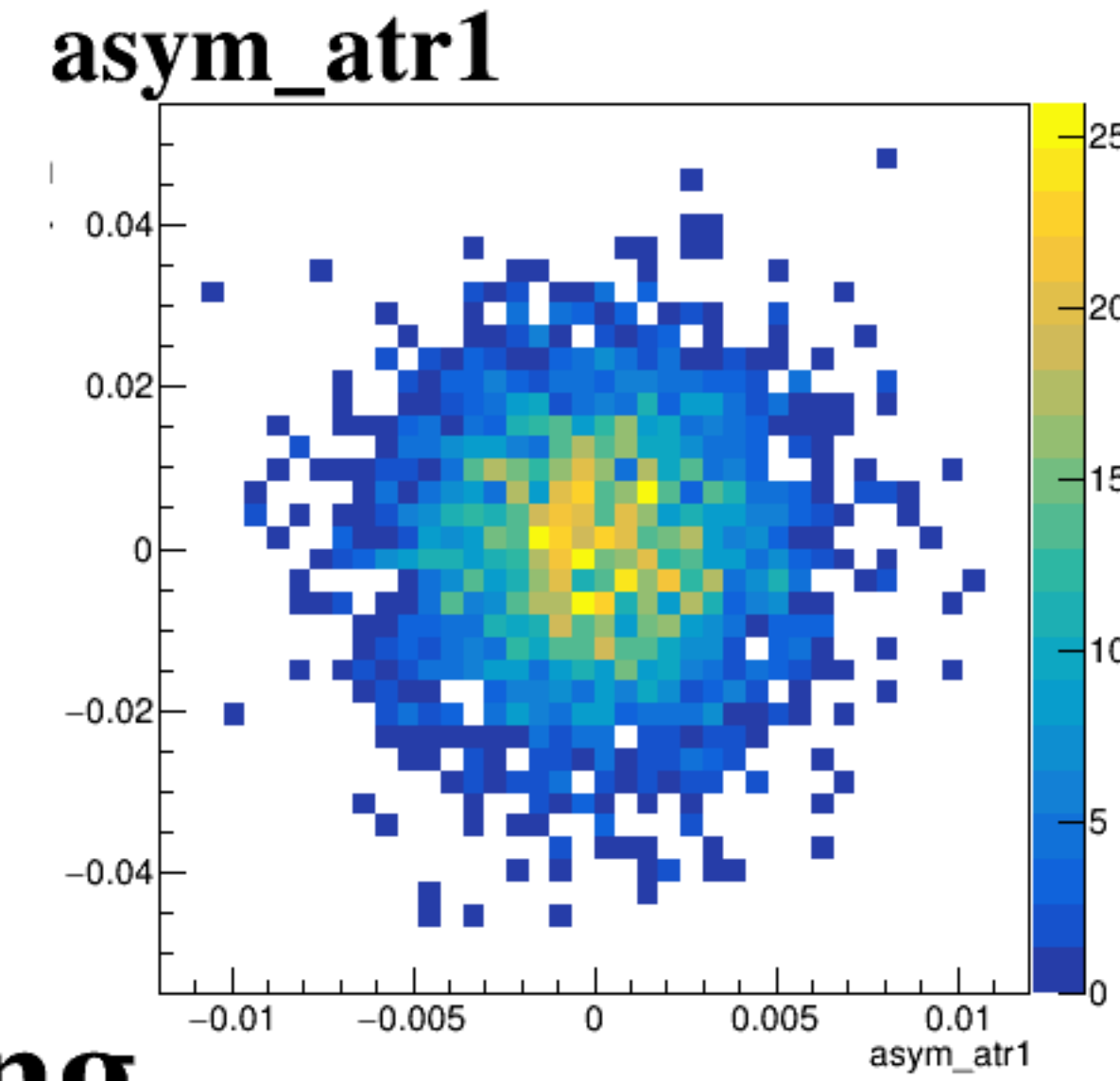
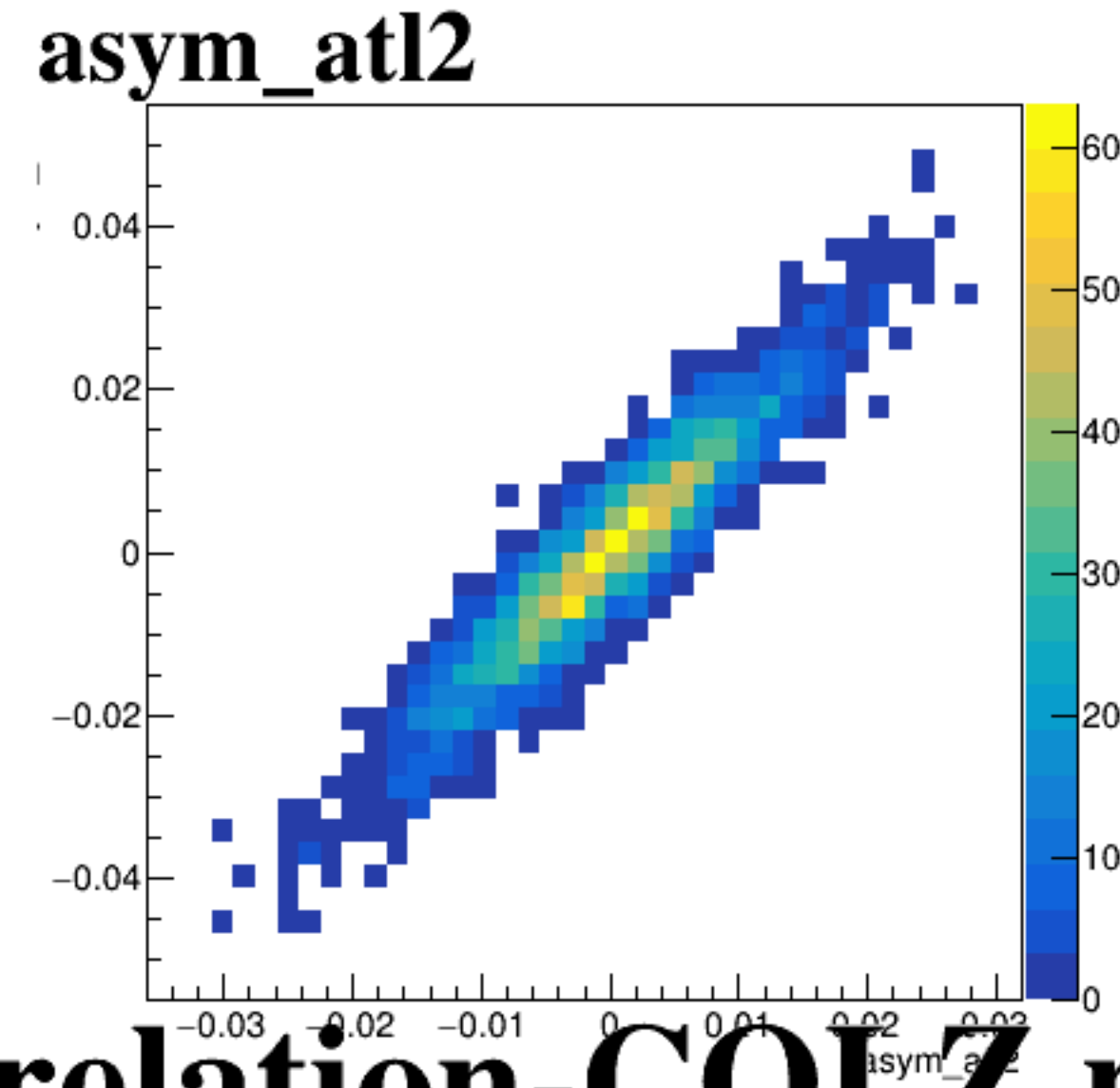
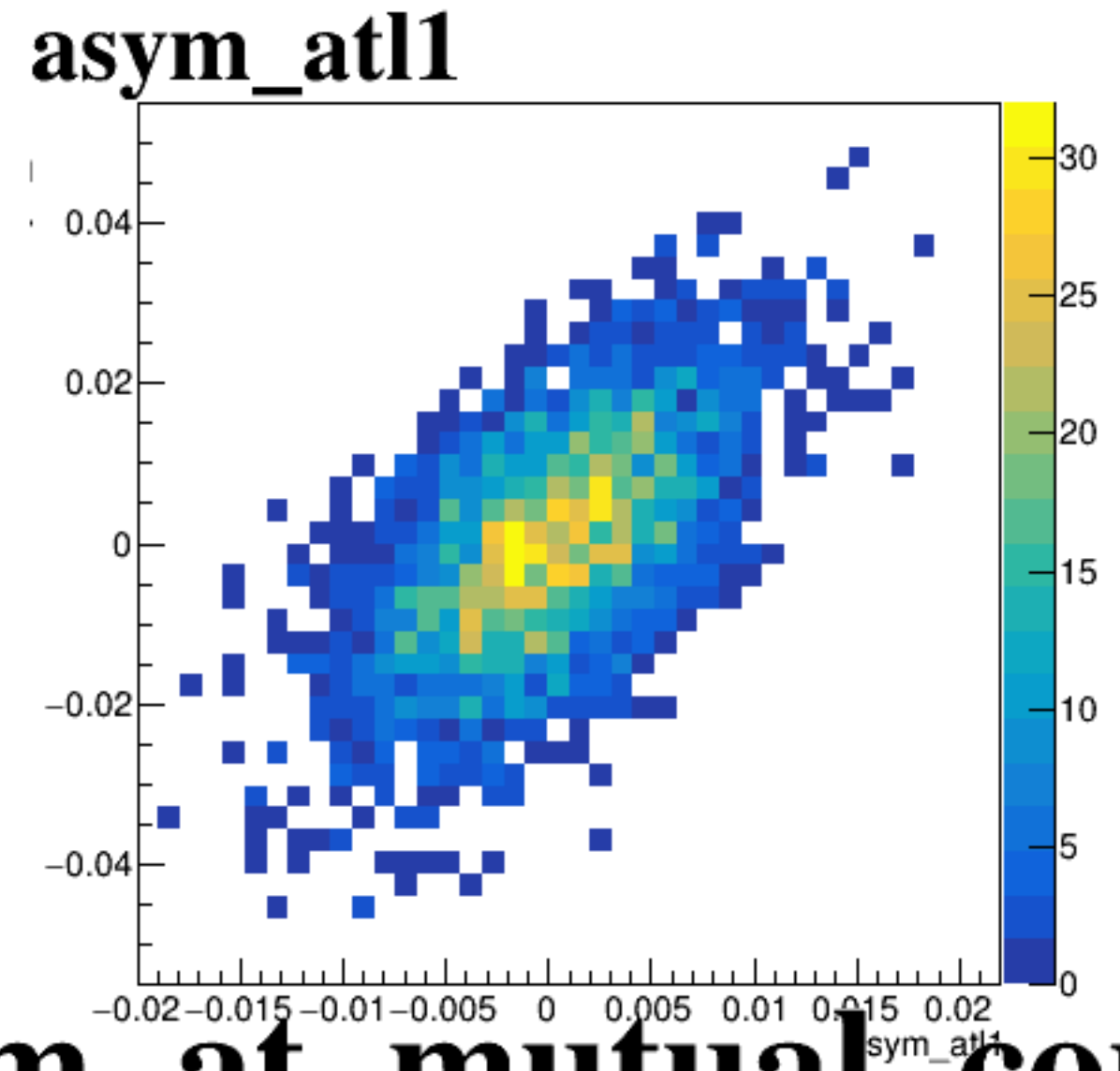
asym_atl2



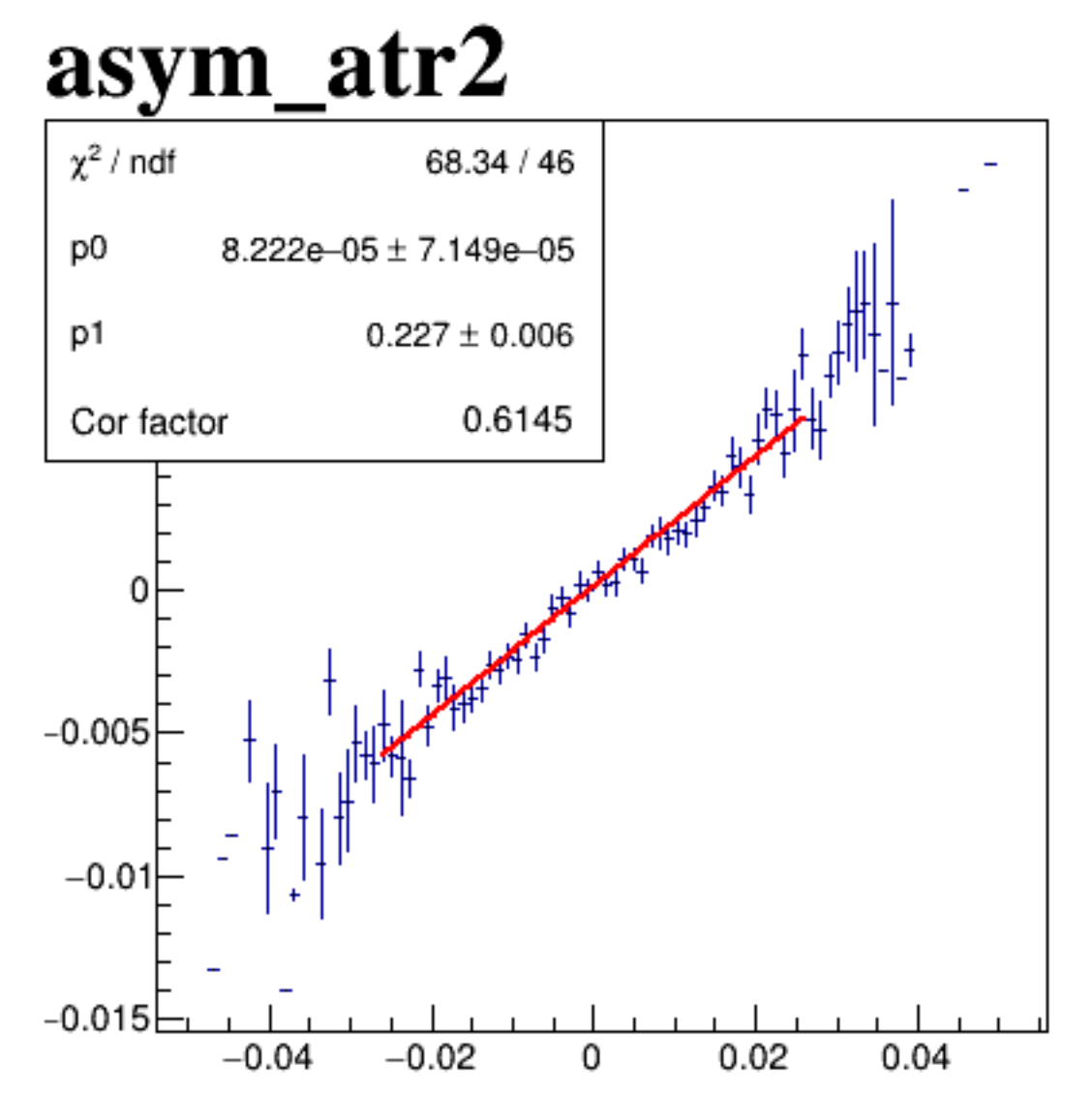
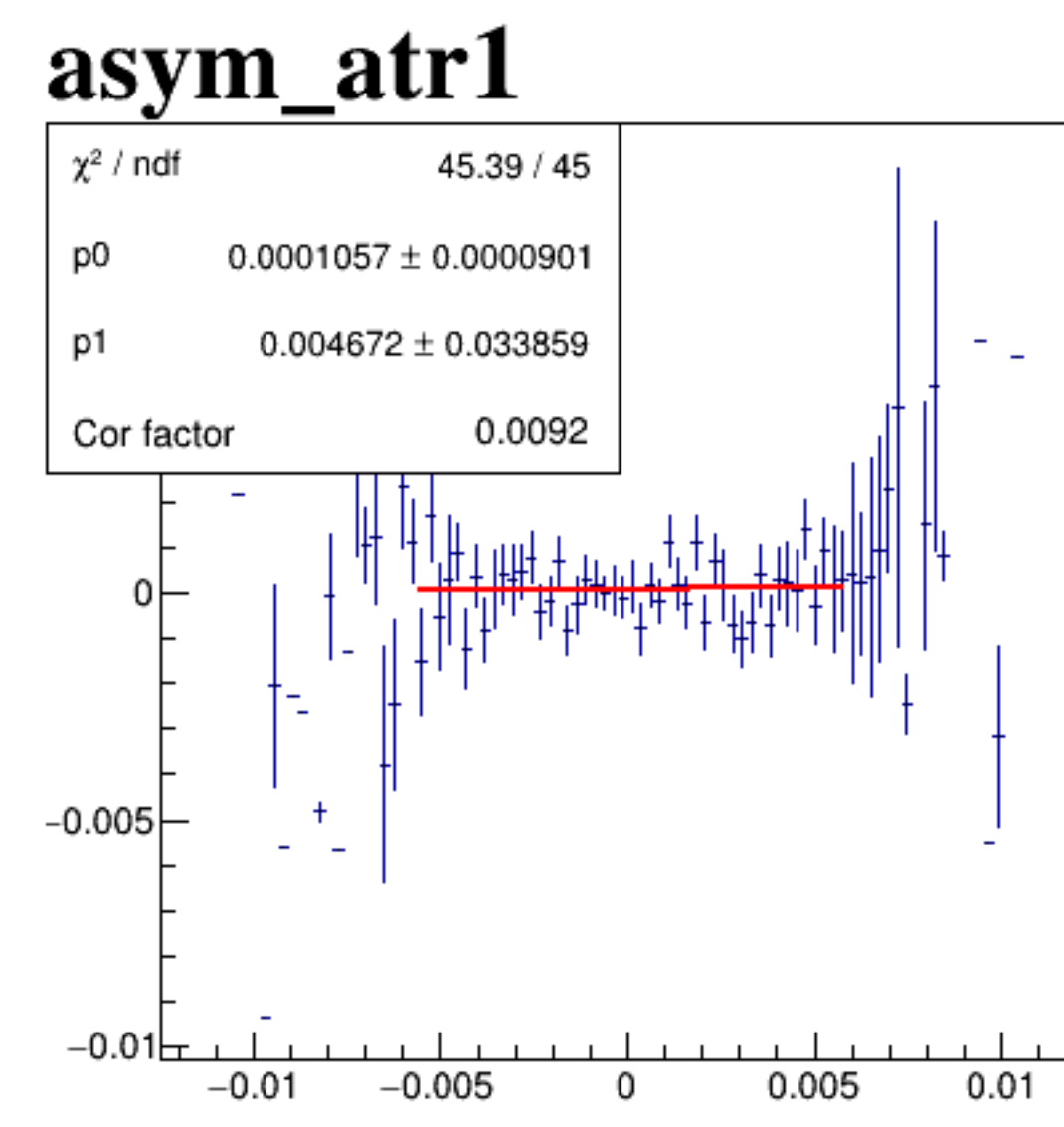
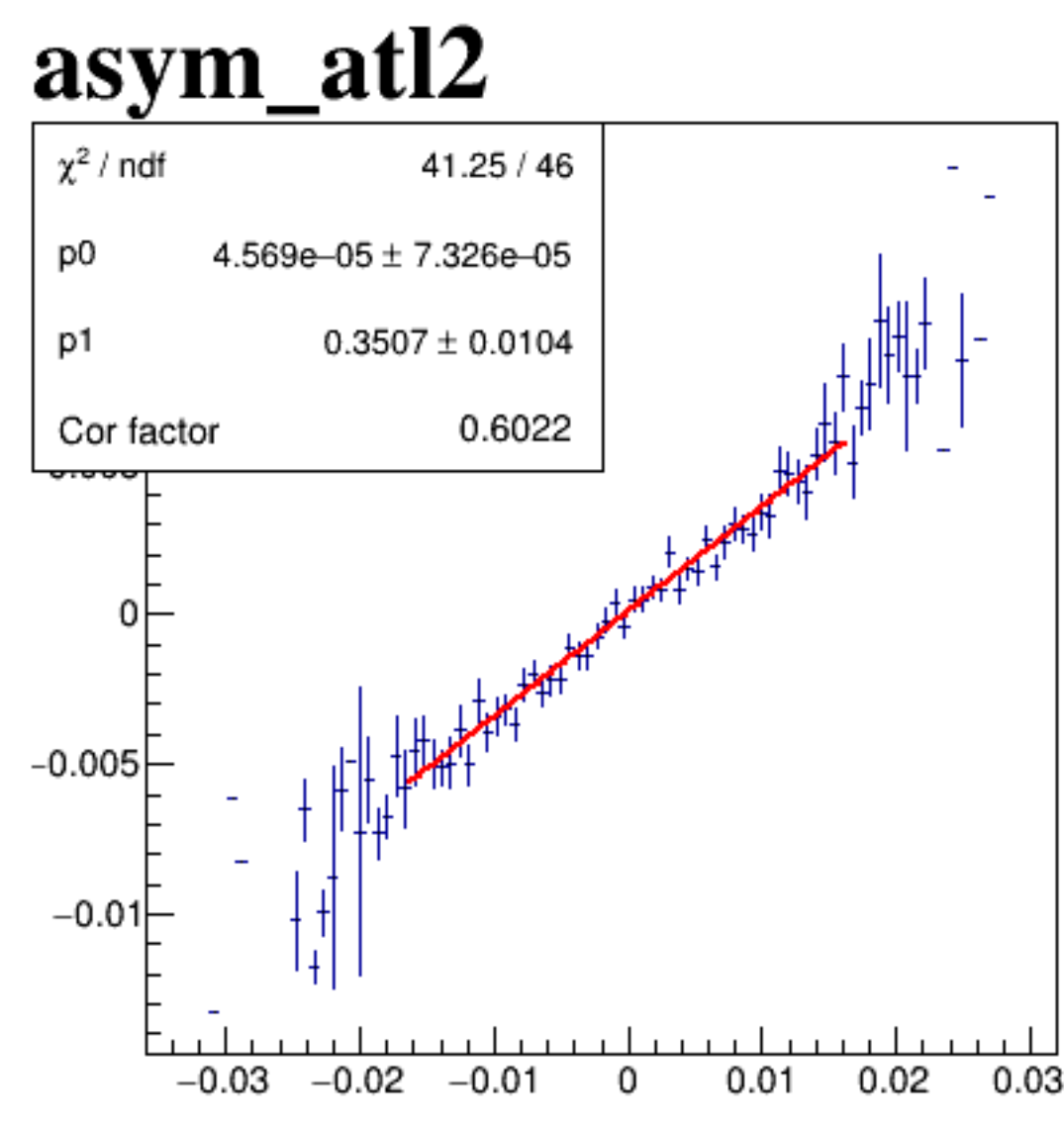
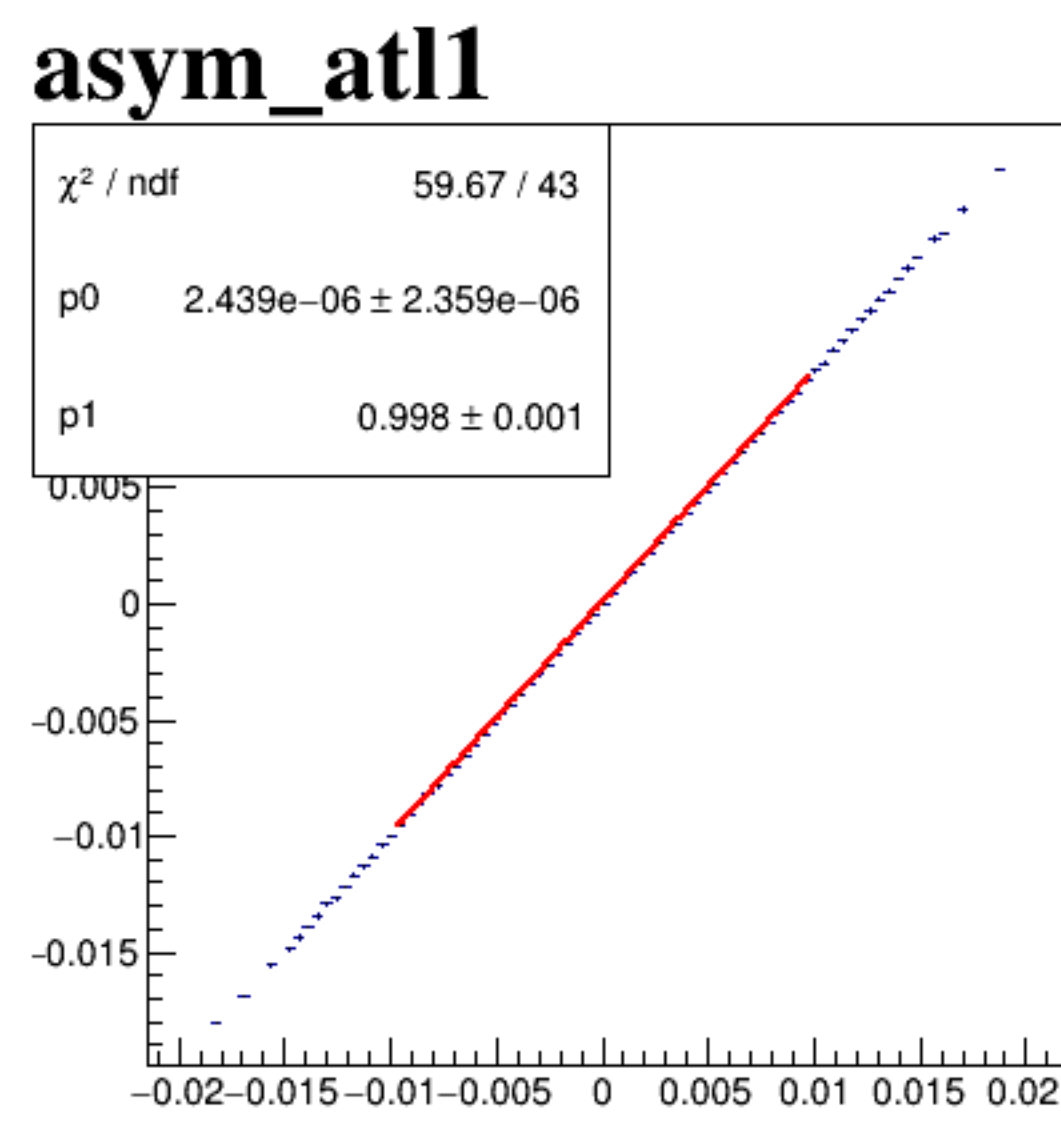
asym_atr1



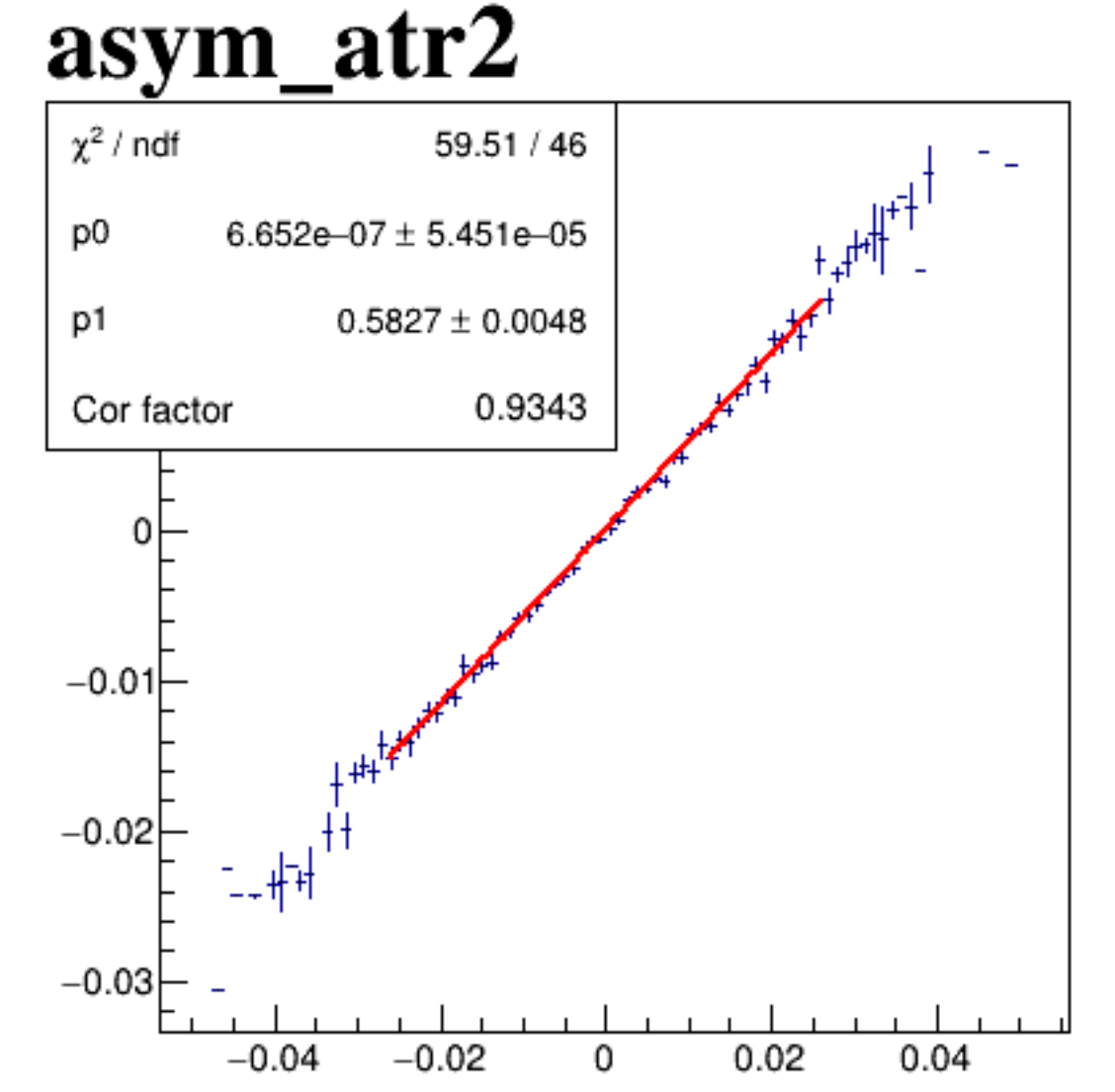
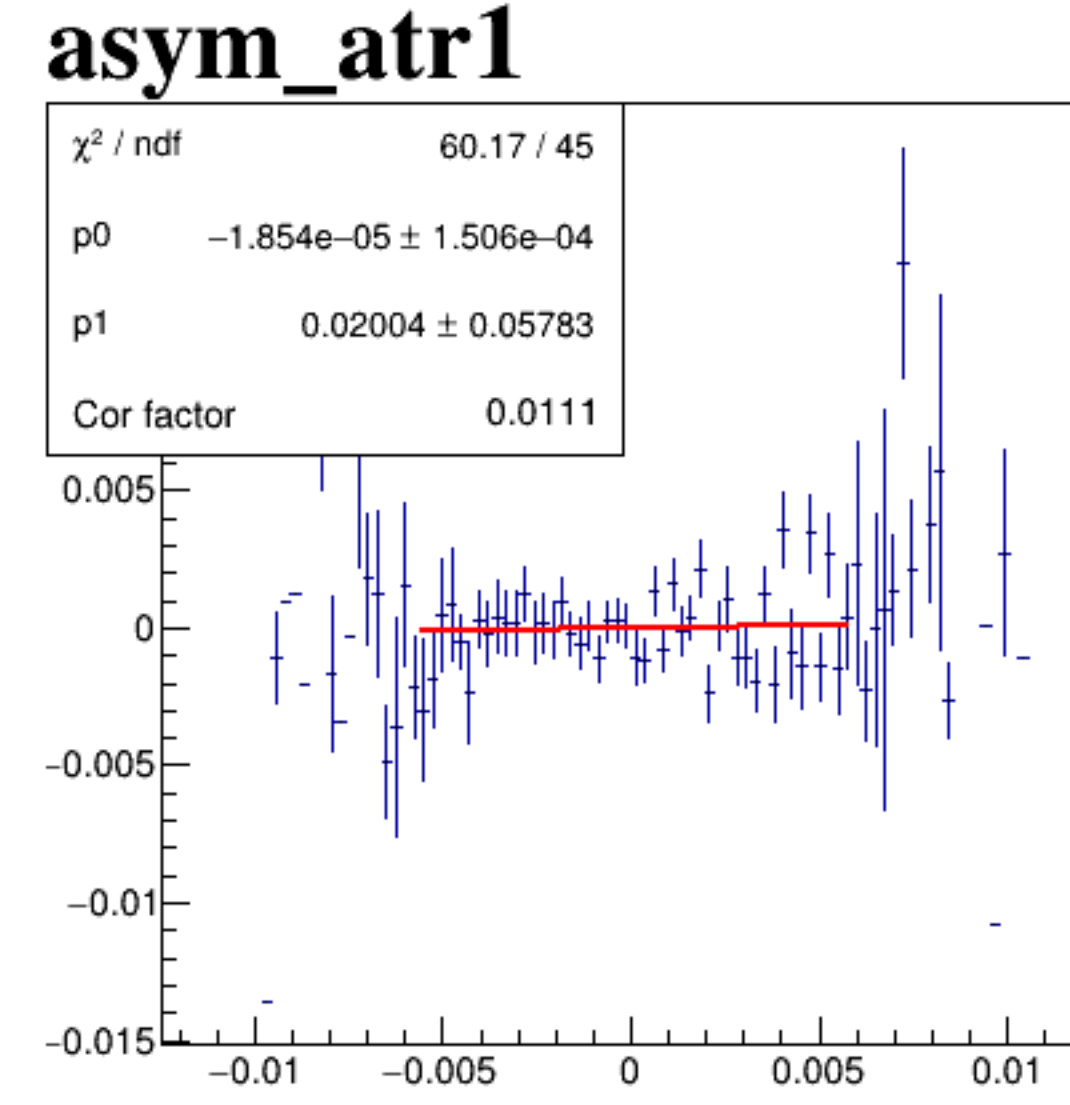
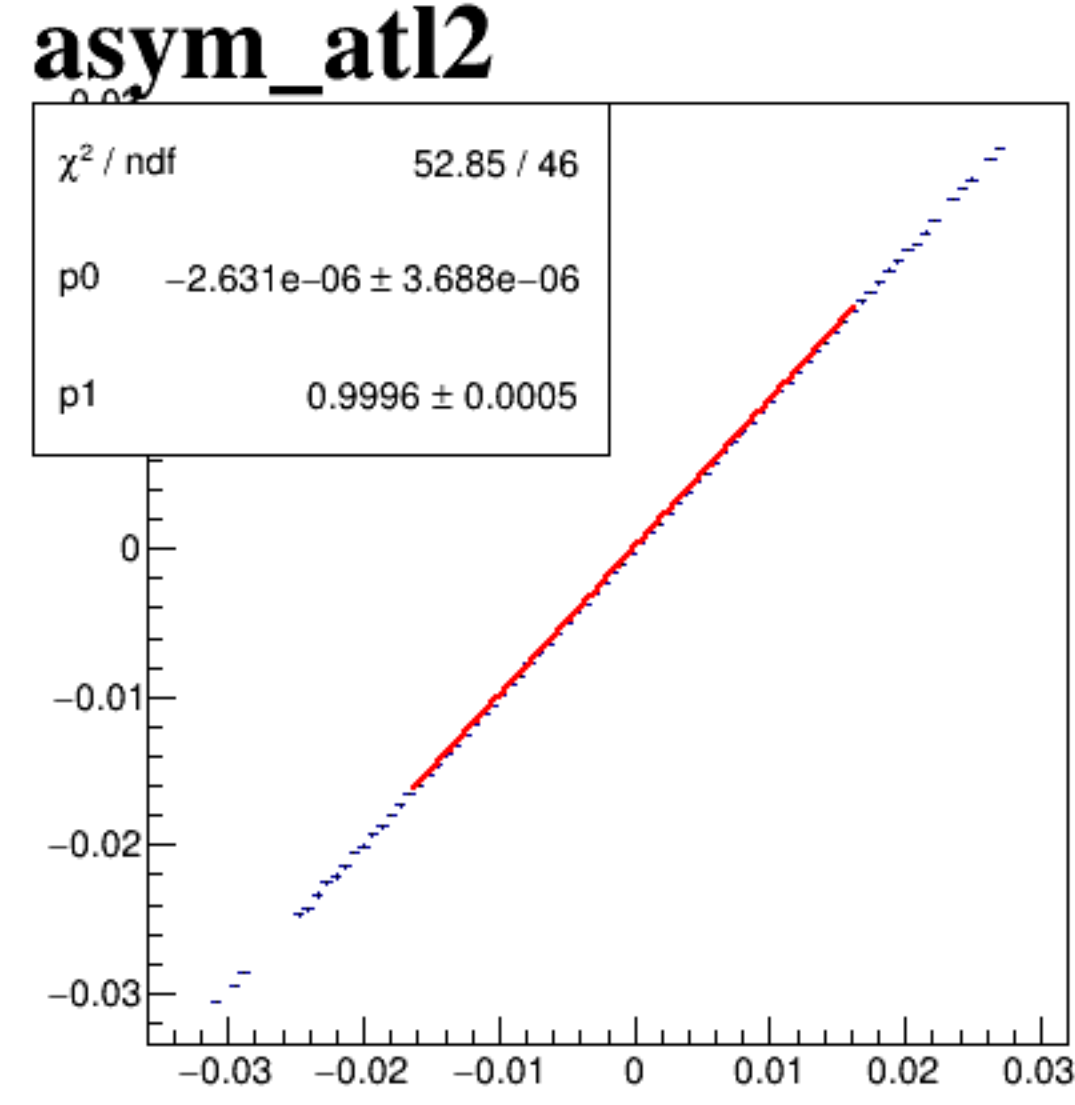
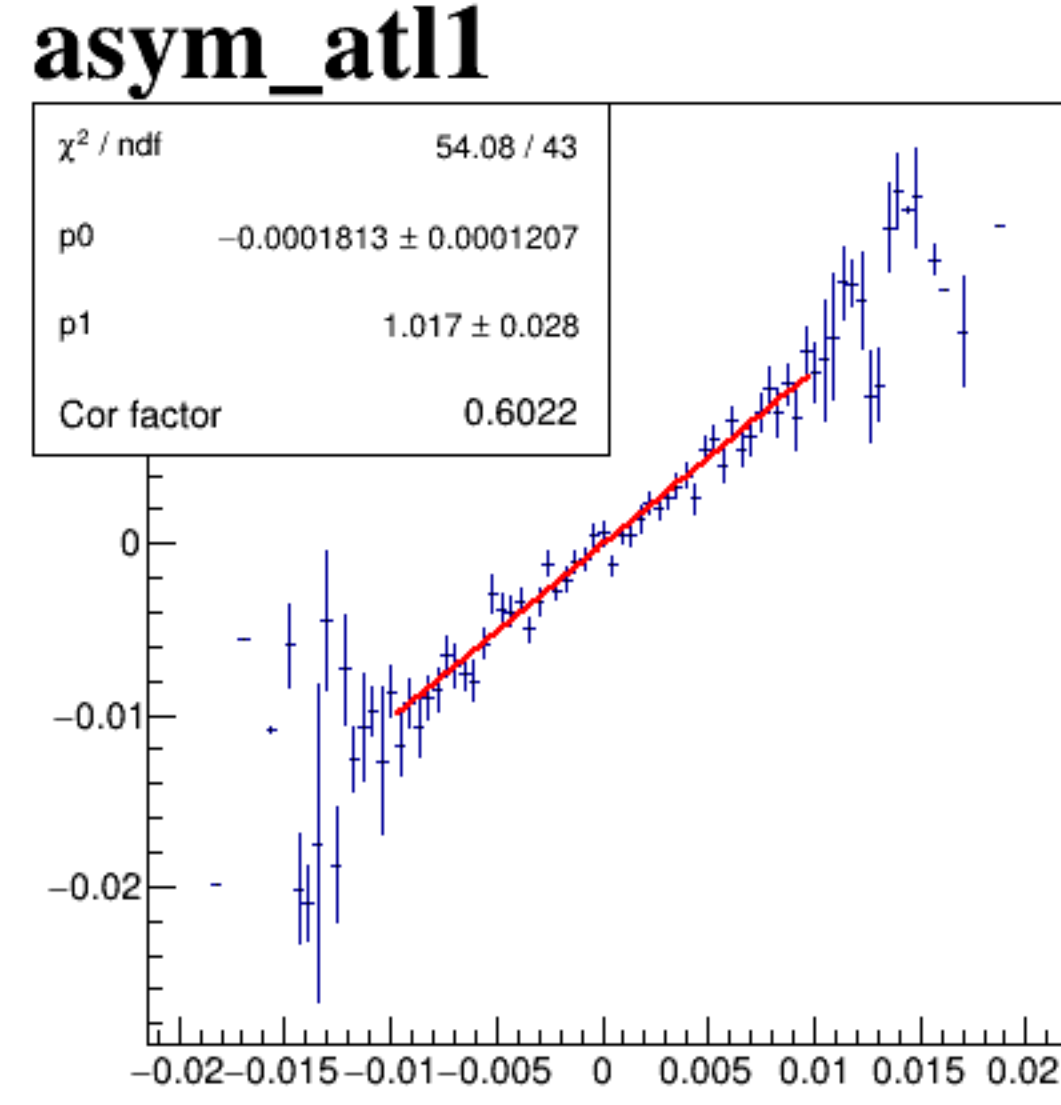
asym_atr2



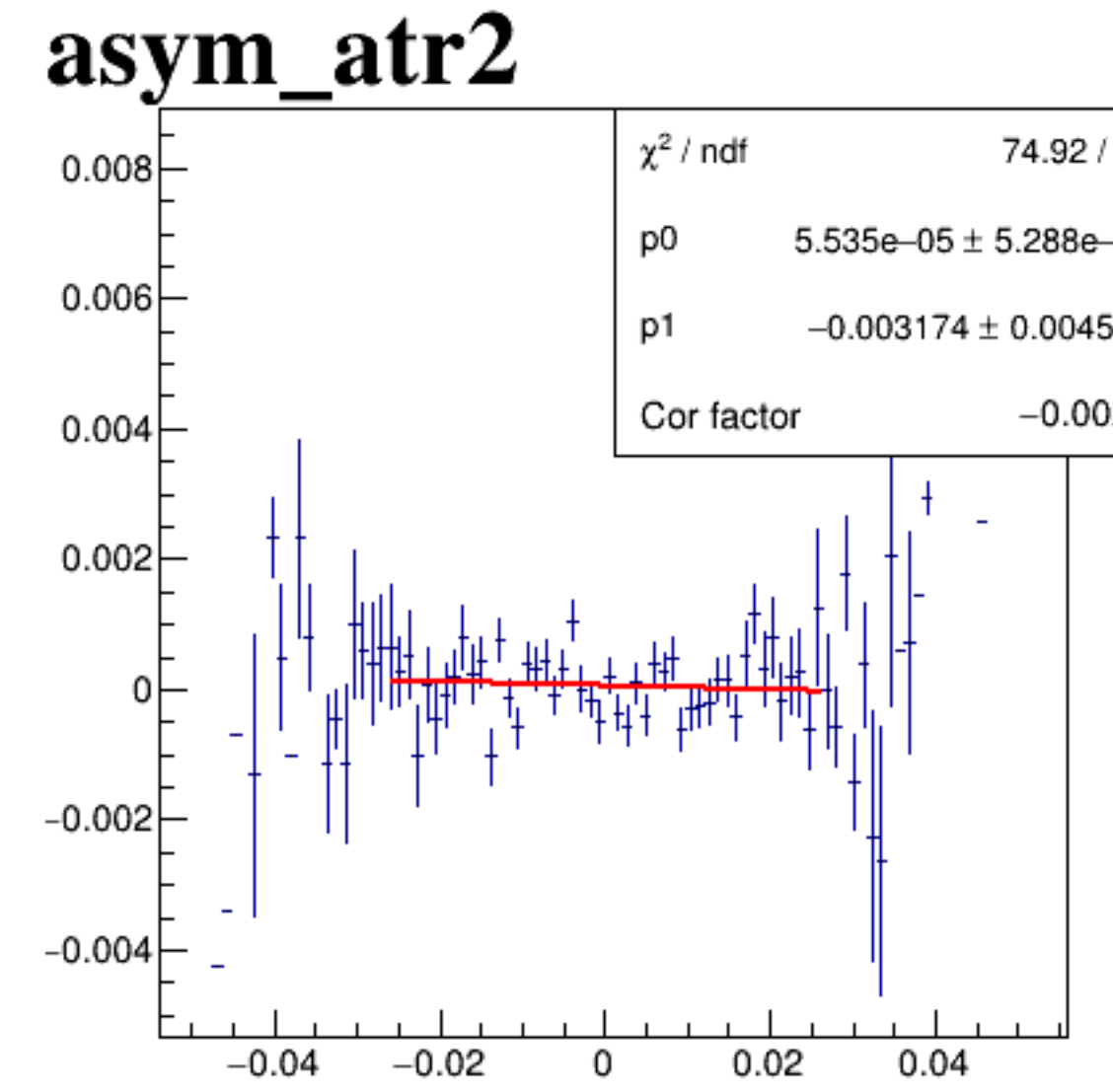
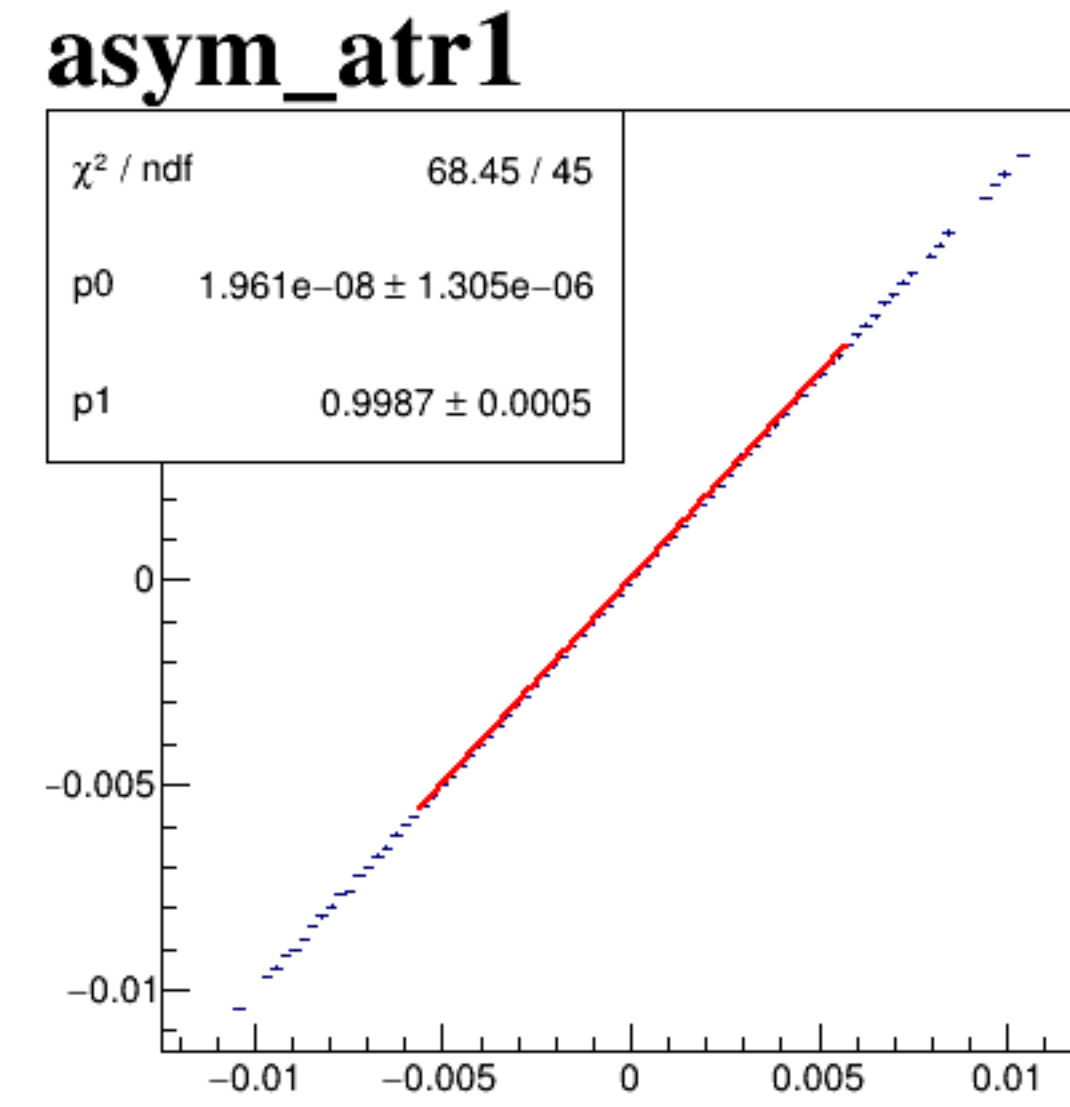
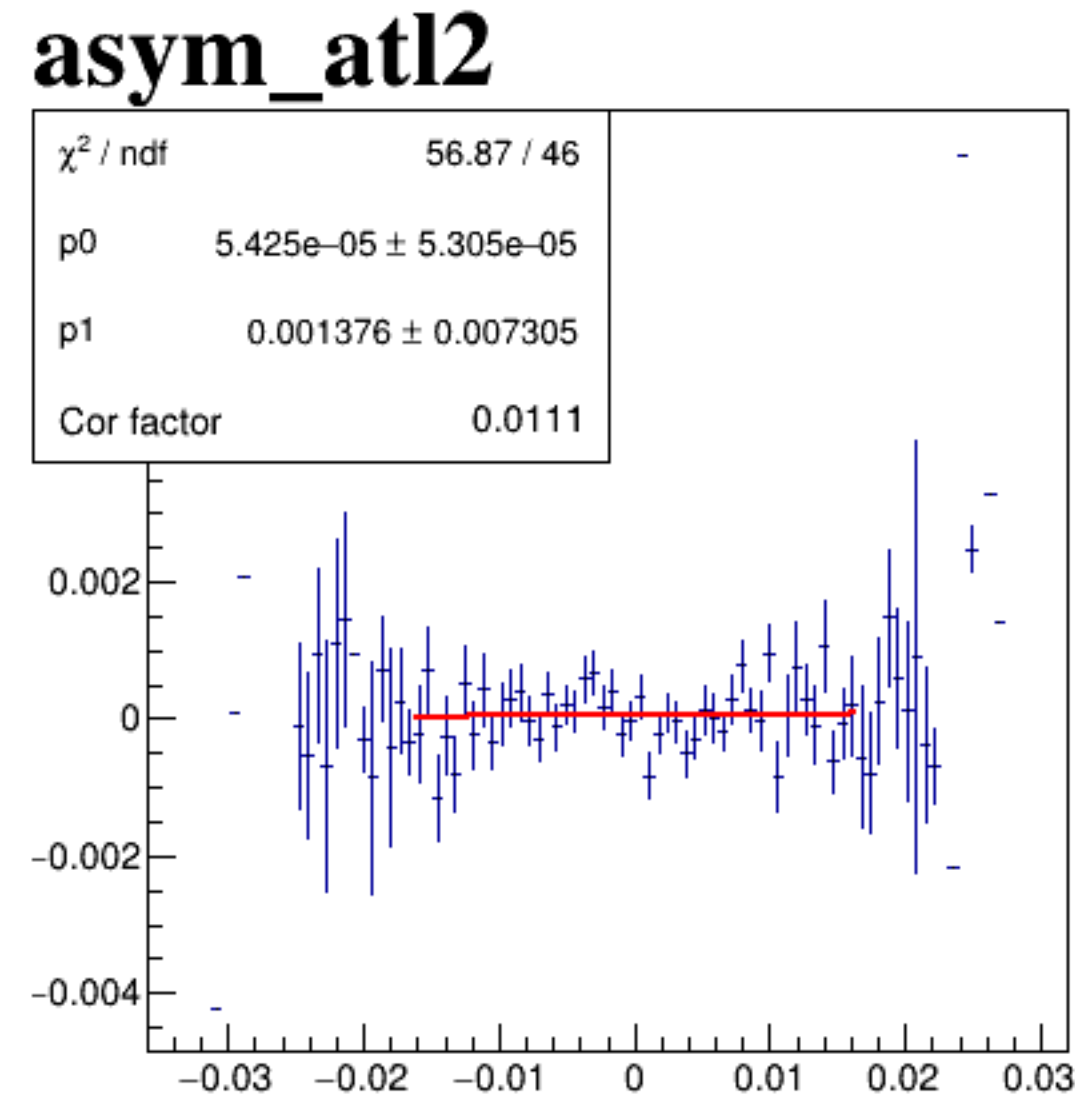
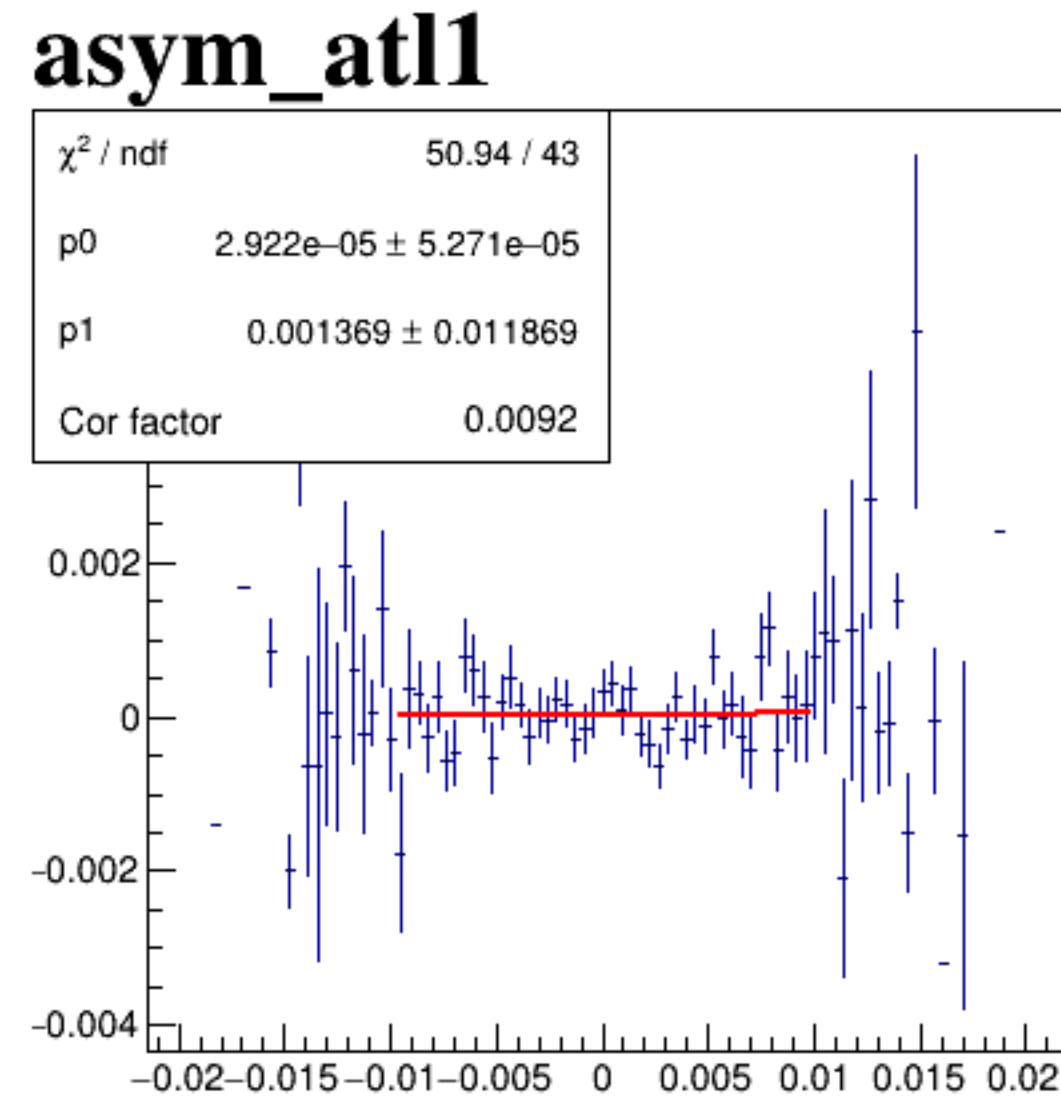
asym_atl1



asym_atl2



asym_atr1



asym_atr2

