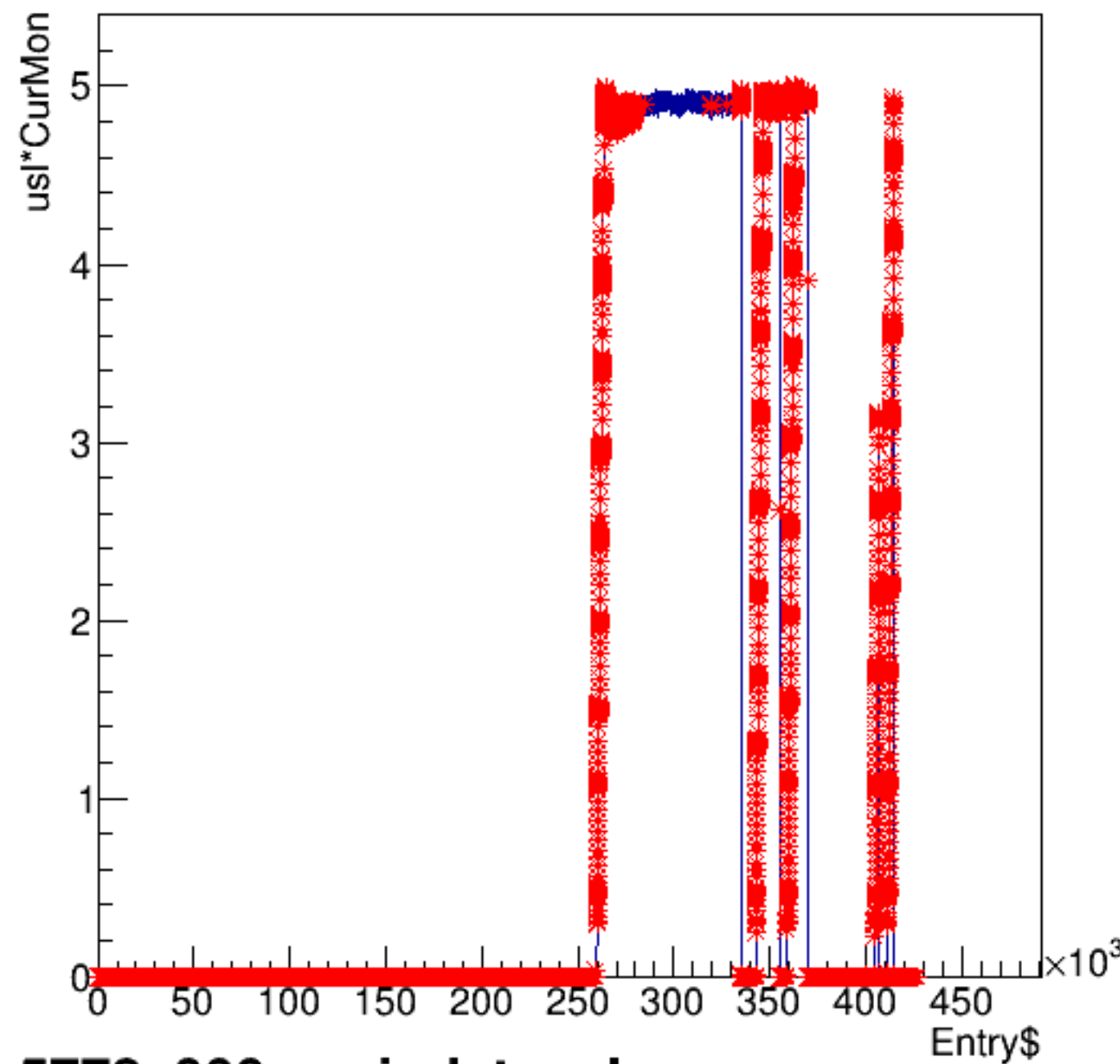
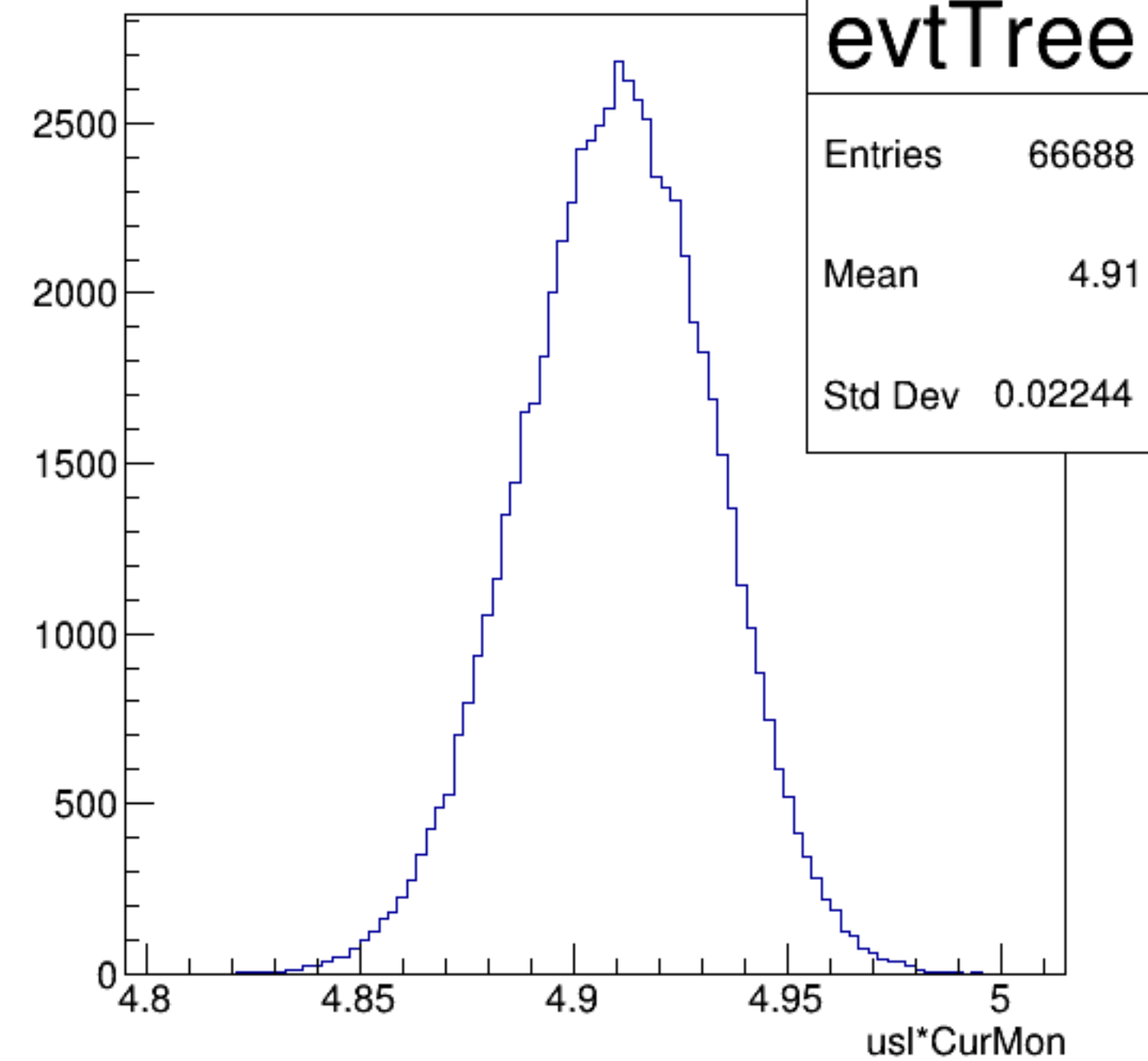


usl*CurMon:Entry\$



run5772_000_maindet_usl.png

usl*CurMon {ErrorFlag==0}



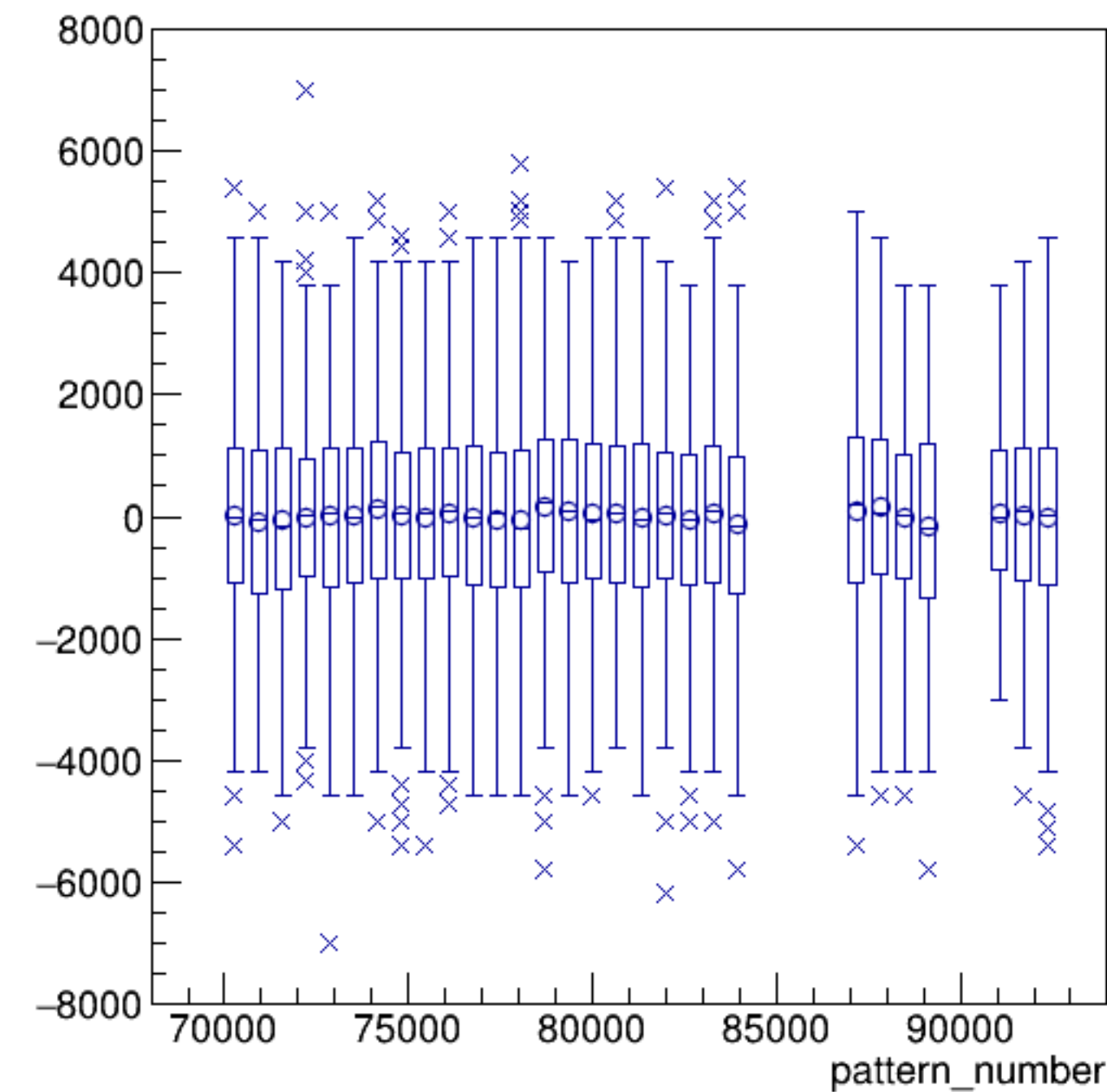
evtTree

Entries 66688

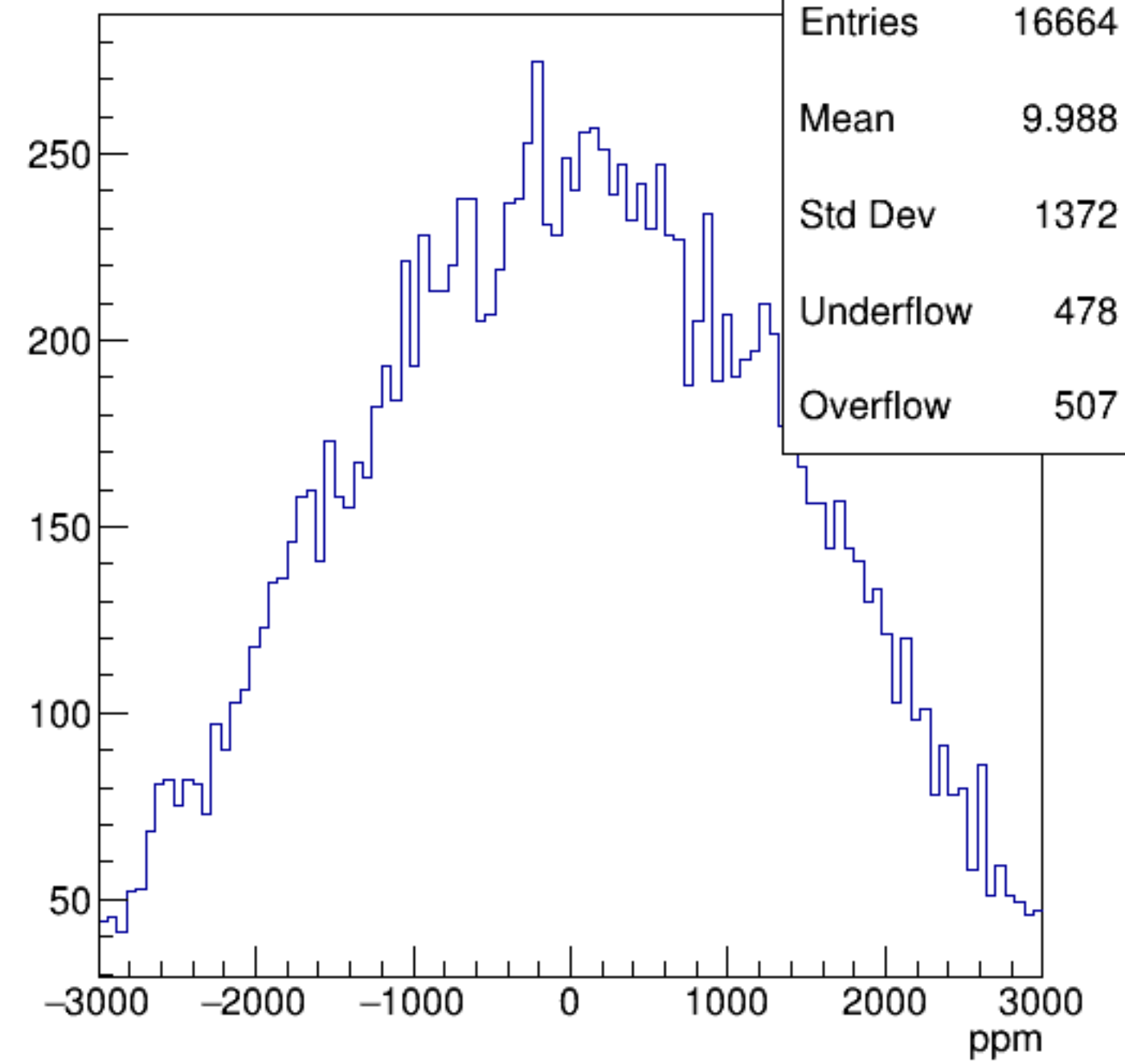
Mean 4.91

Std Dev 0.02244

asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl



Entries 16664

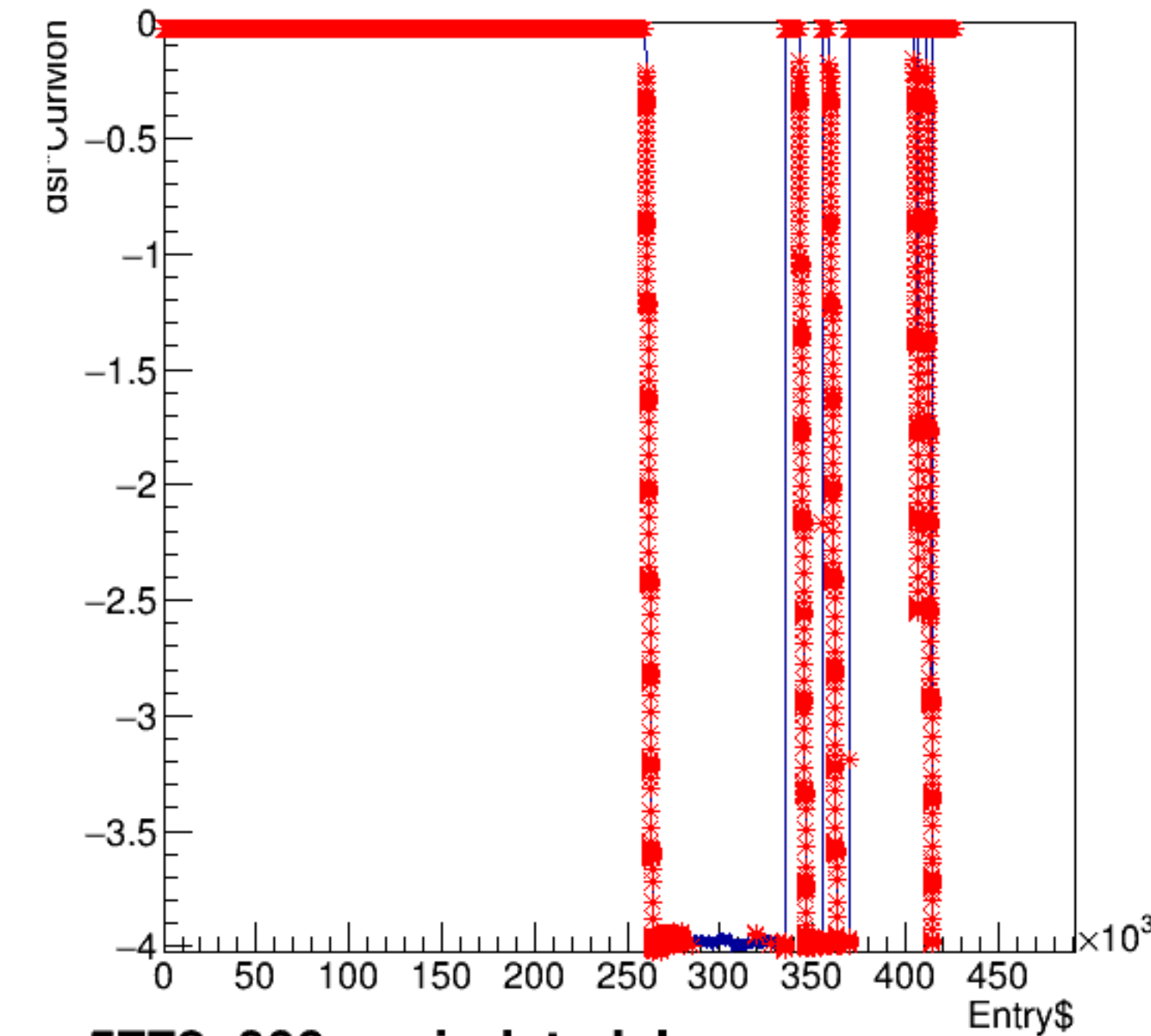
Mean 9.988

Std Dev 1372

Underflow 478

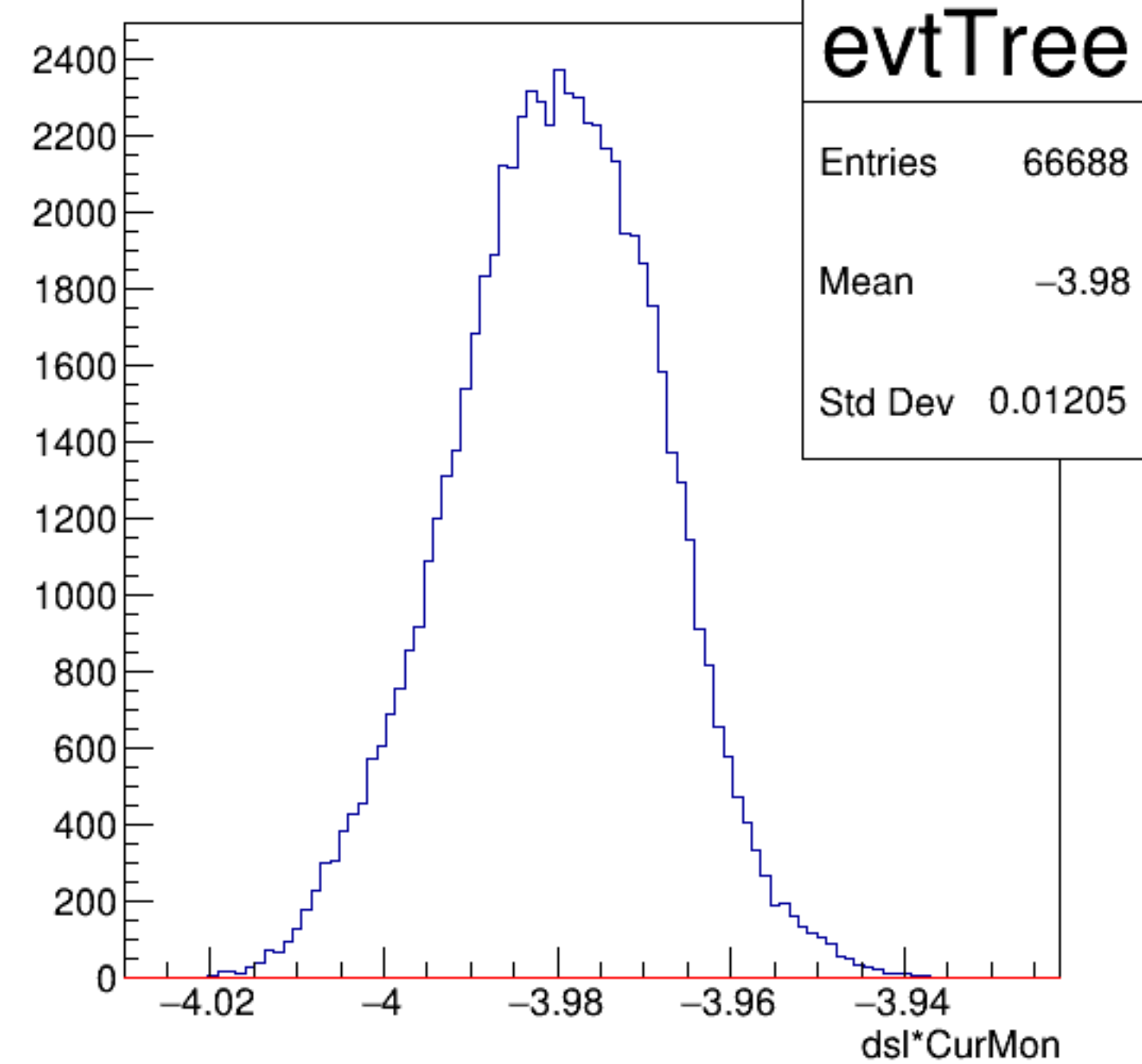
Overflow 507

dsl*CurMon:Entry\$



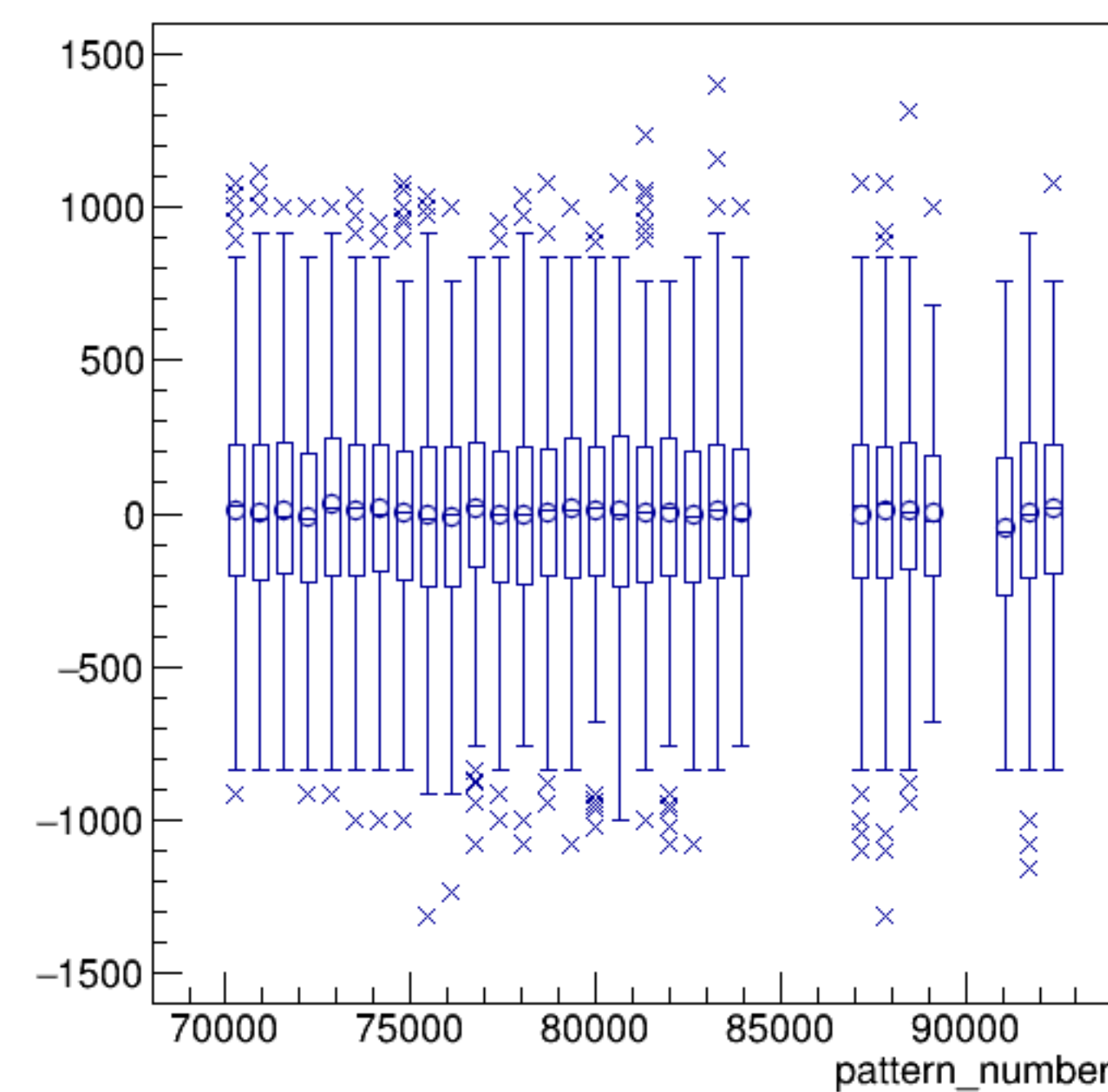
run5772_000_maindet_dsl.png

dsl*CurMon {ErrorFlag==0}

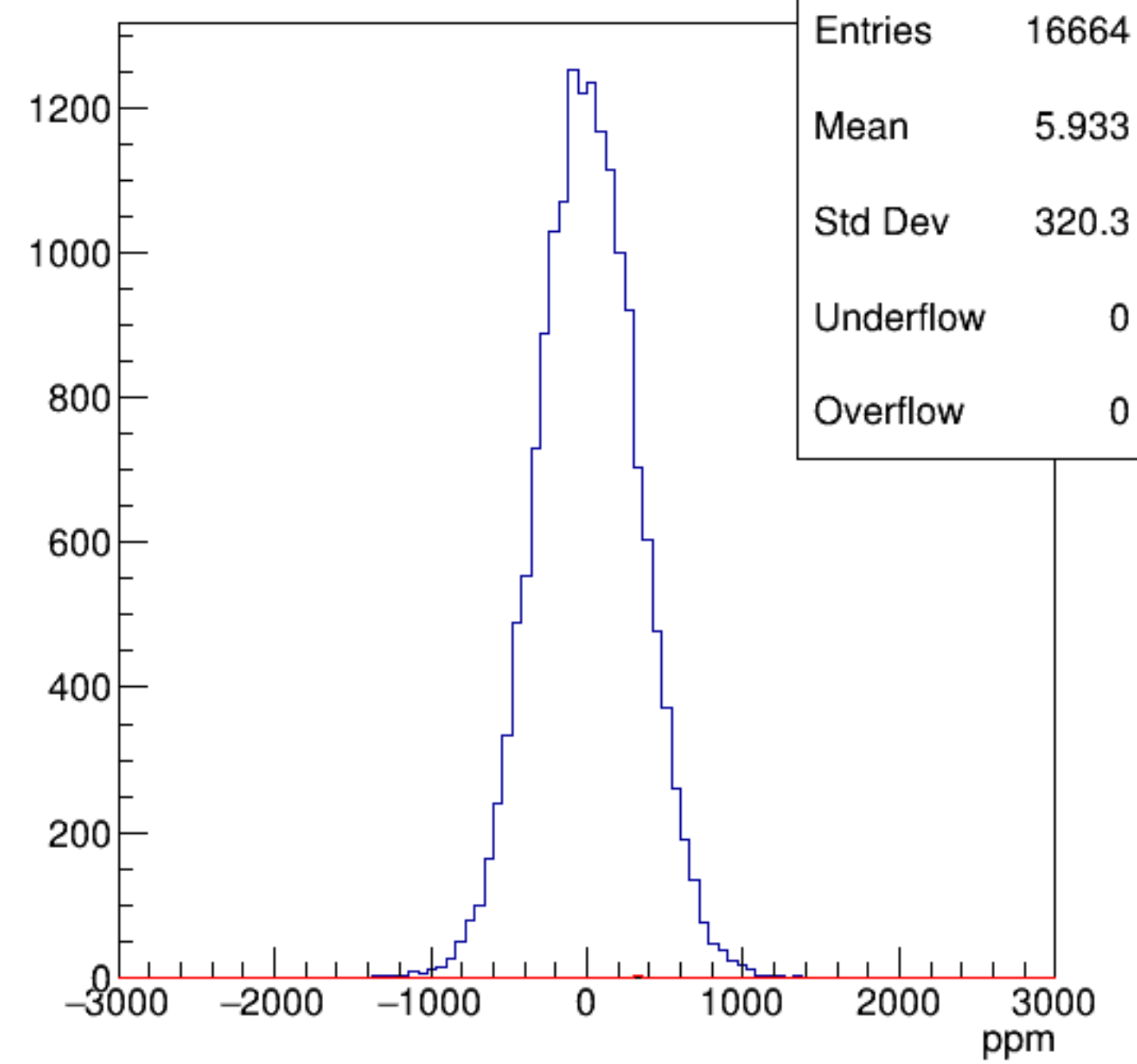


evtTree	
Entries	66688
Mean	-3.98
Std Dev	0.01205

asym_dsl/ppm:pattern_number {ErrorFlag==0}

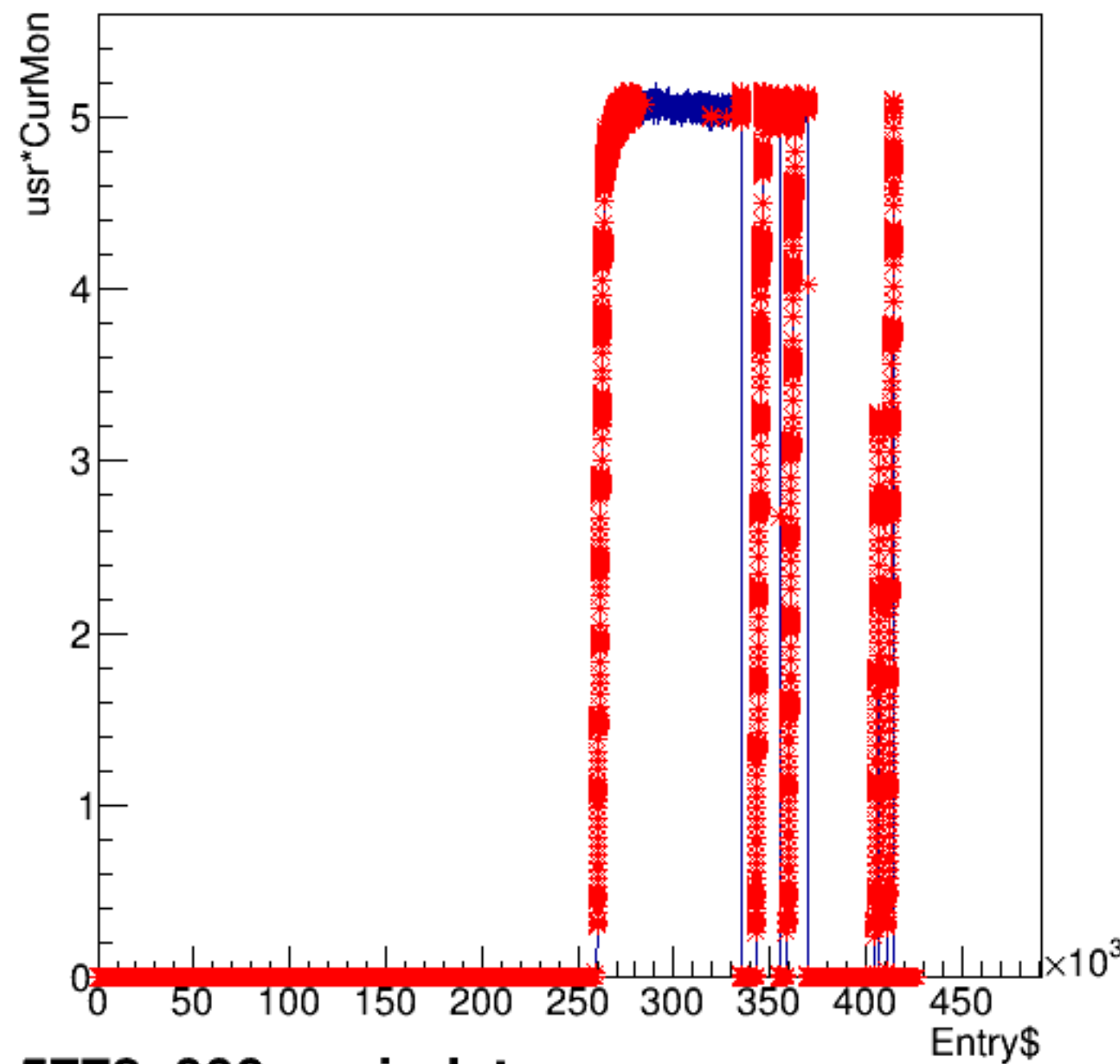


asym_dsl



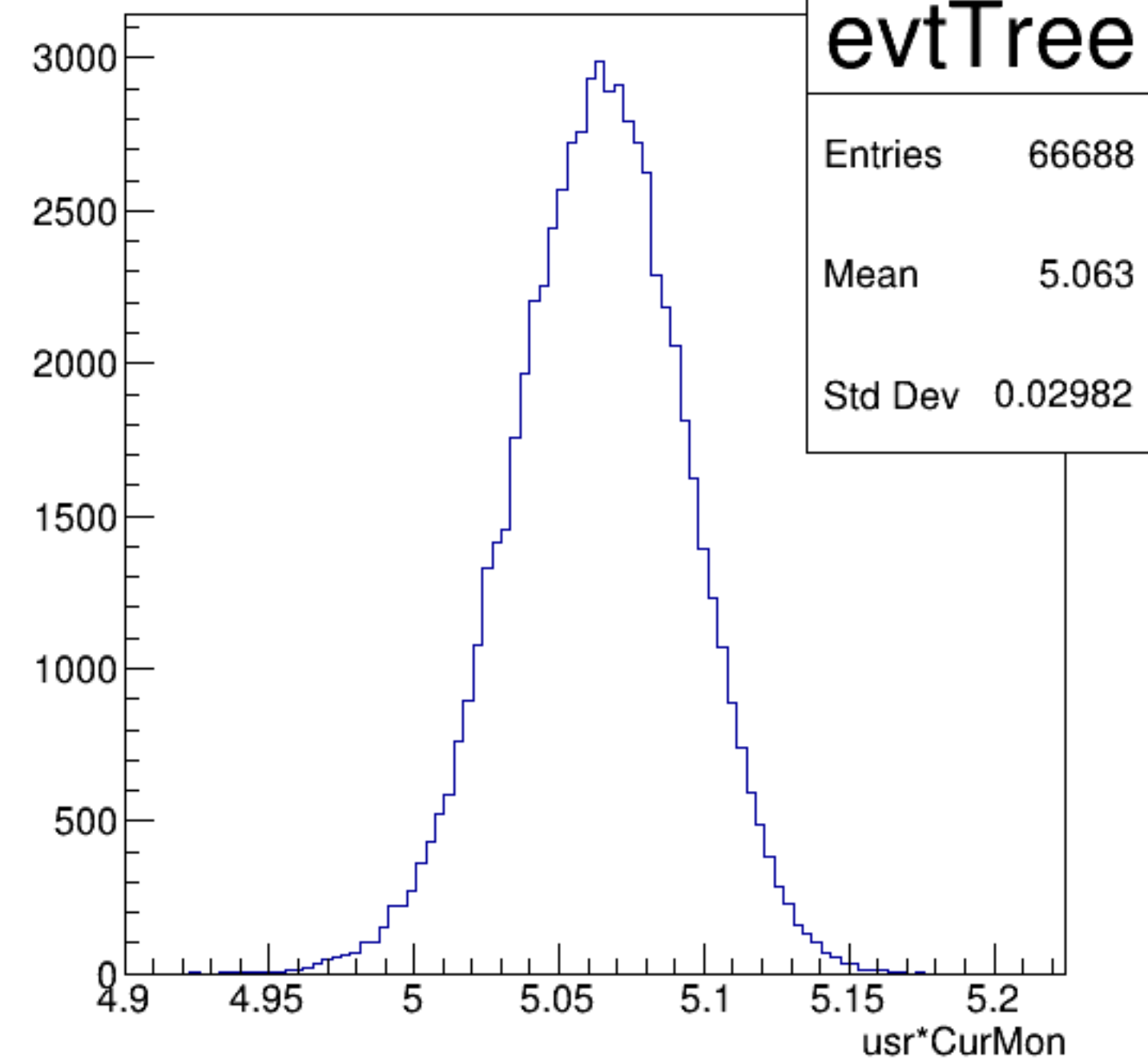
Entries	16664
Mean	5.933
Std Dev	320.3
Underflow	0
Overflow	0

usr*CurMon:Entry\$

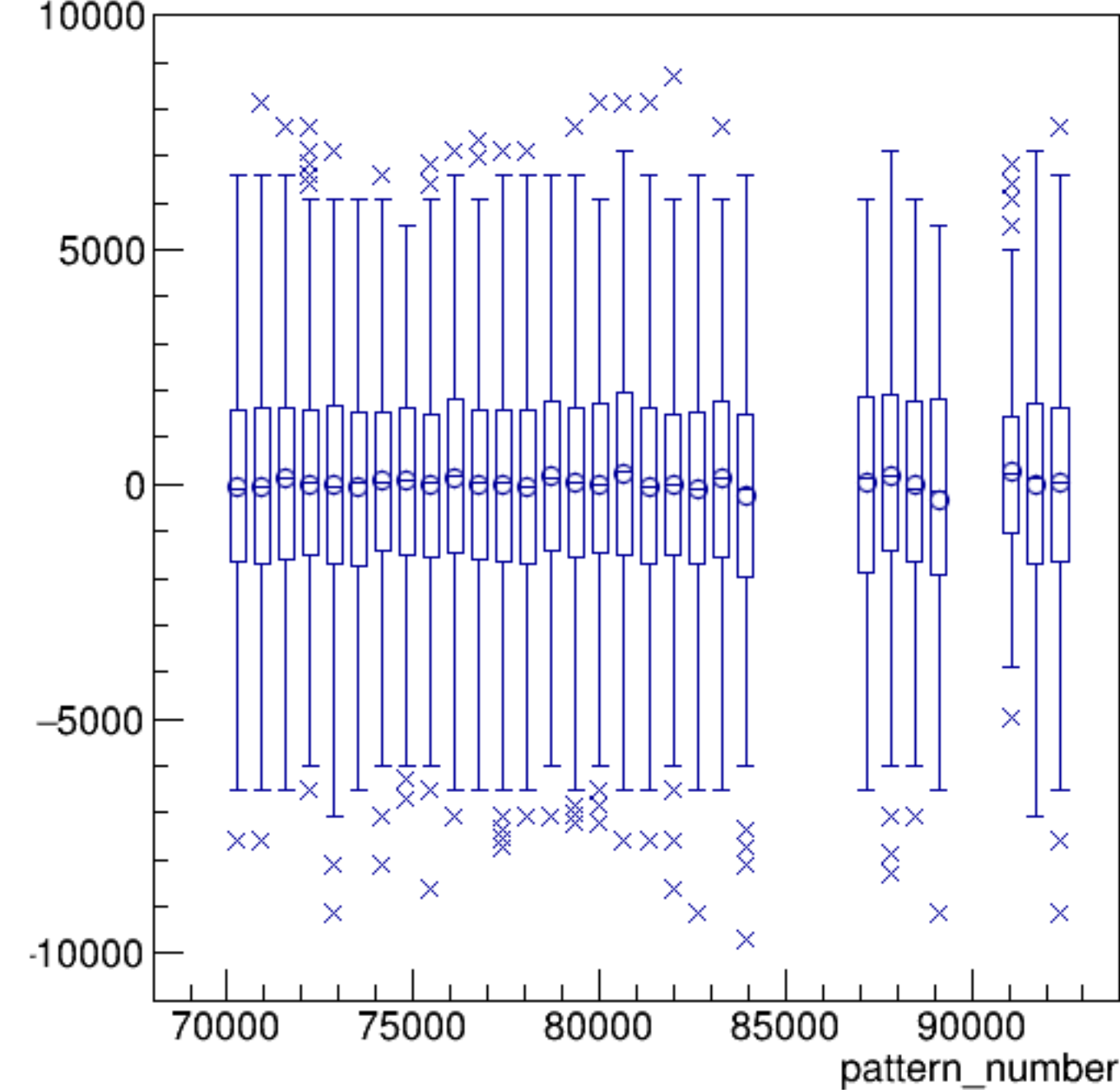


run5772_000_maindet_usr.png

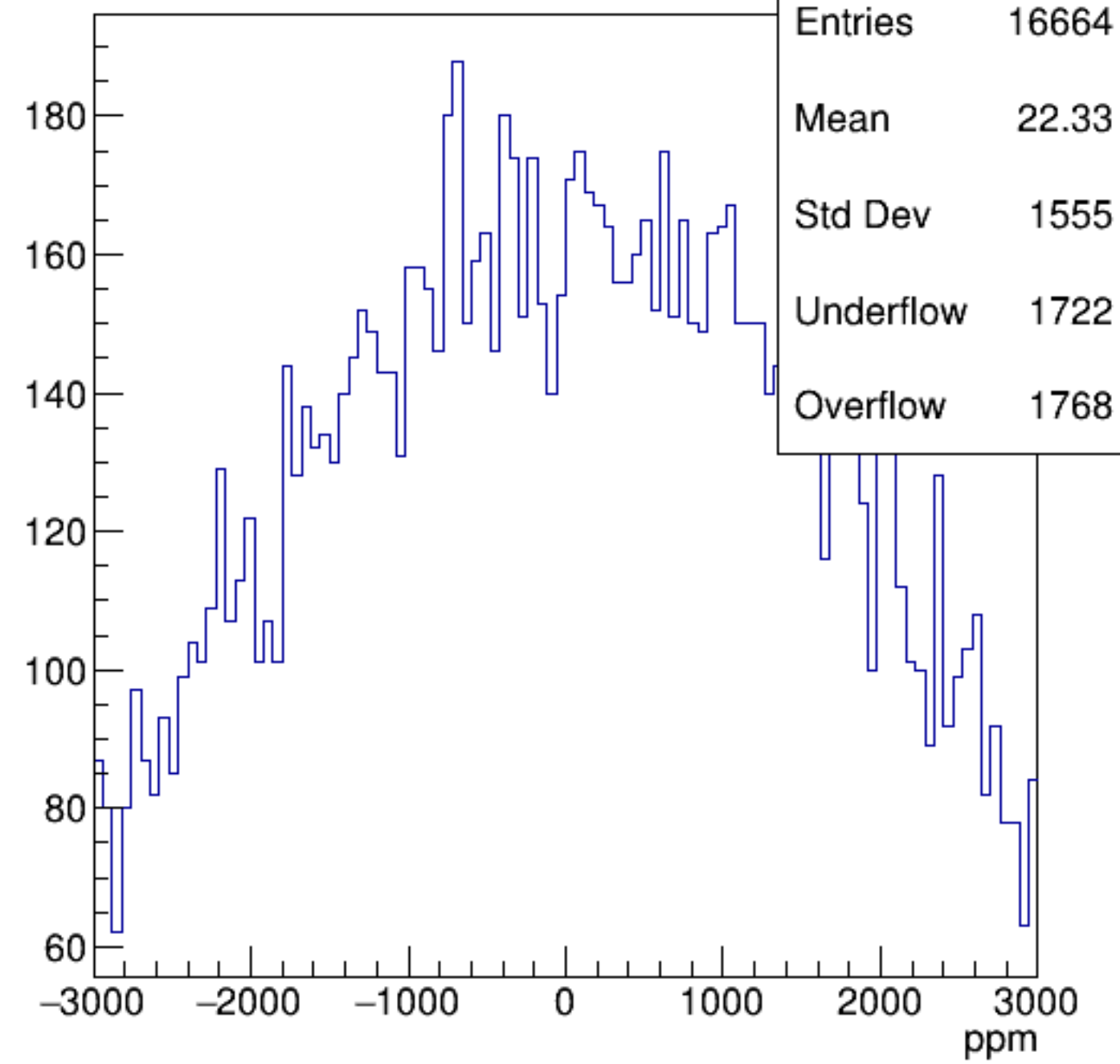
usr*CurMon {ErrorFlag==0}

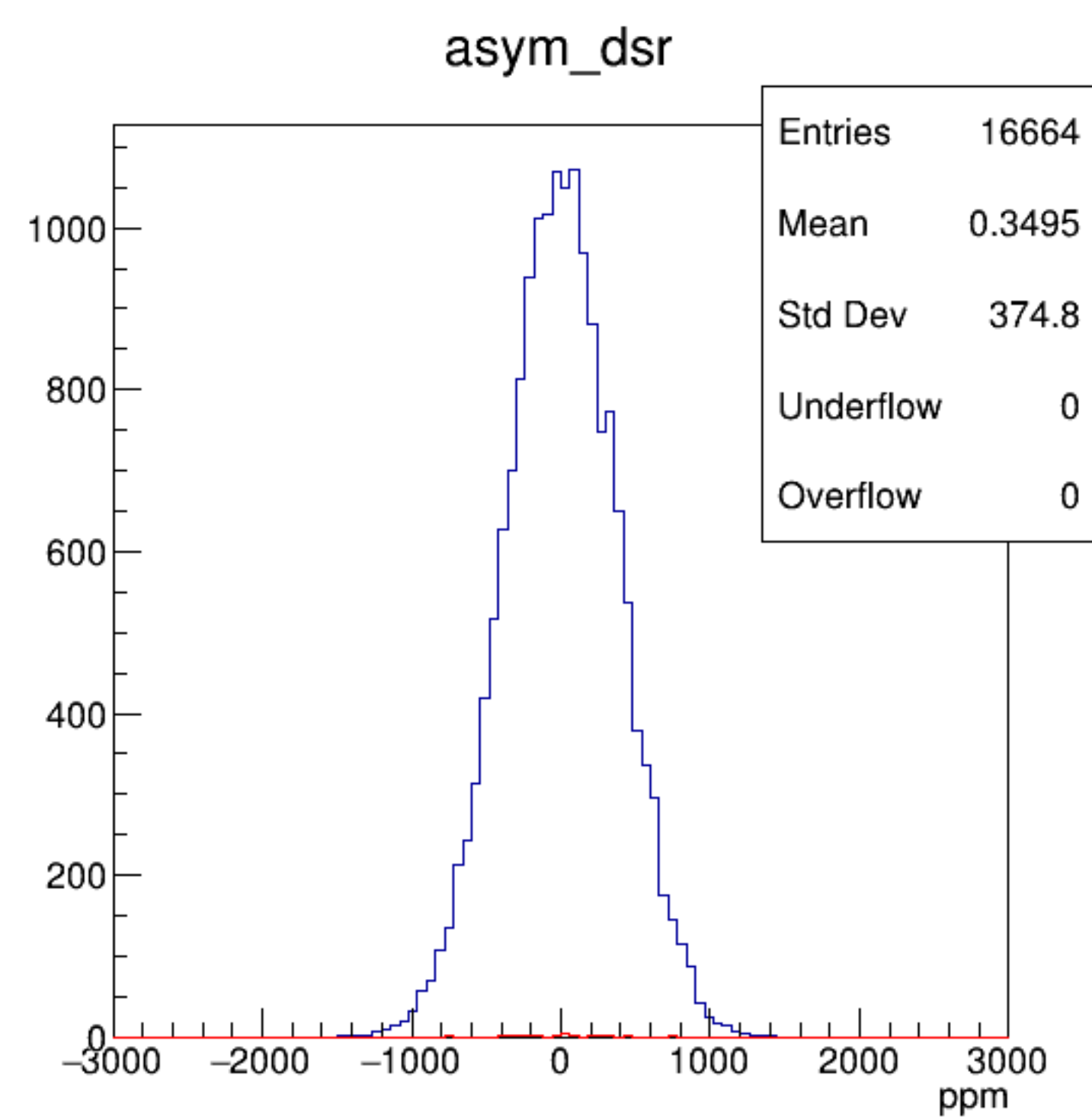
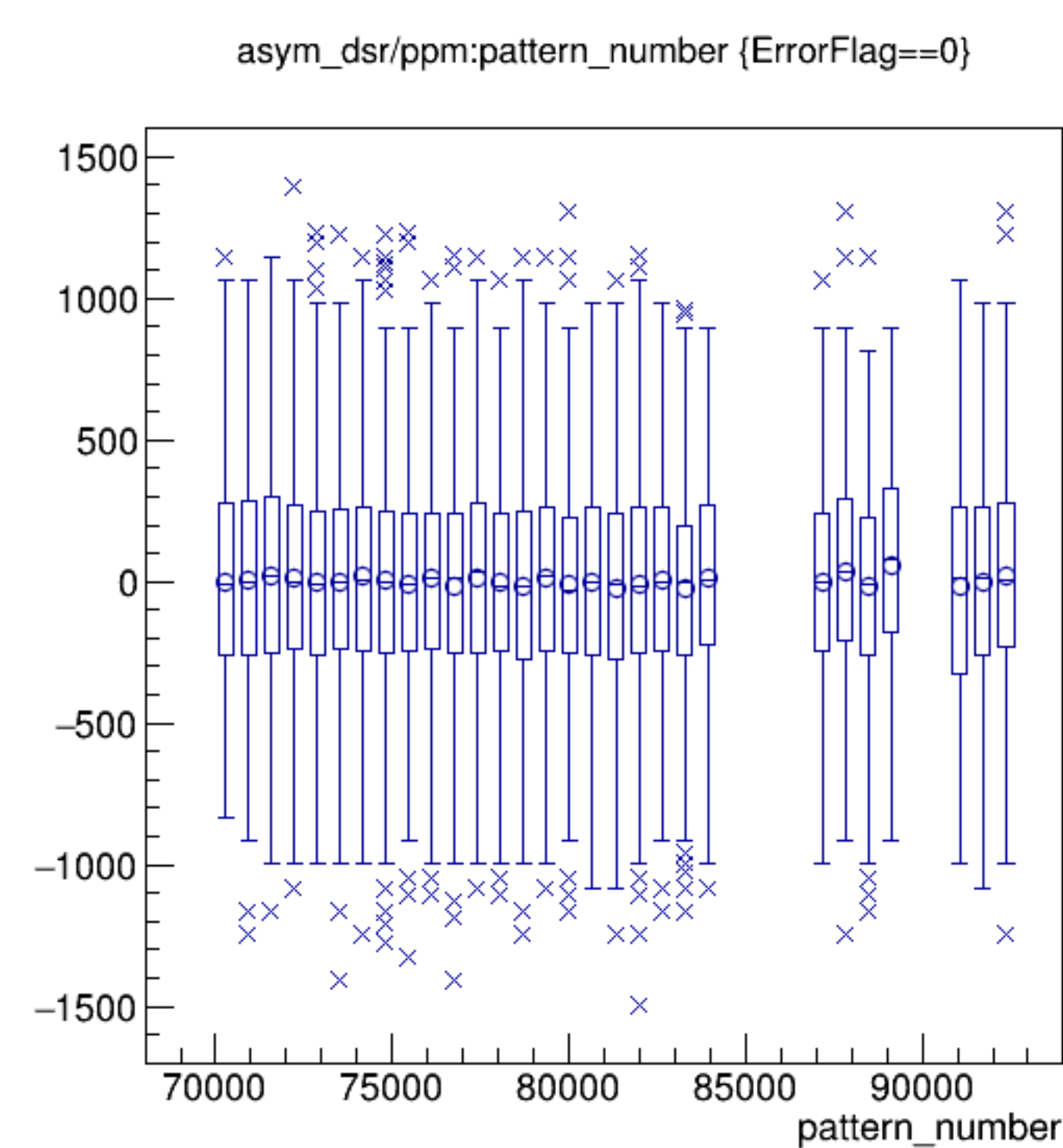
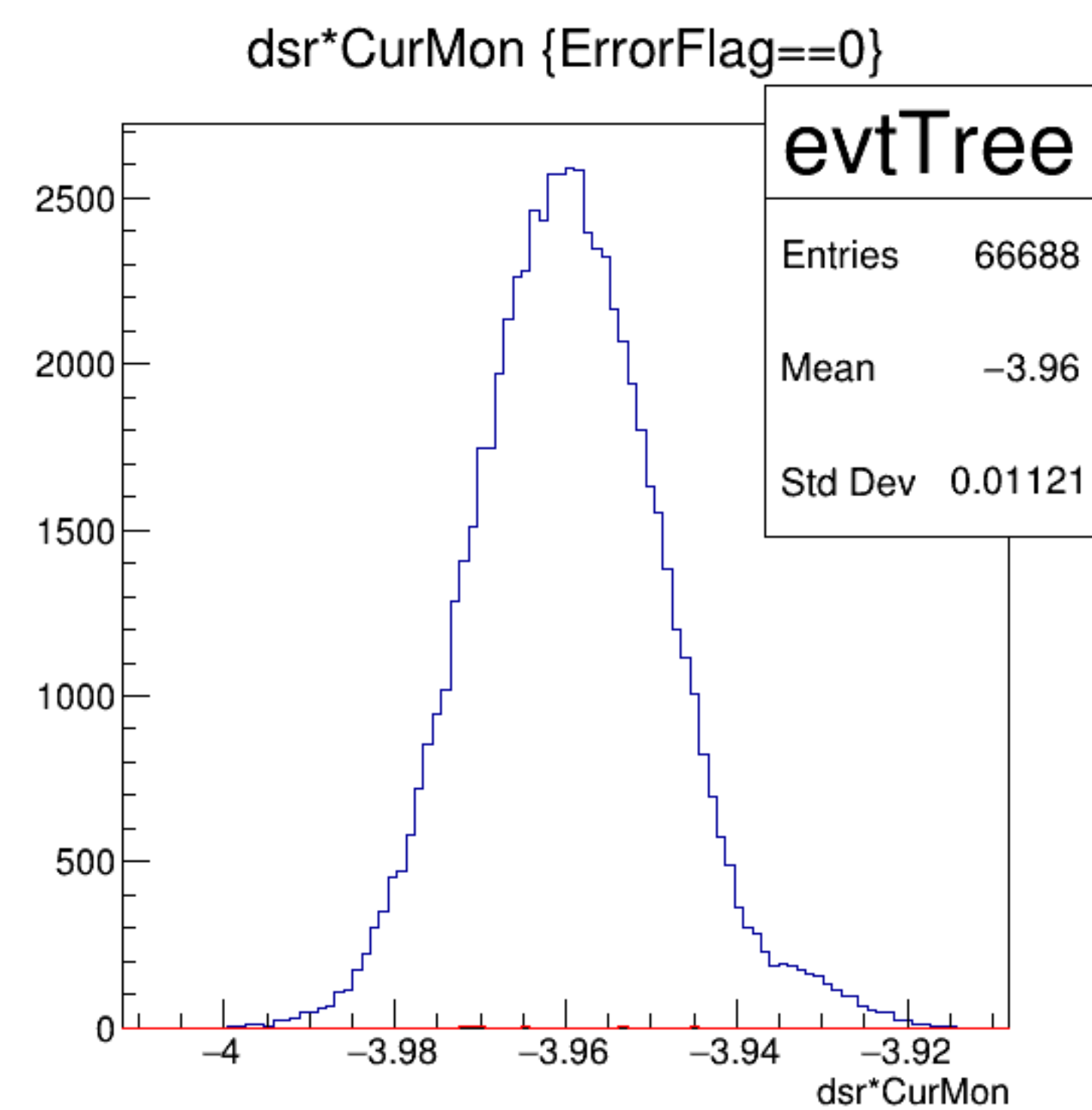
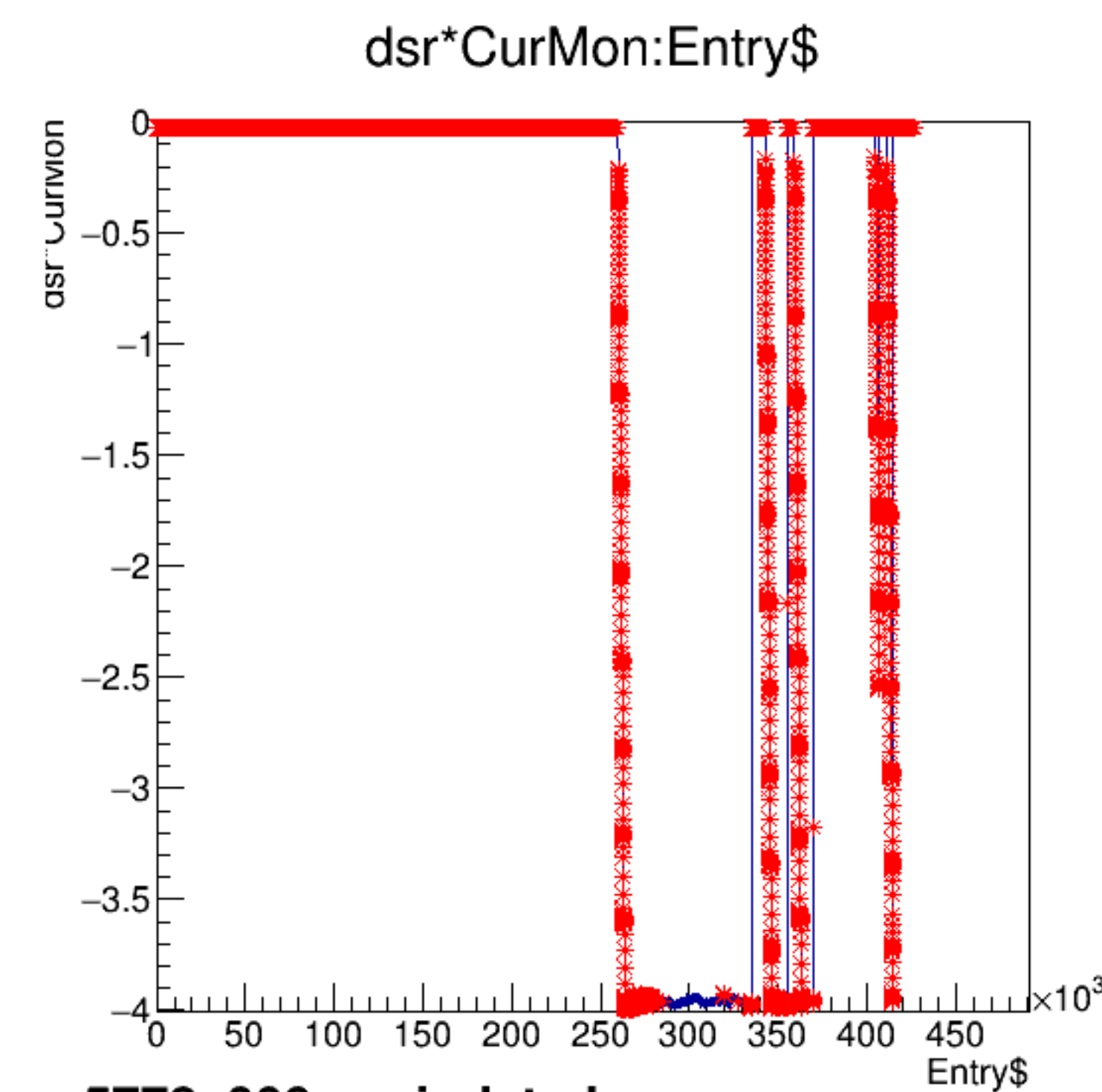


asym_usr/ppm:pattern_number {ErrorFlag==0}

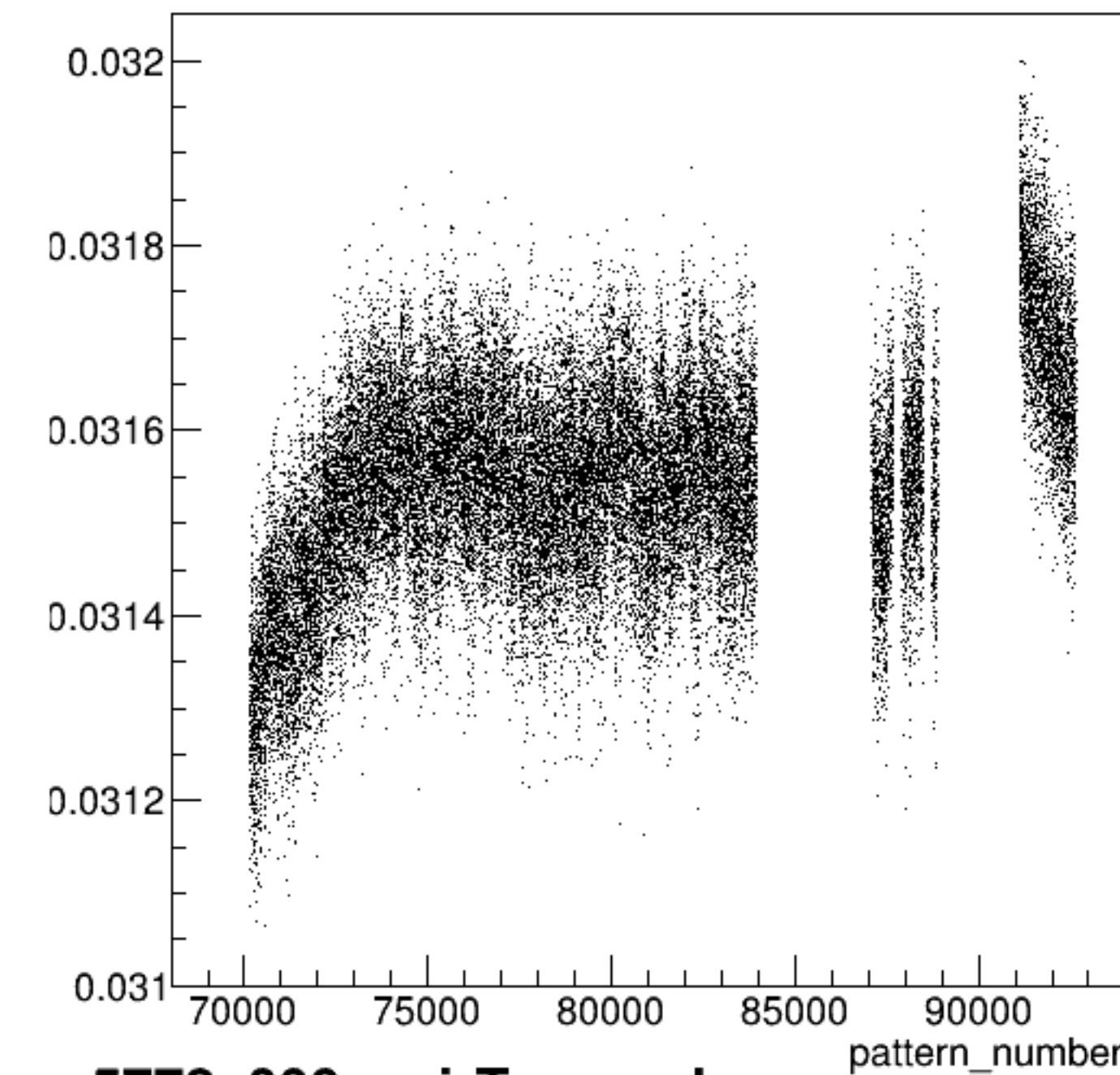


asym_usr



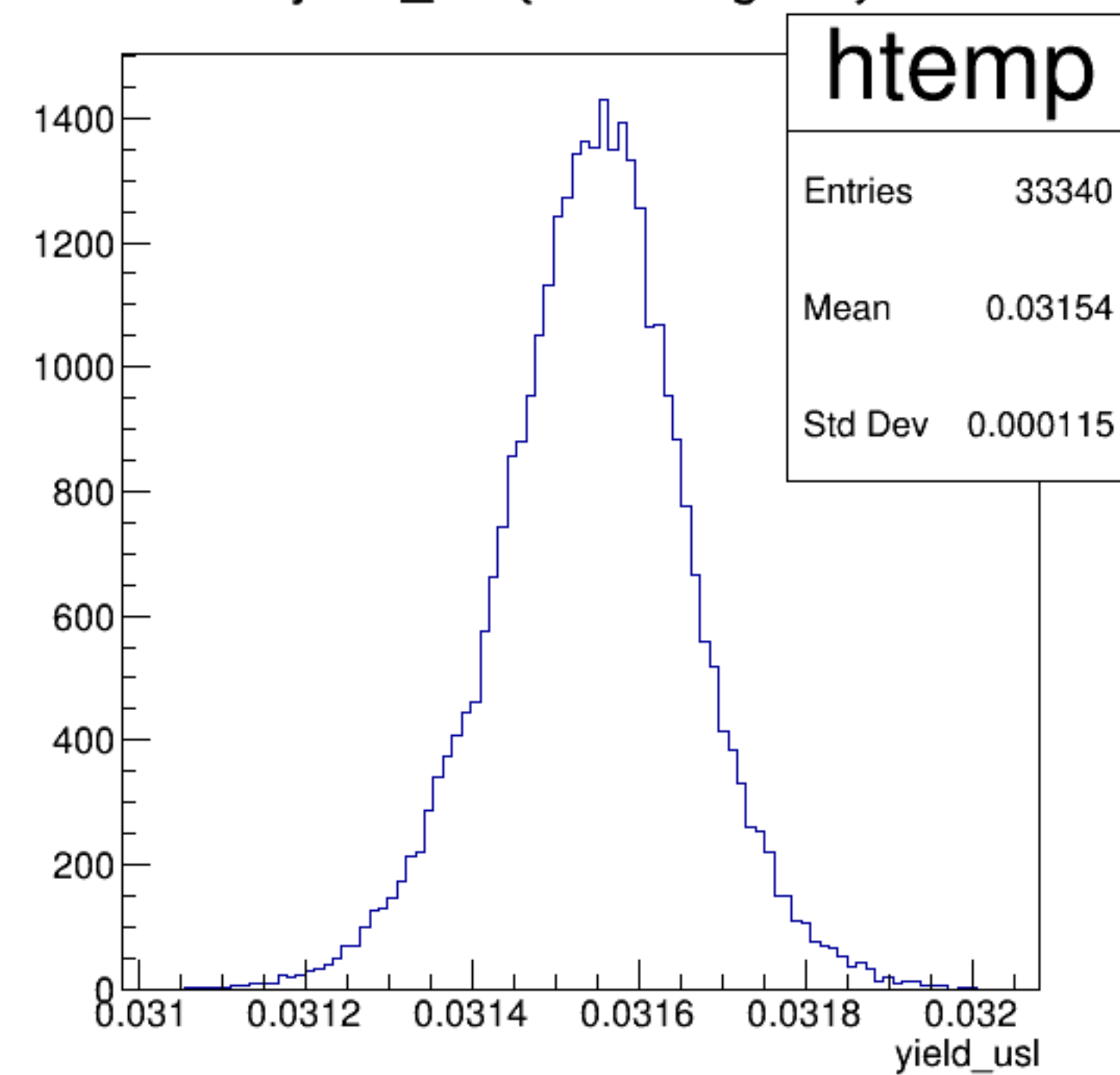


yield_usl:pattern_number {ErrorFlag==0}

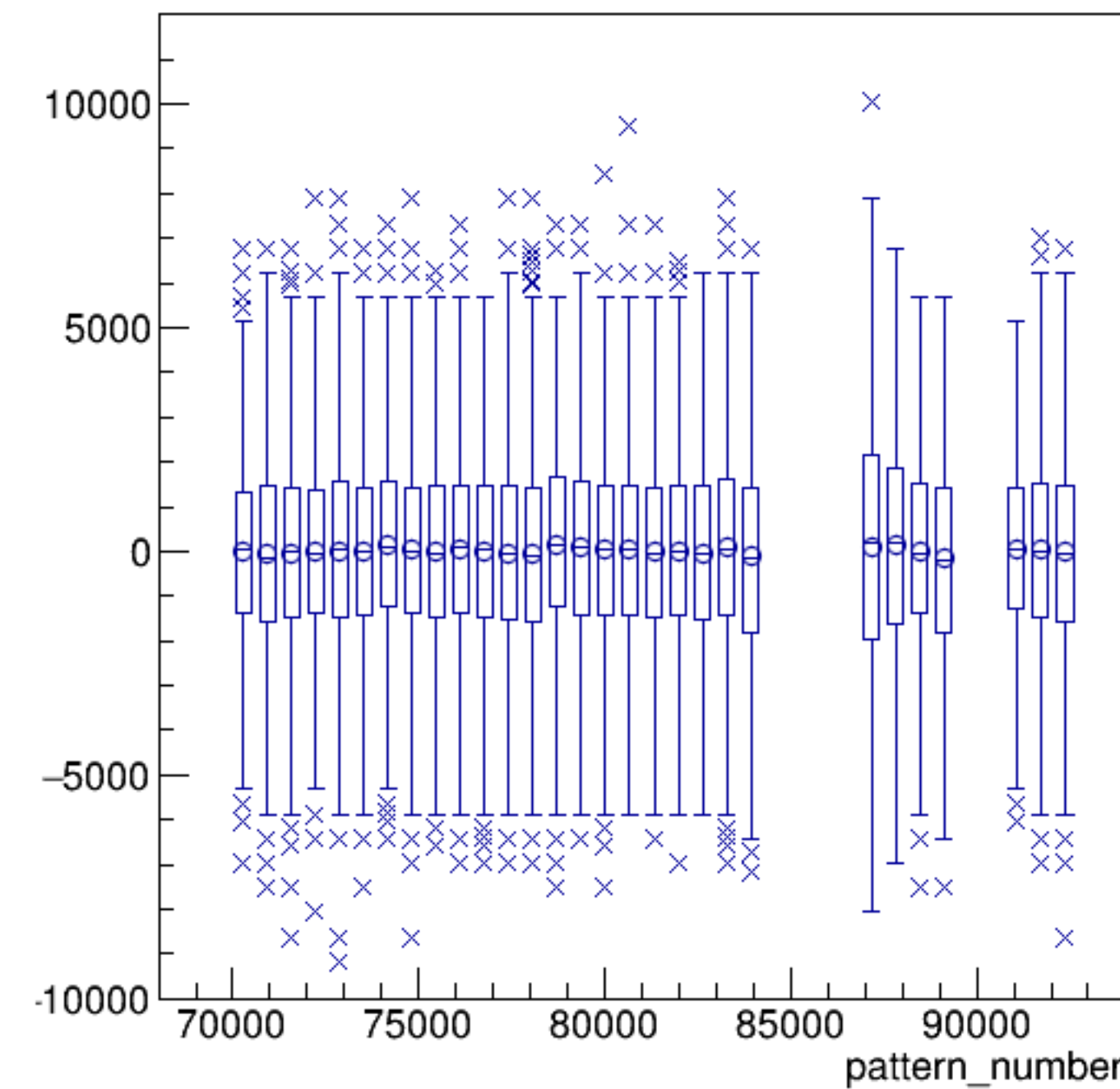


run5772_000_pairTree_usl.png

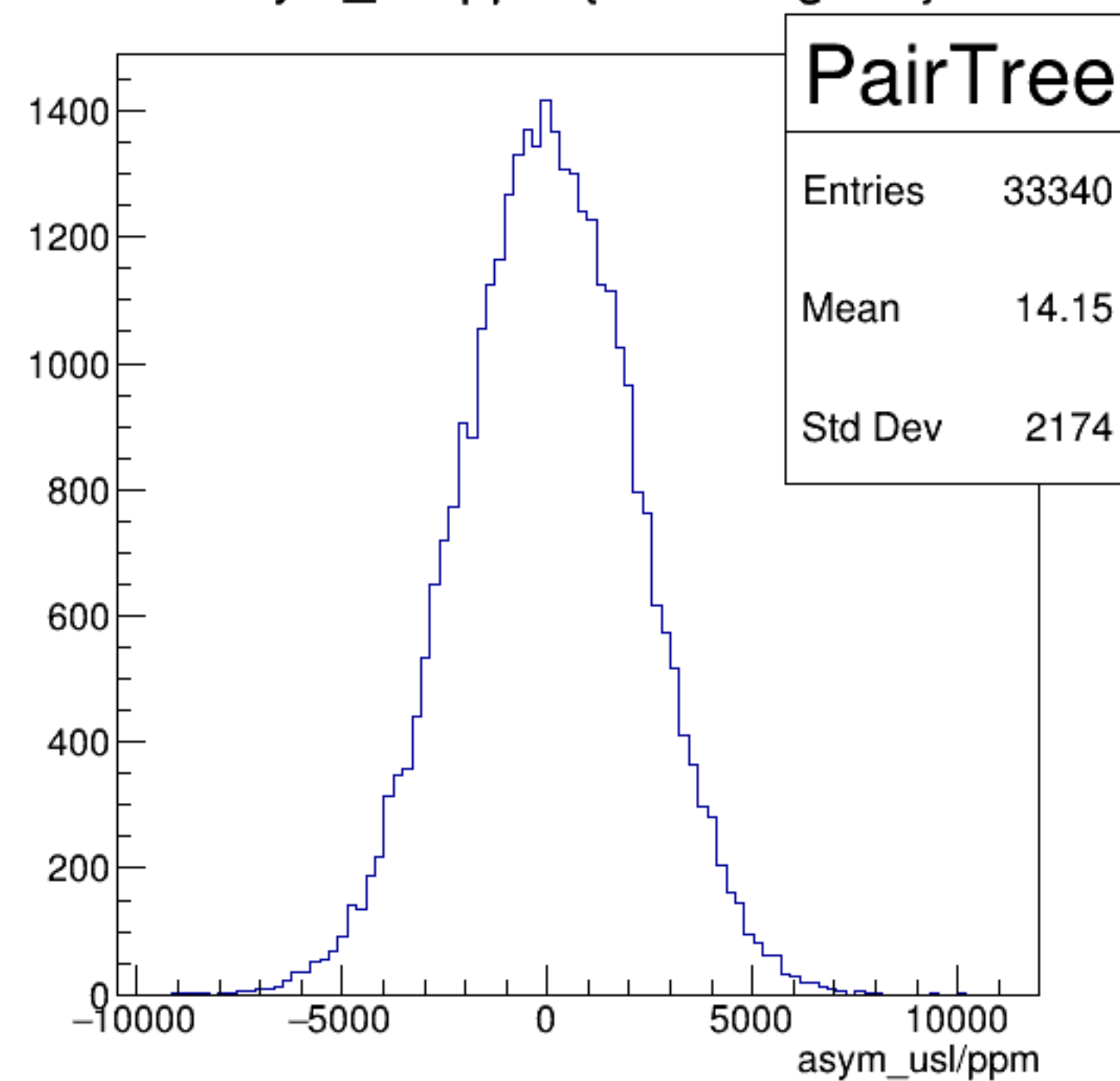
yield_usl {ErrorFlag==0}



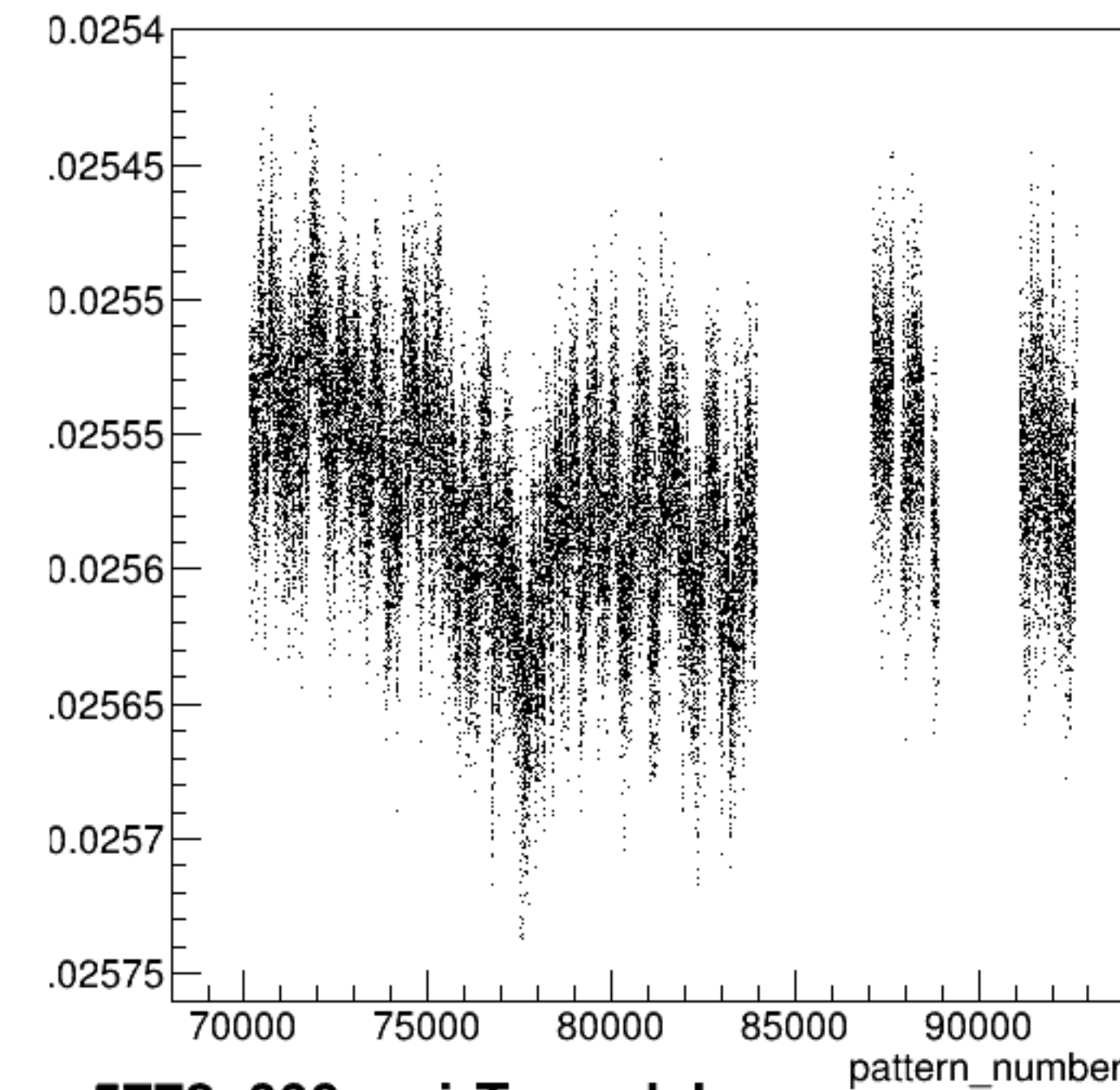
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl/ppm {ErrorFlag==0}

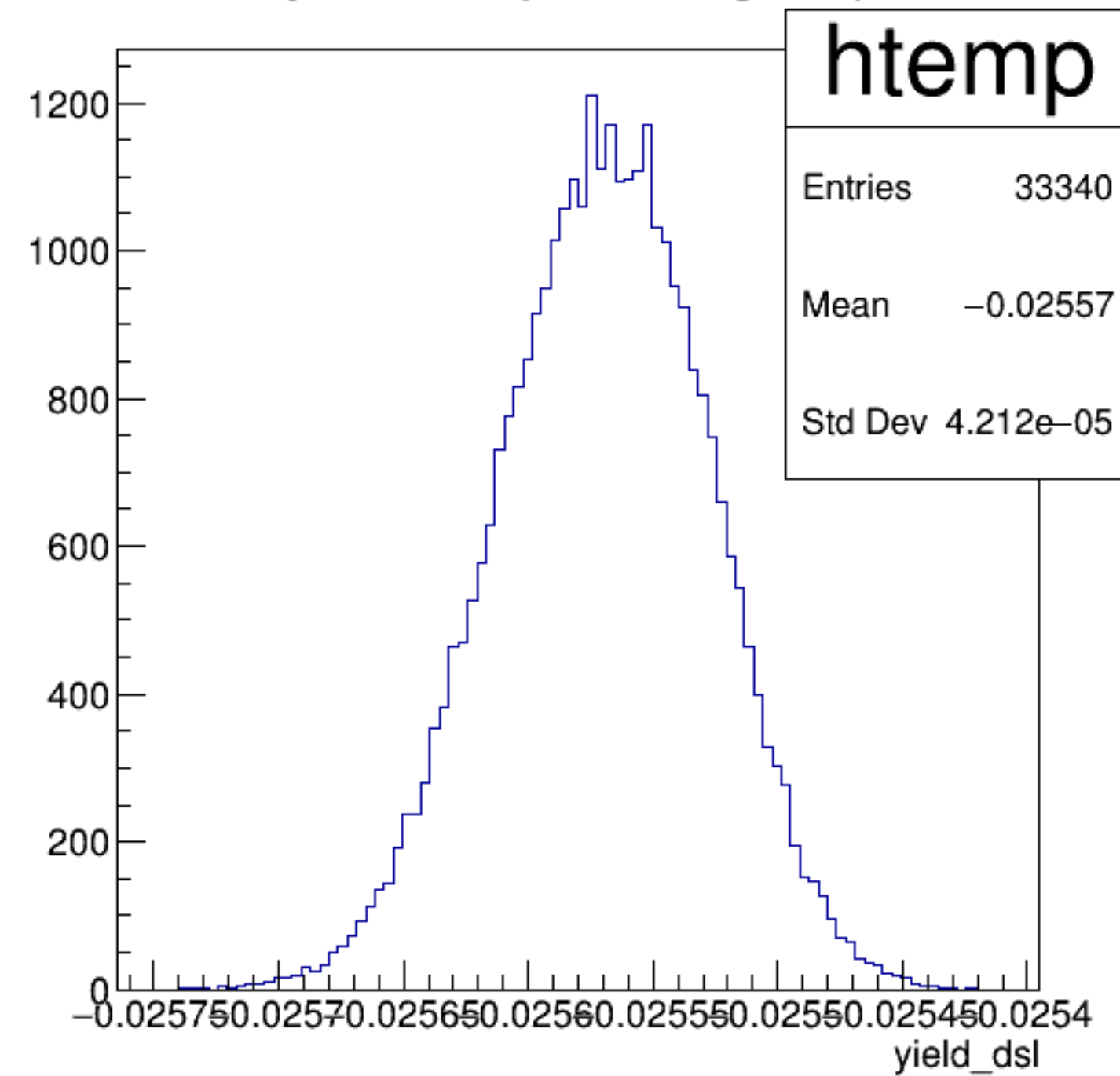


yield_dsl:pattern_number {ErrorFlag==0}

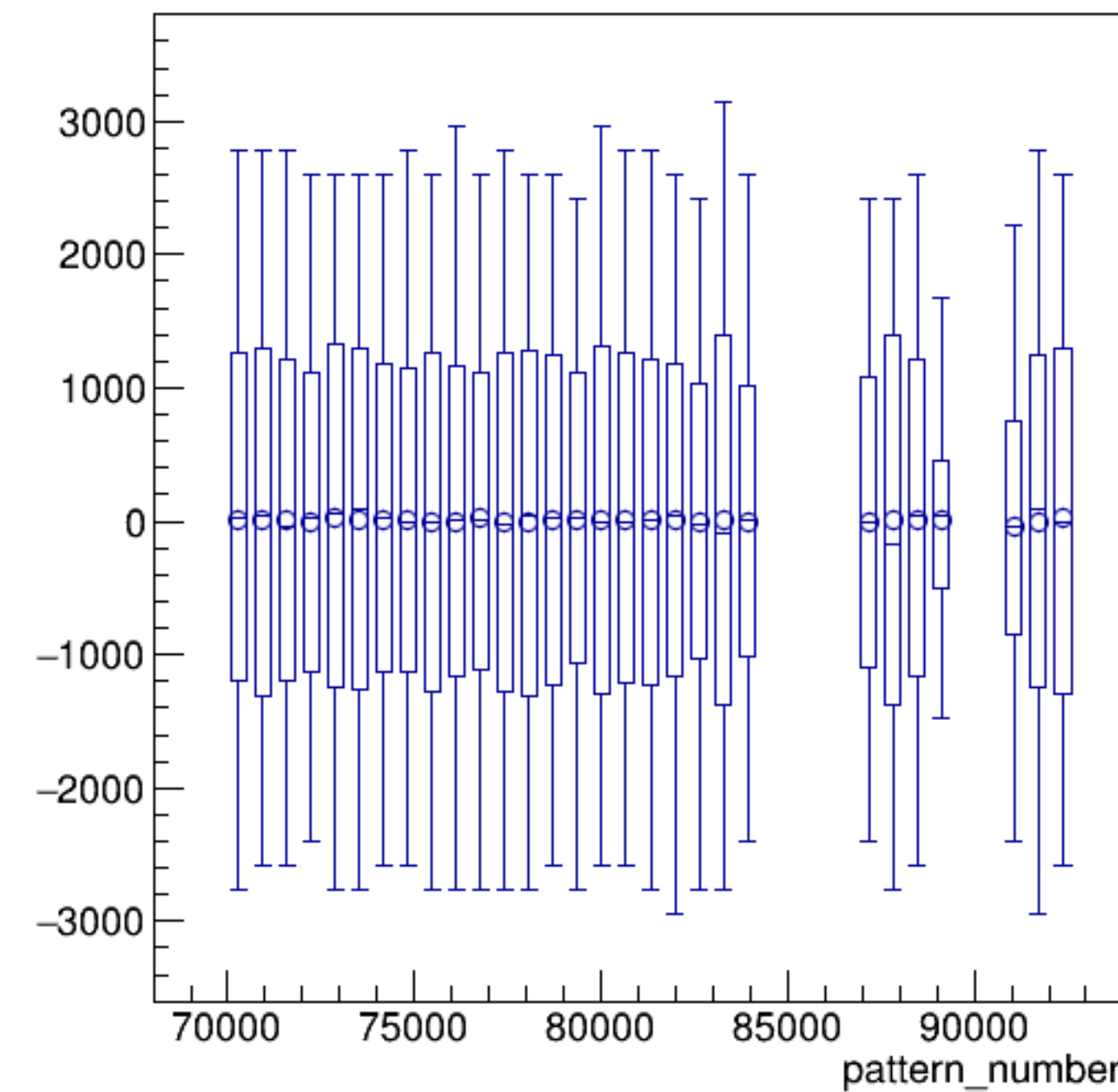


run5772_000_pairTree_dsl.png

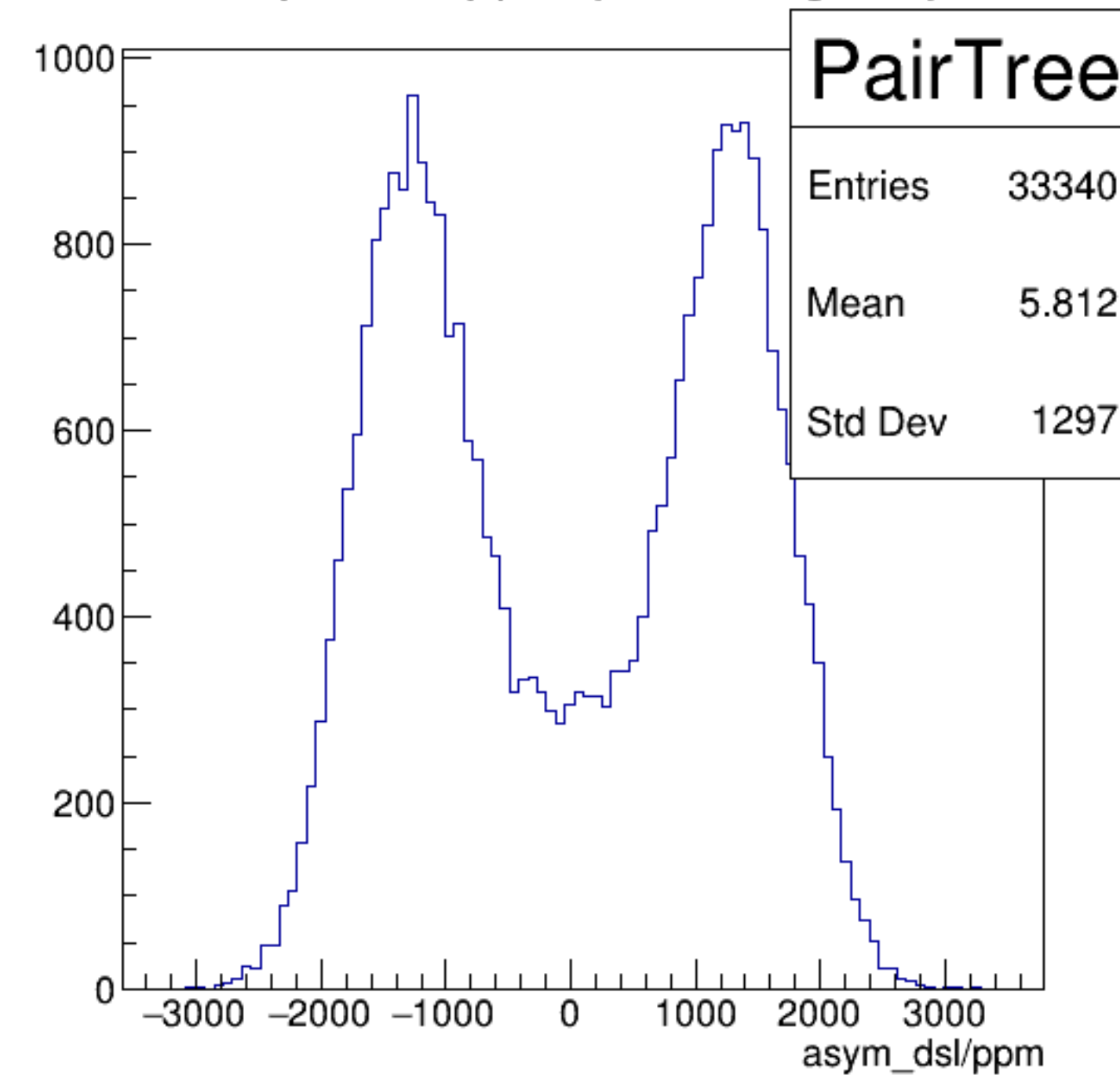
yield_dsl {ErrorFlag==0}



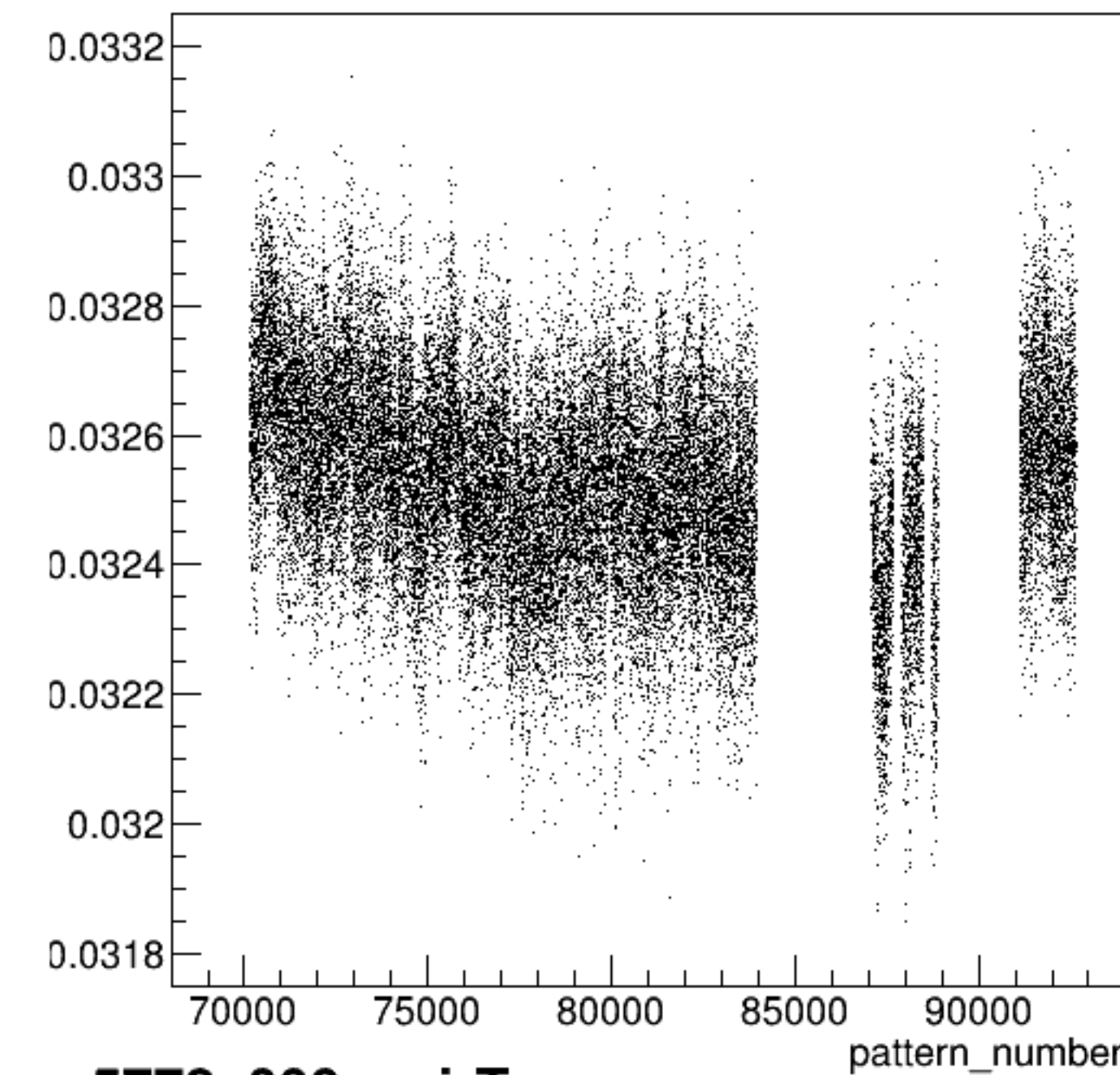
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl/ppm {ErrorFlag==0}

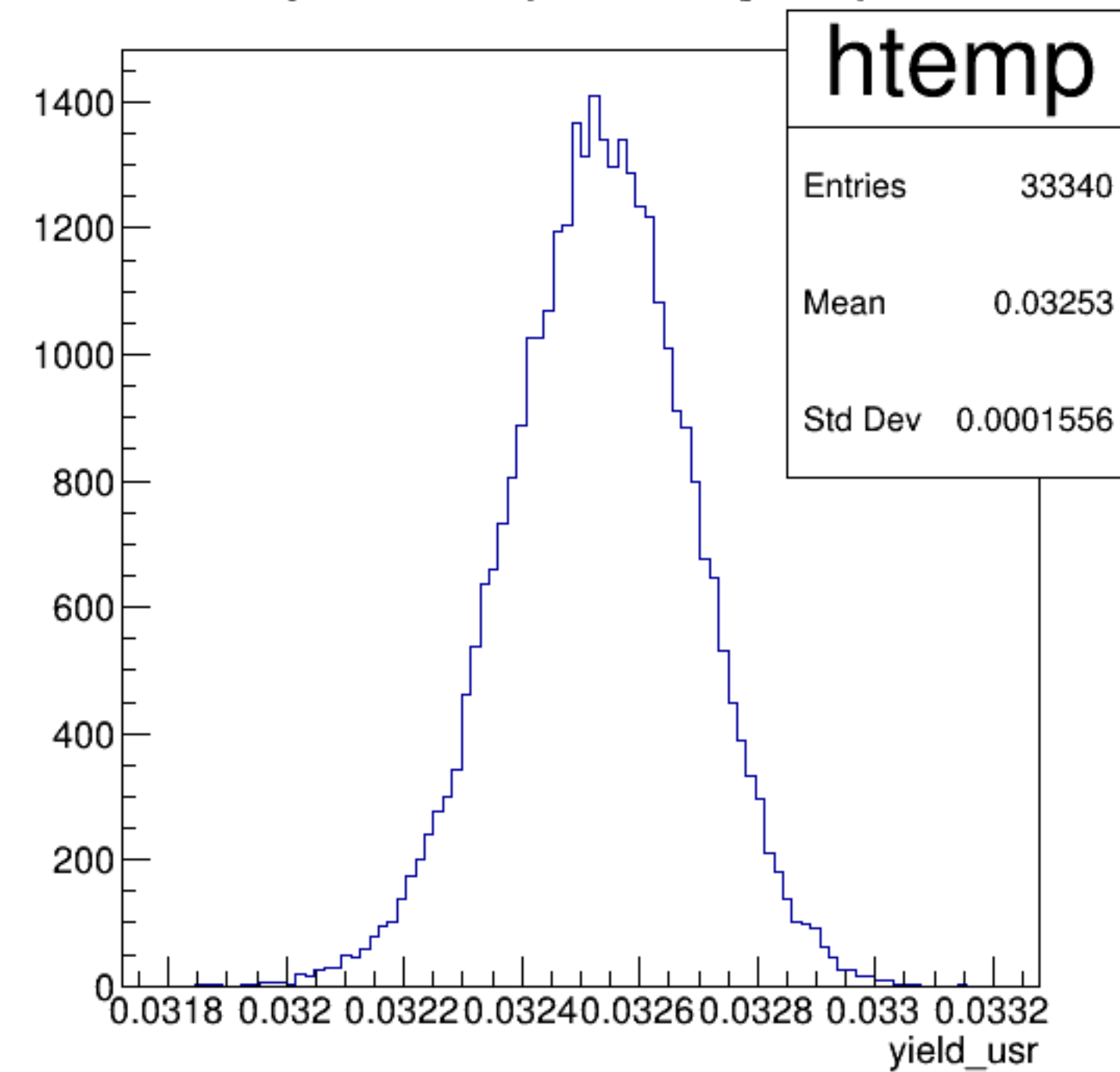


yield_usr:pattern_number {ErrorFlag==0}

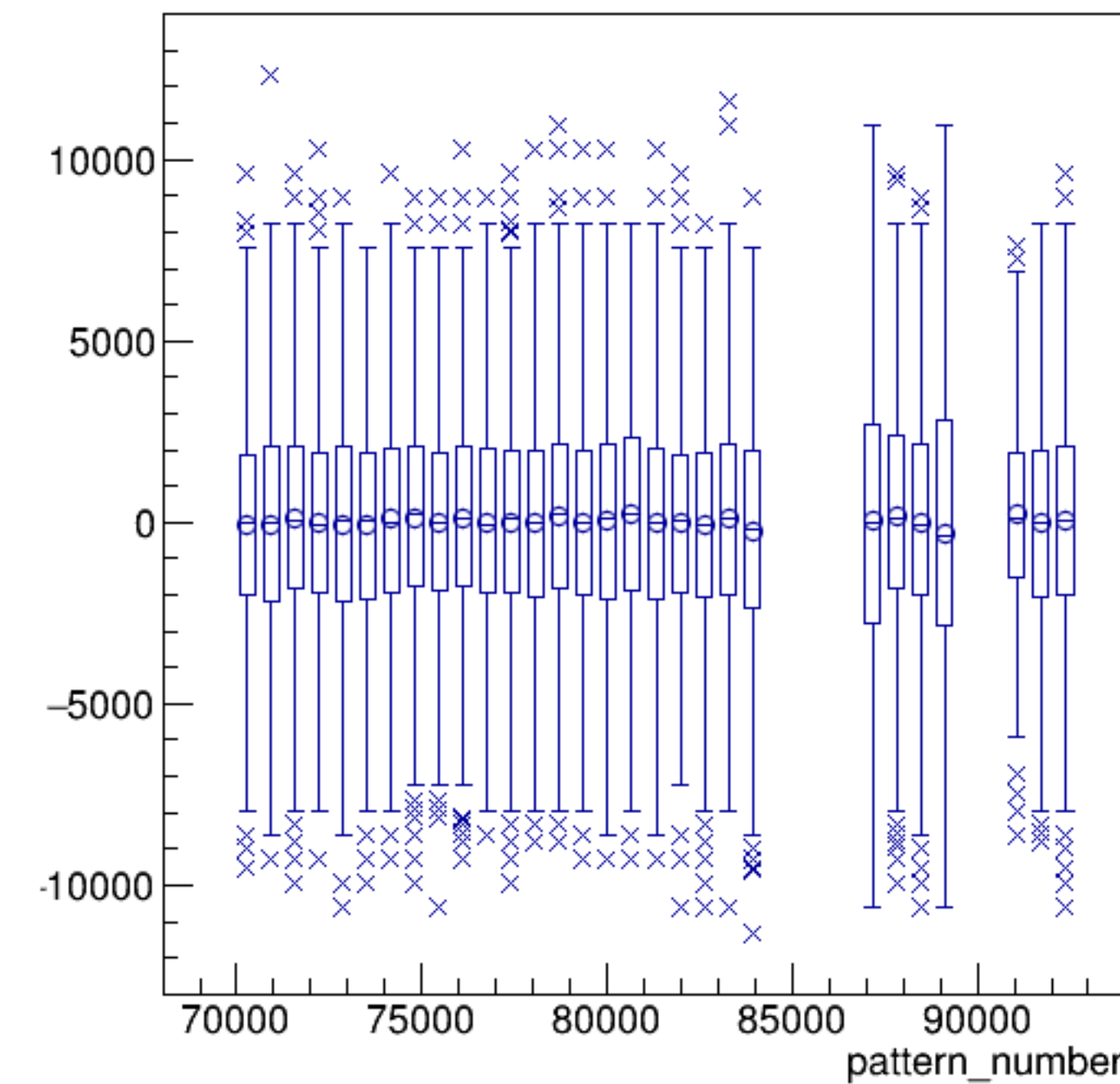


run5772_000_pairTree_usr.png

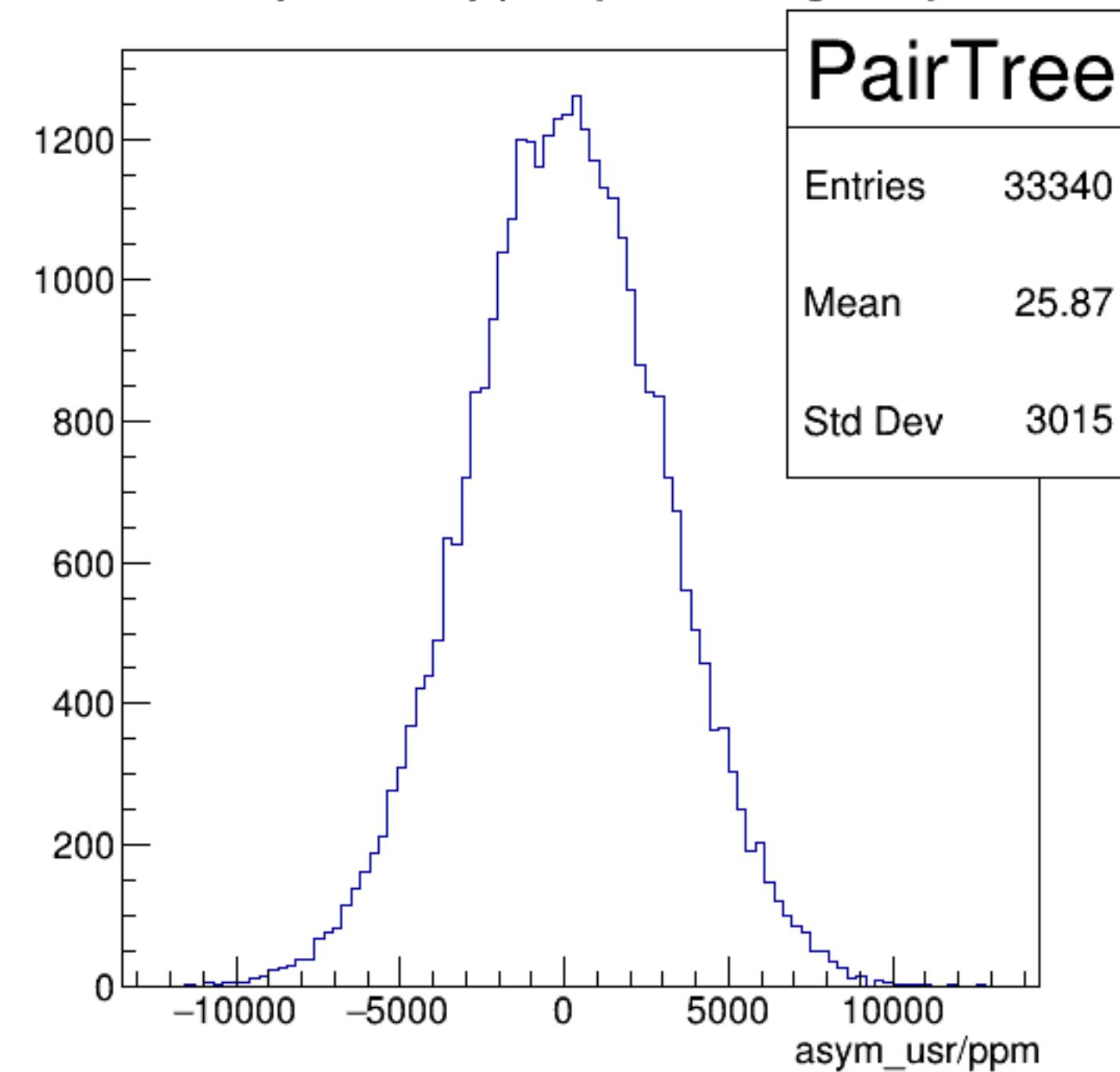
yield_usr {ErrorFlag==0}



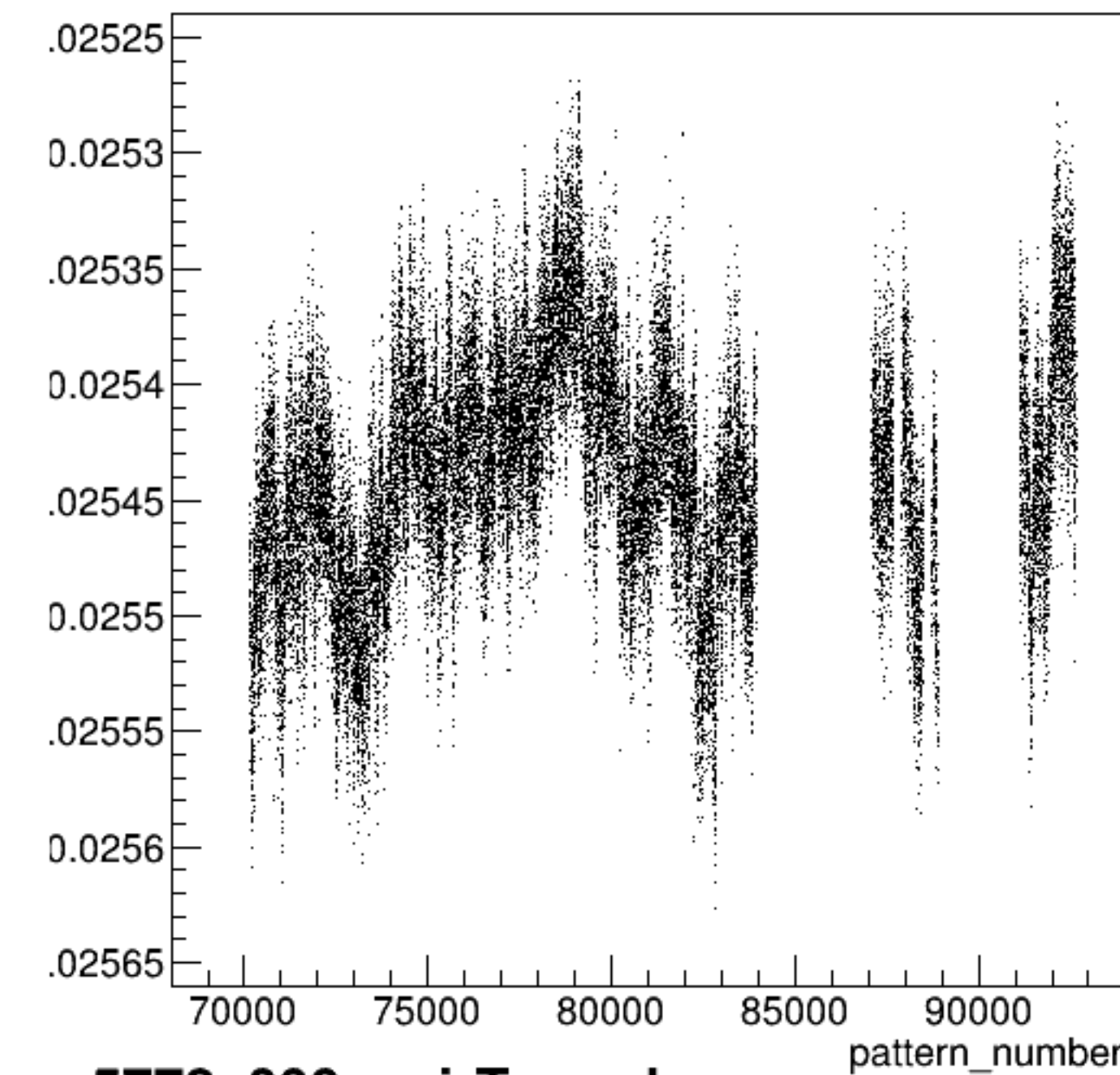
asym_usr/ppm:pattern_number {ErrorFlag==0}



asym_usr/ppm {ErrorFlag==0}

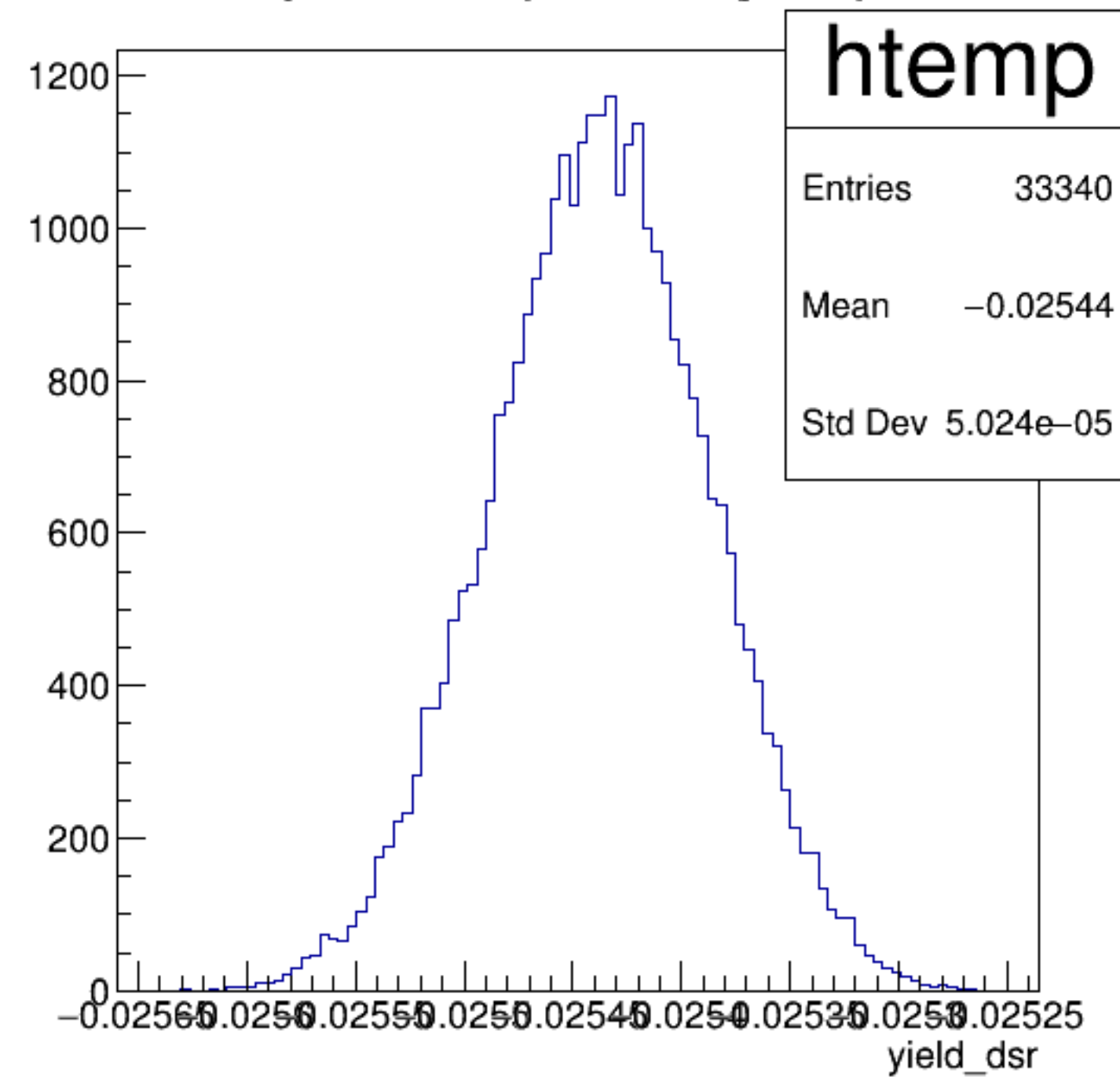


yield_dsr:pattern_number {ErrorFlag==0}

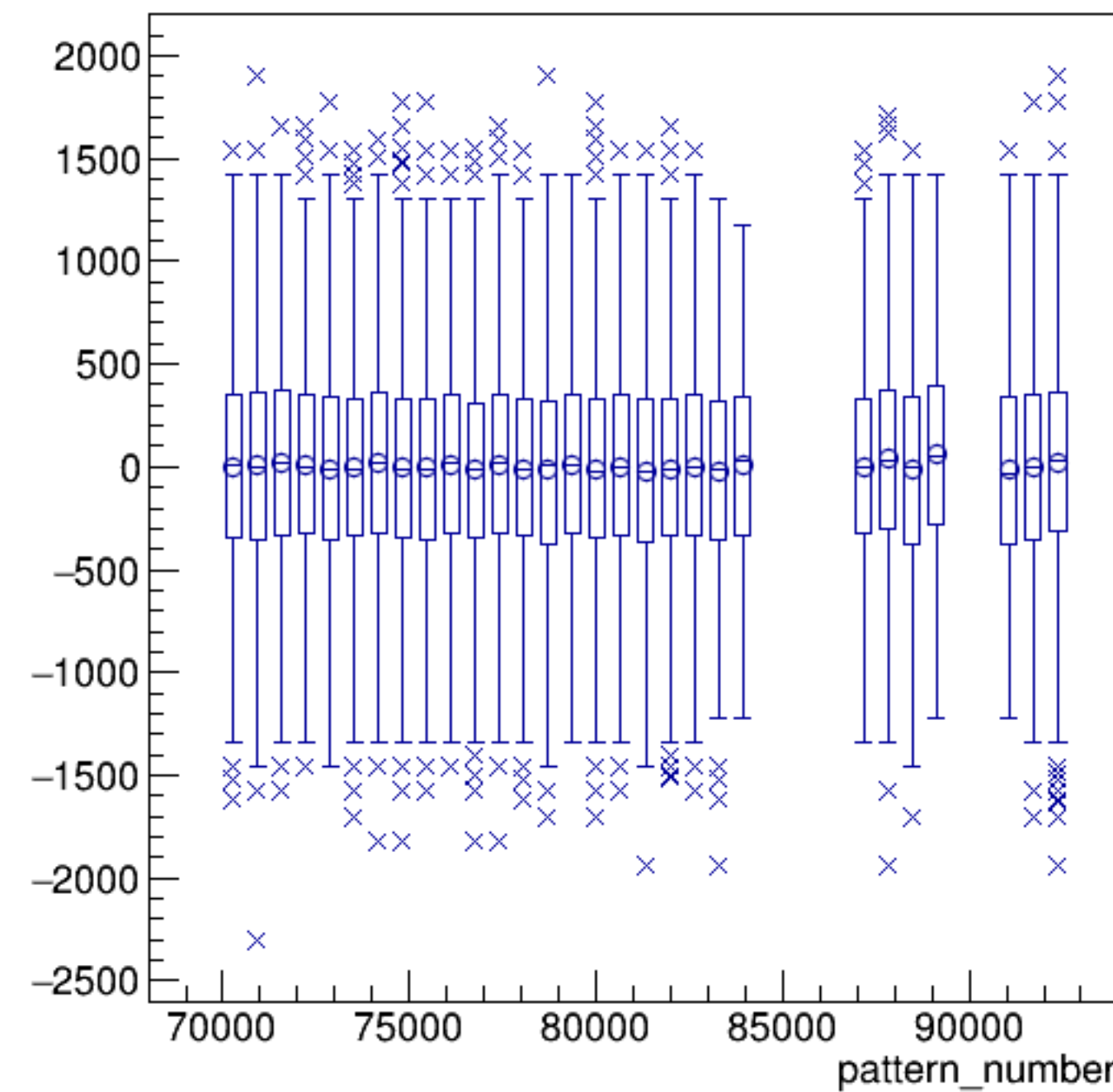


run5772_000_pairTree_dsr.png

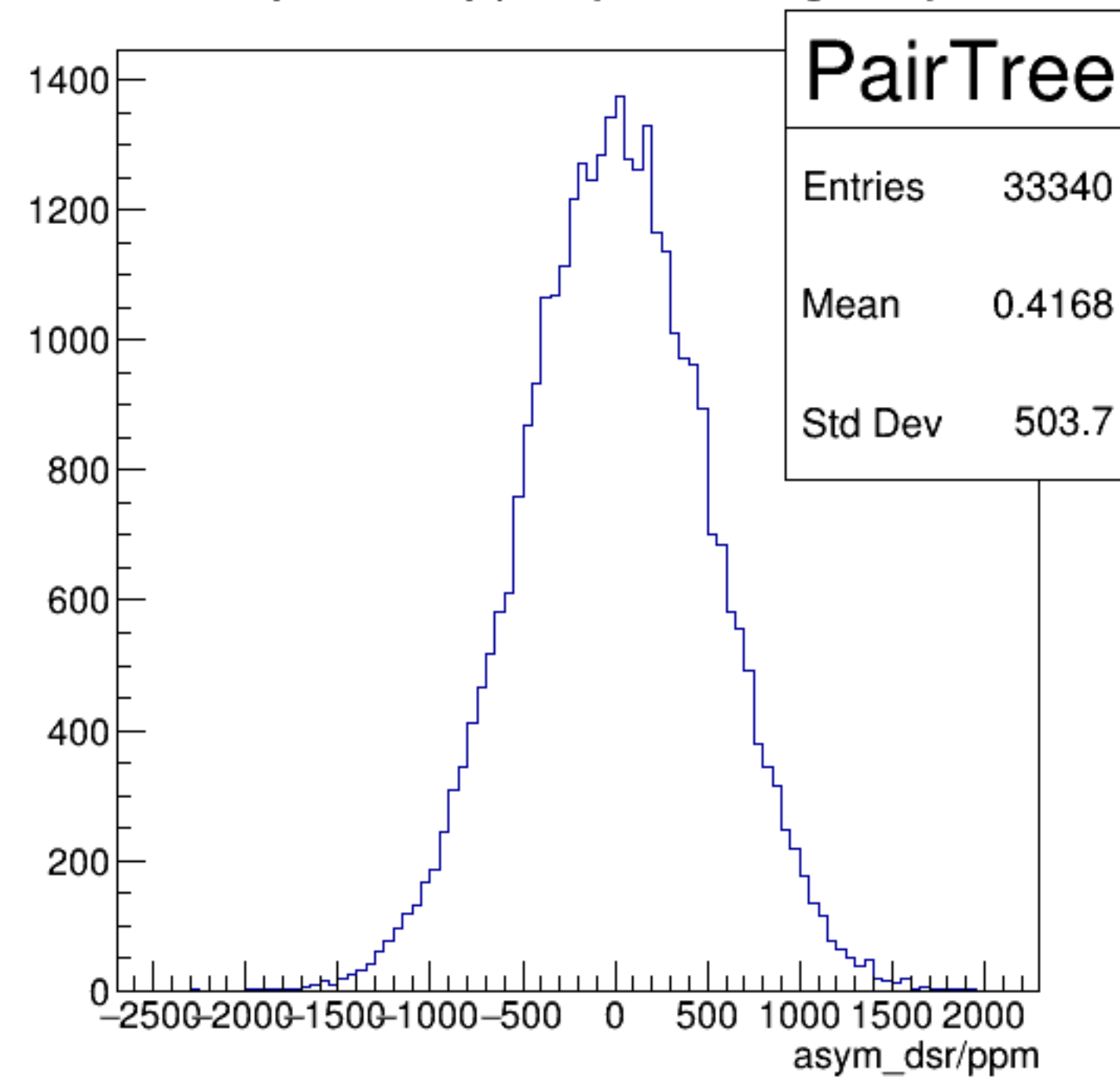
yield_dsr {ErrorFlag==0}



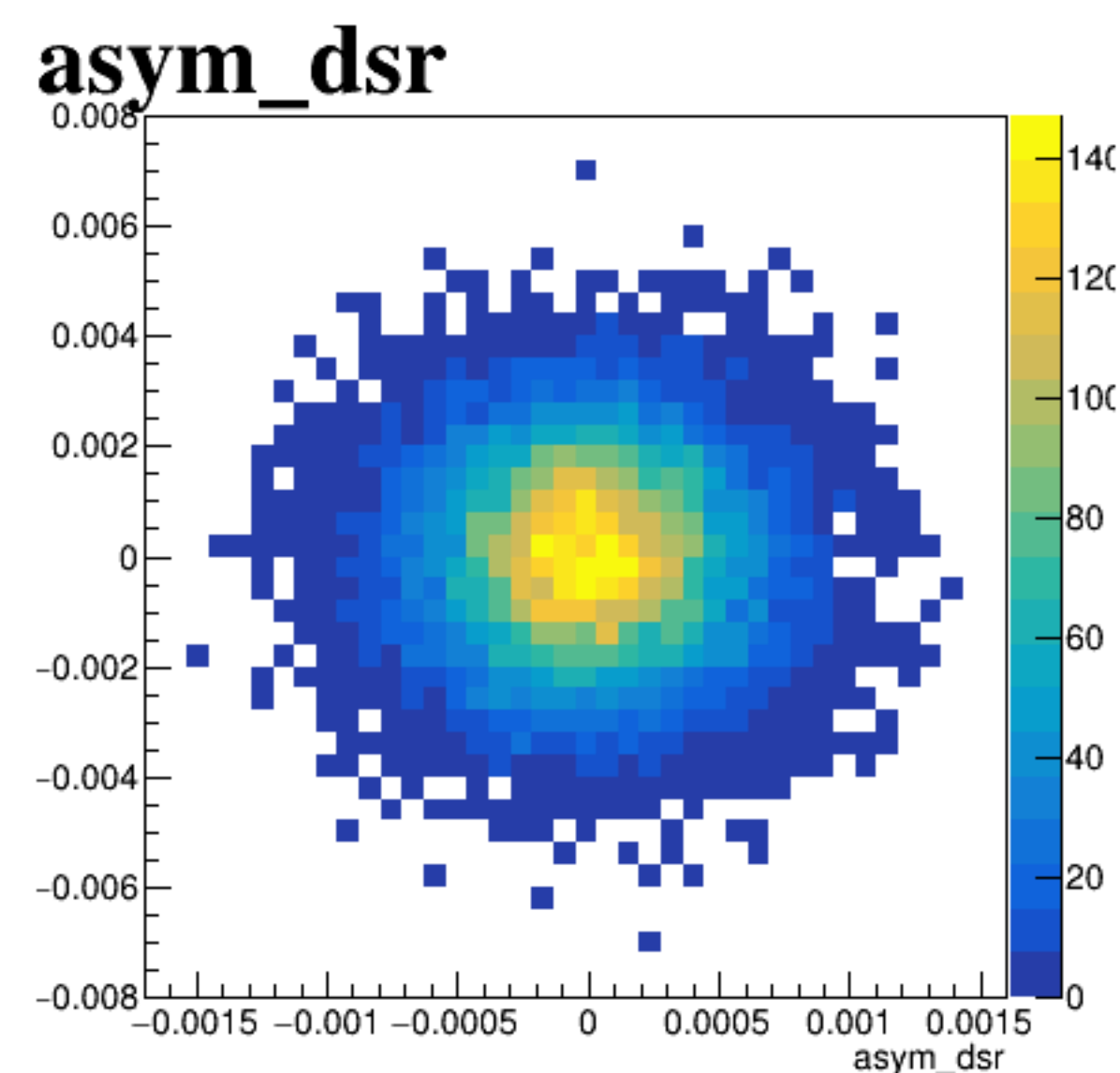
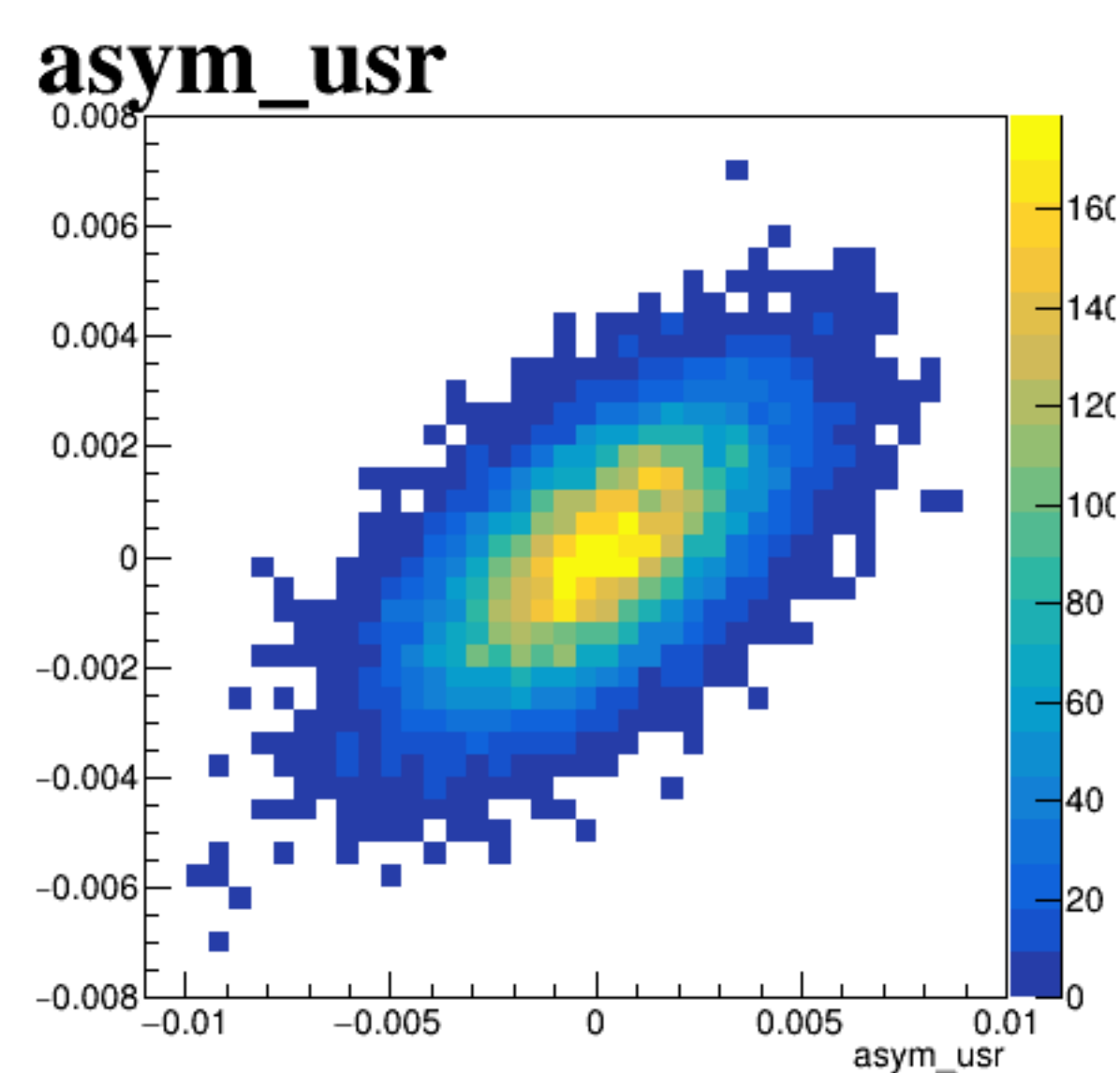
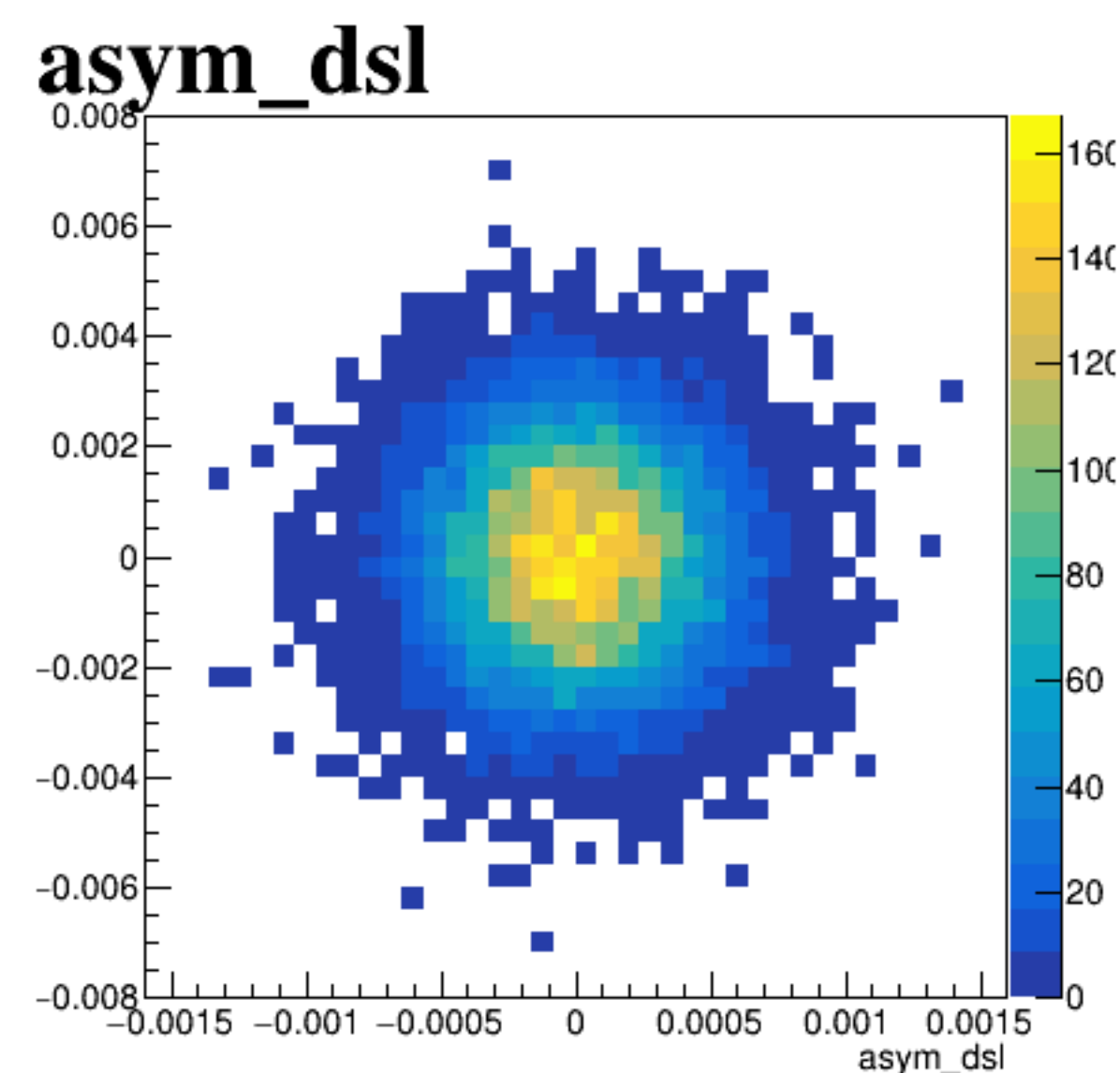
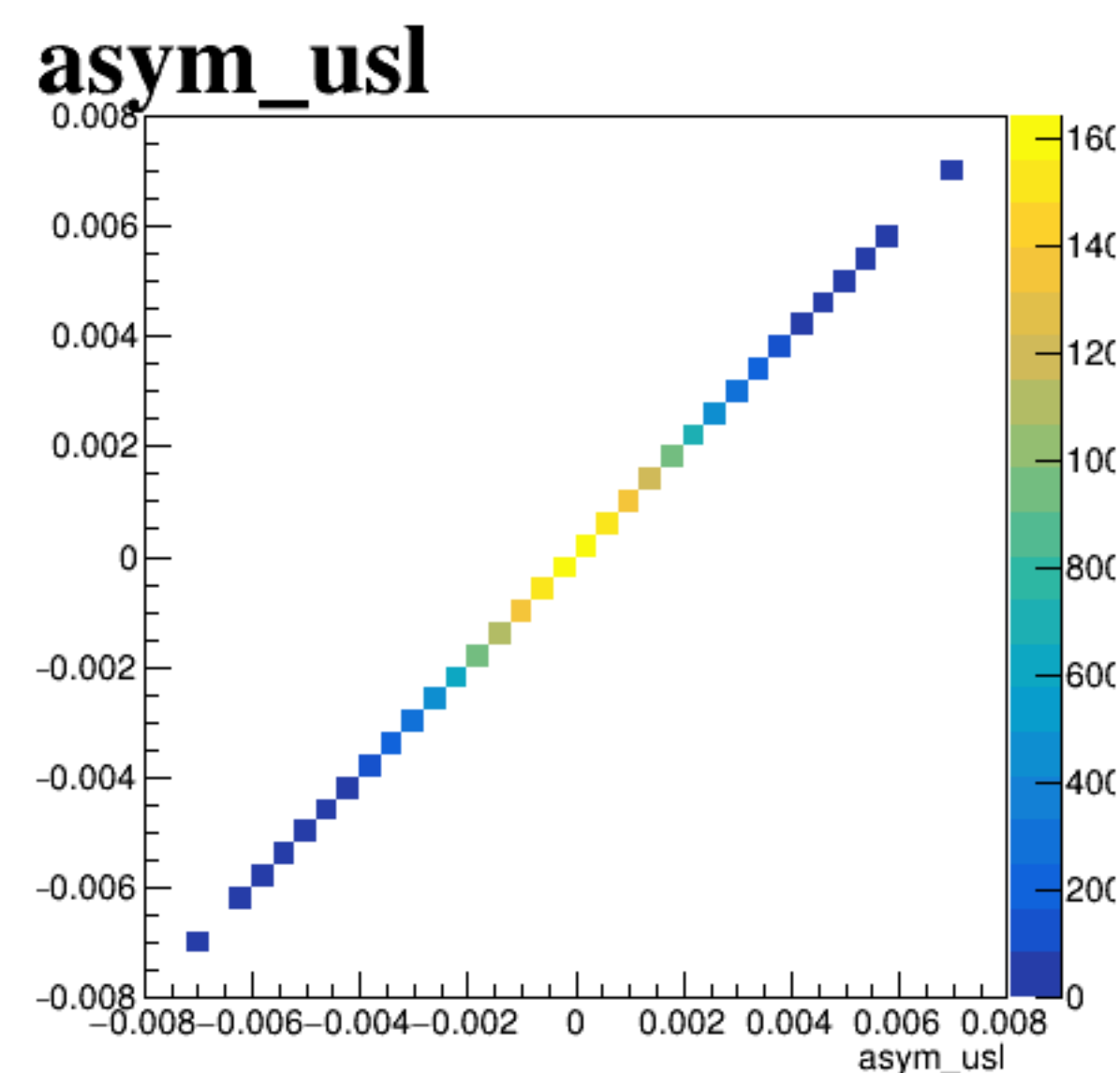
asym_dsr/ppm:pattern_number {ErrorFlag==0}



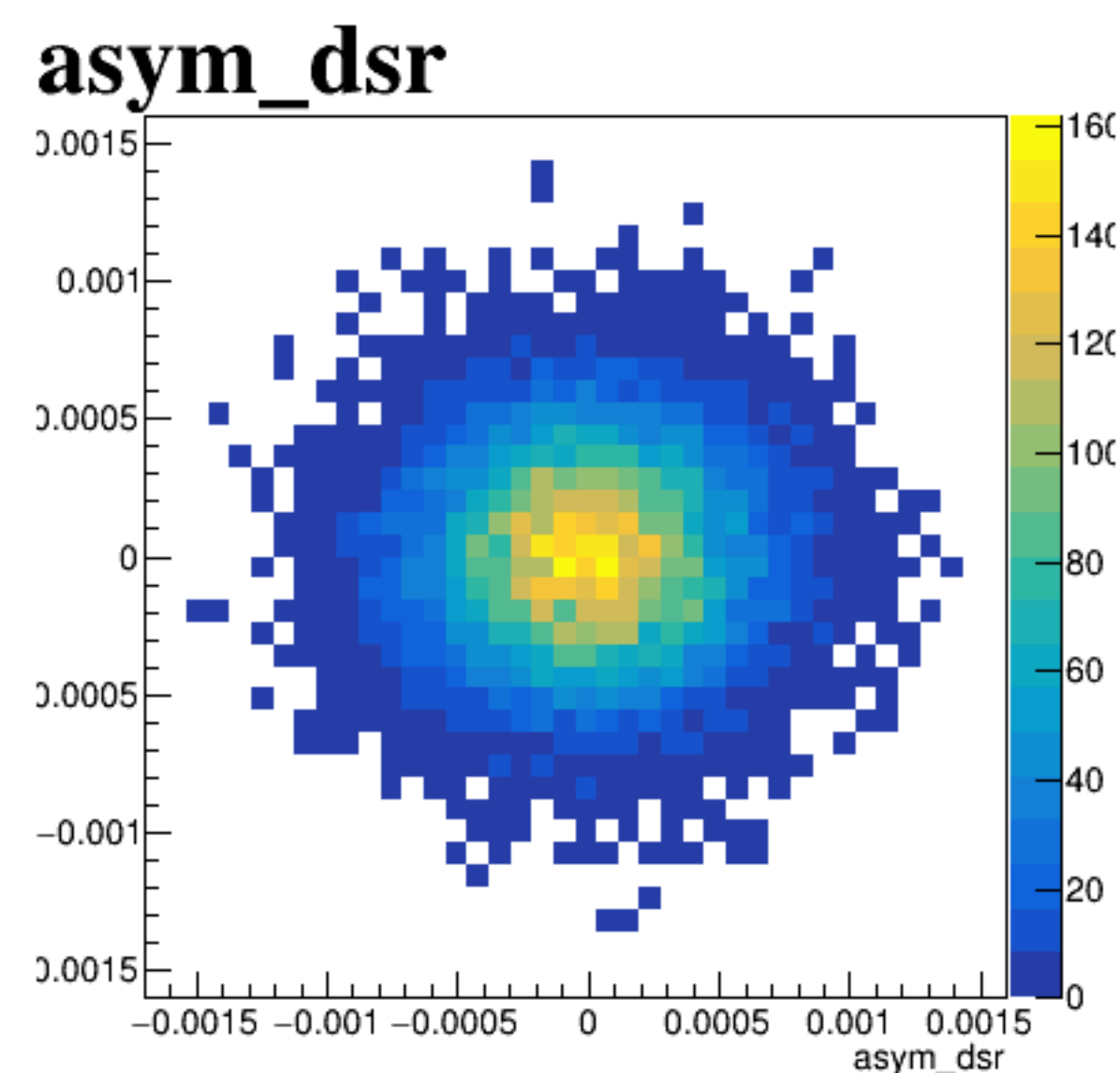
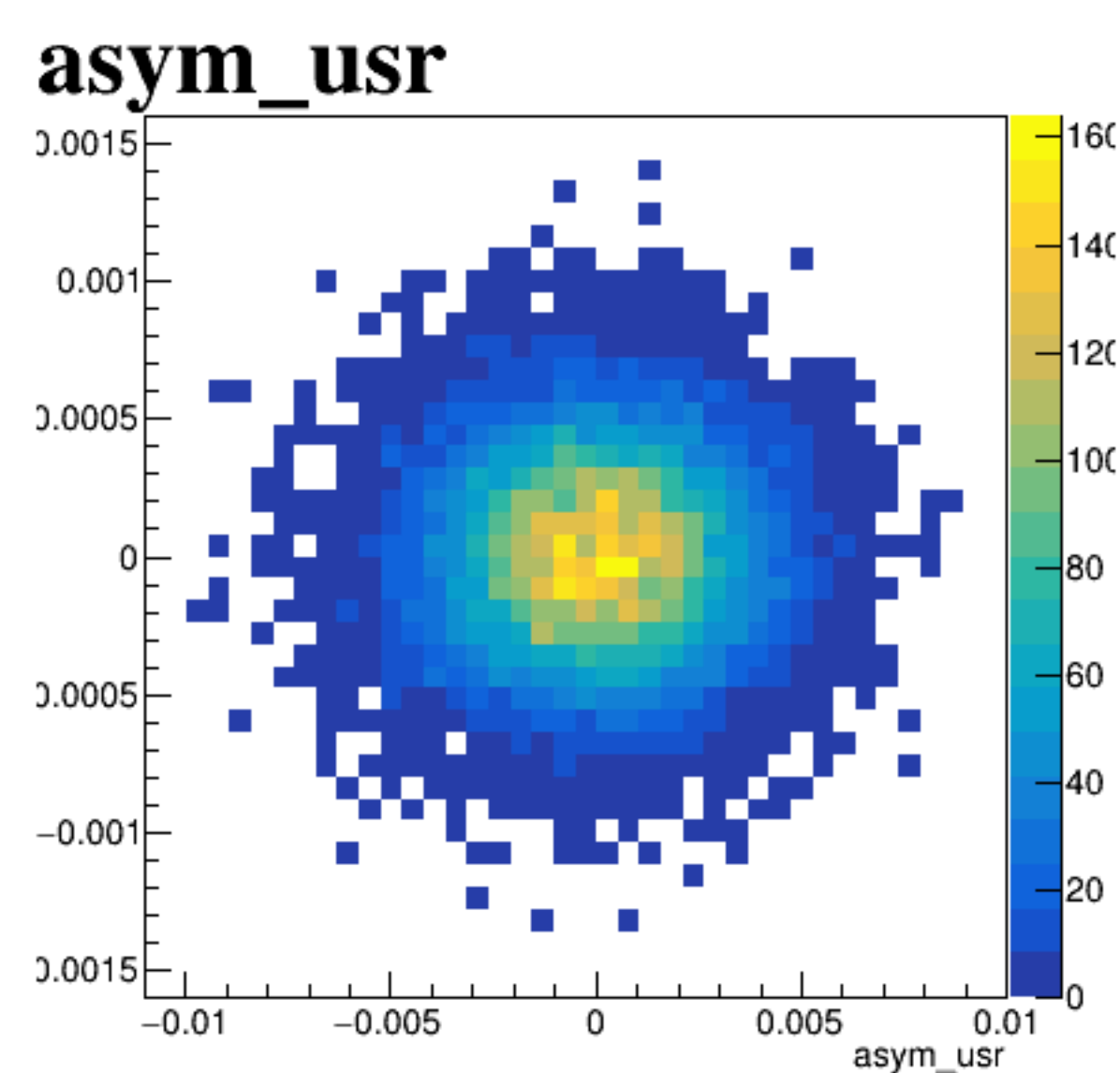
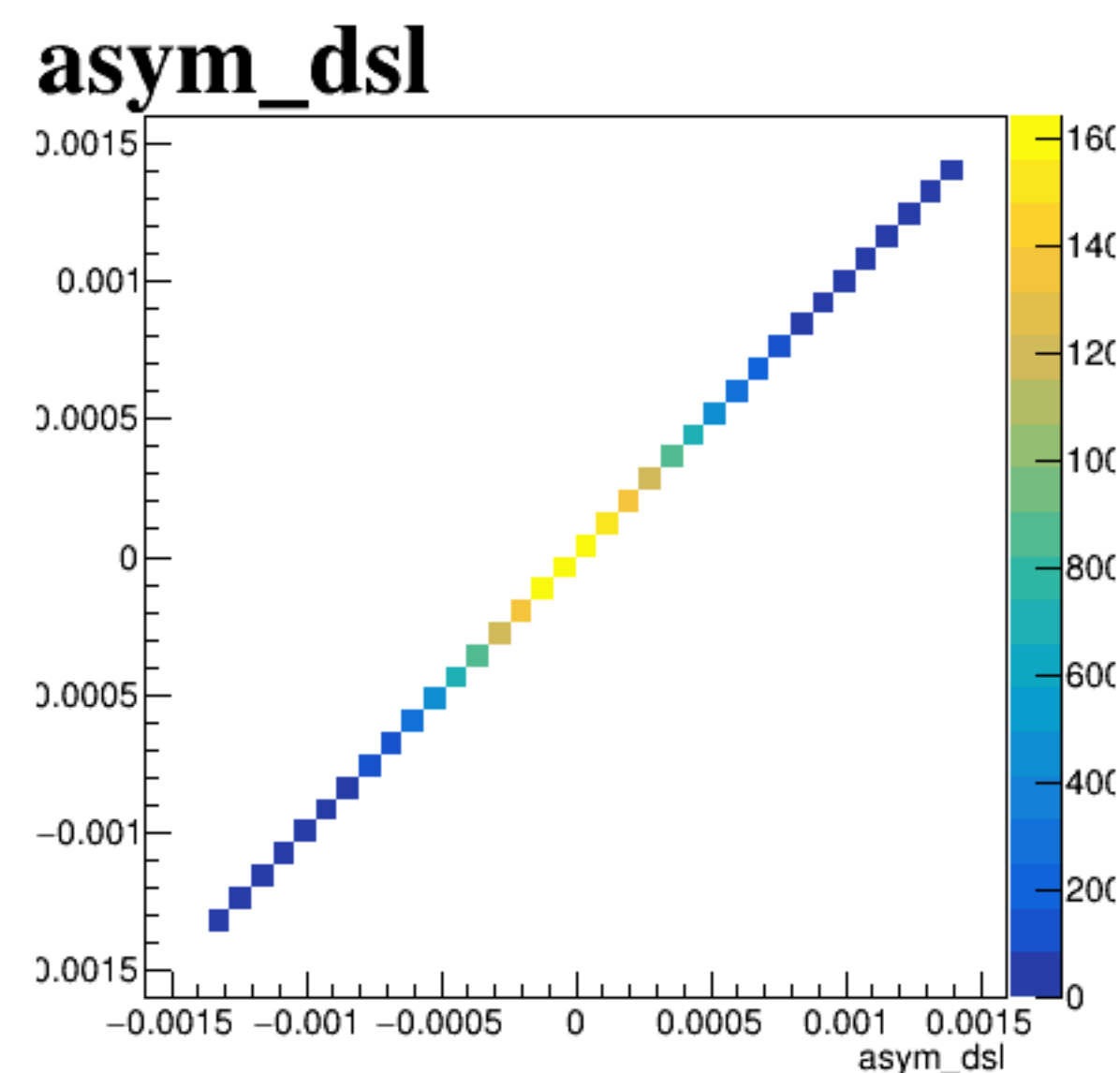
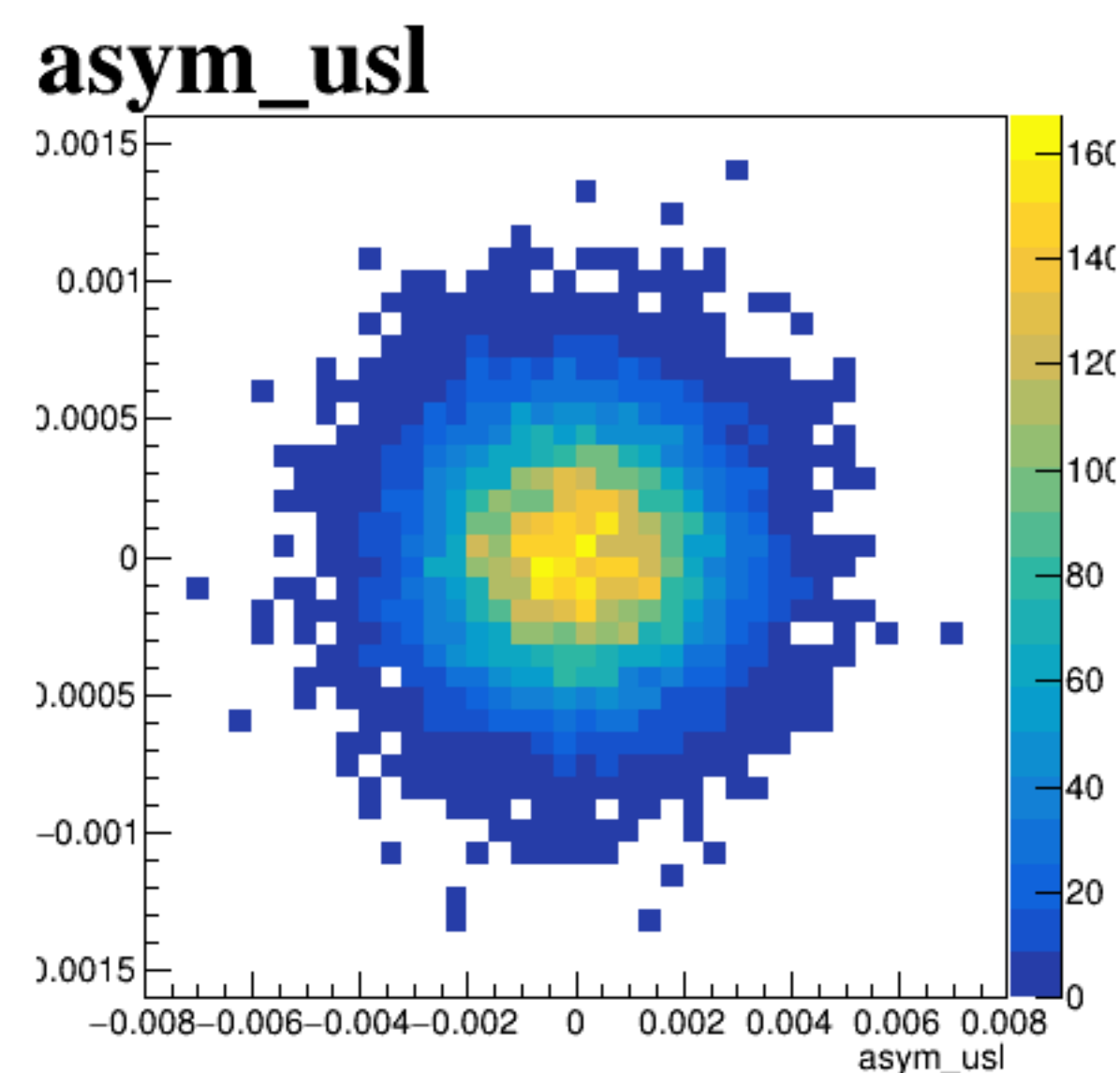
asym_dsr/ppm {ErrorFlag==0}



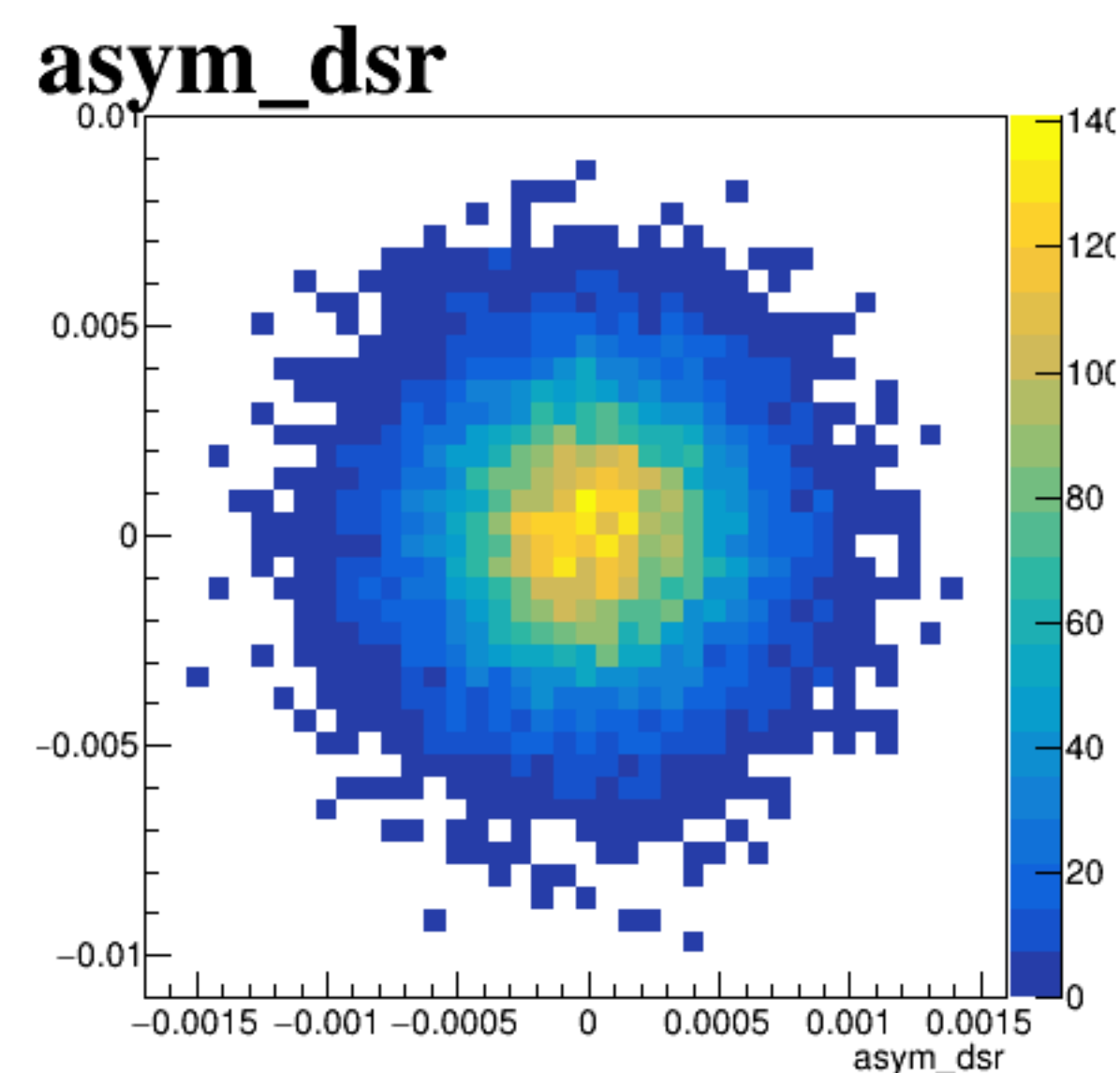
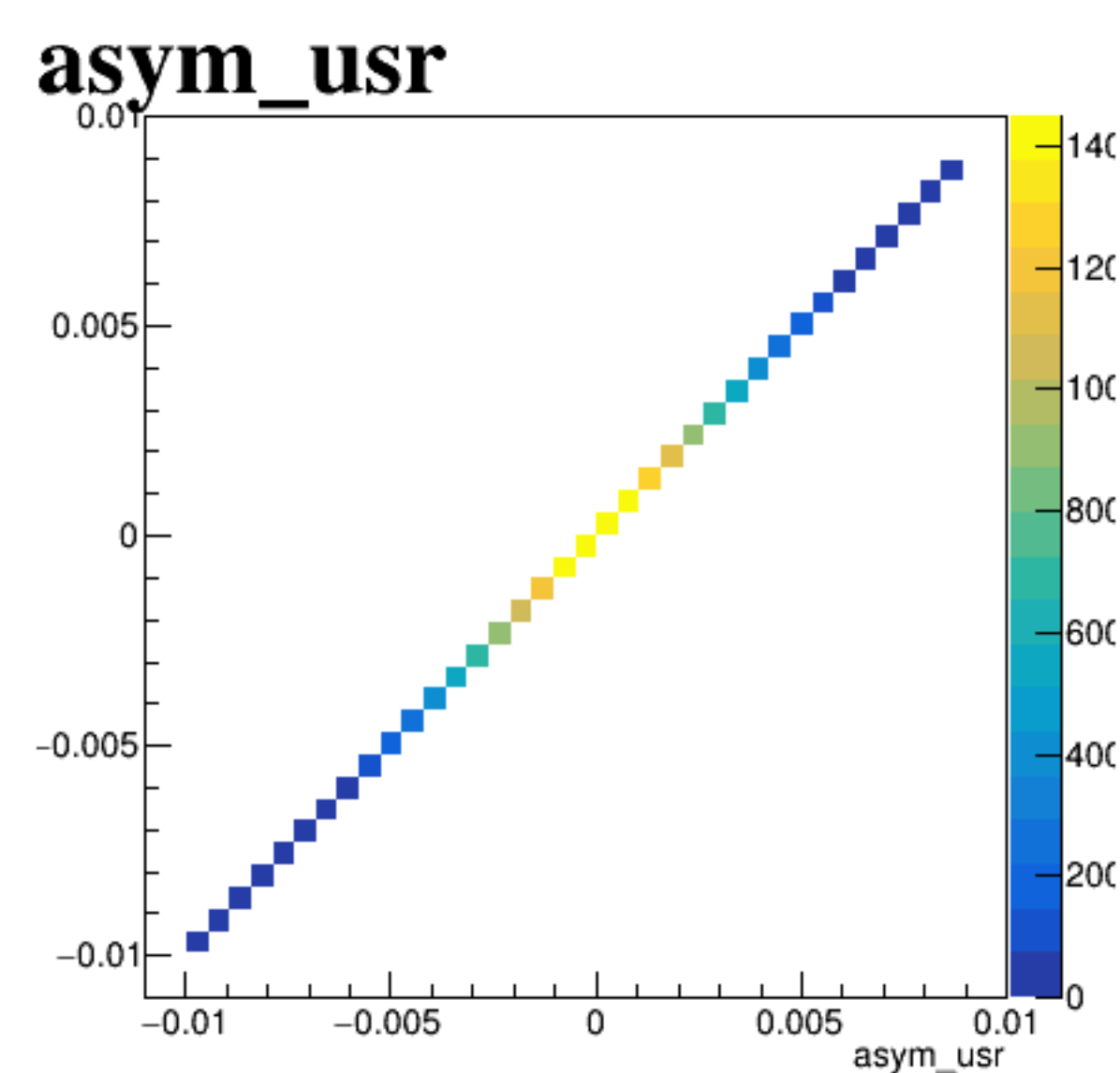
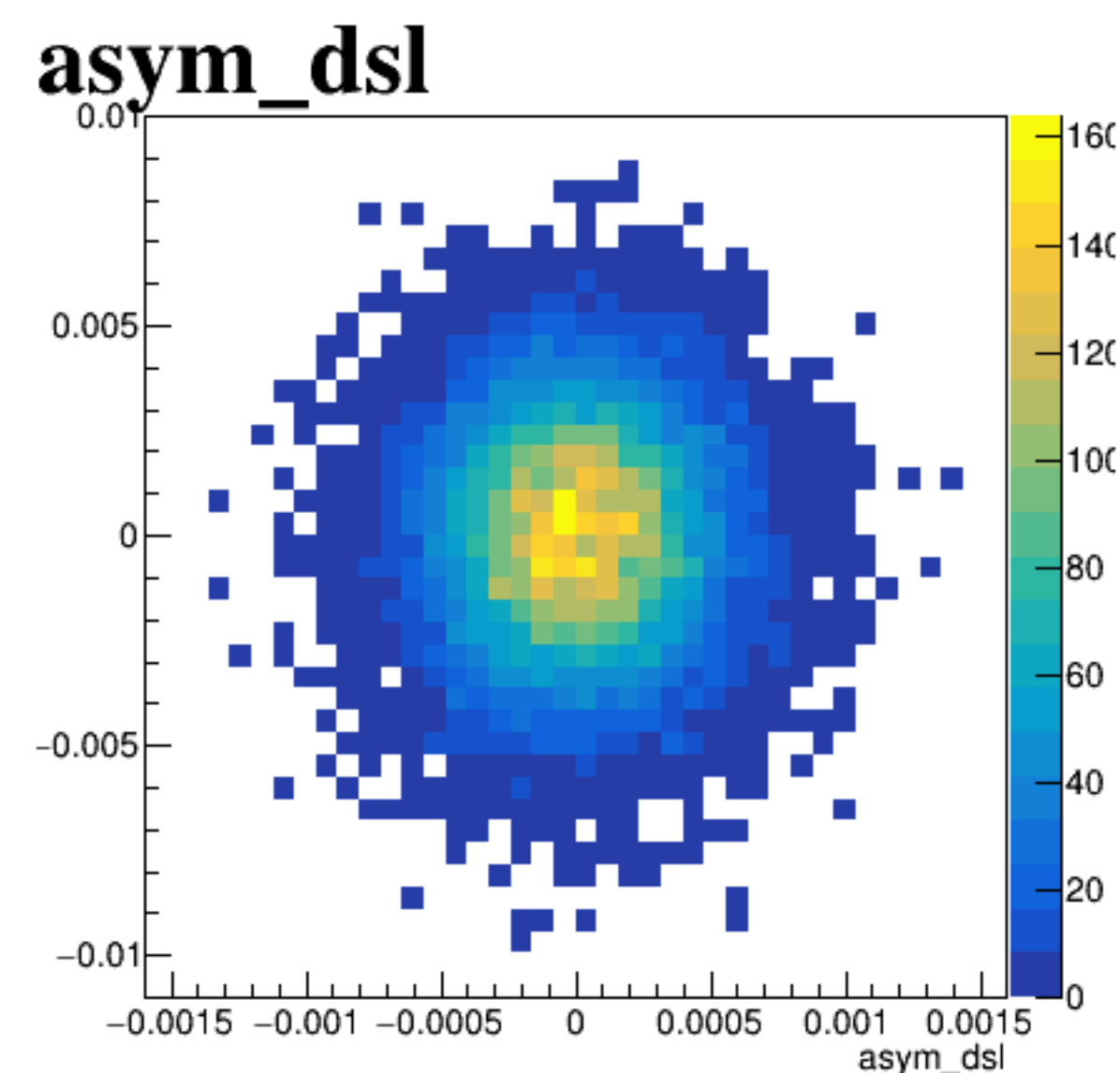
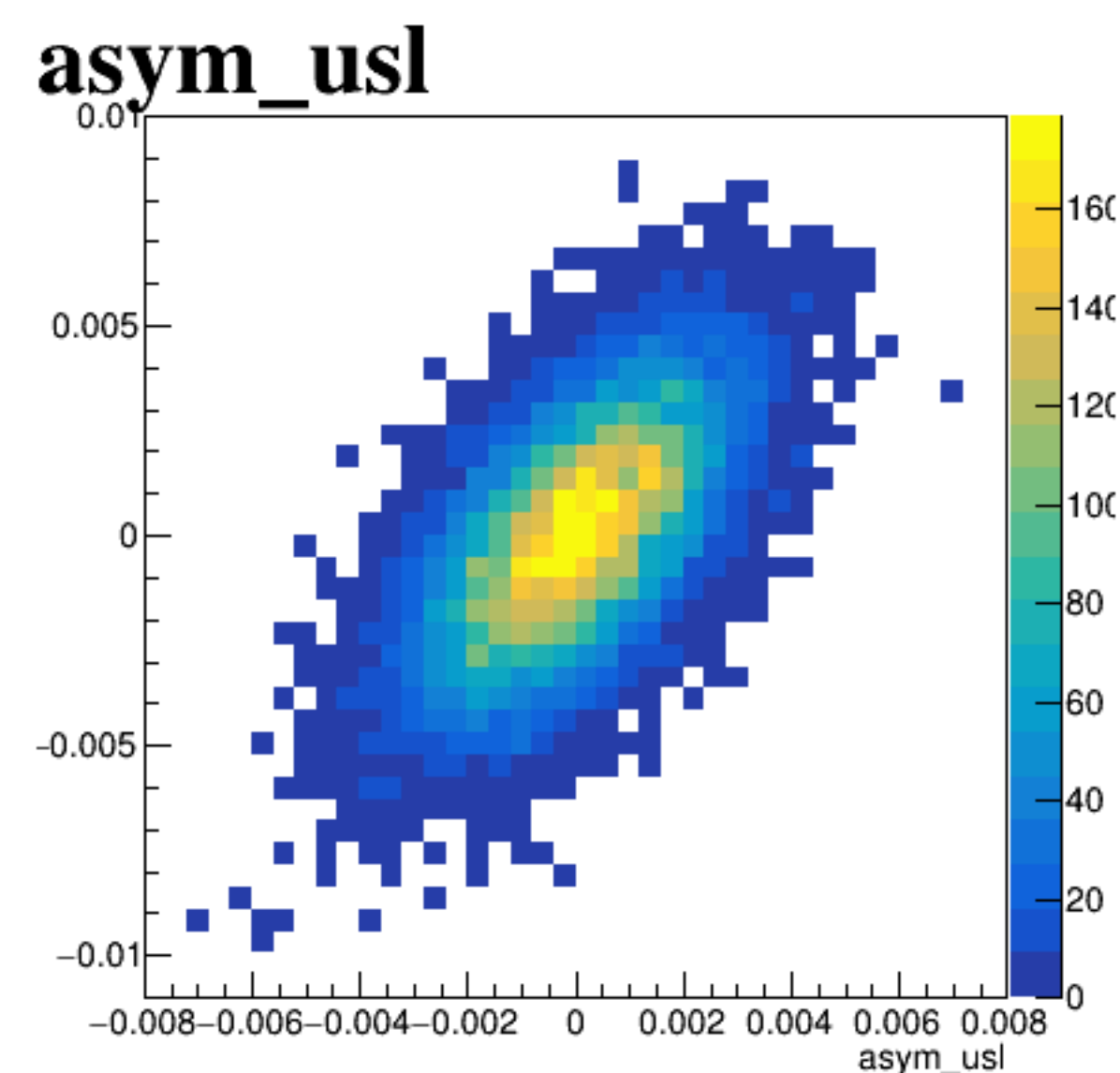
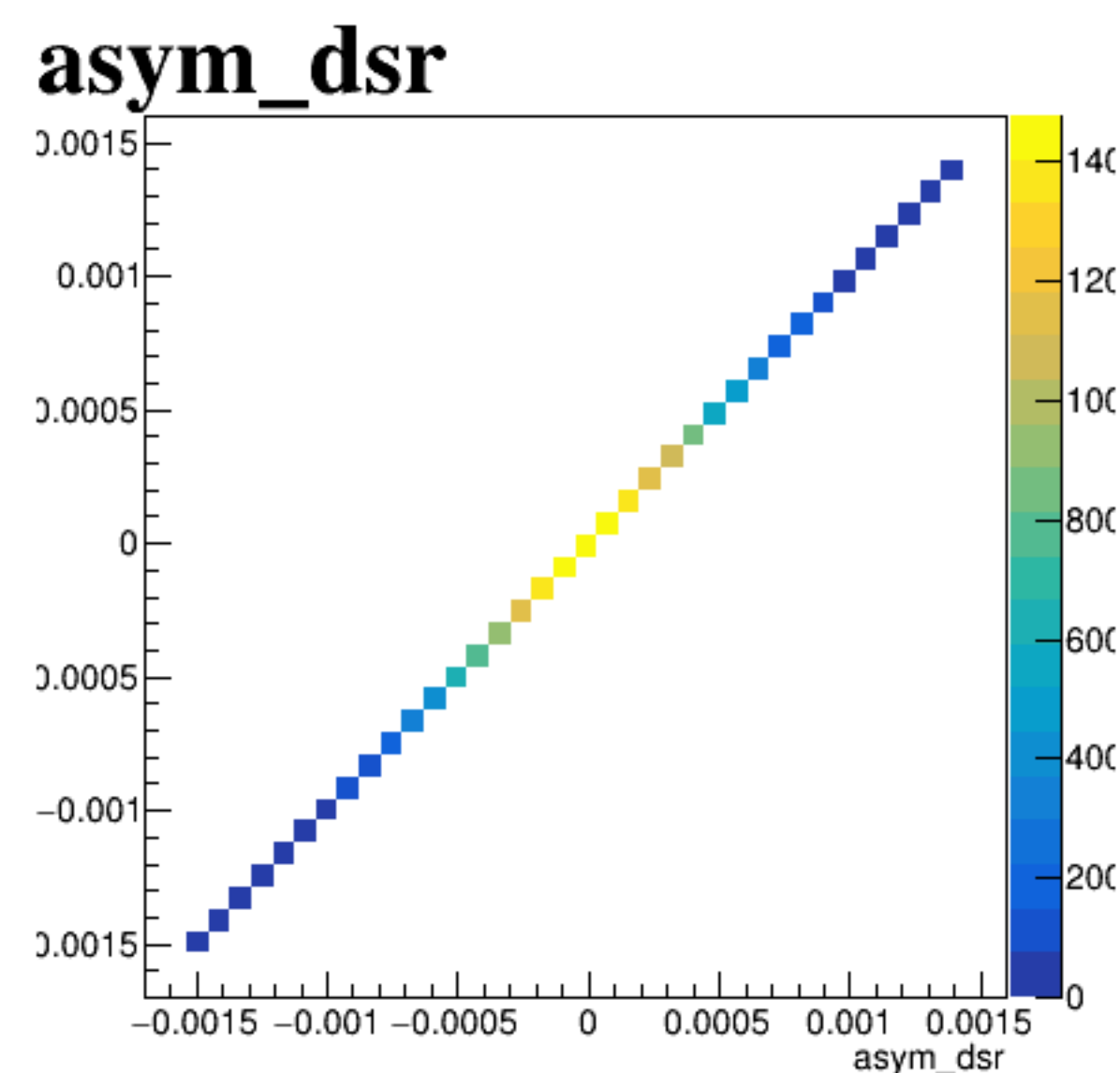
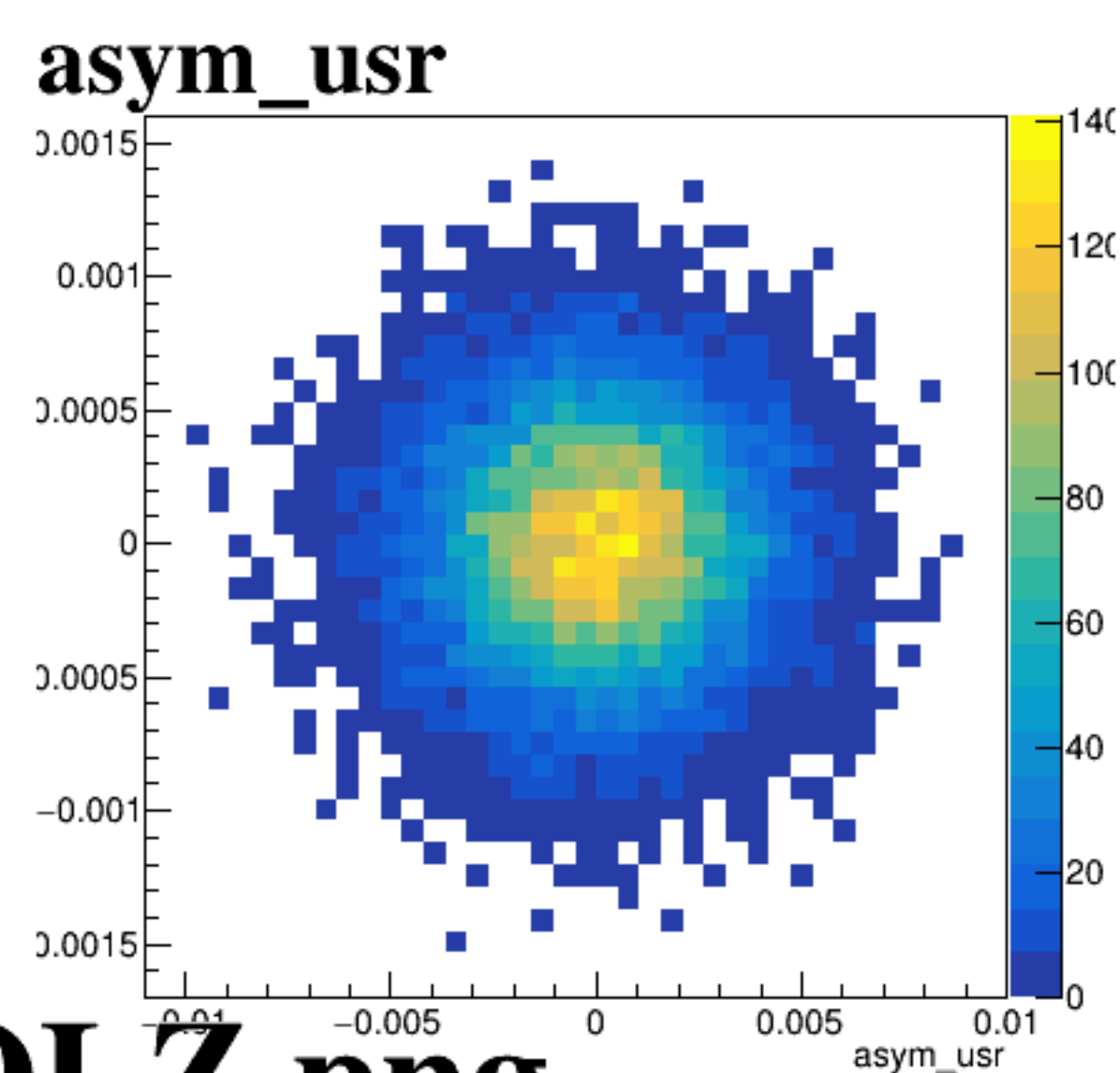
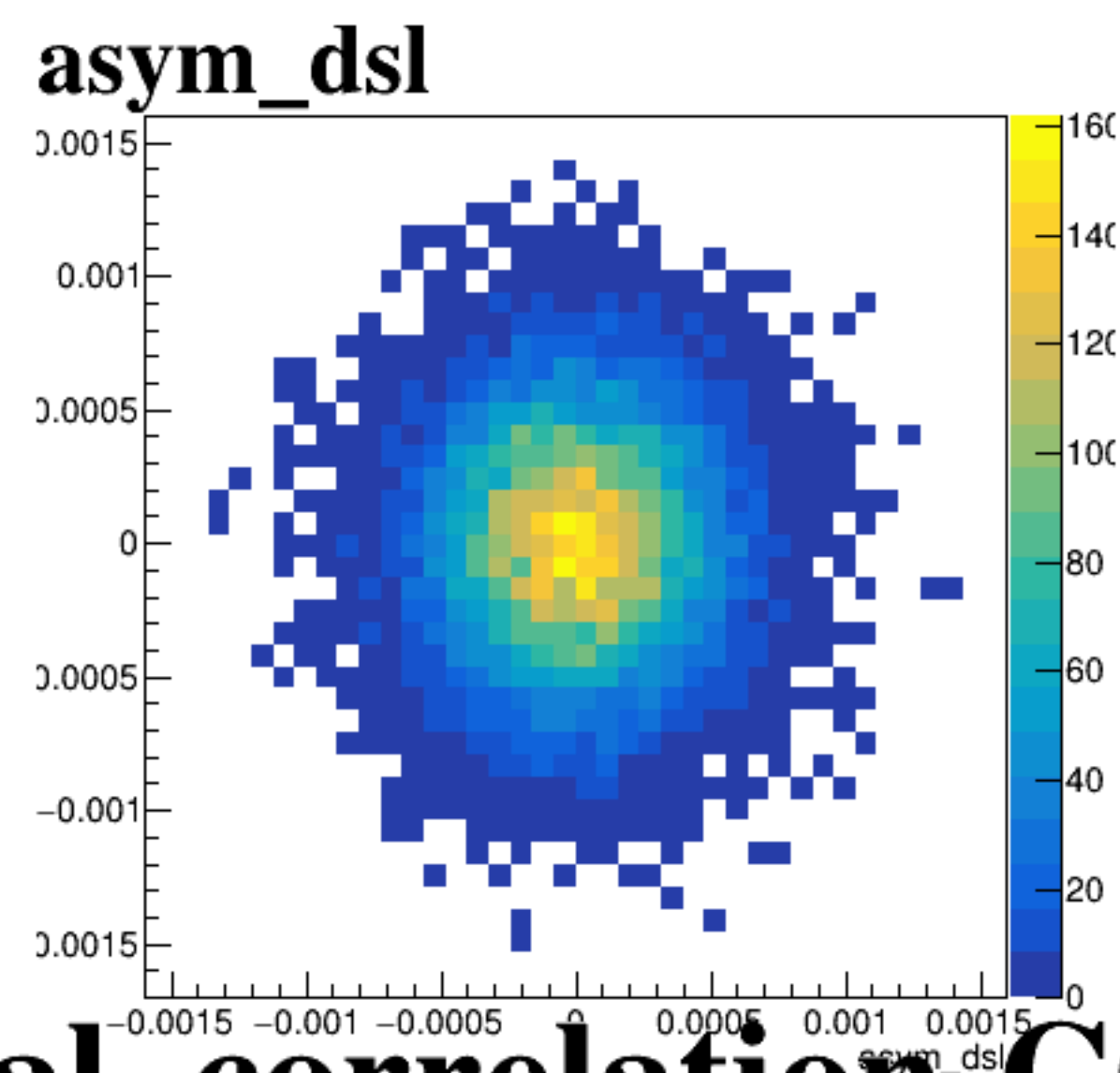
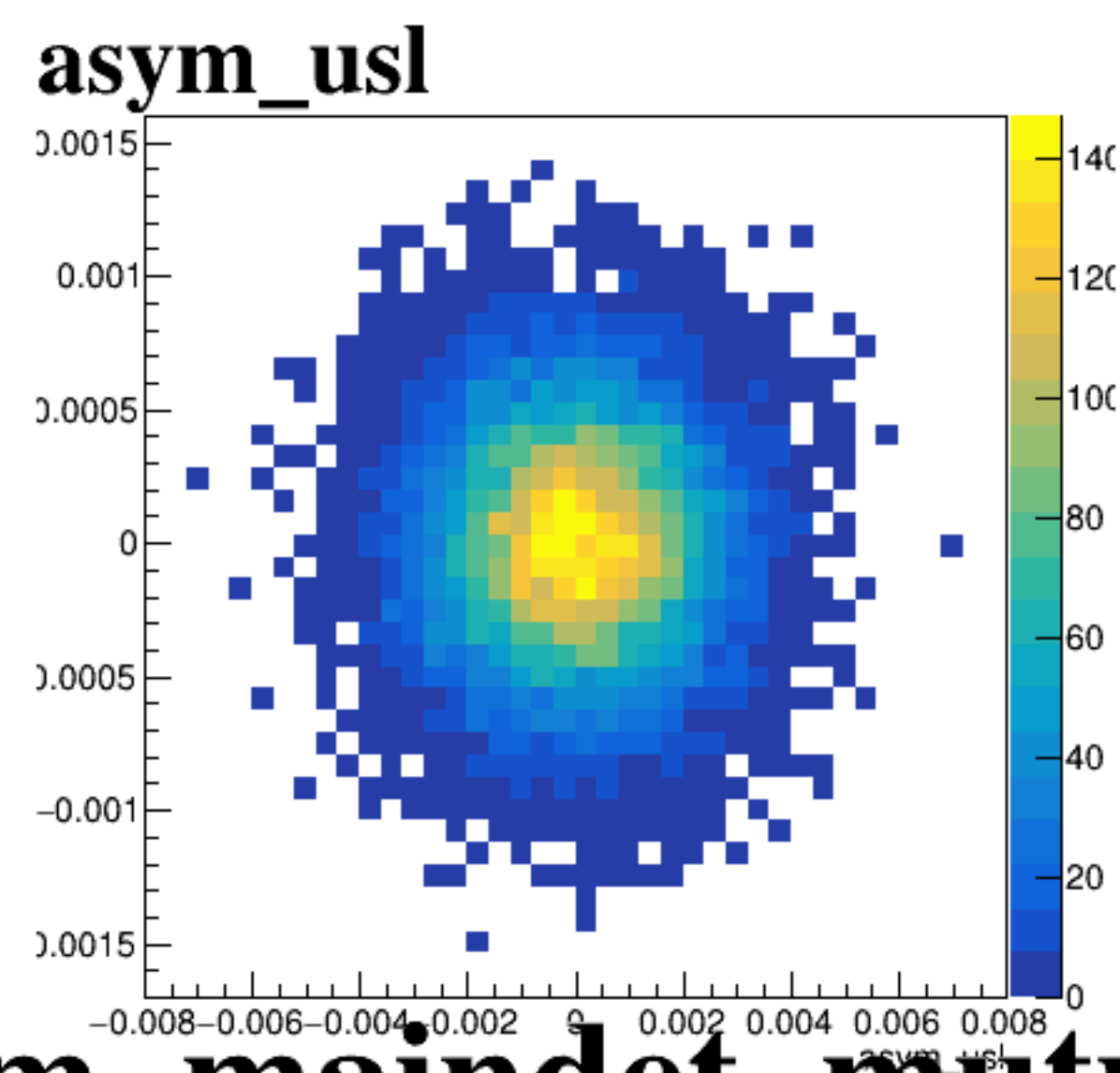
asym_usl



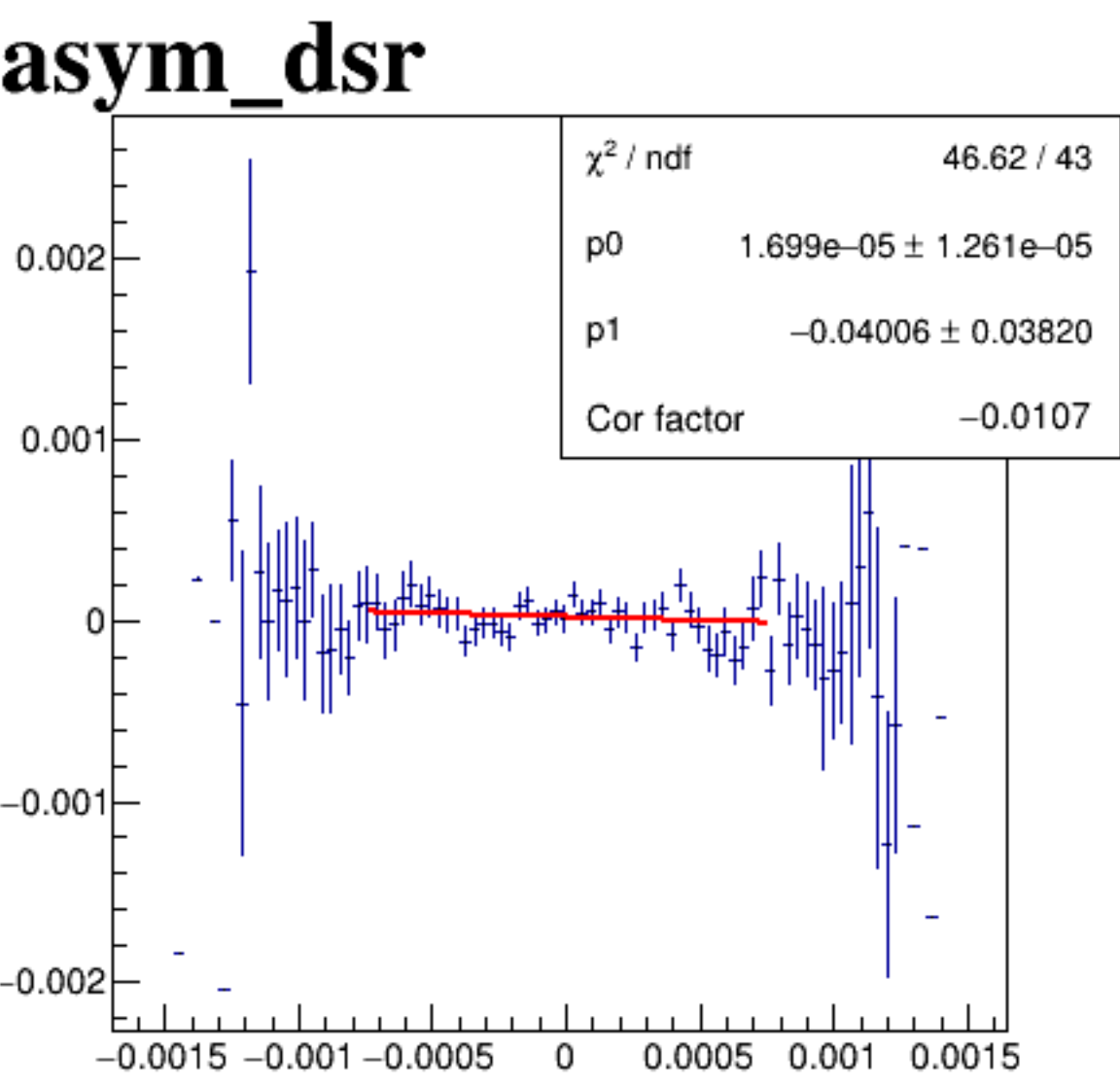
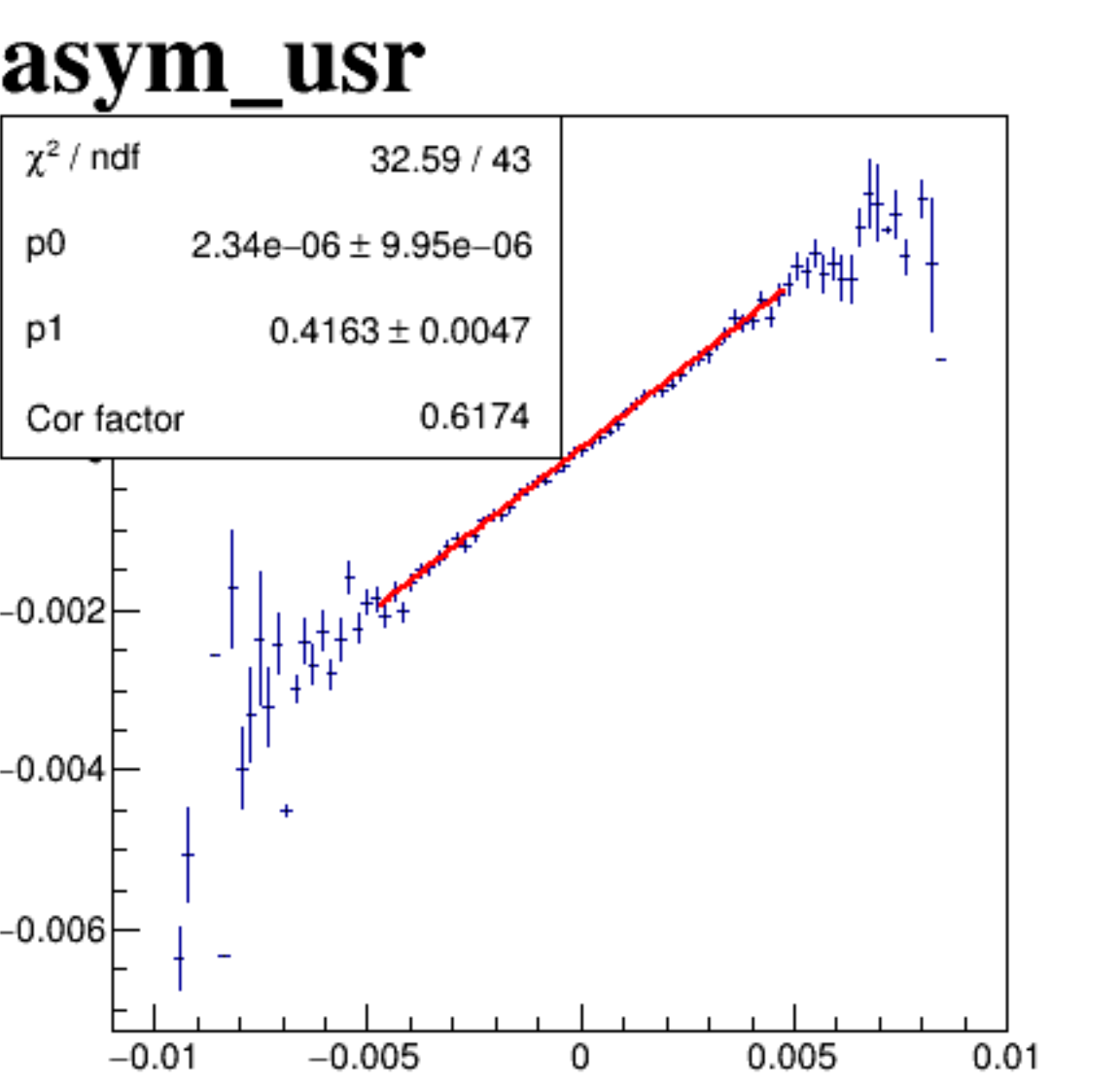
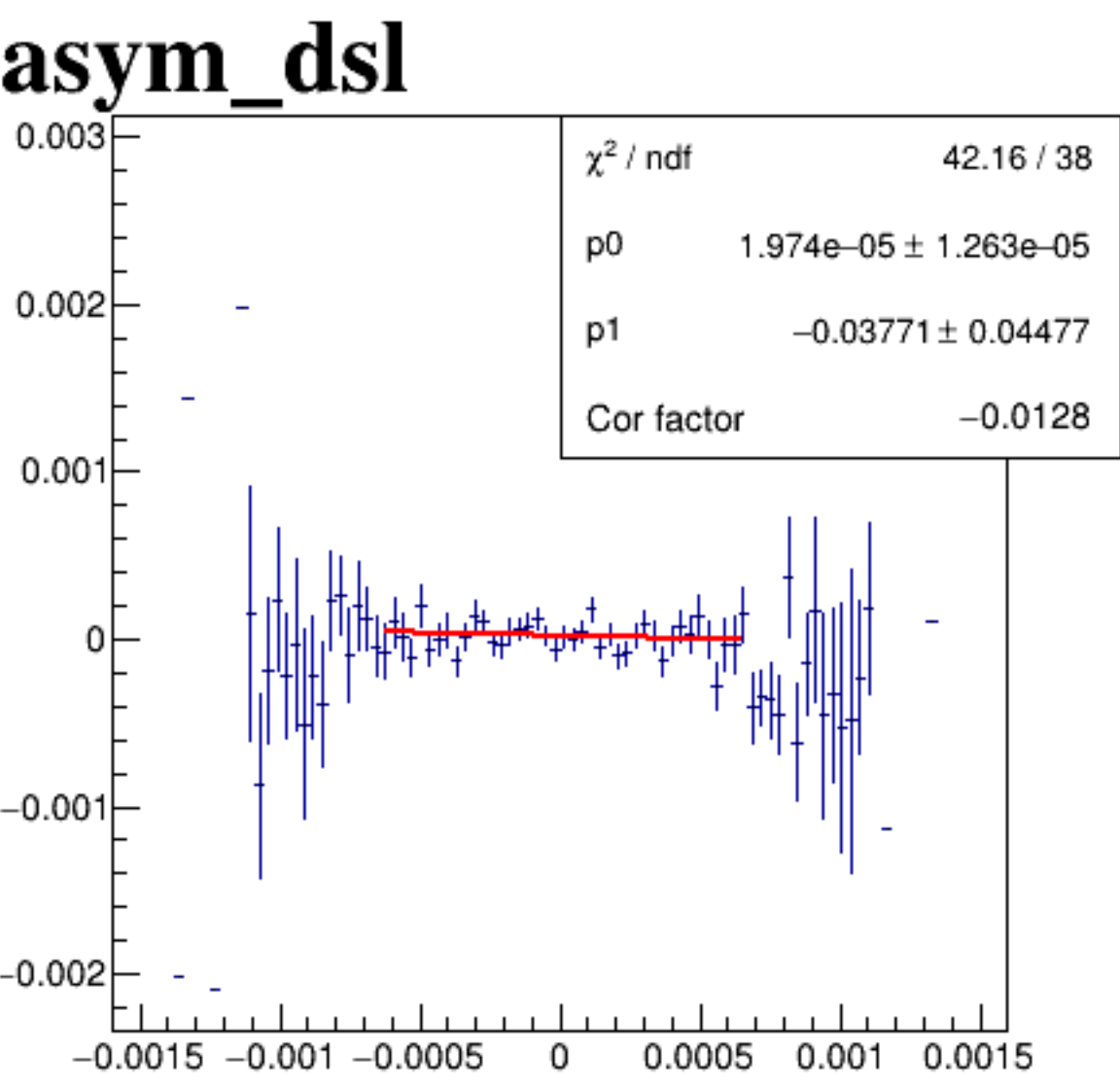
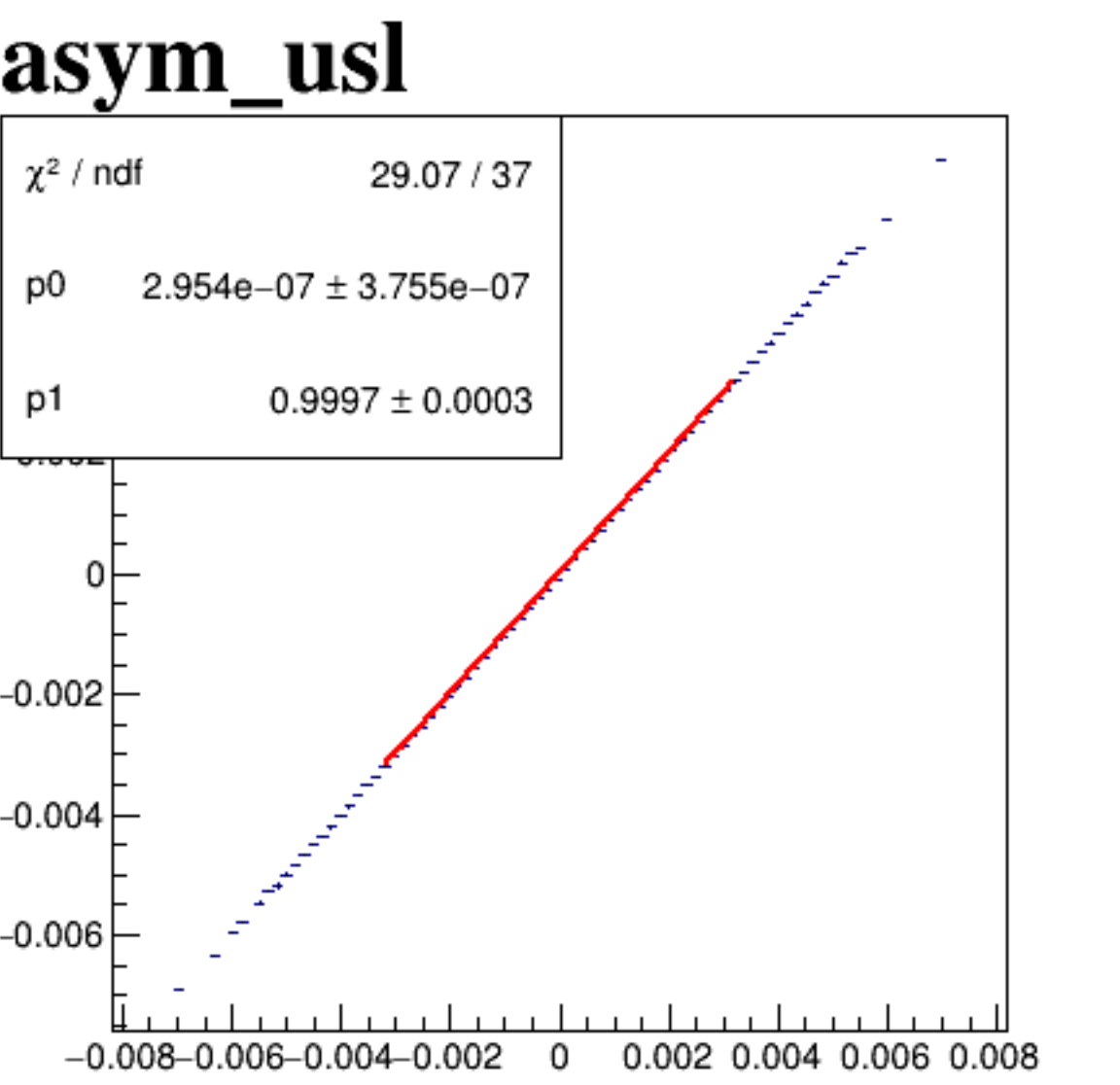
asym_dsl



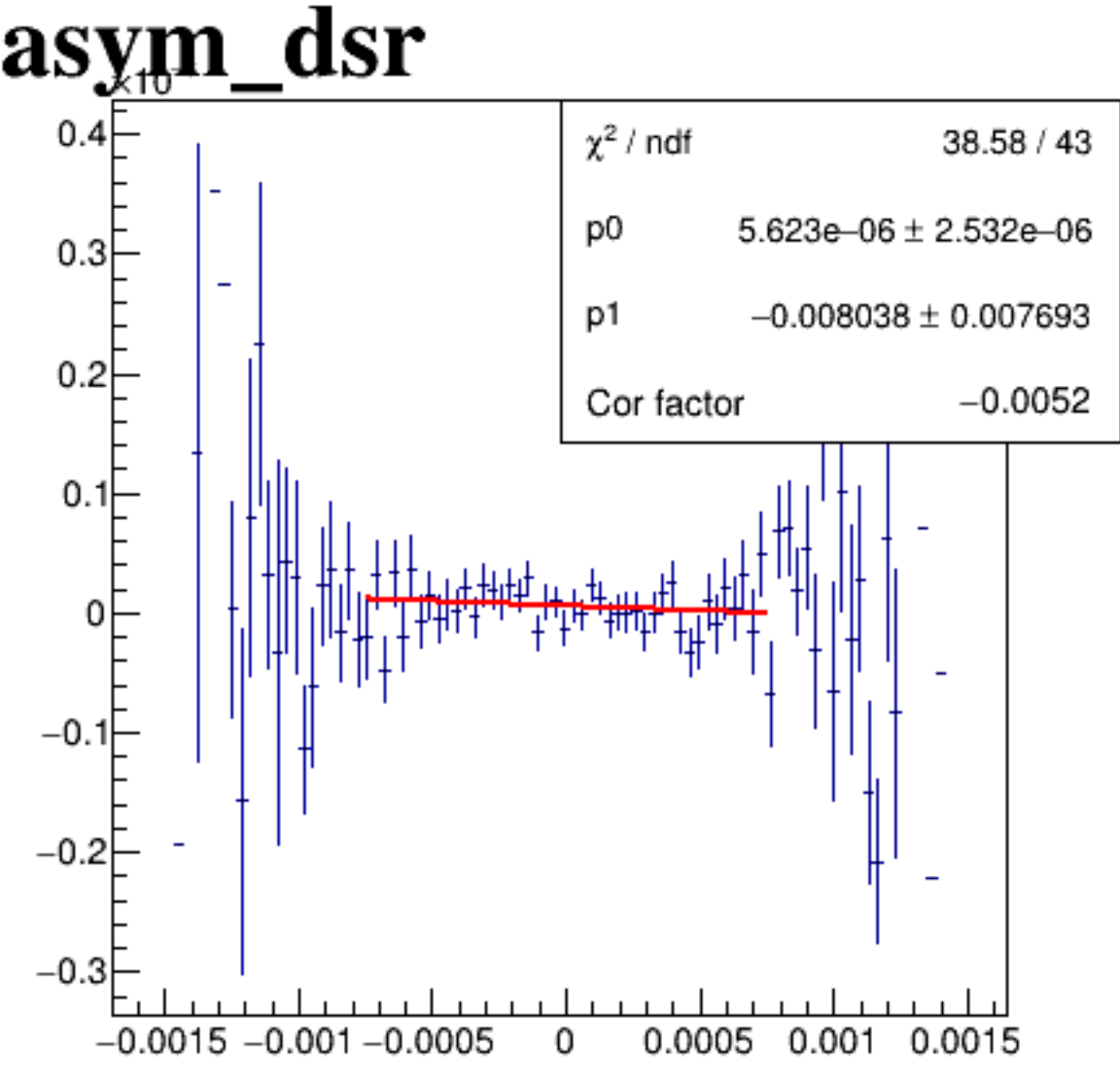
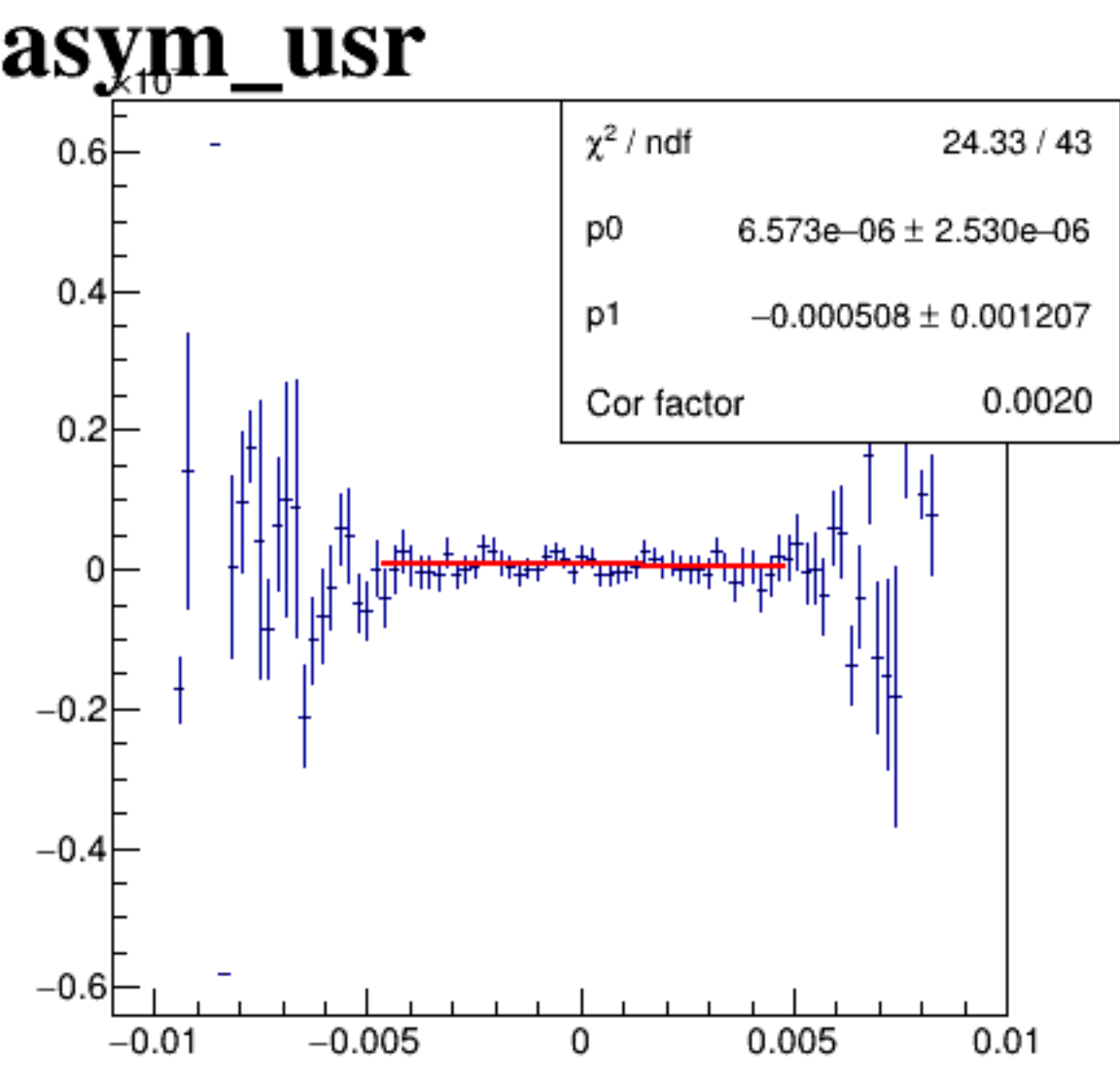
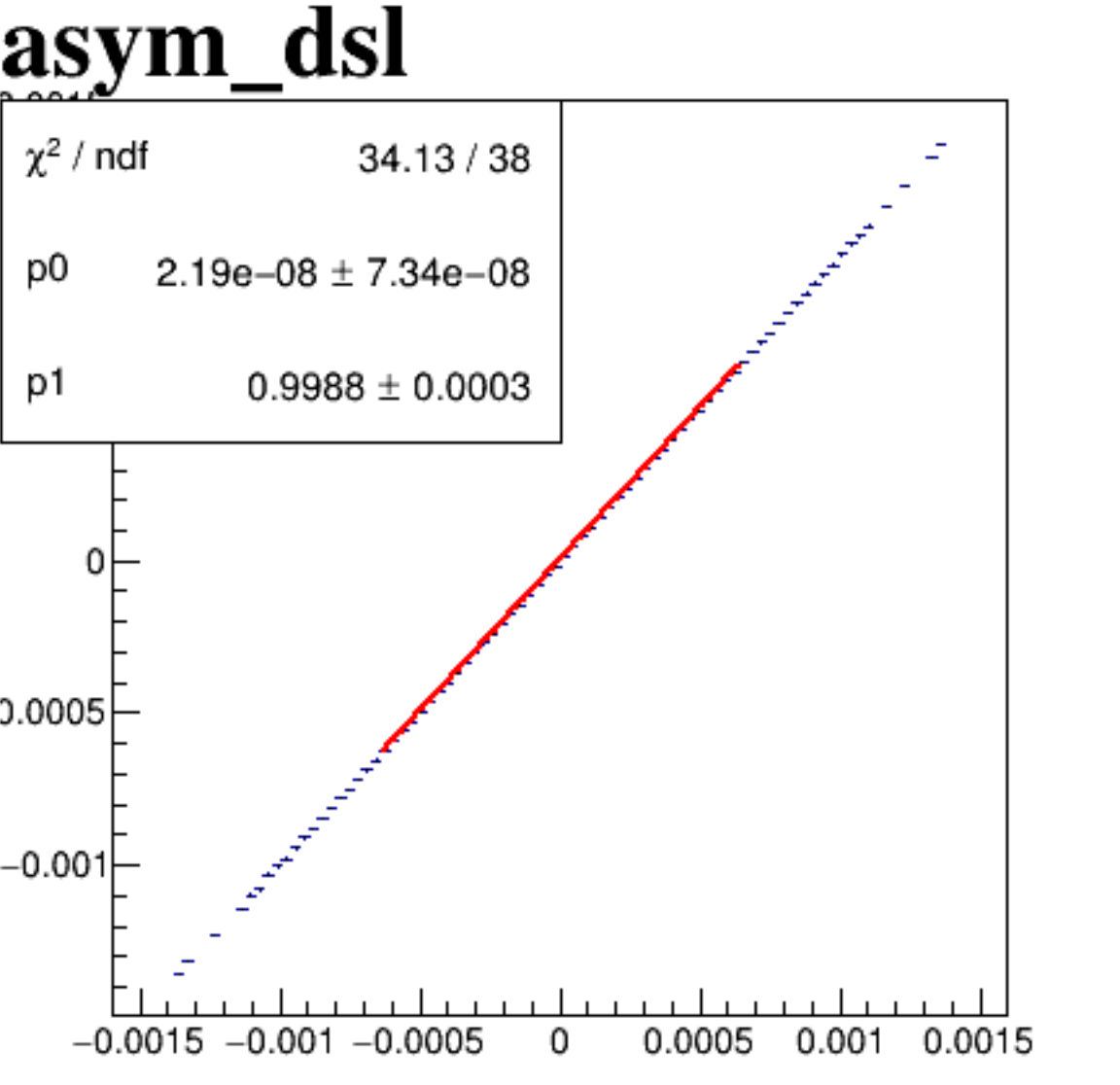
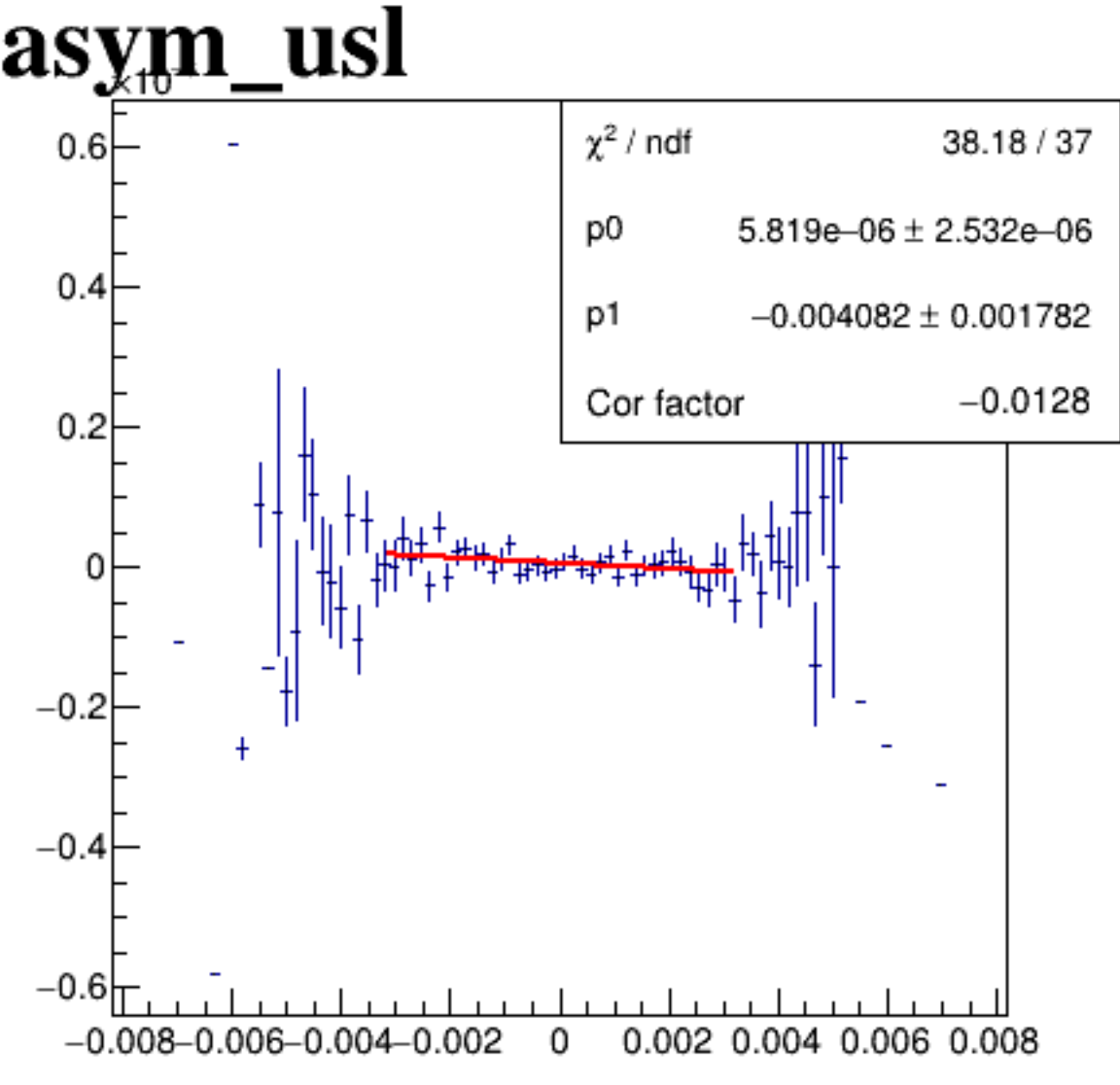
asym_usr

**asym_dsr**

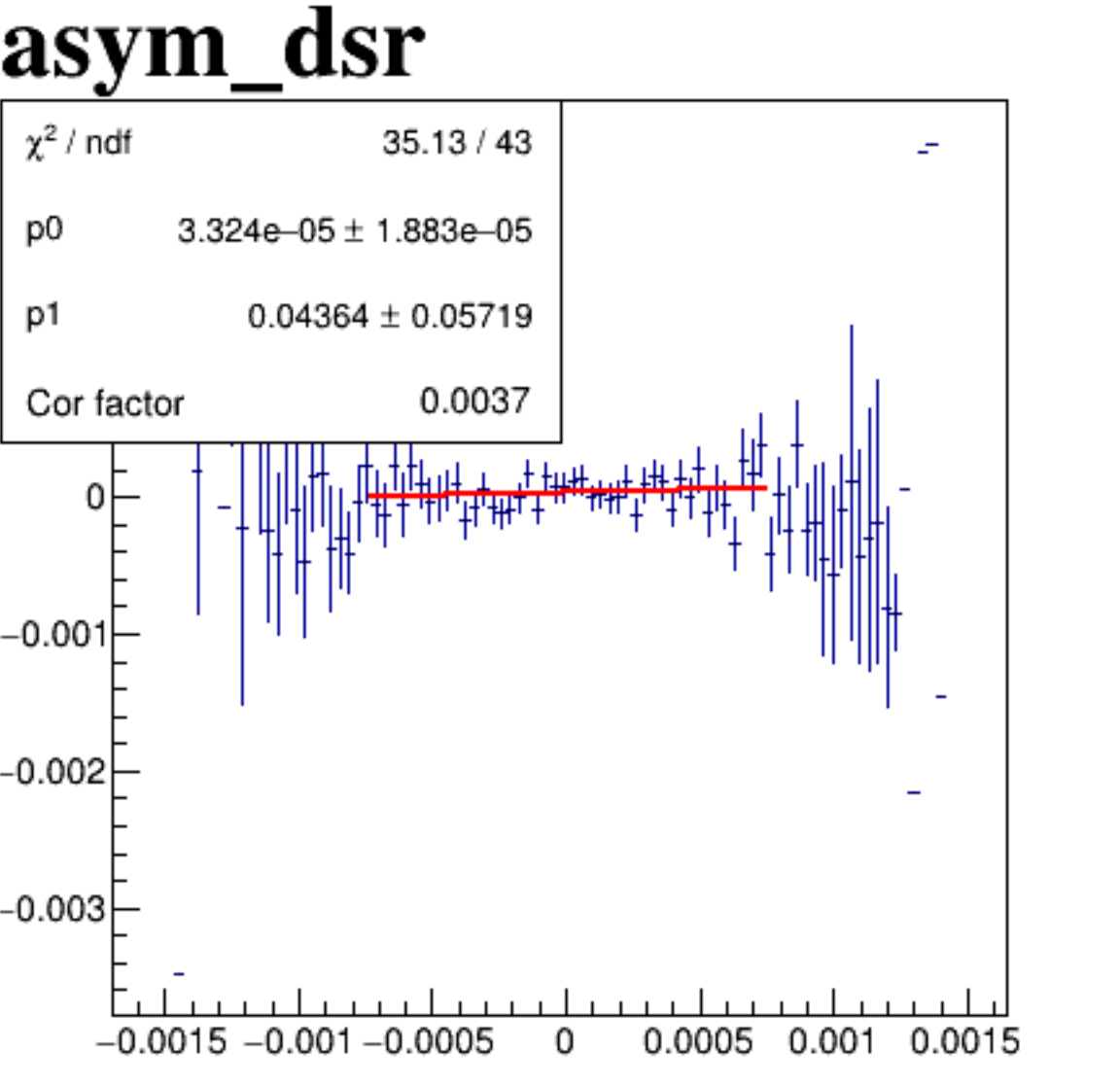
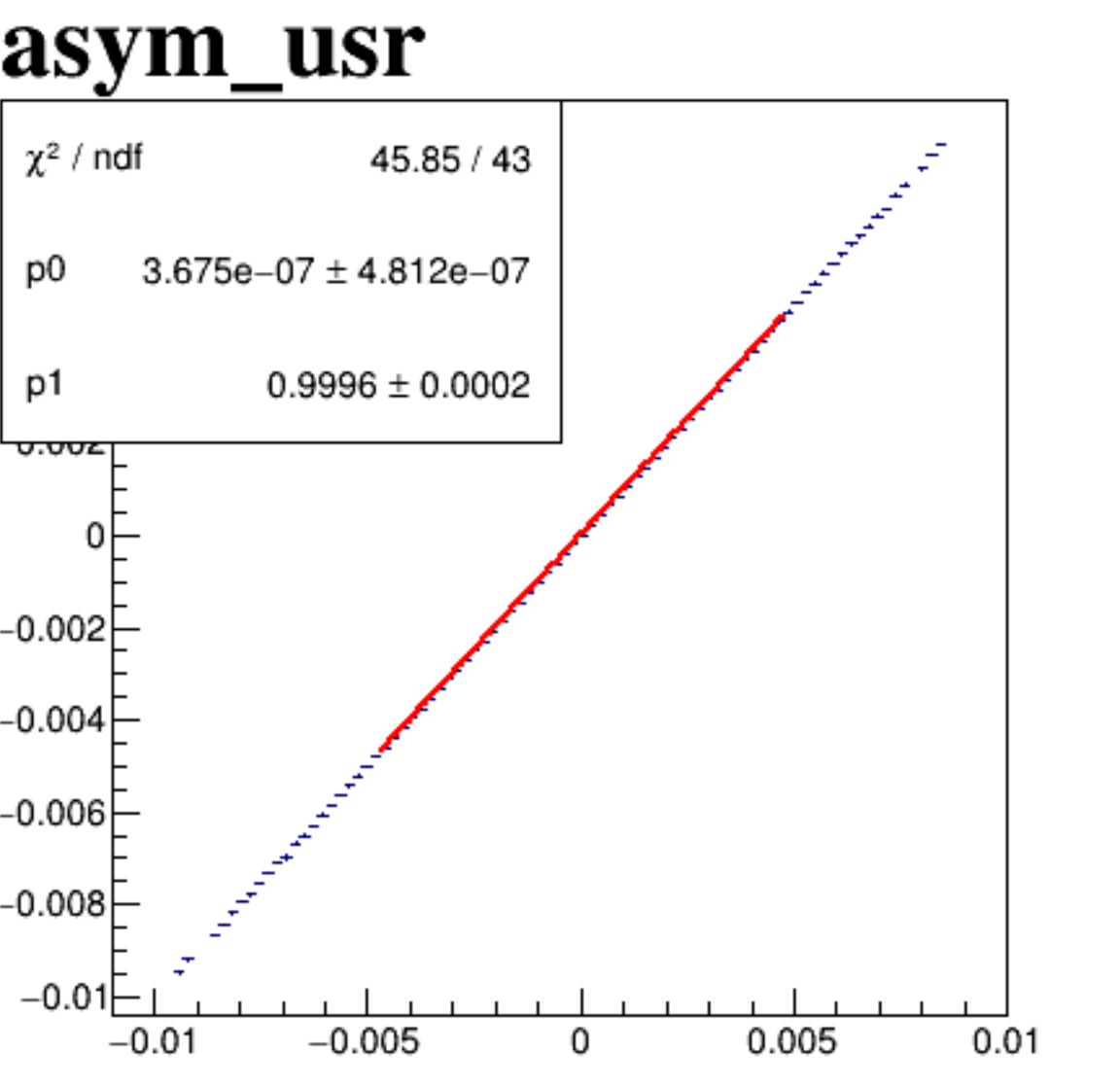
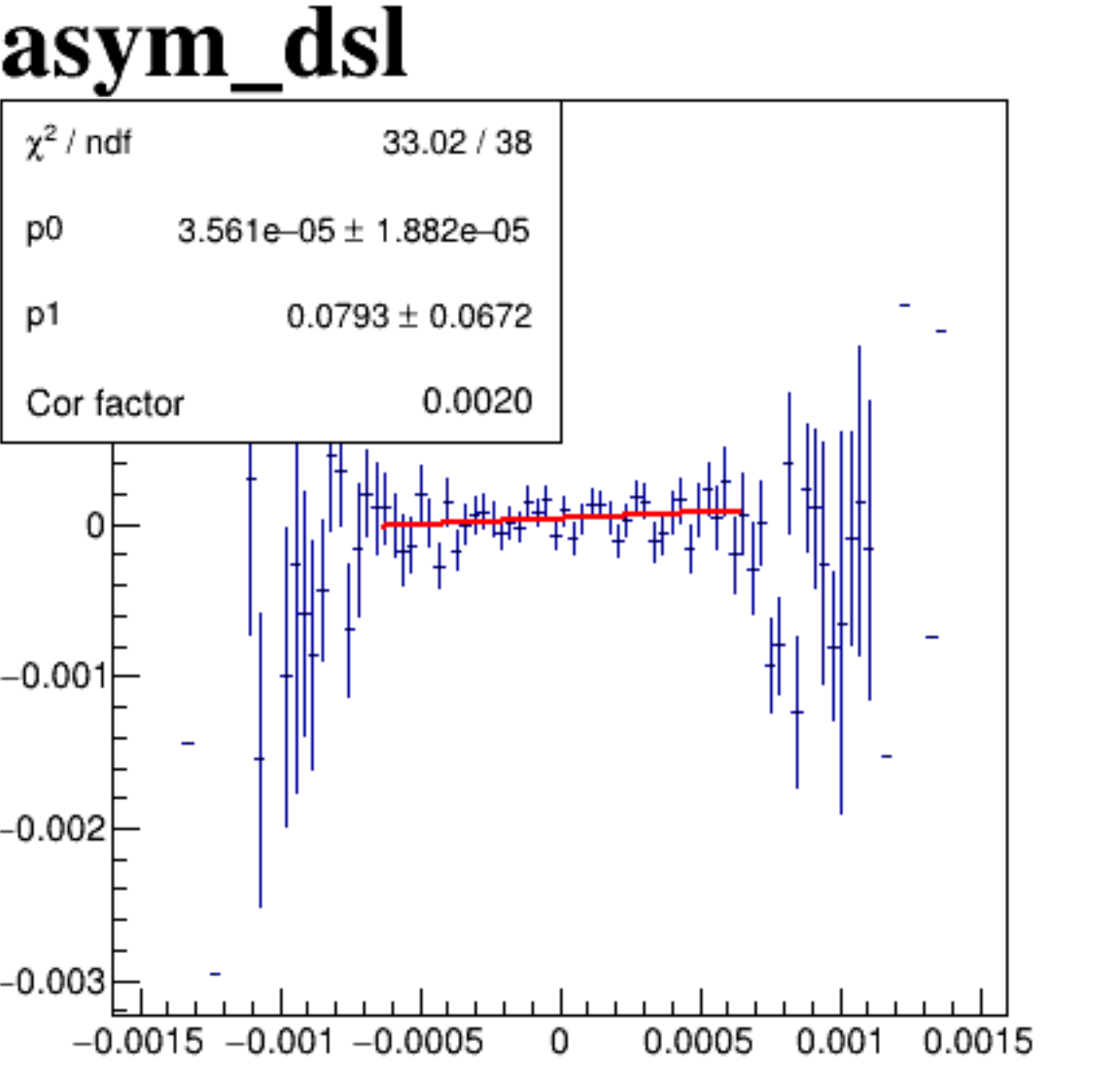
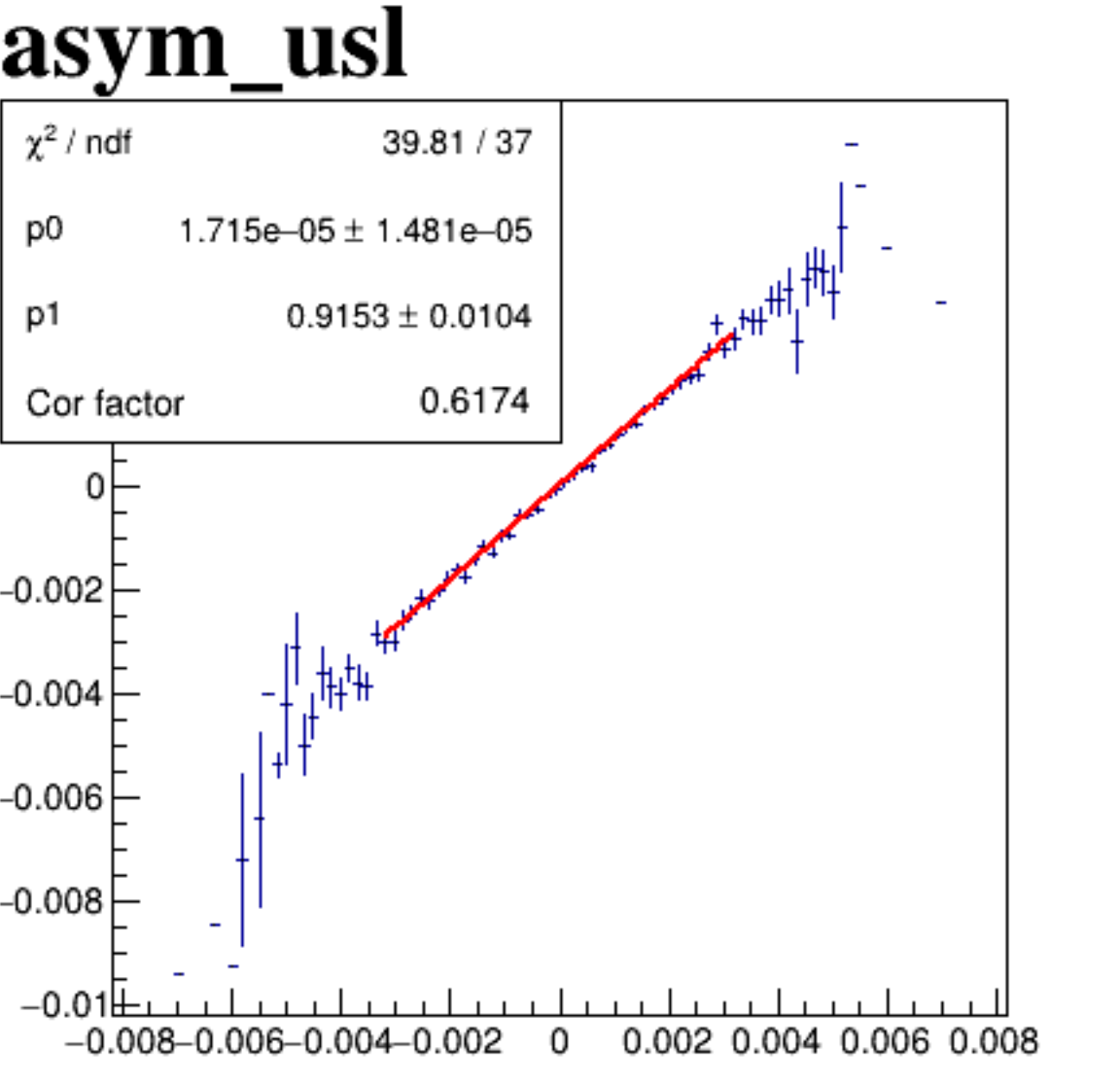
asym_usl



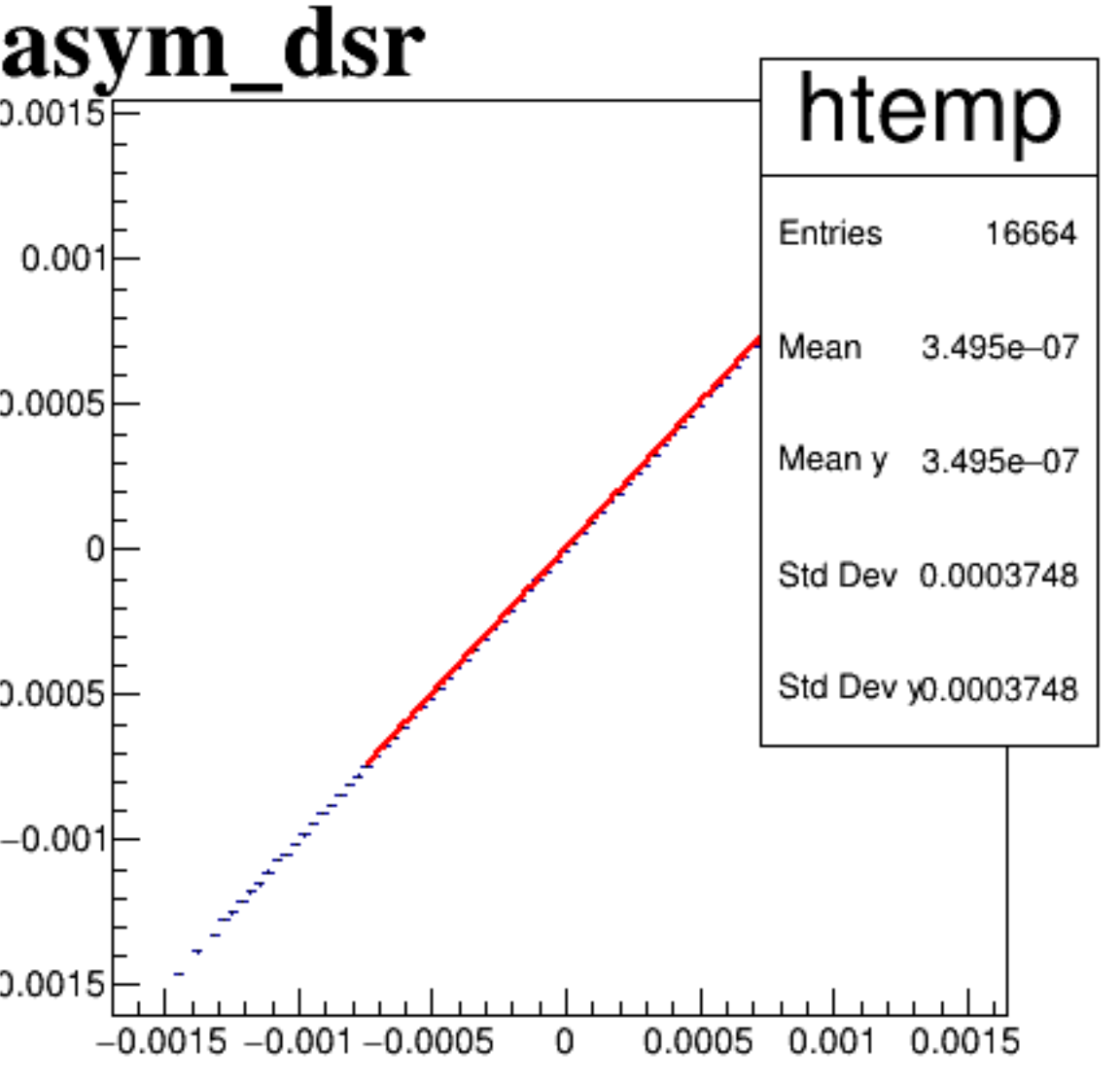
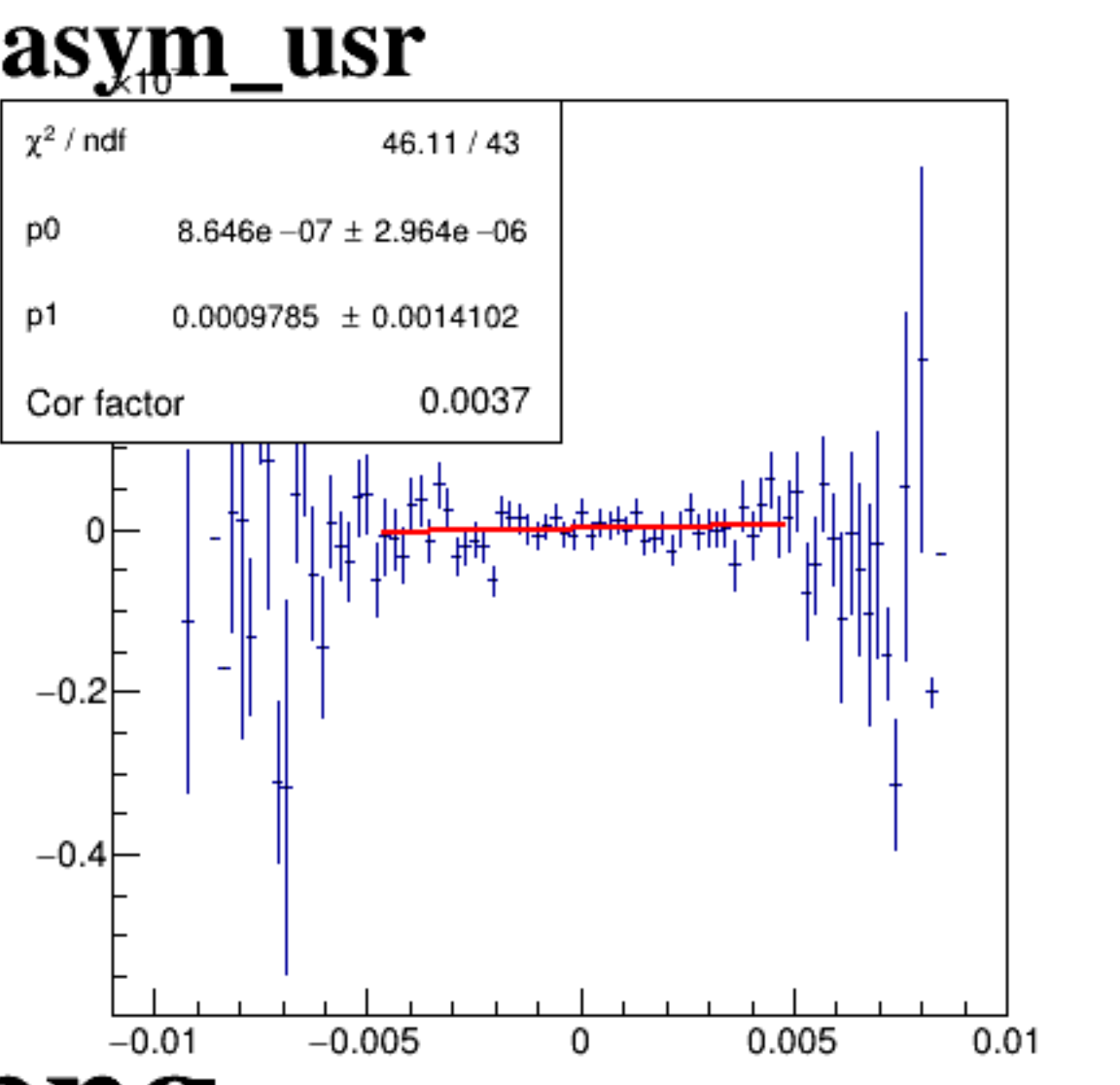
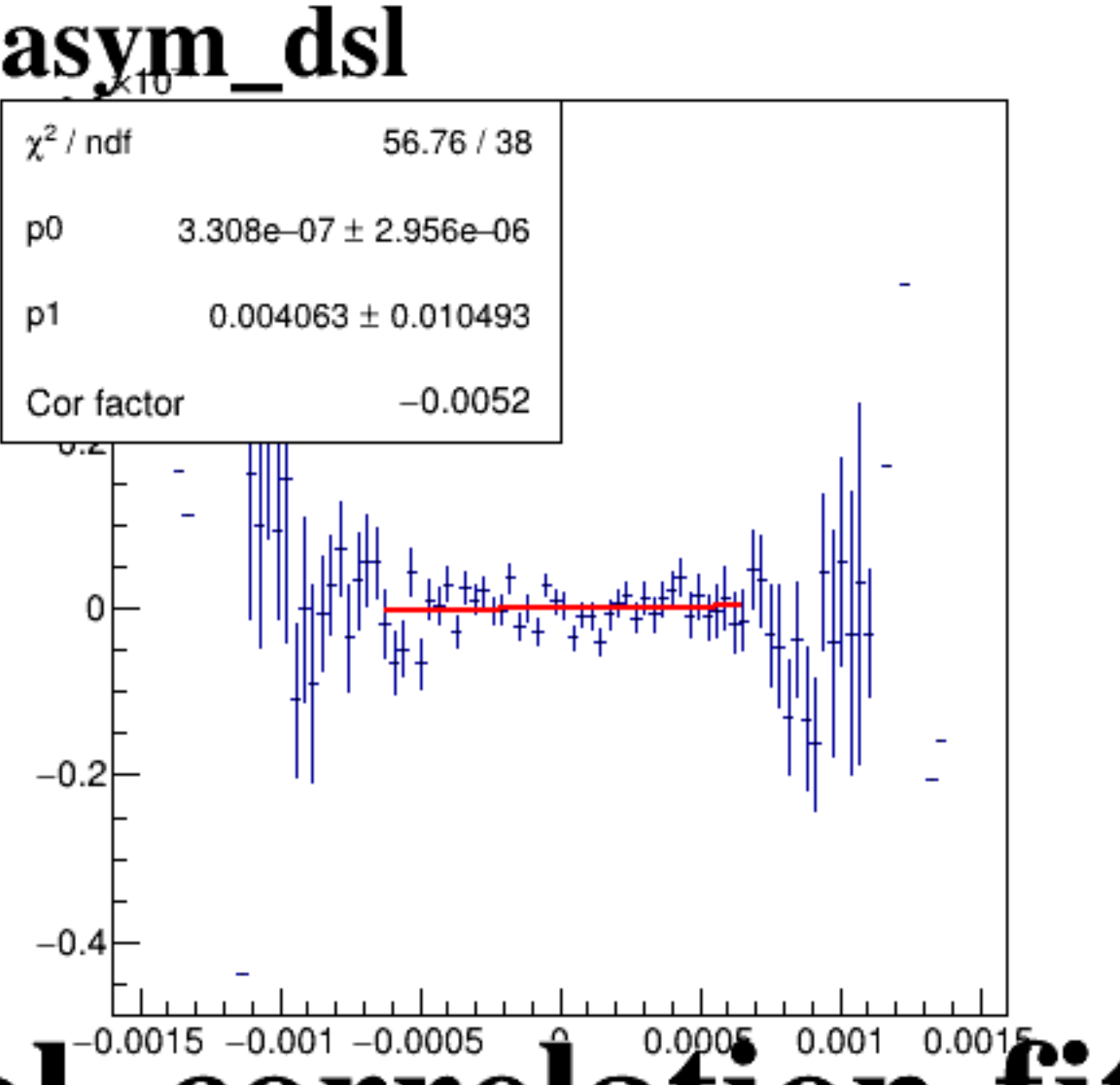
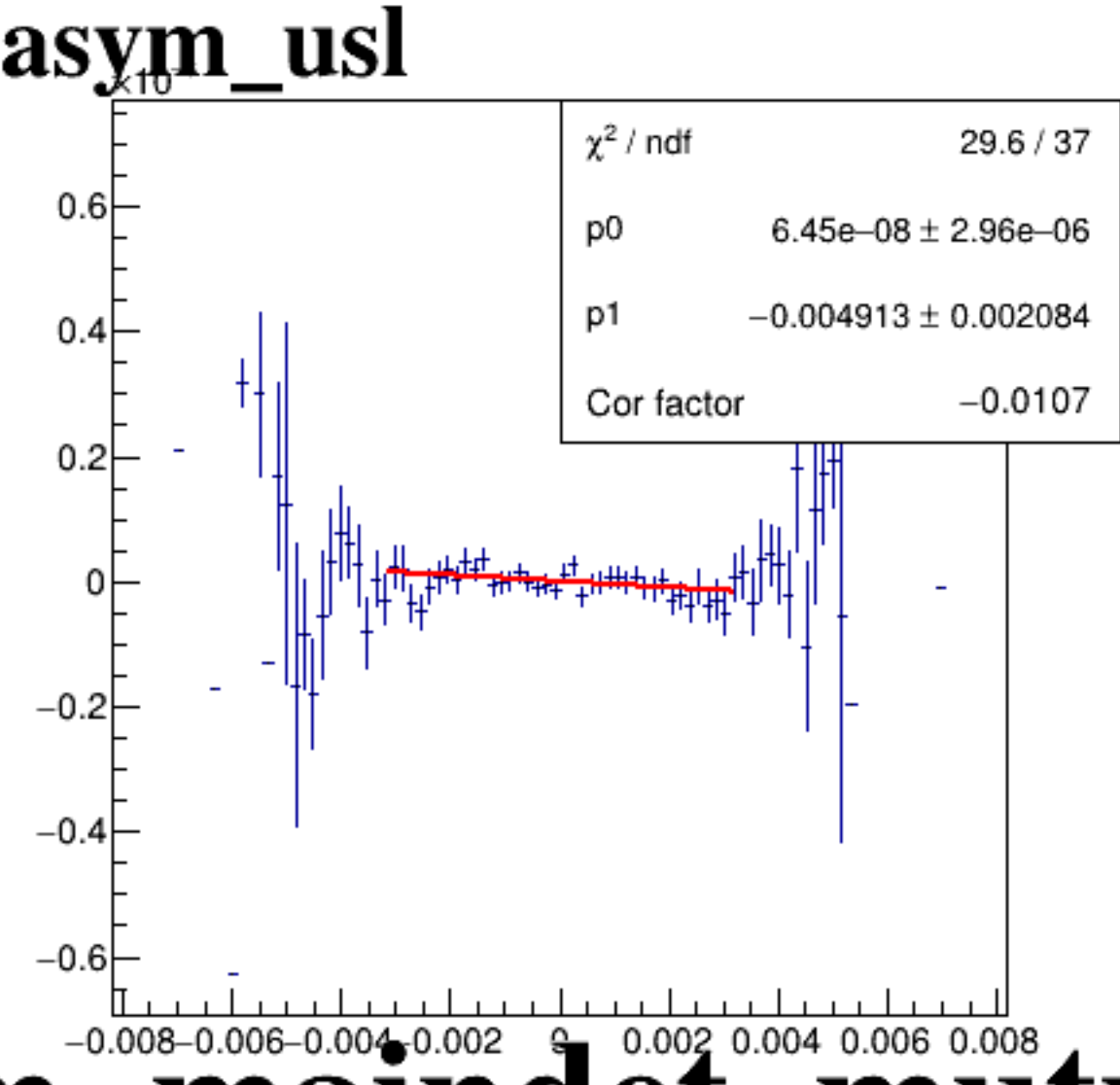
asym_dsl



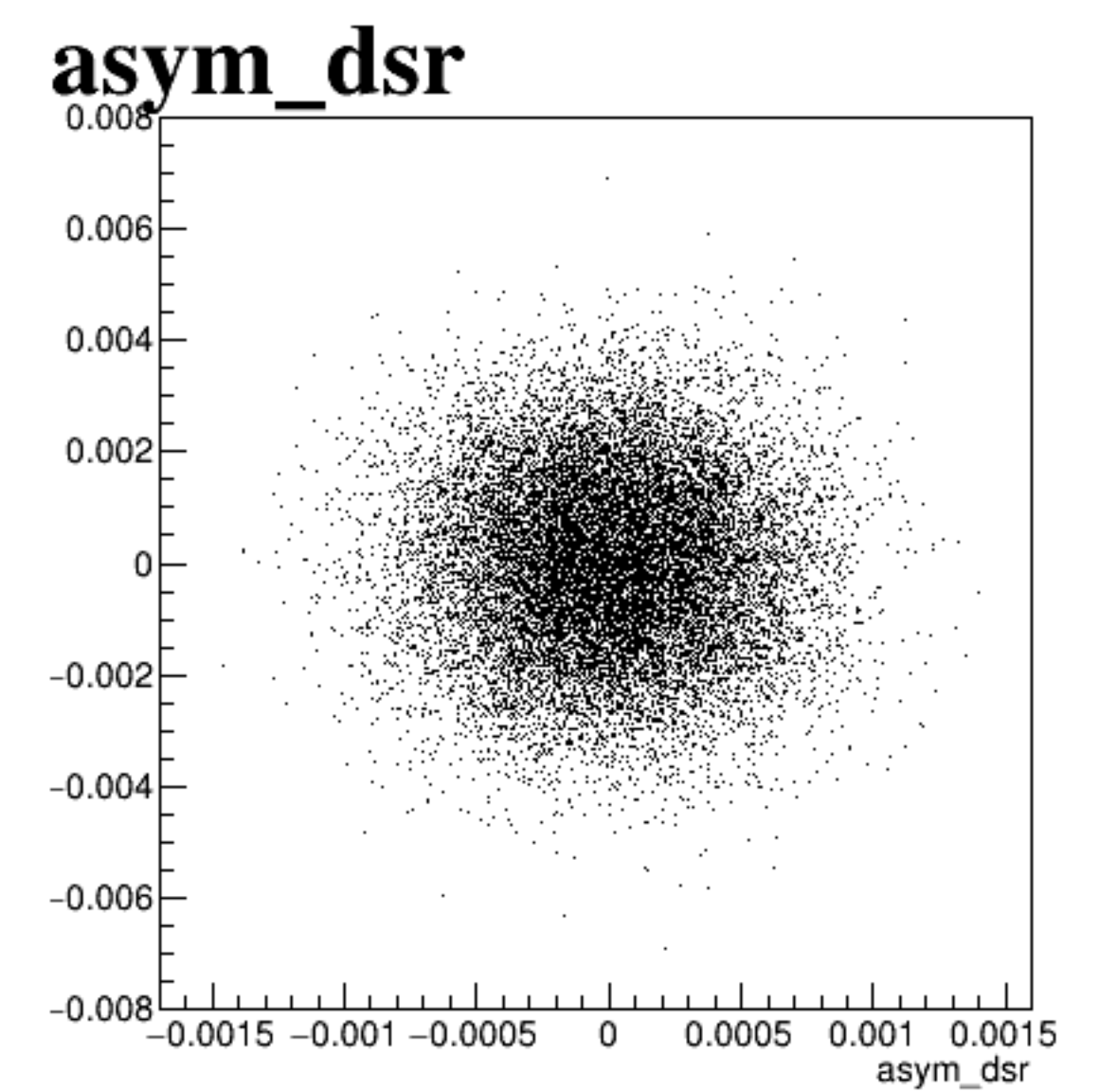
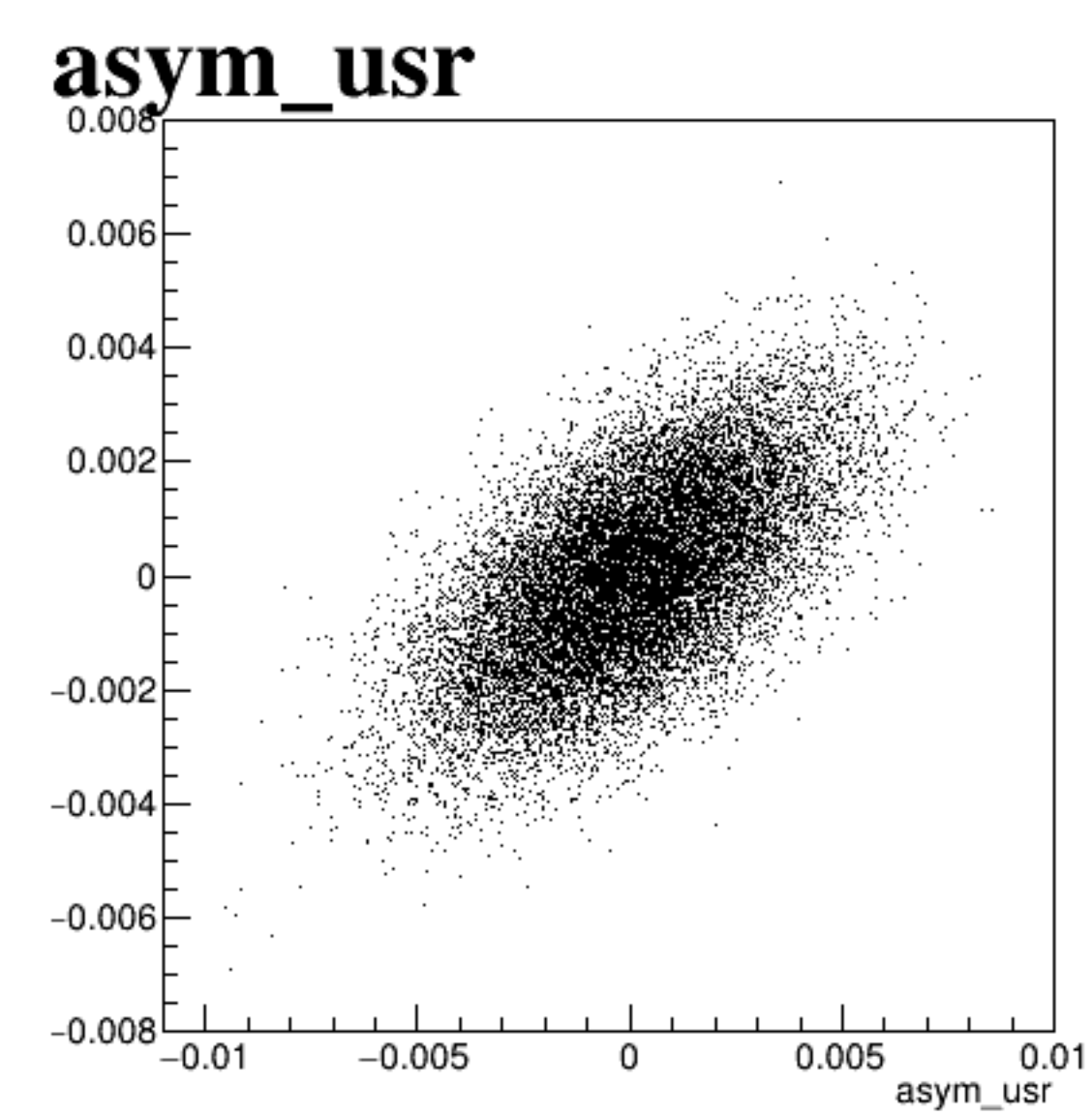
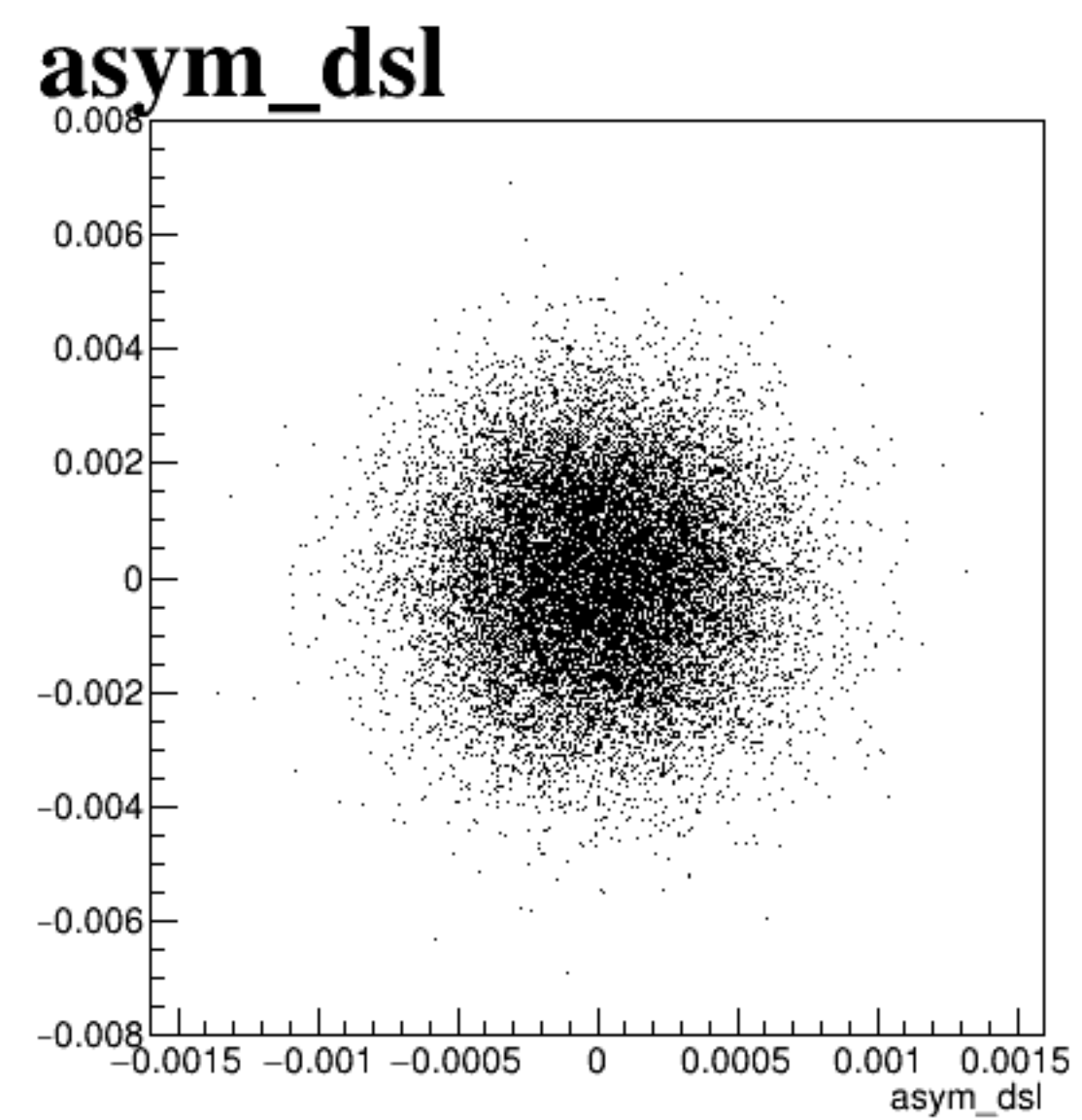
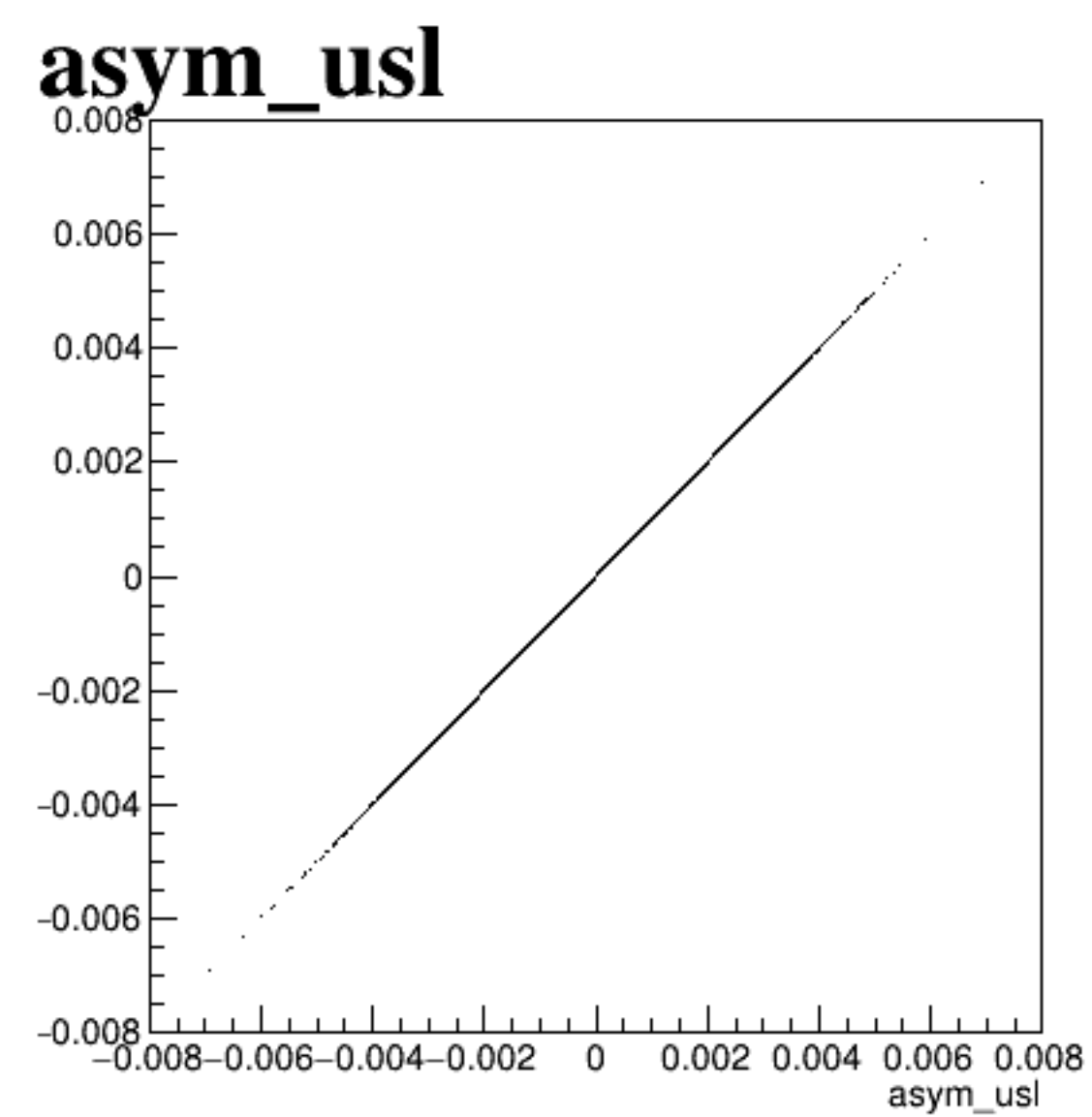
asym_usr



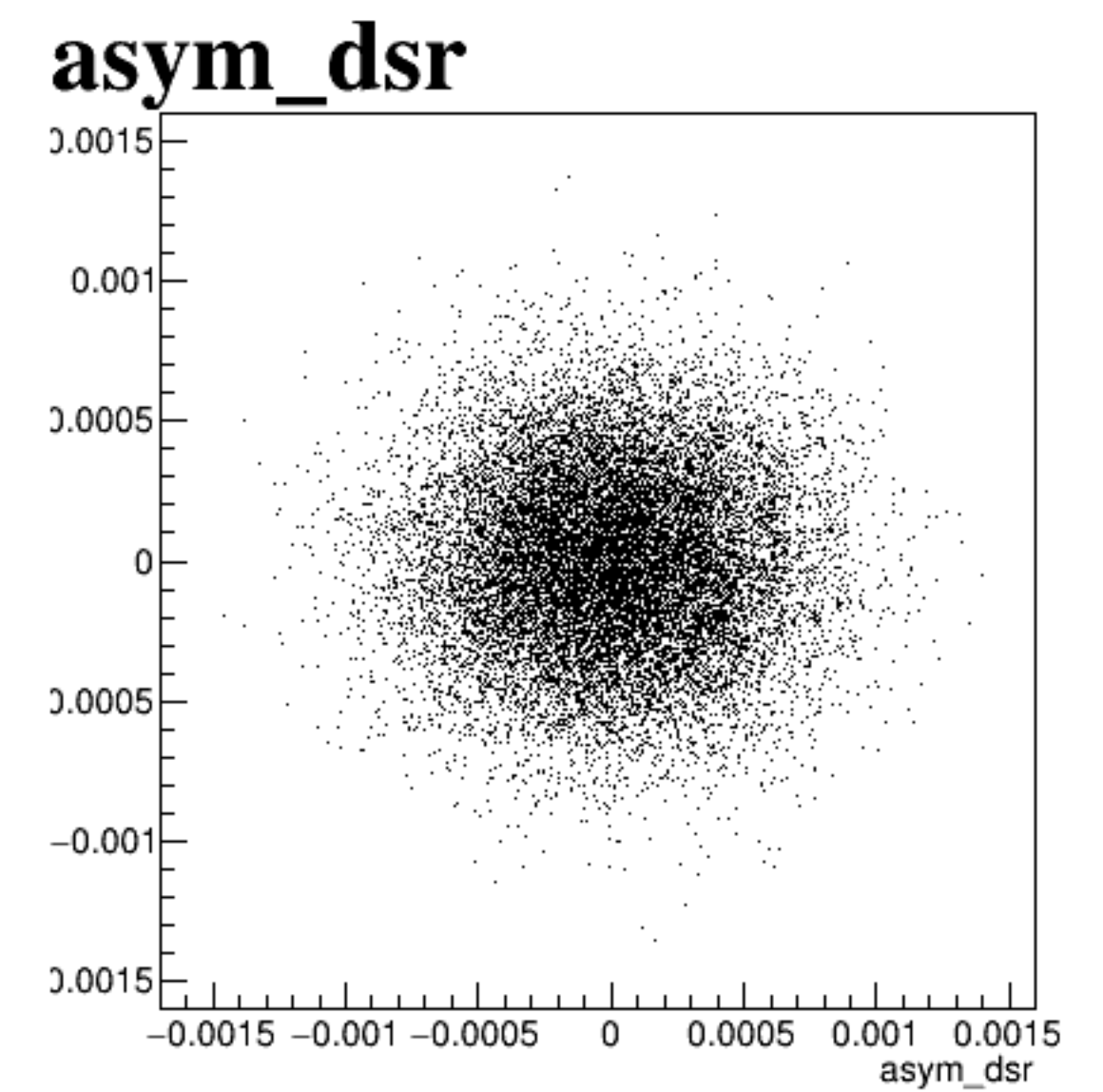
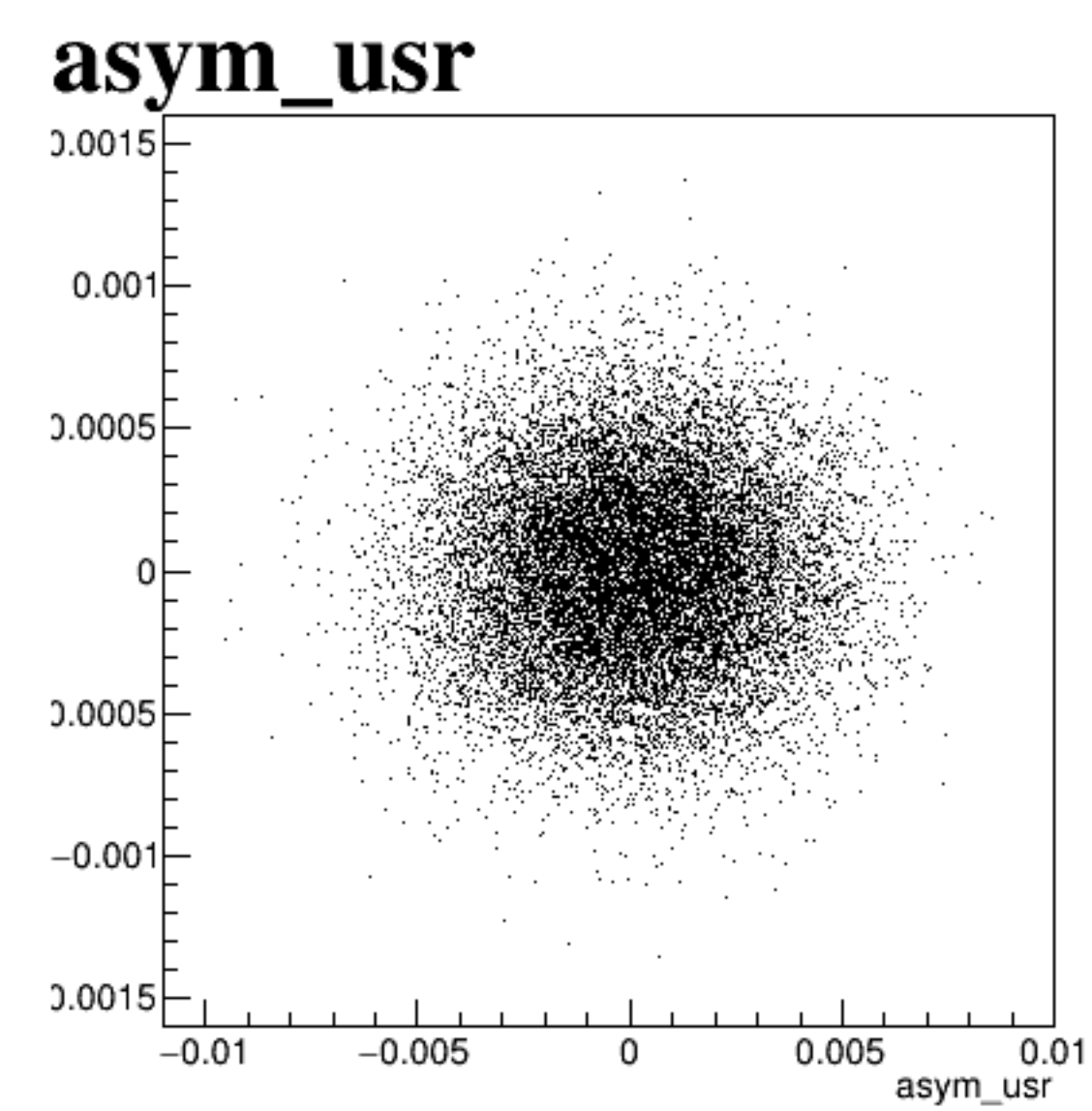
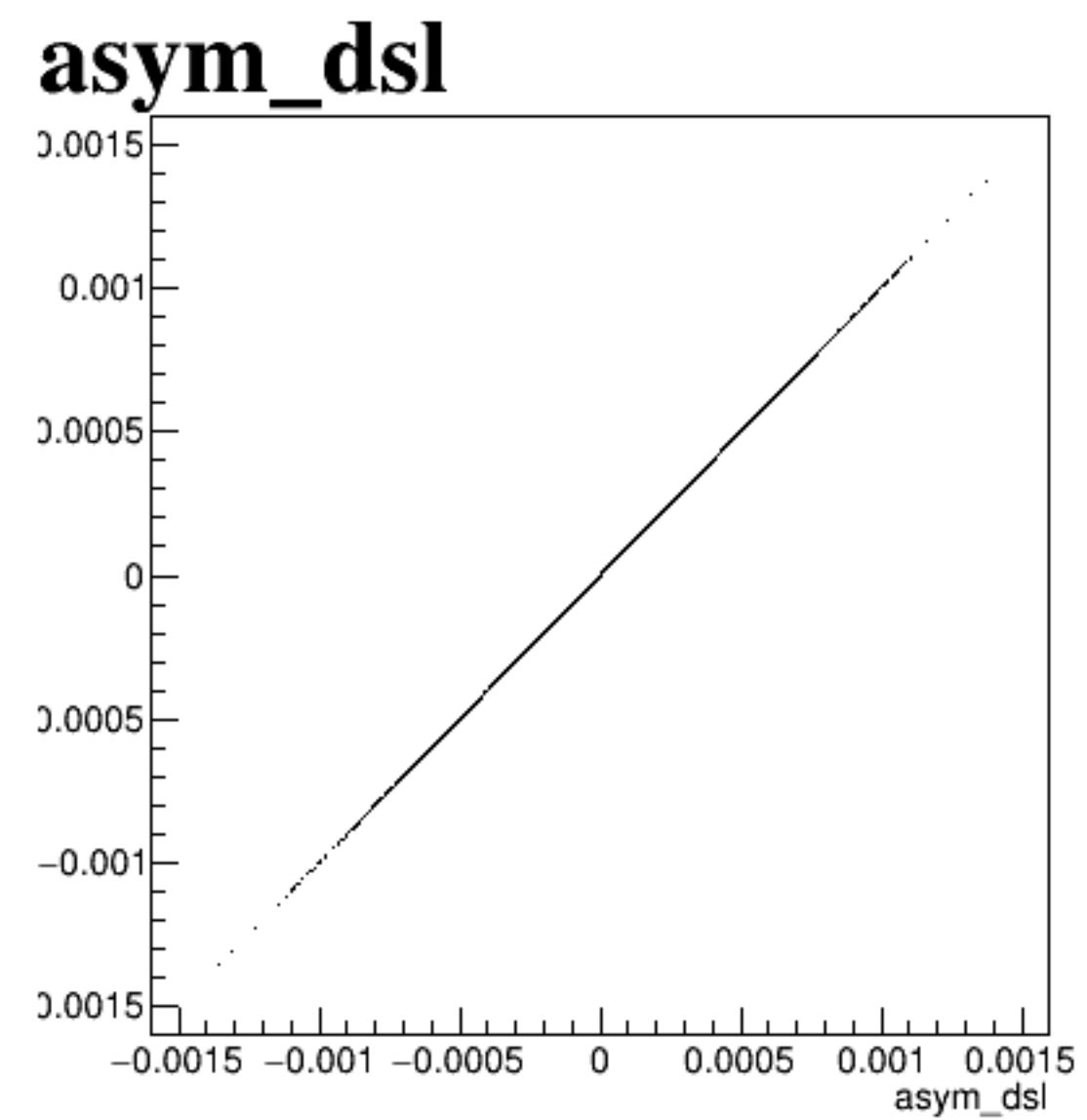
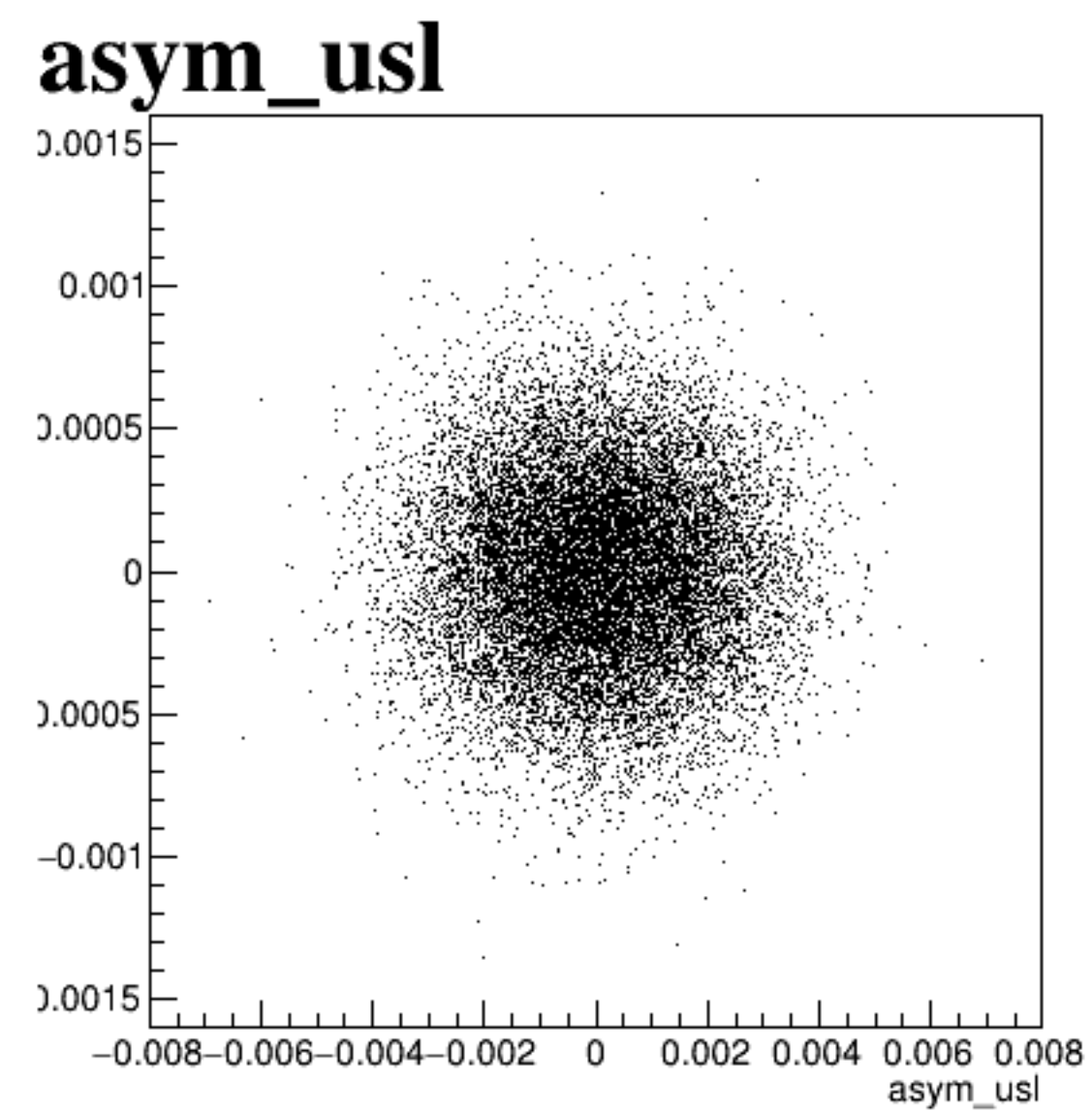
asym_dsr



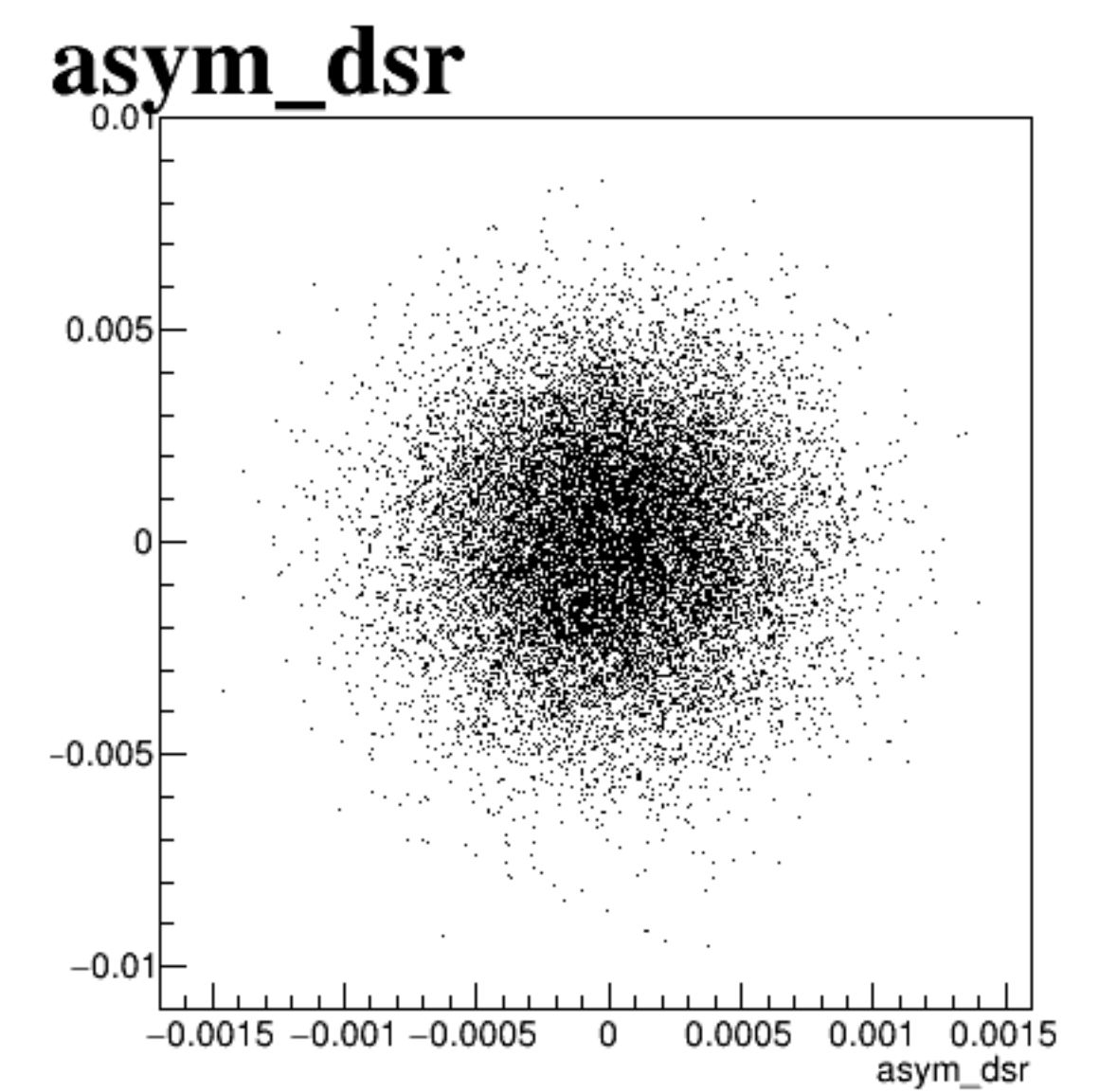
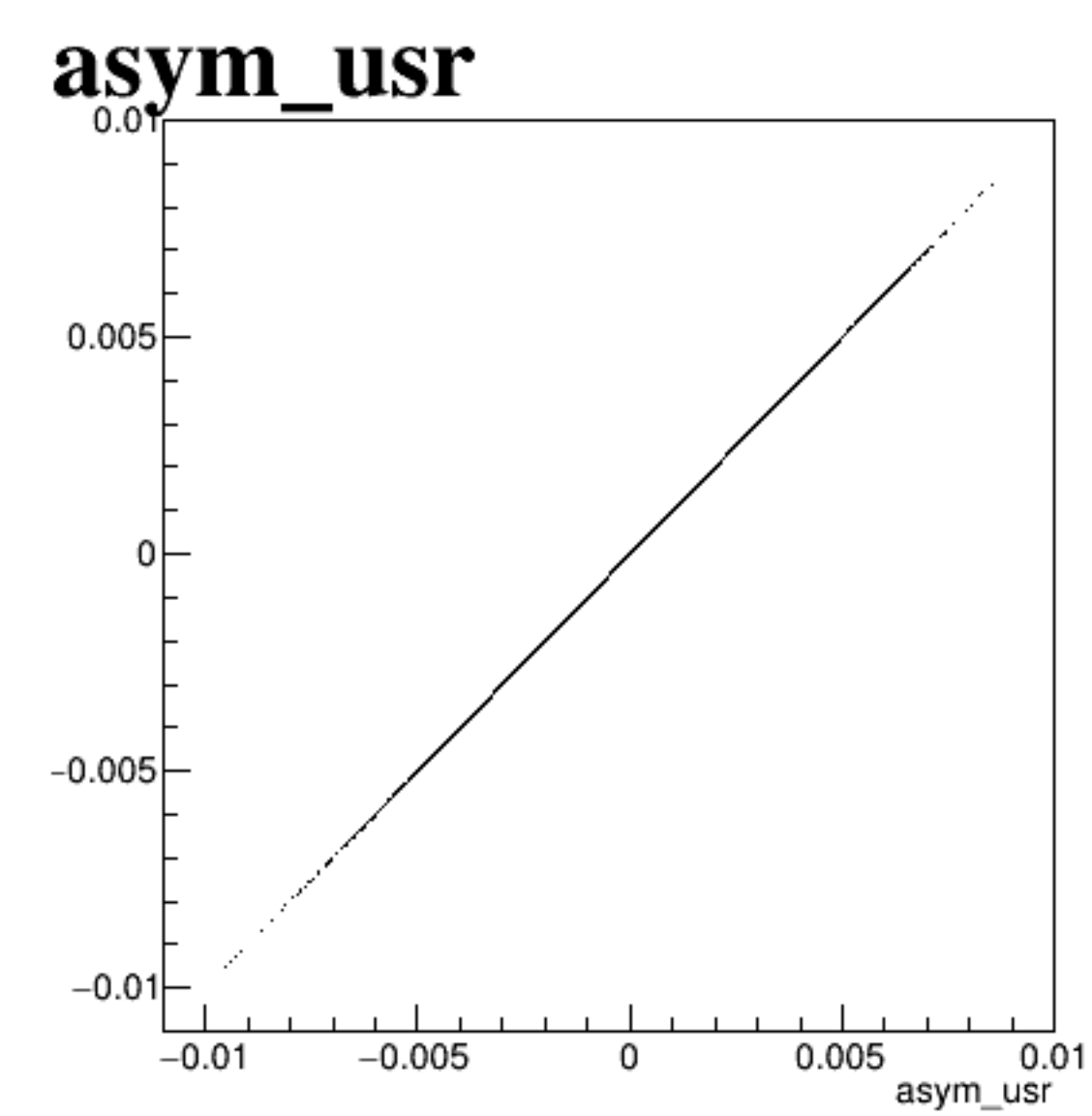
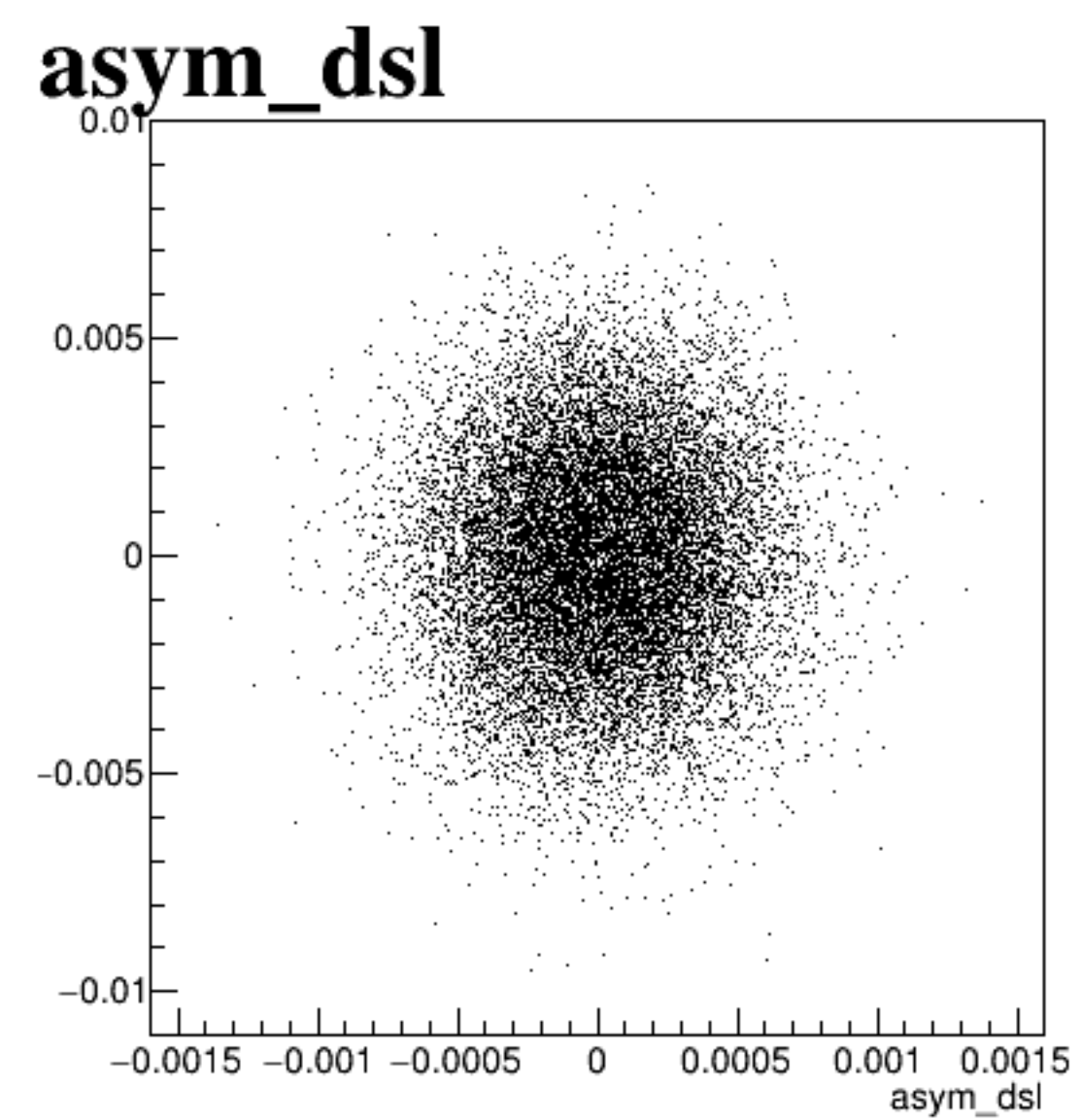
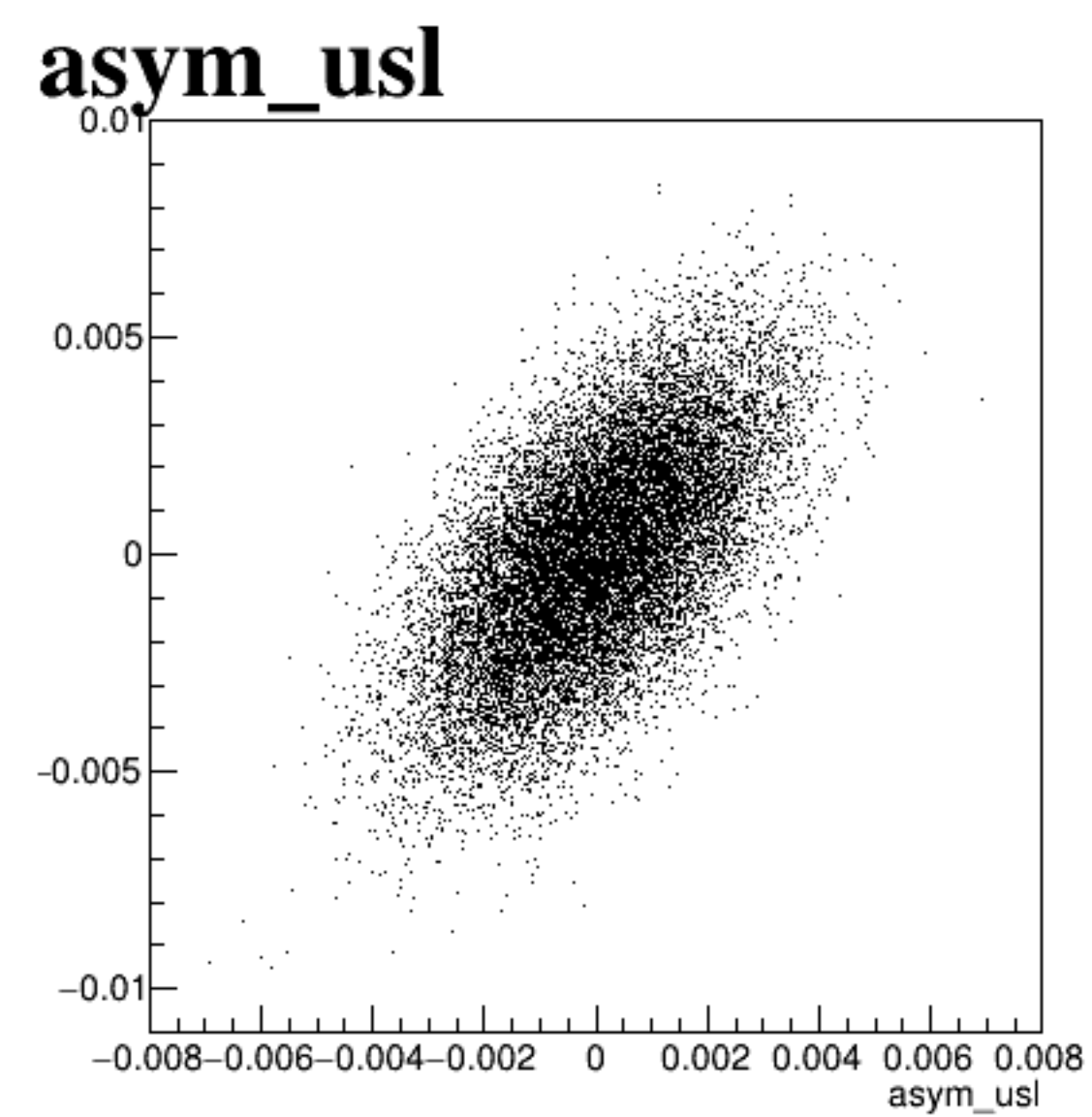
asym_usl



asym_dsl



asym_usr

**asym_dsr**