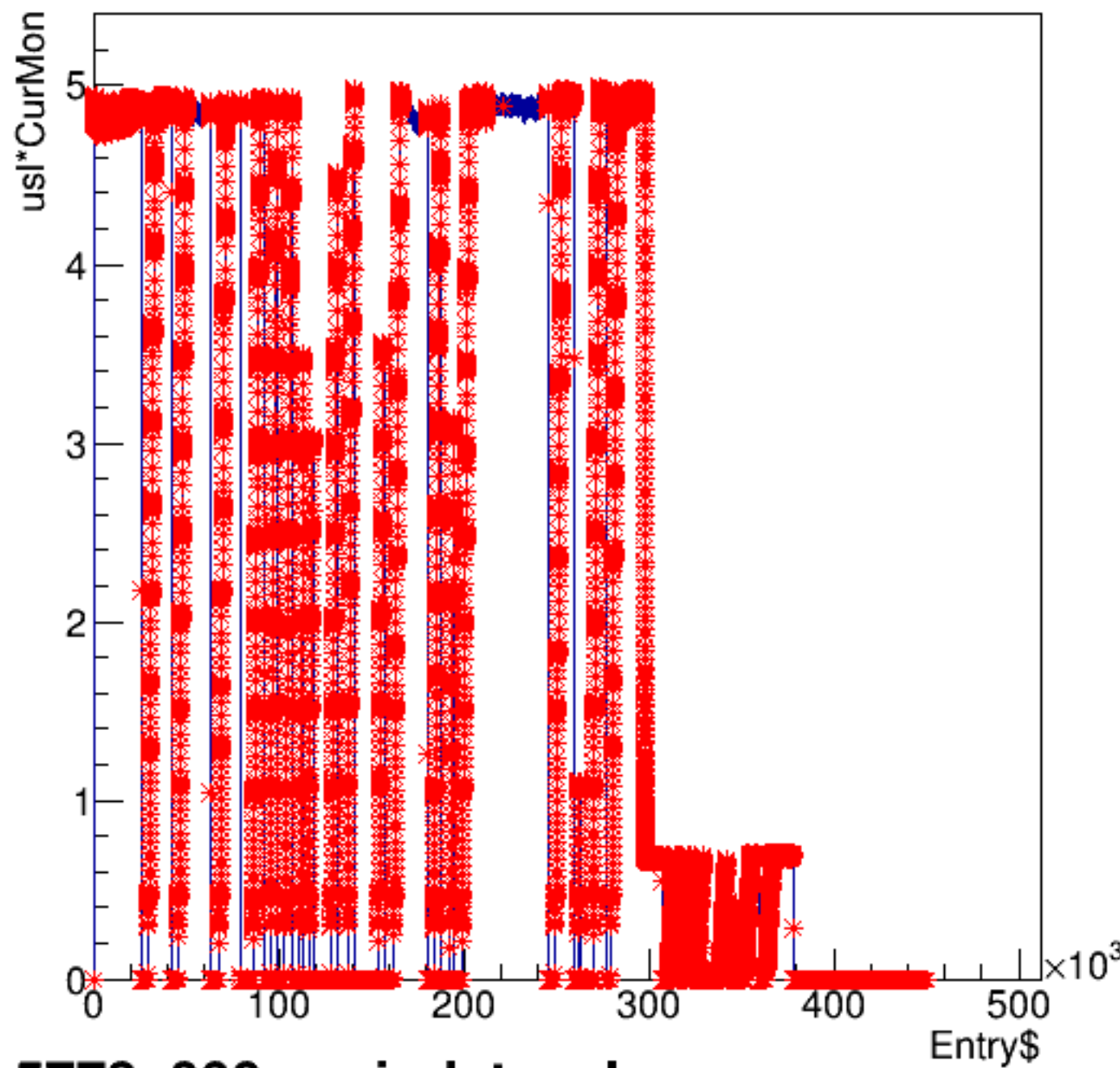
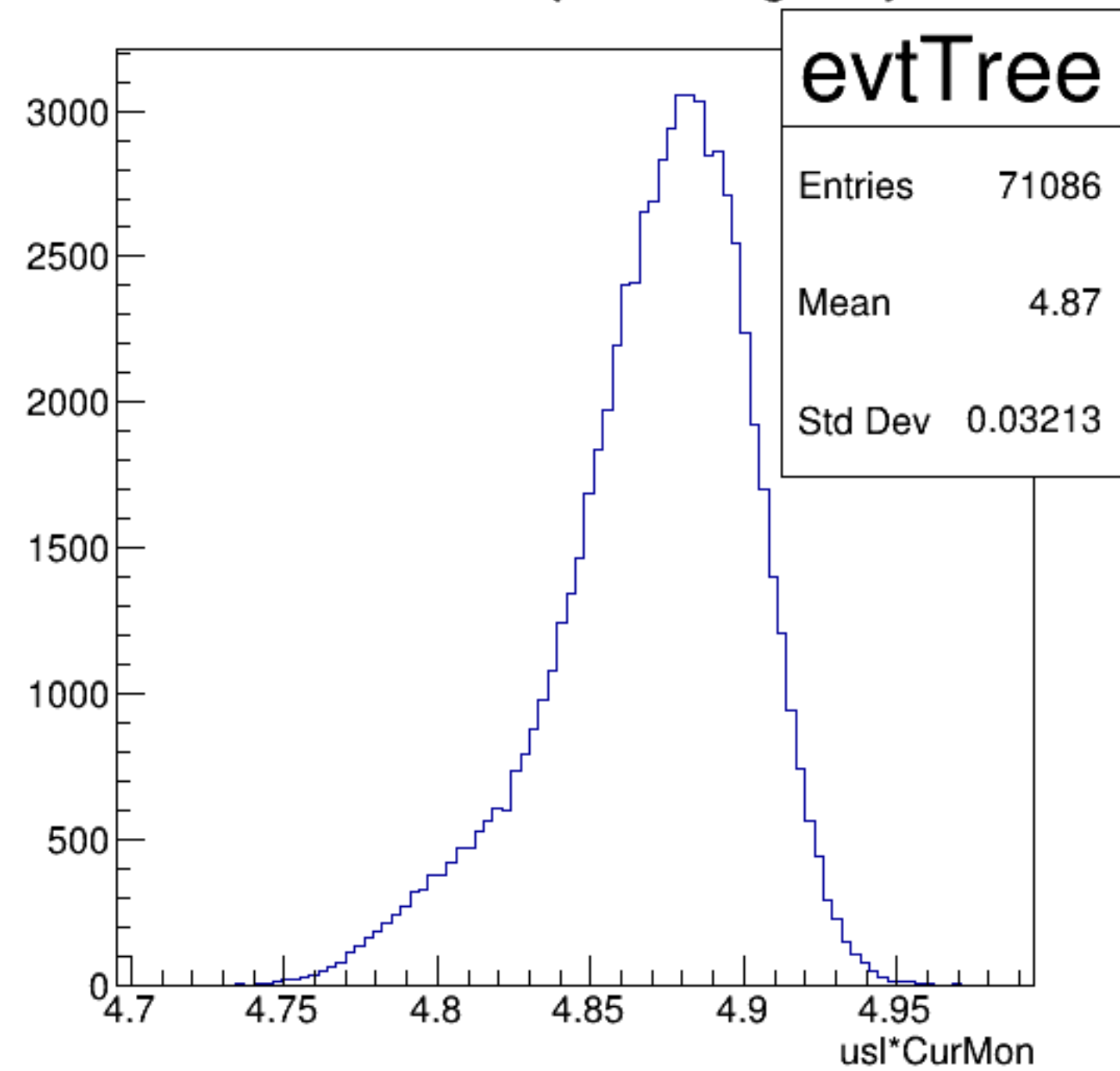


usl\*CurMon:Entry\$

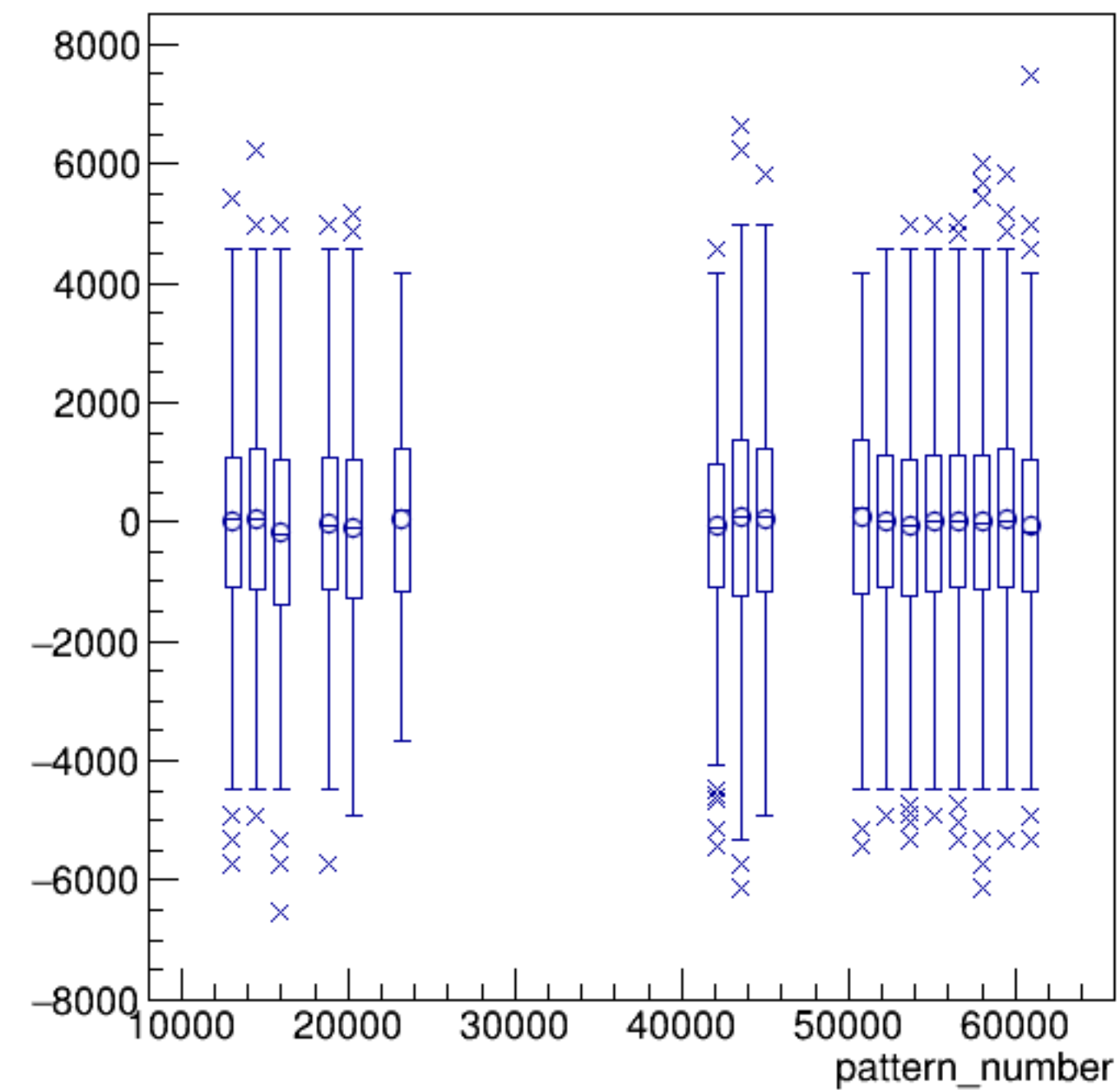


run5773\_000\_maindet\_usl.png

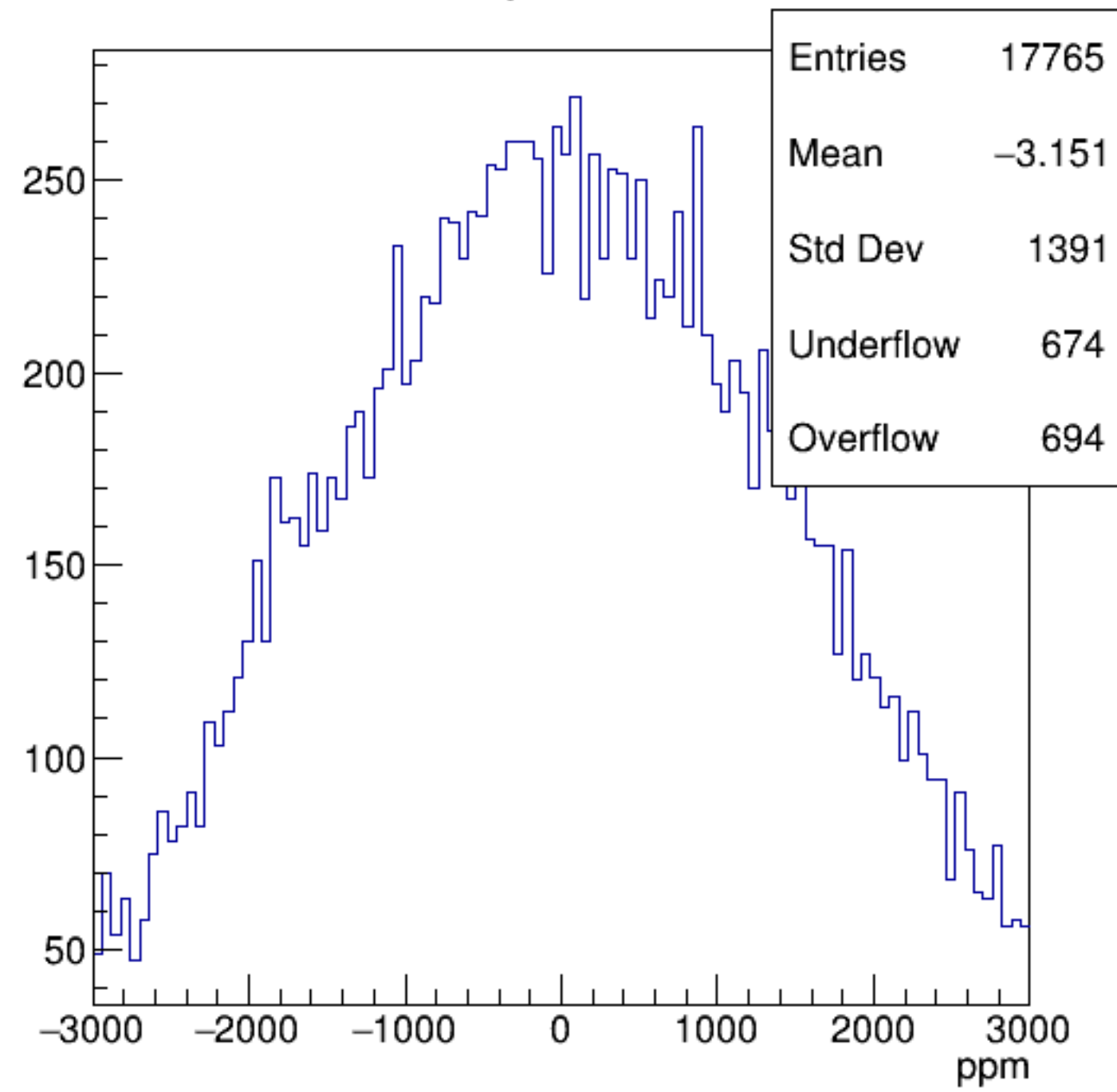
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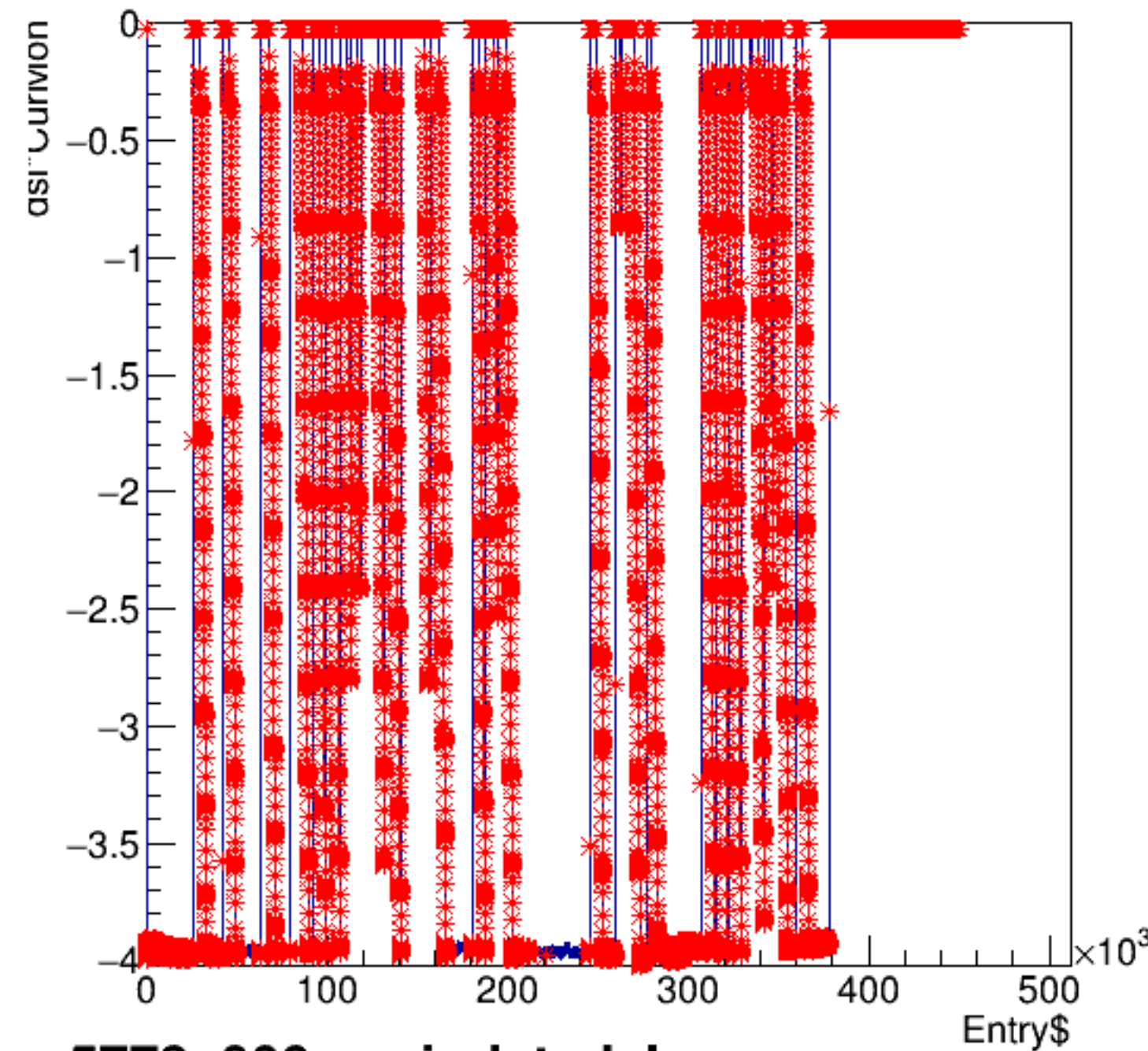
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asym\_usl

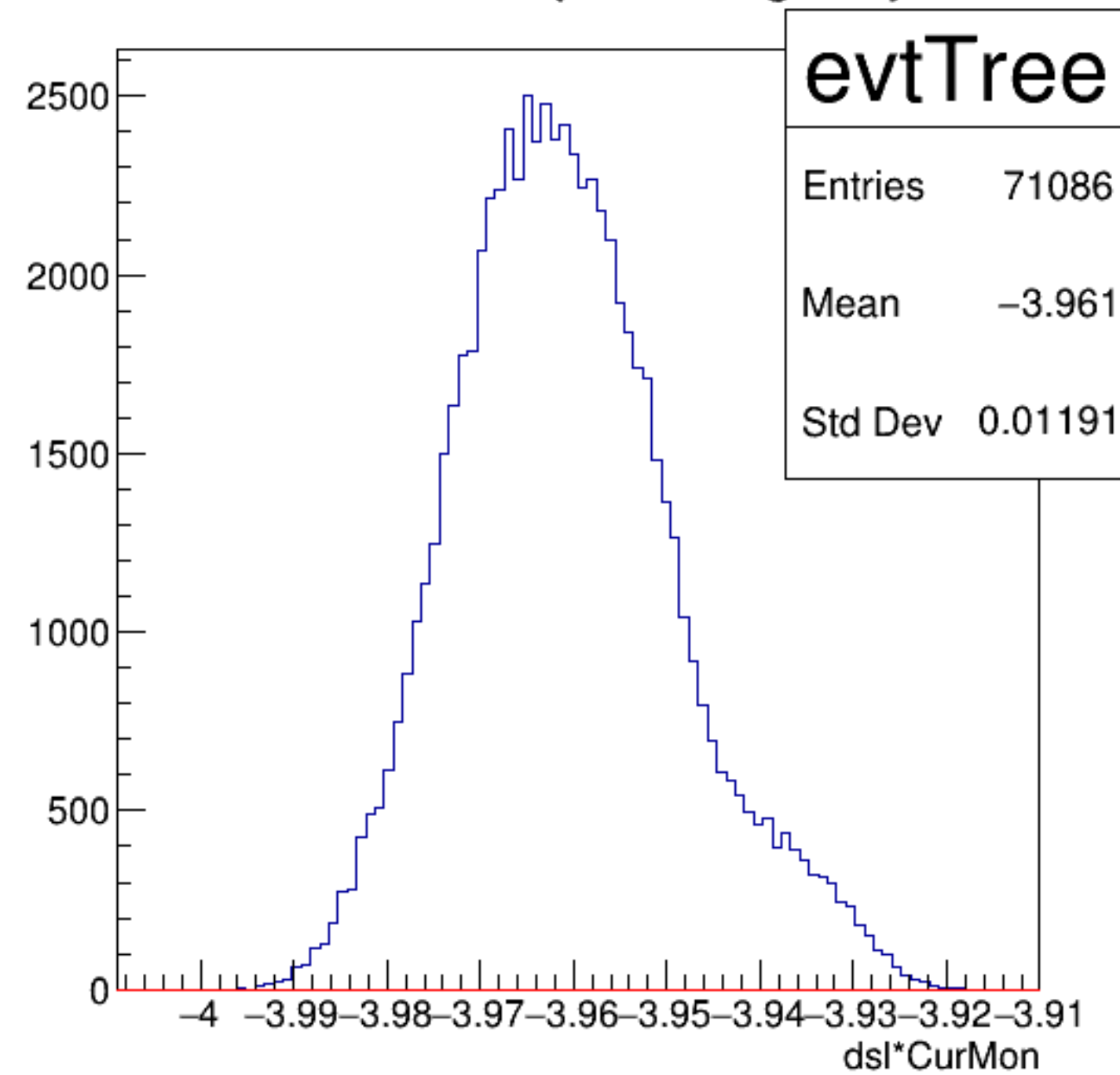


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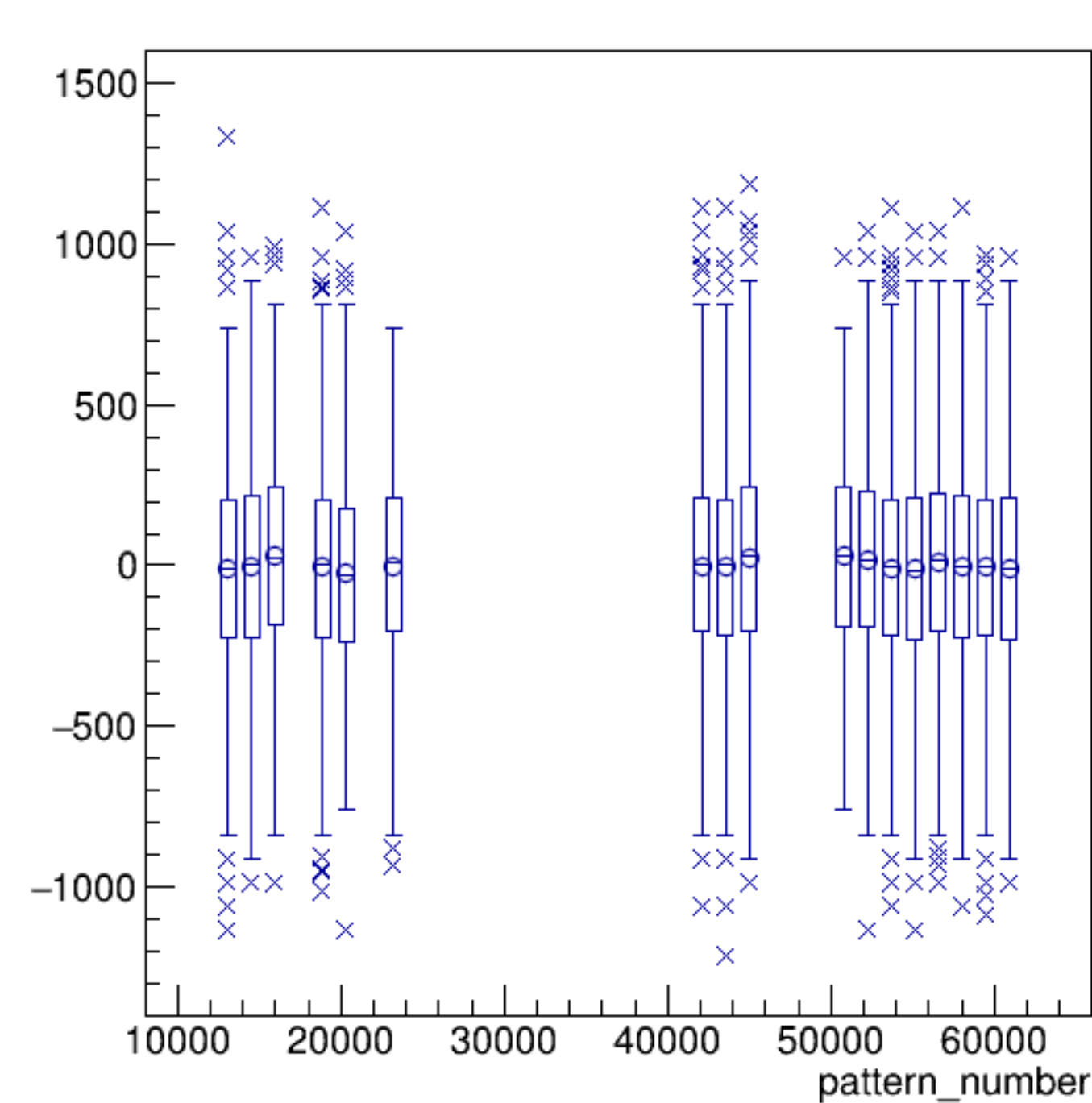


run5773\_000\_maindet\_dsl.png

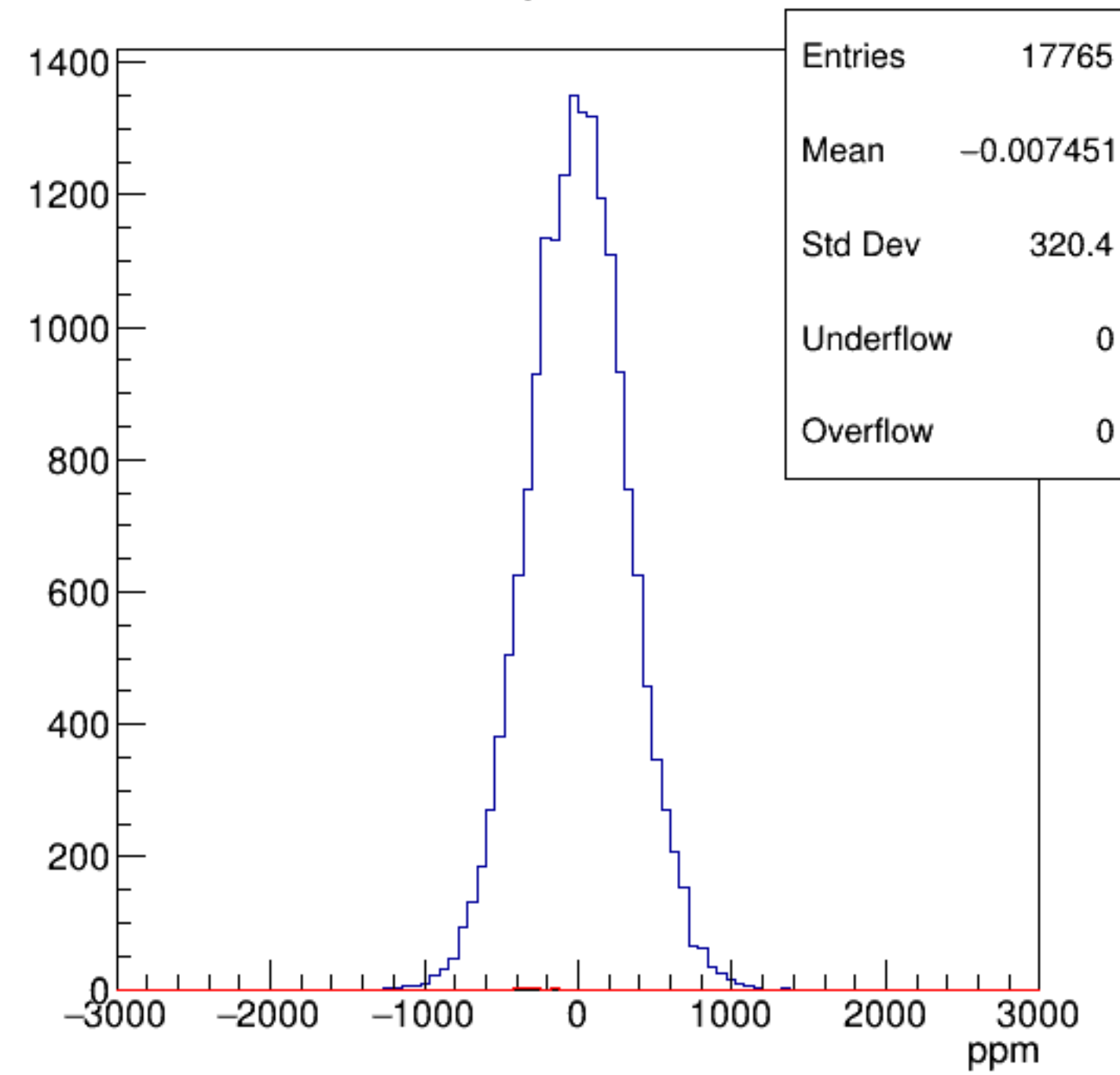
dsl\*CurMon {ErrorFlag==0}



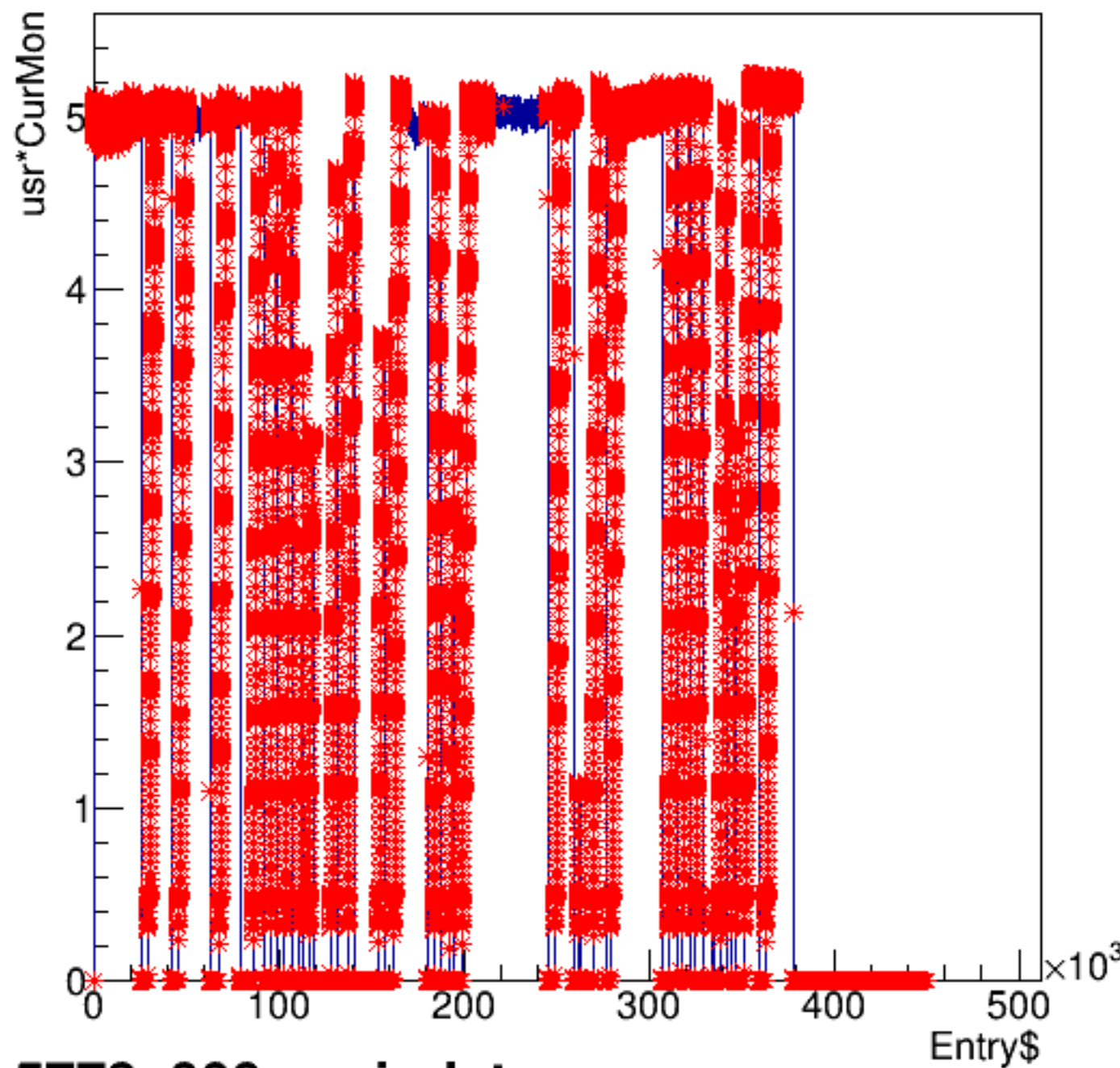
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asym\_dsl

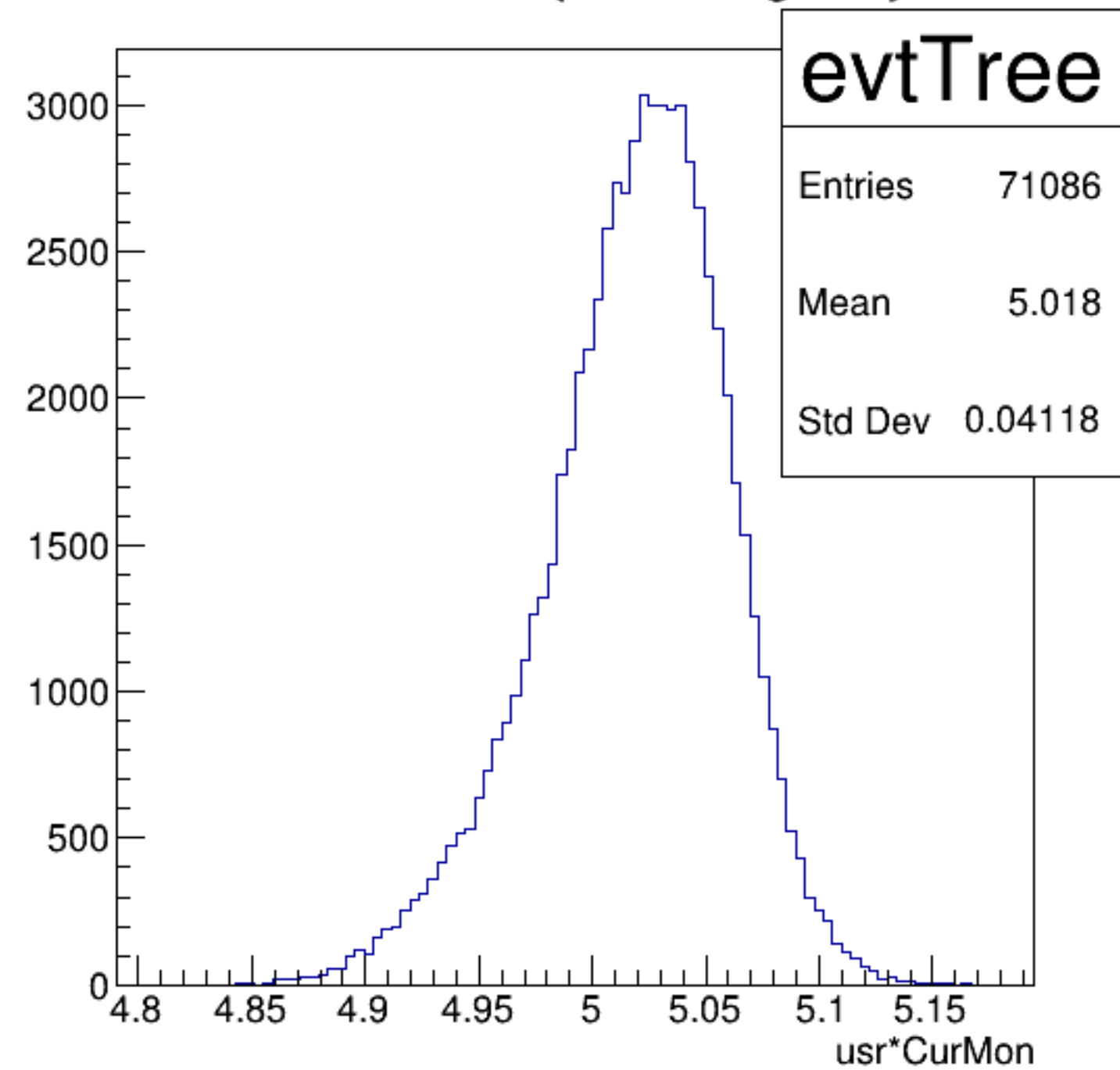


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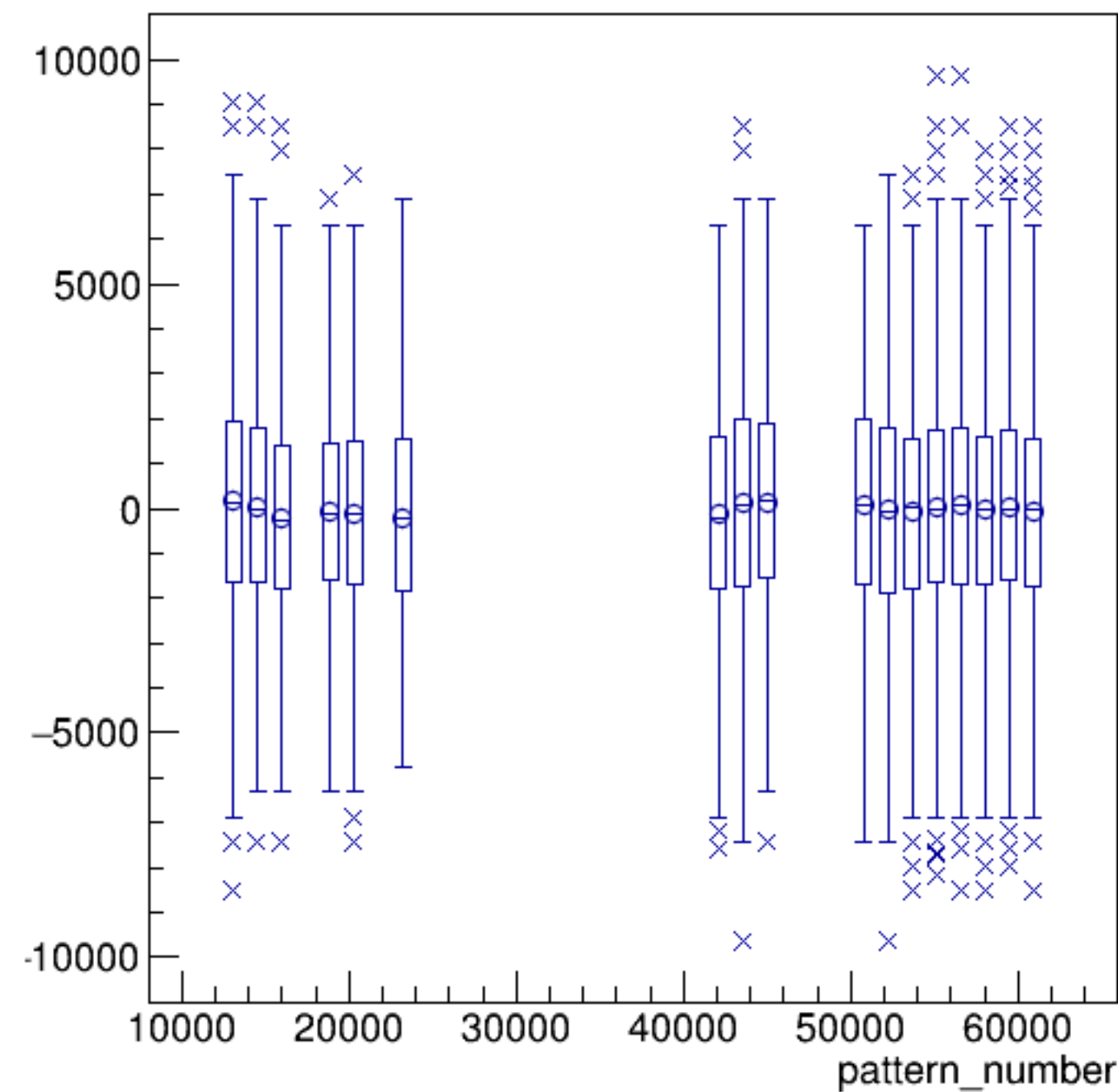


run5773\_000\_maintet\_usr.png

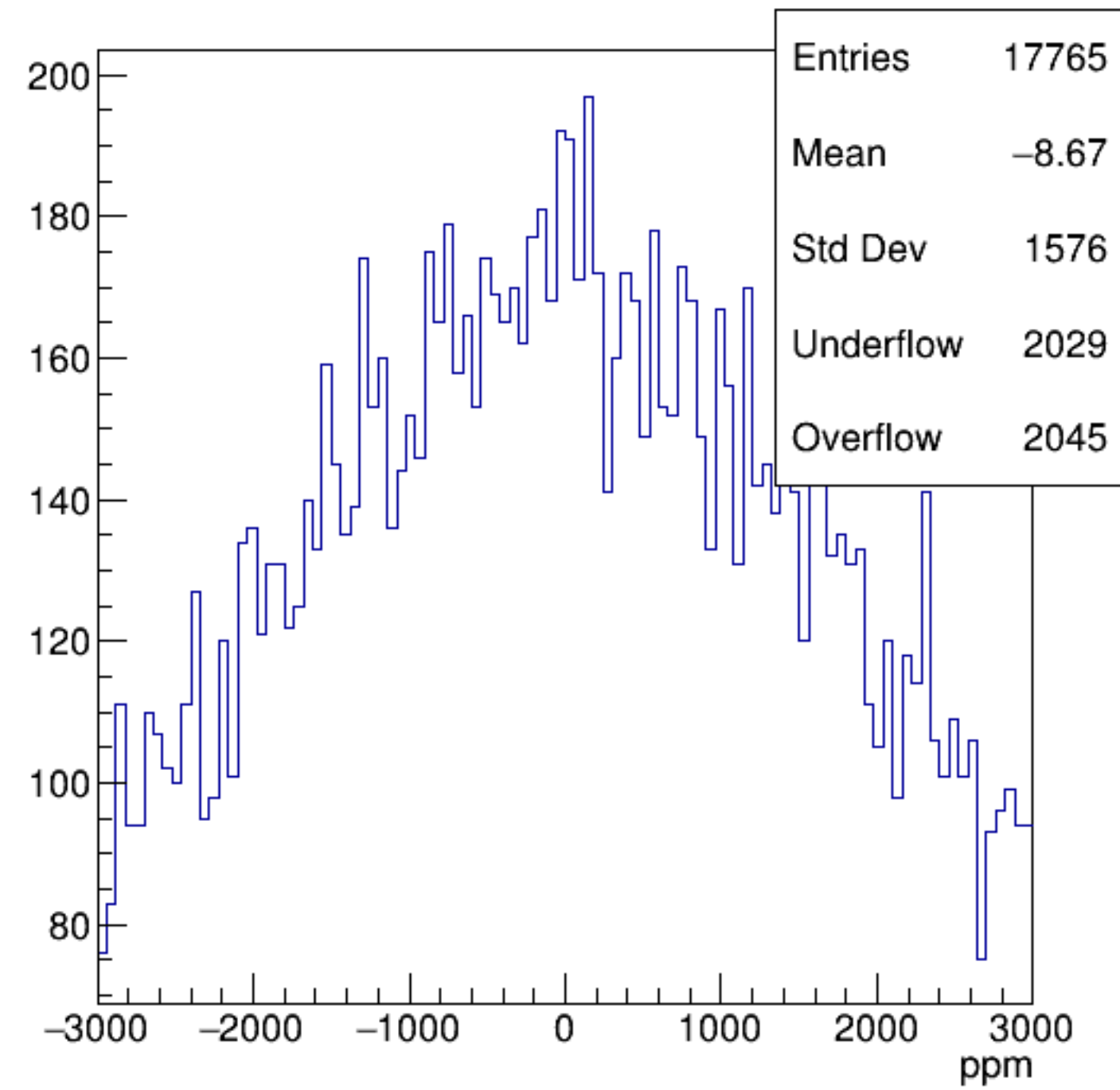
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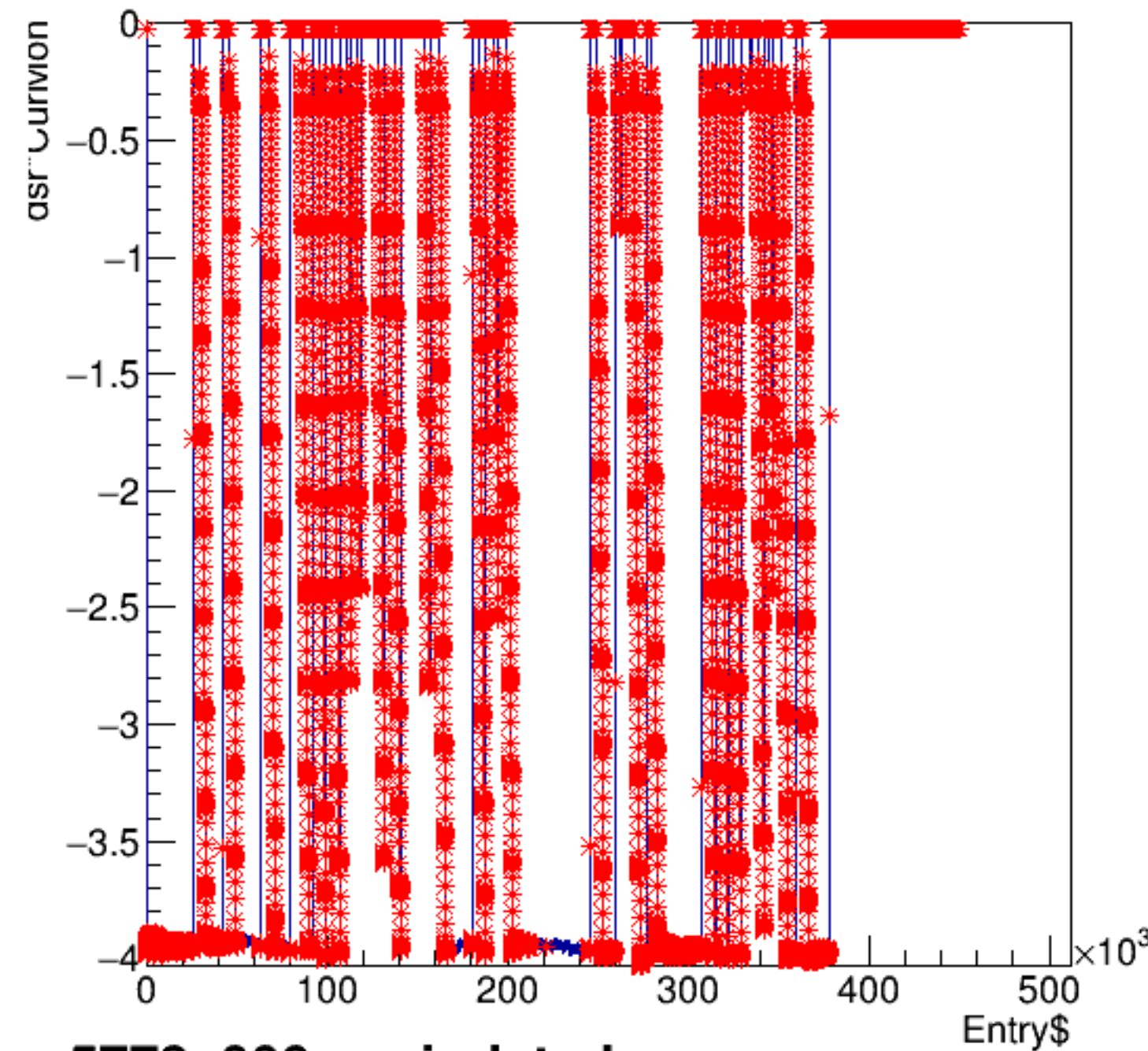
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asym\_usr

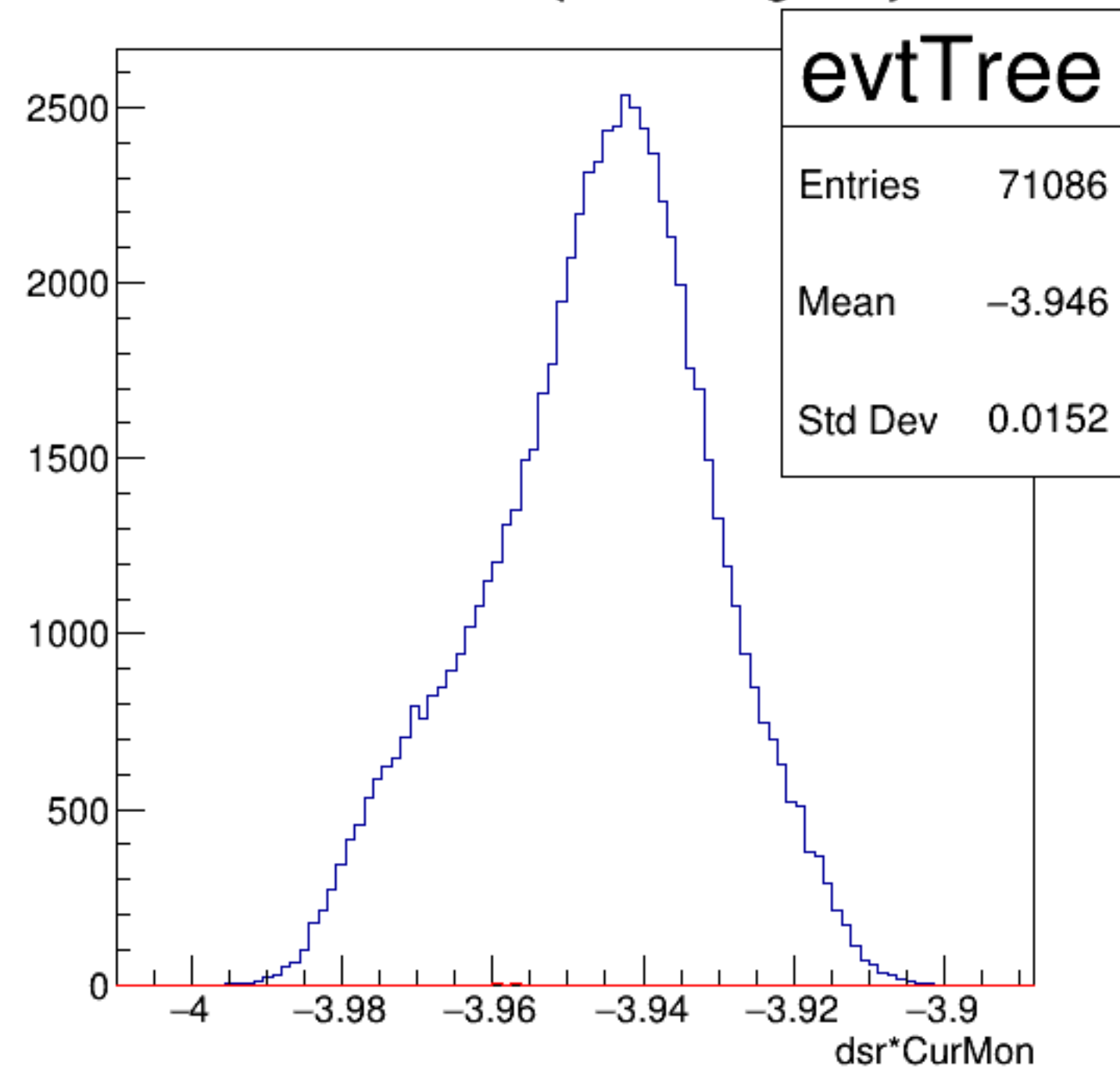


dsr\*CurMon:Entry\$

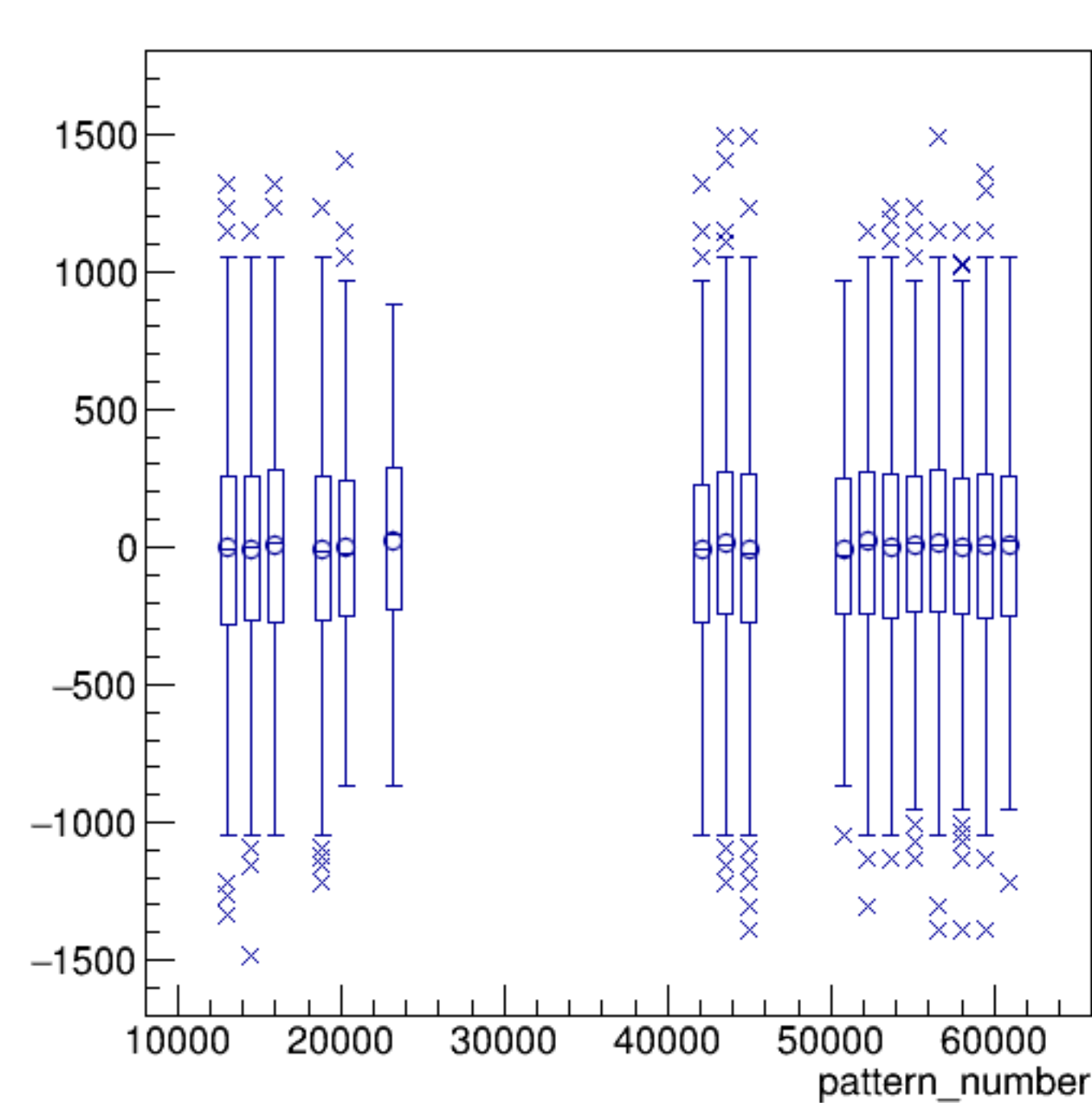


run5773\_000\_maintet\_dsr.png

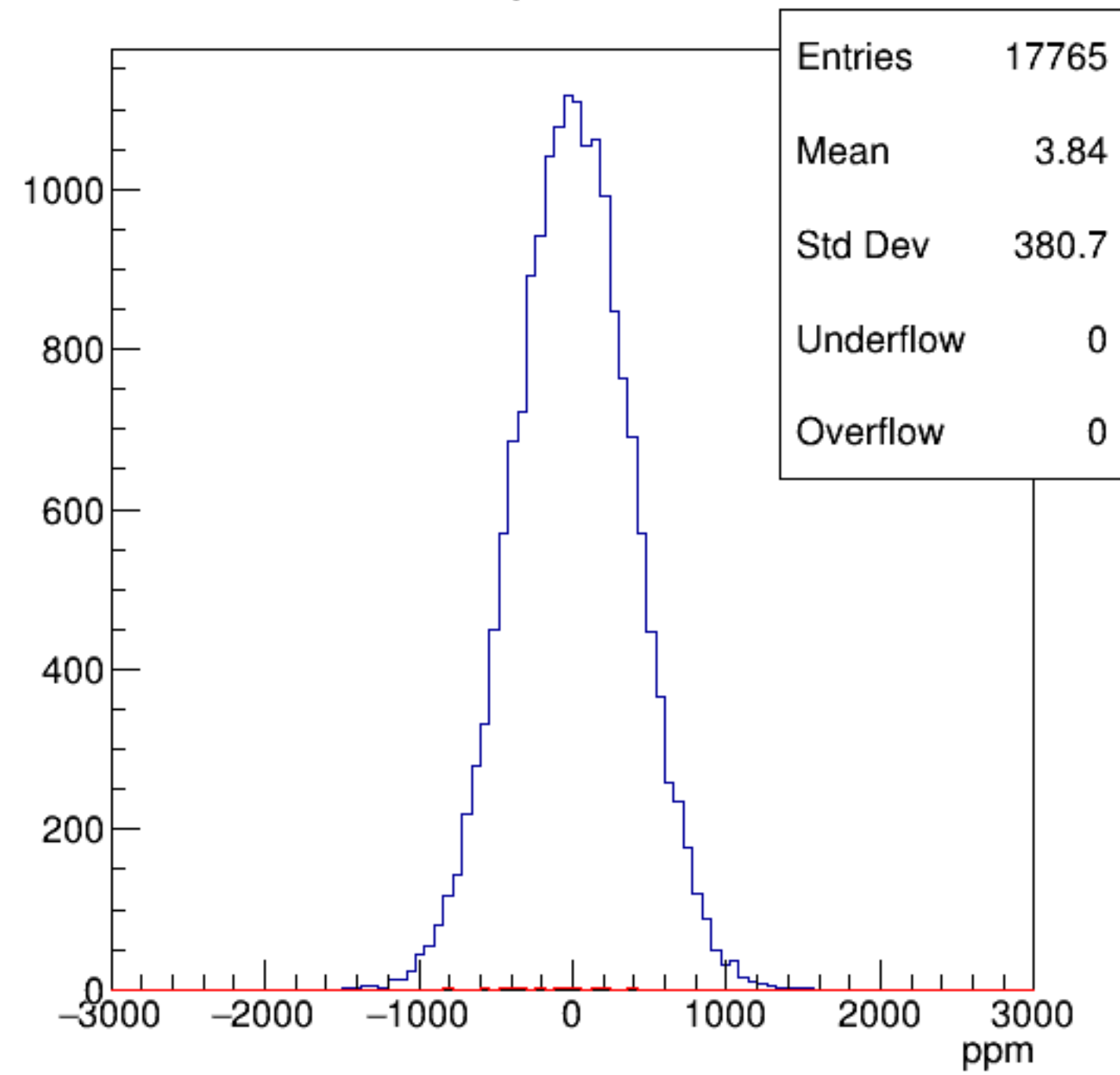
dsr\*CurMon {ErrorFlag==0}



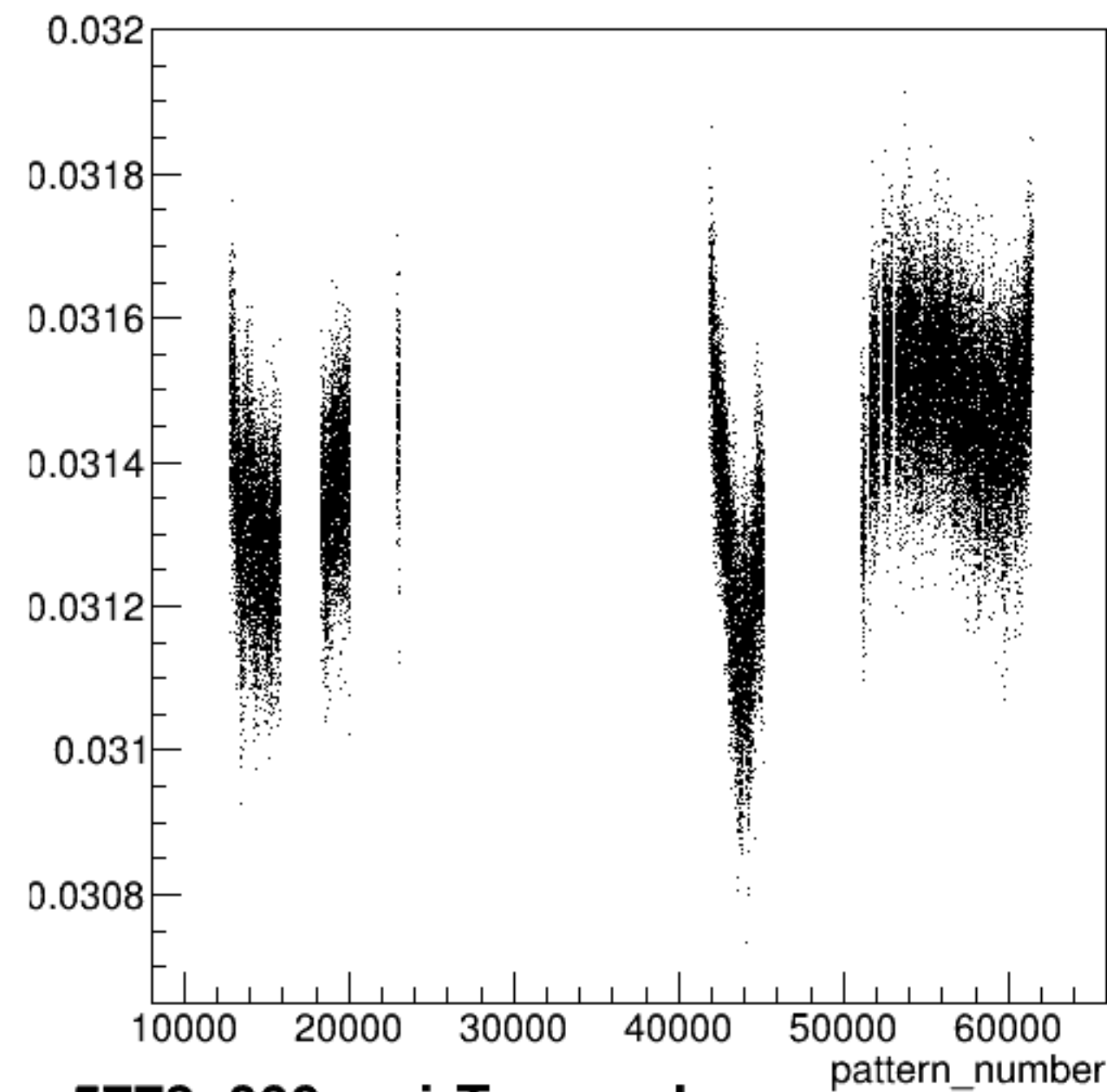
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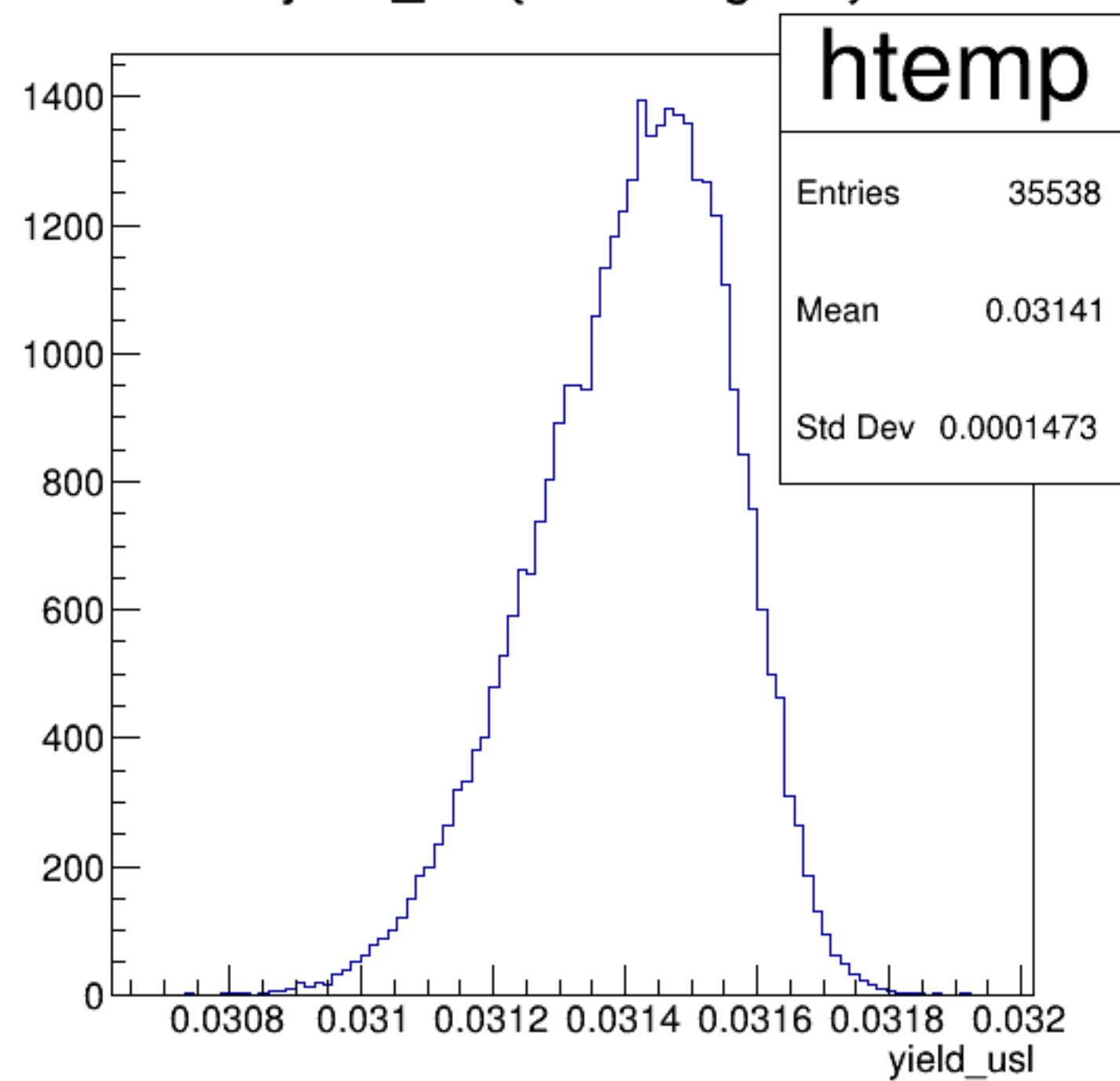
asym\_dsr



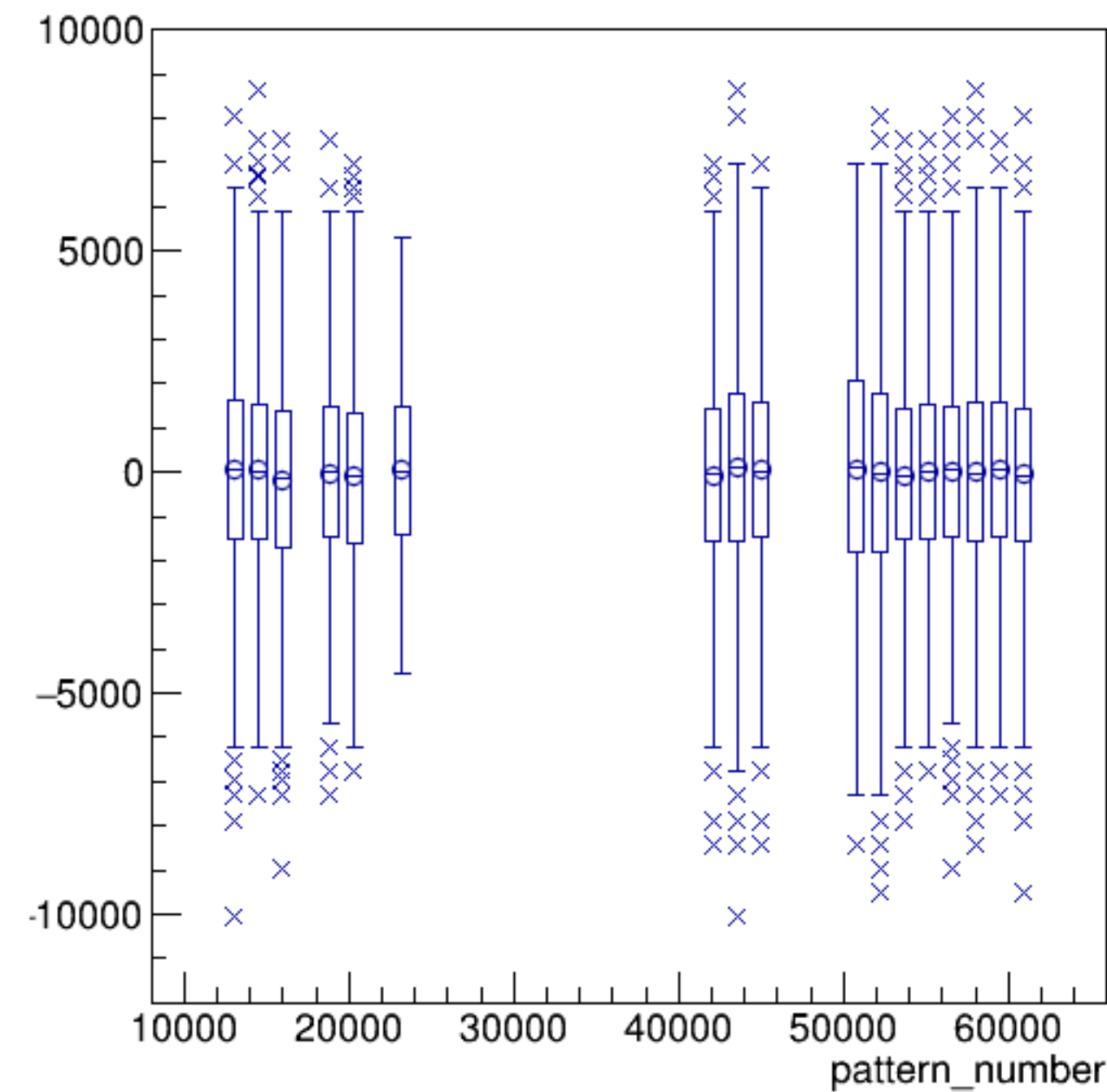
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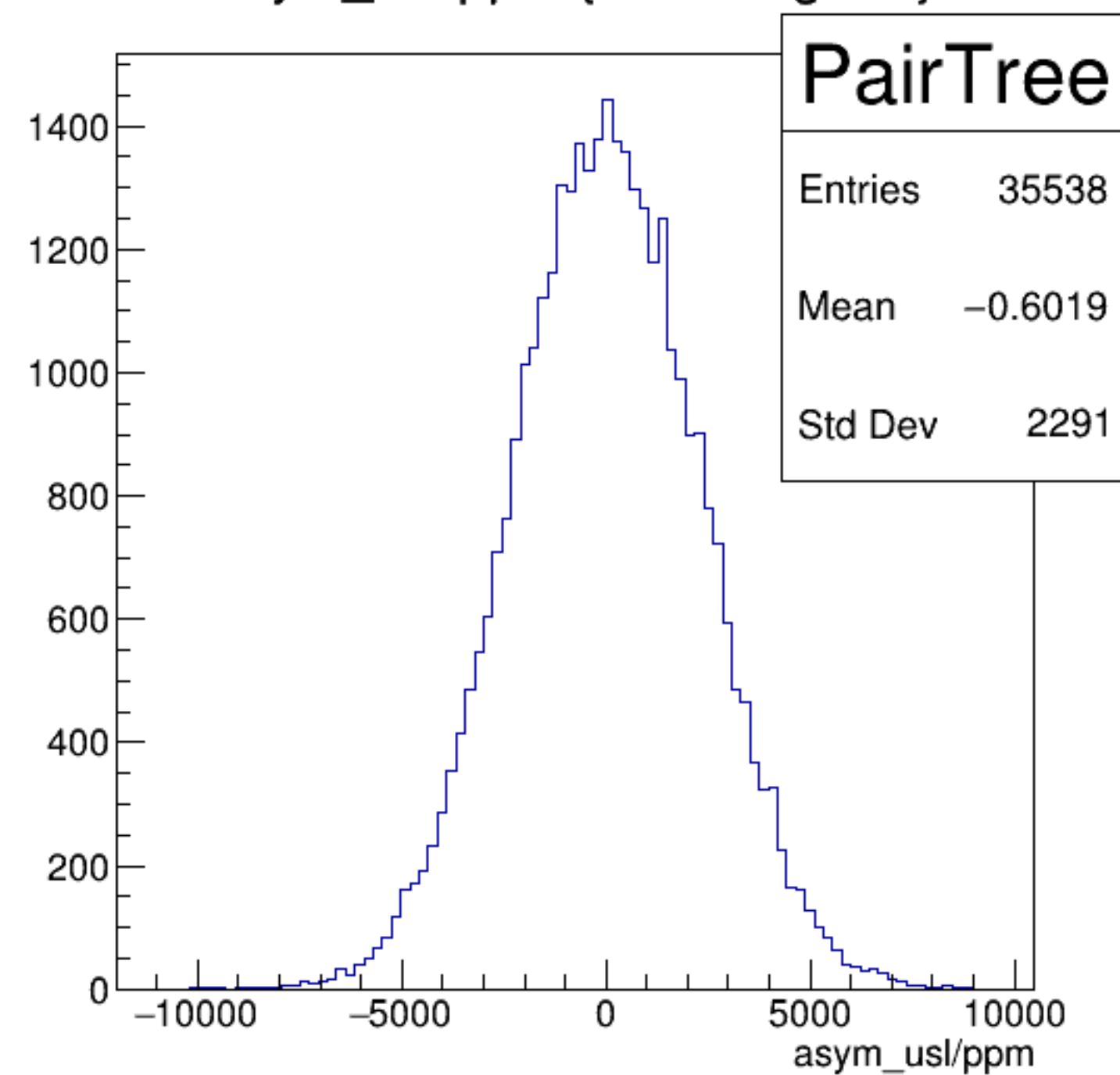
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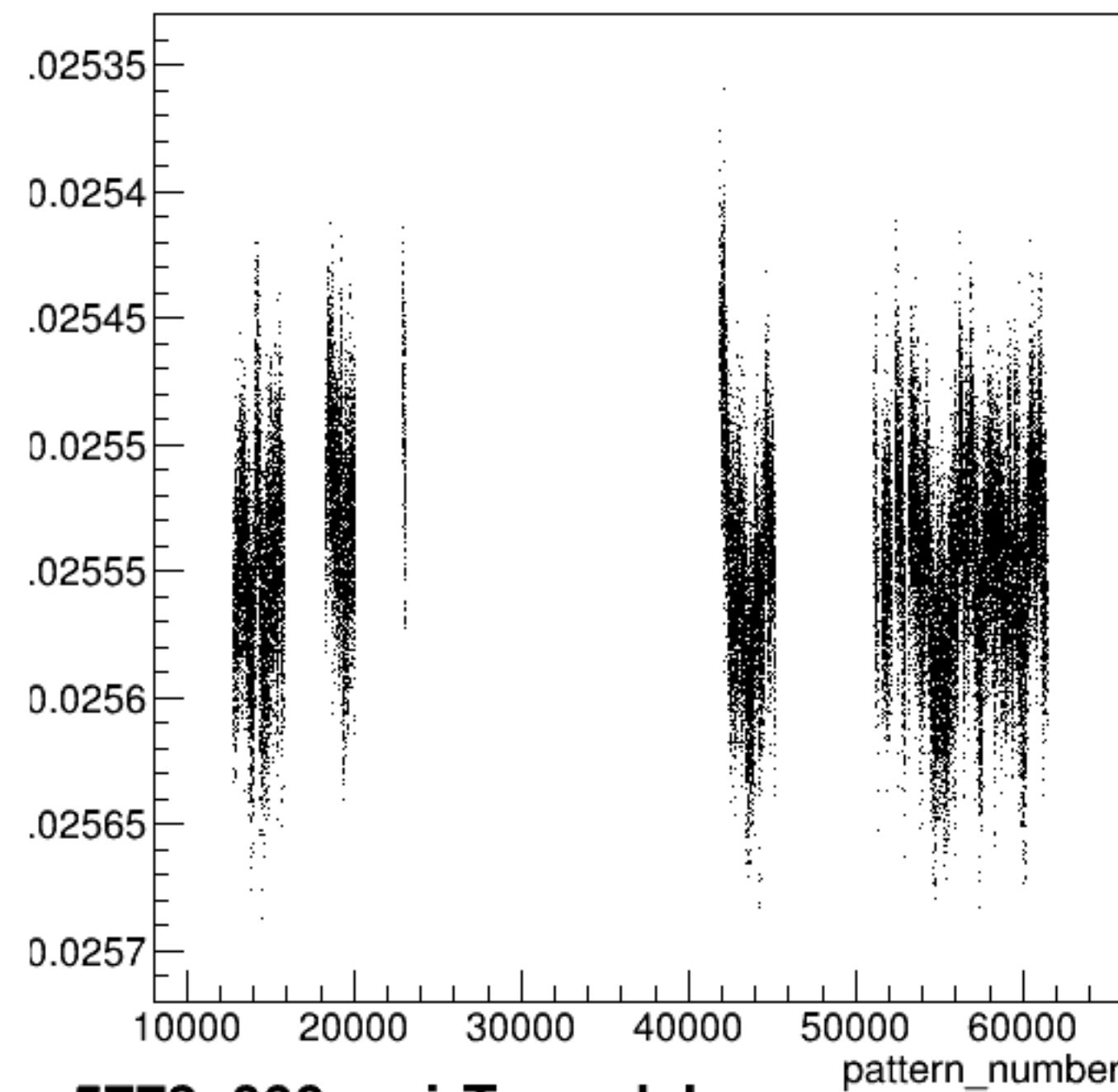
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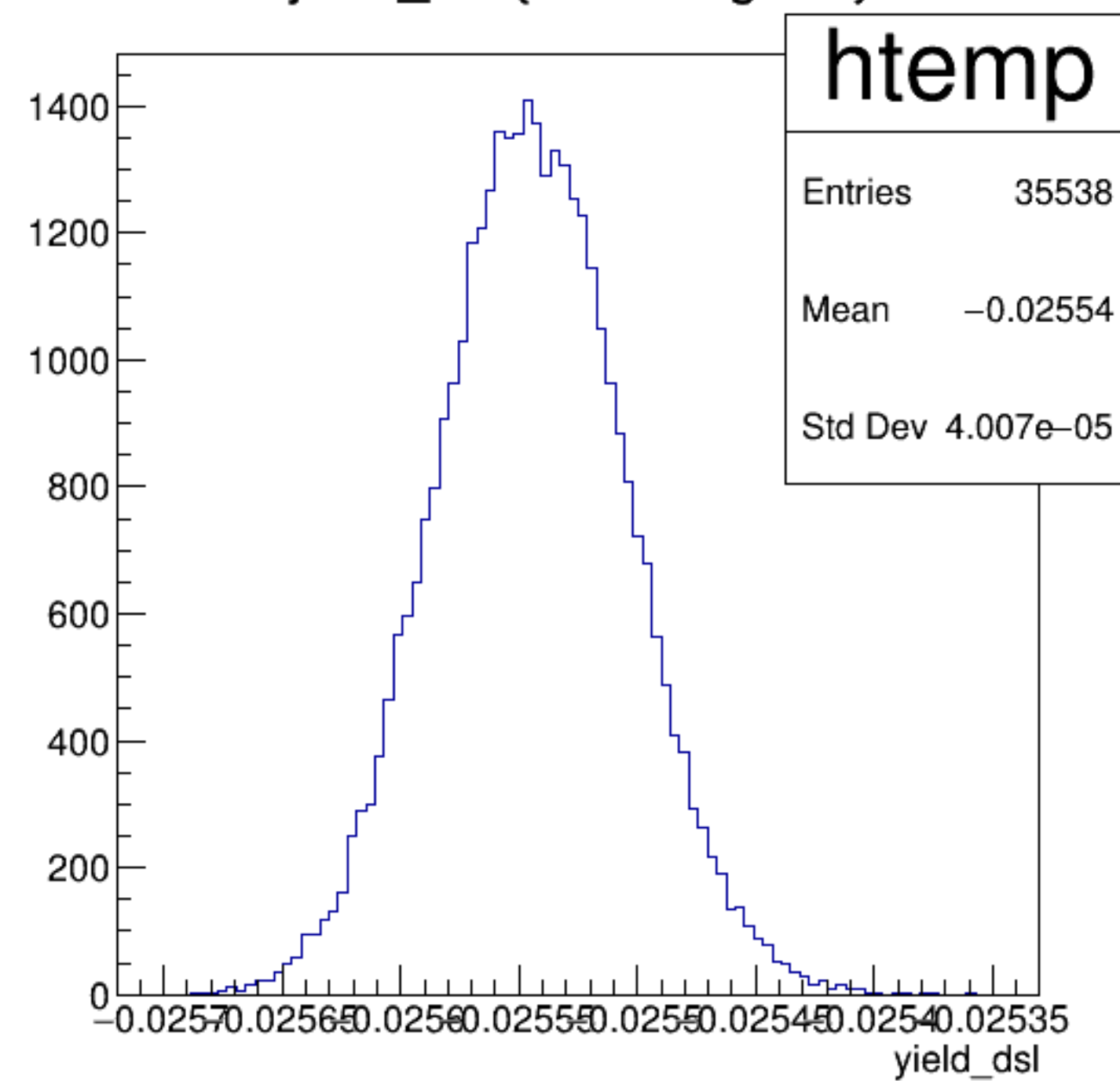
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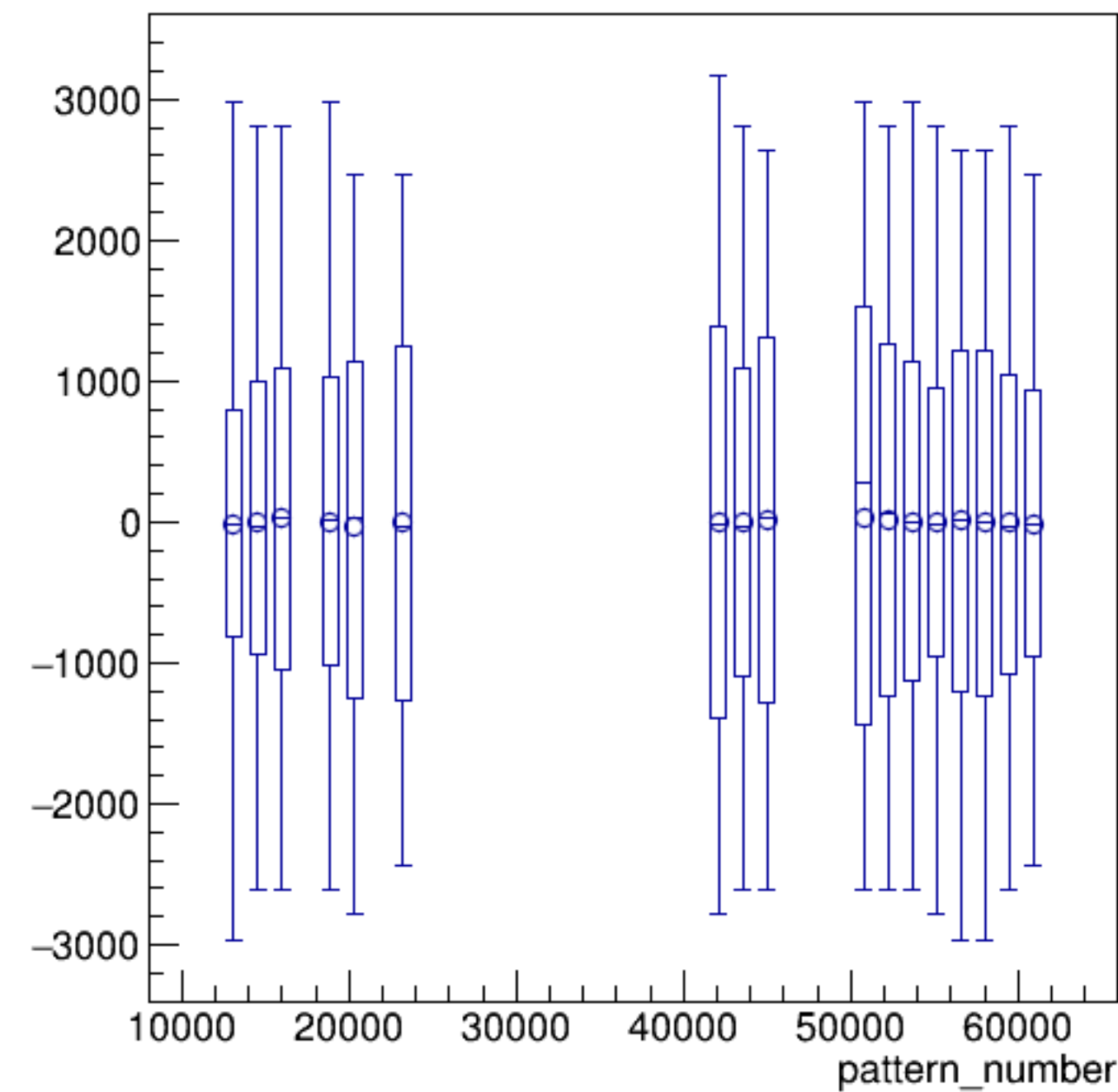
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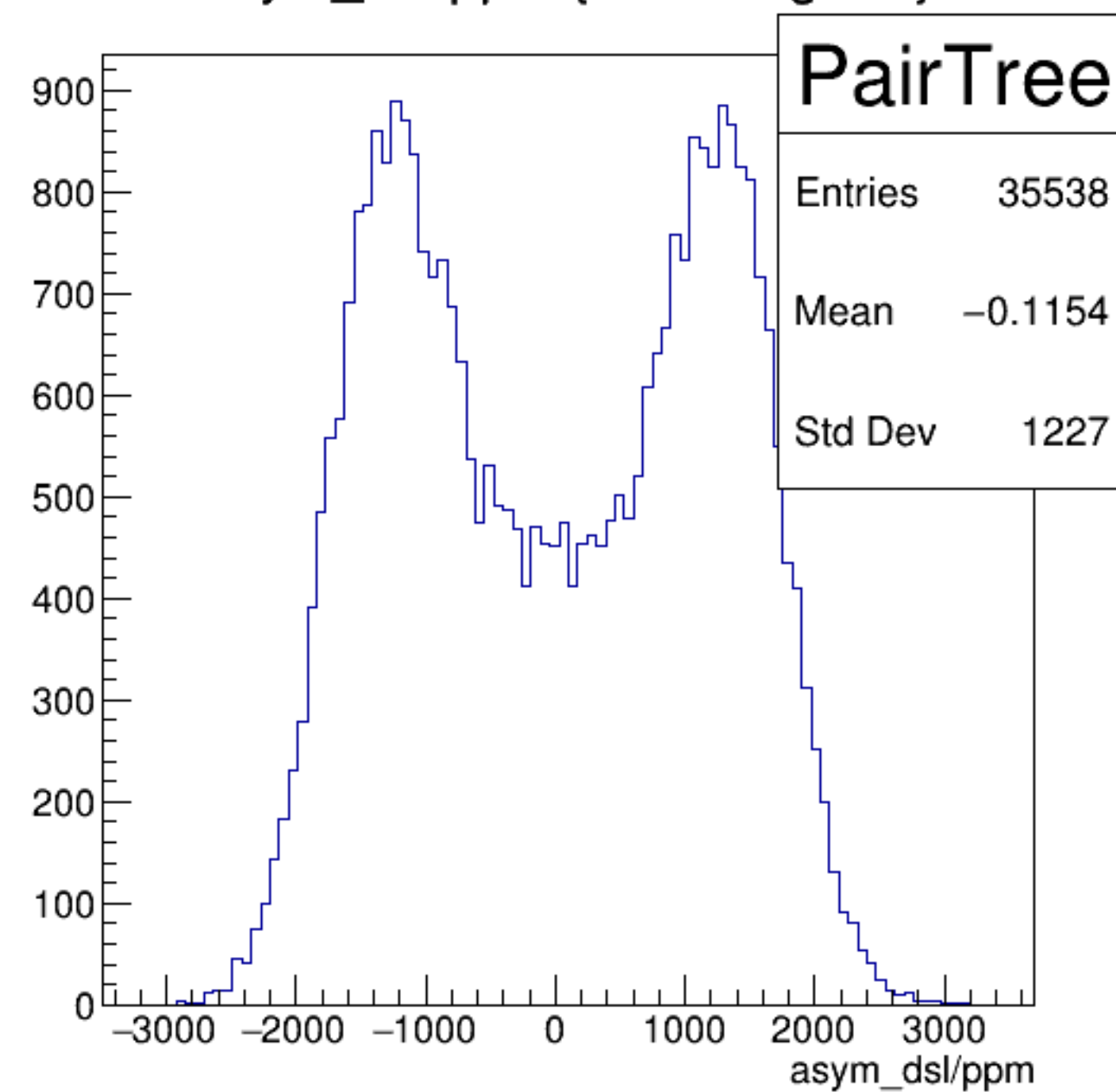
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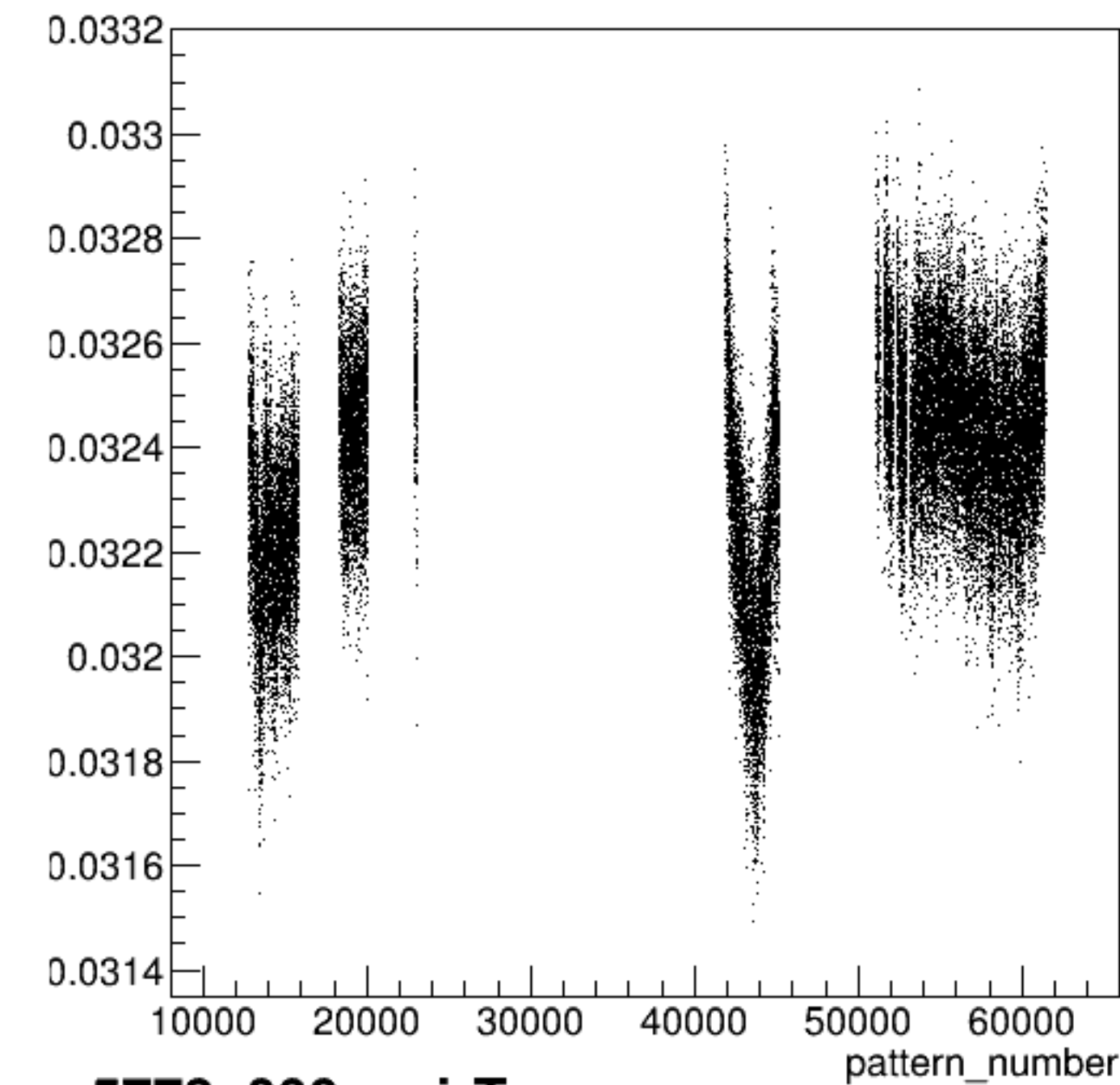
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asym\_dsl/ppm {ErrorFlag==0}

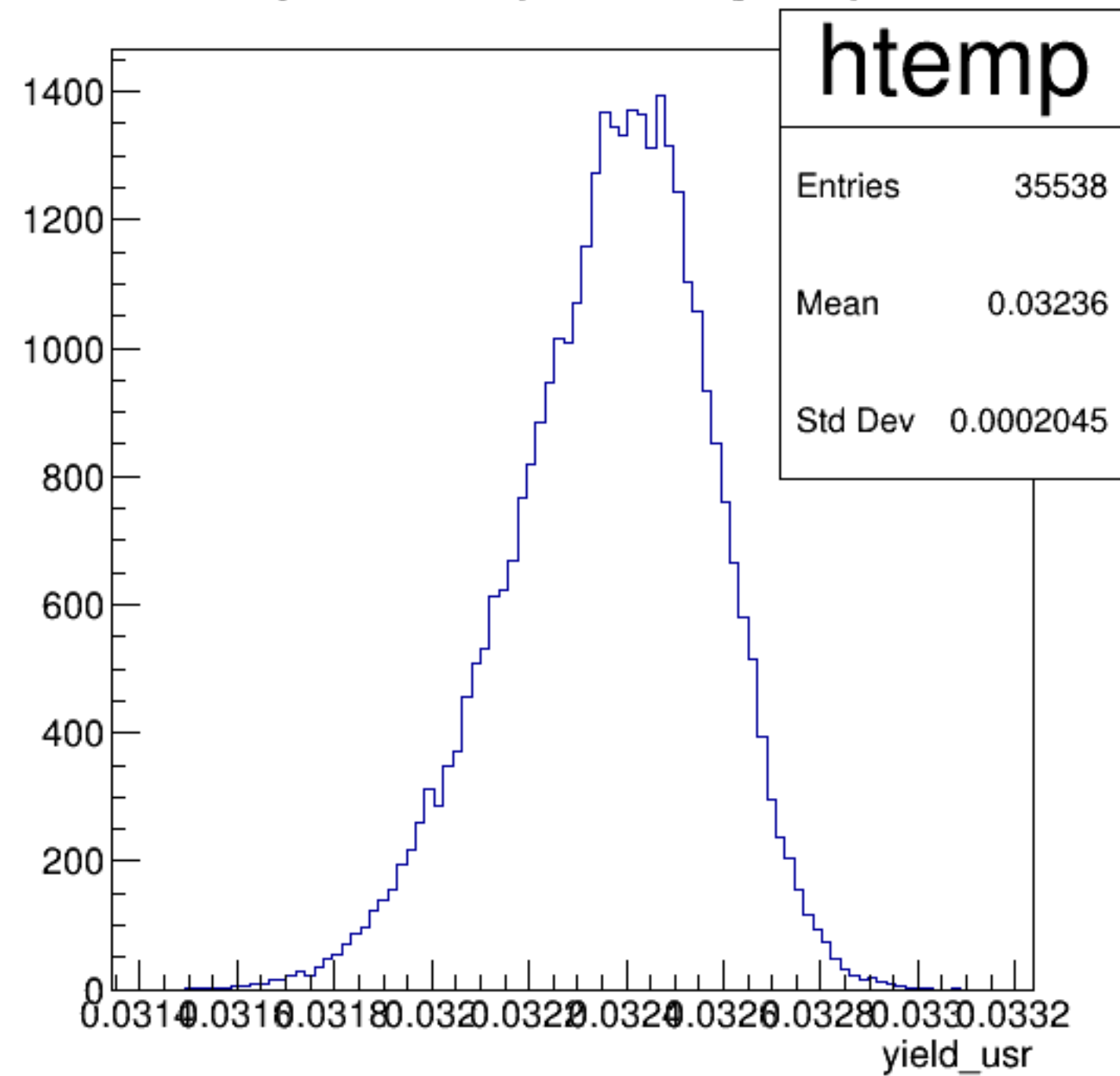


yield\_usr:pattern\_number {ErrorFlag==0}

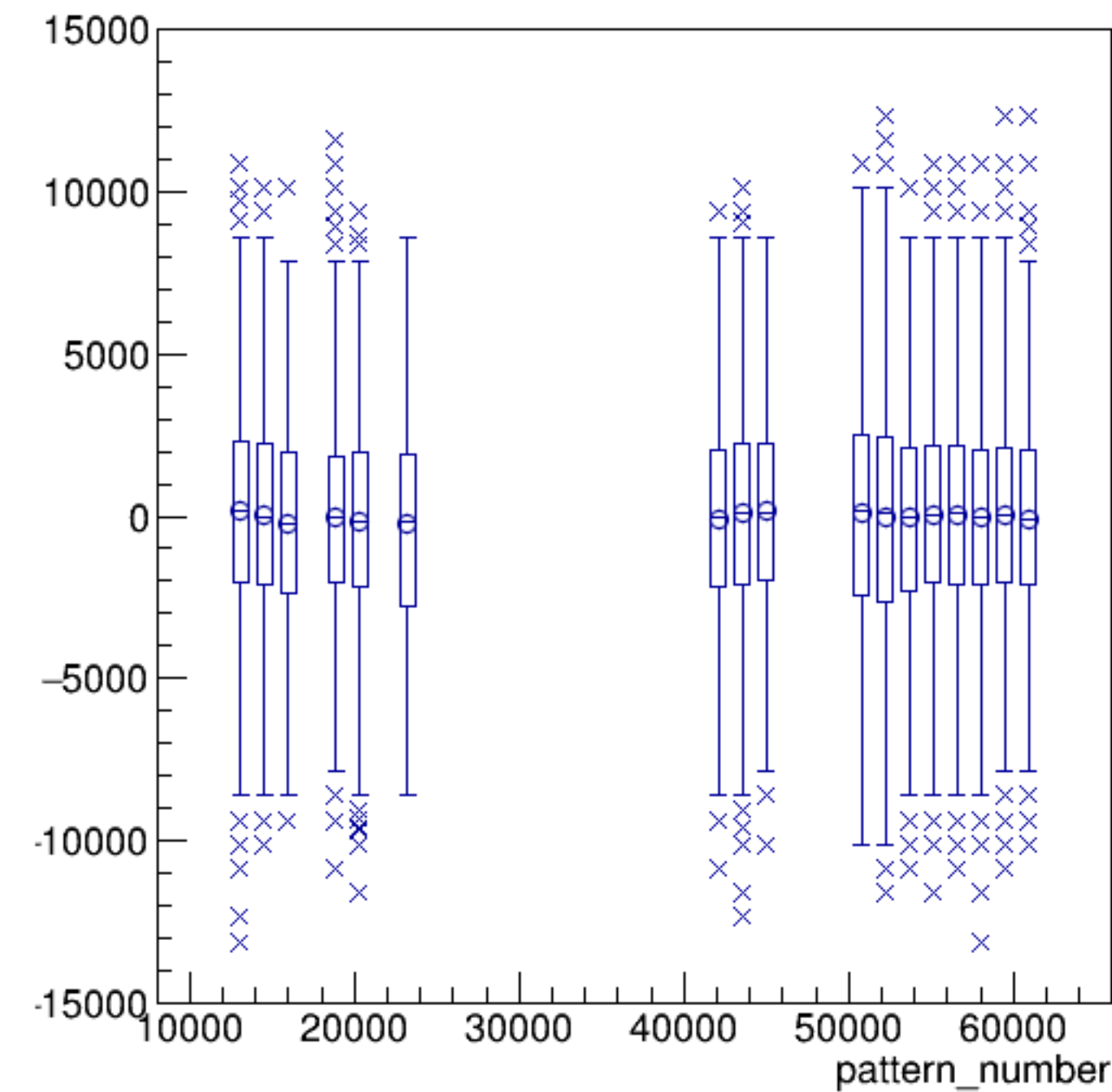


run5773\_000\_pairTree\_usr.png

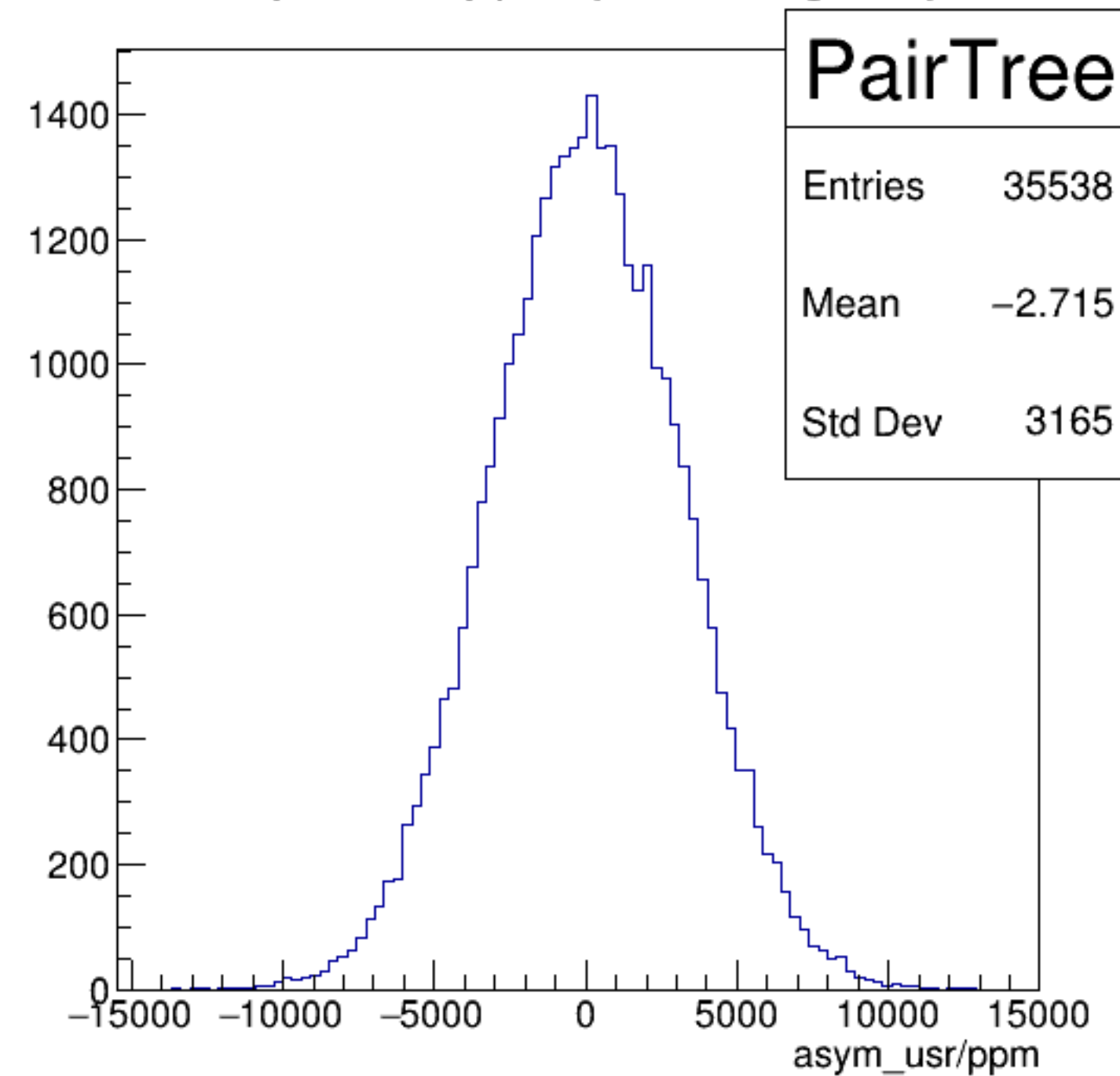
yield\_usr {ErrorFlag==0}



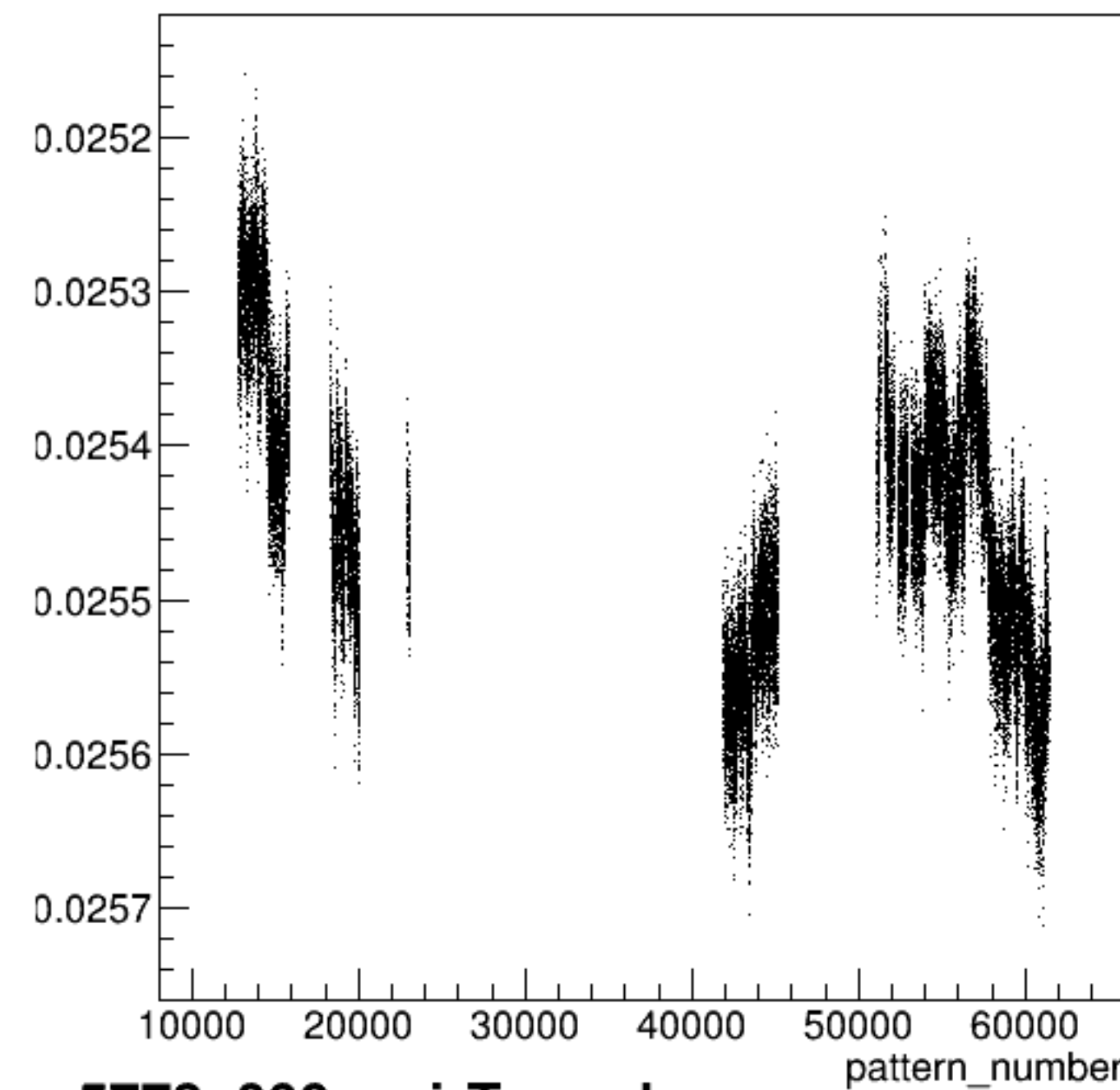
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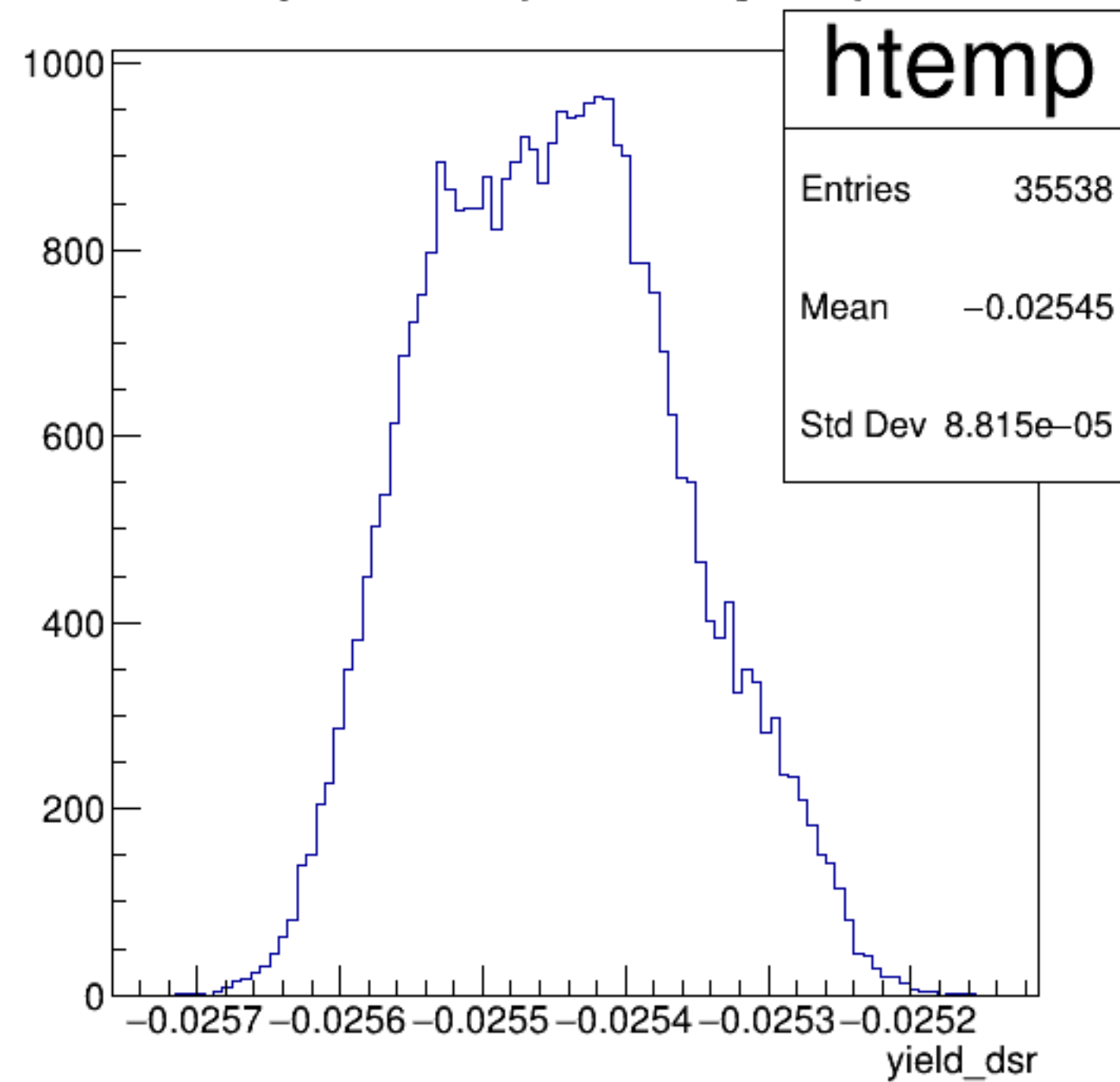


yield\_dsr:pattern\_number {ErrorFlag==0}

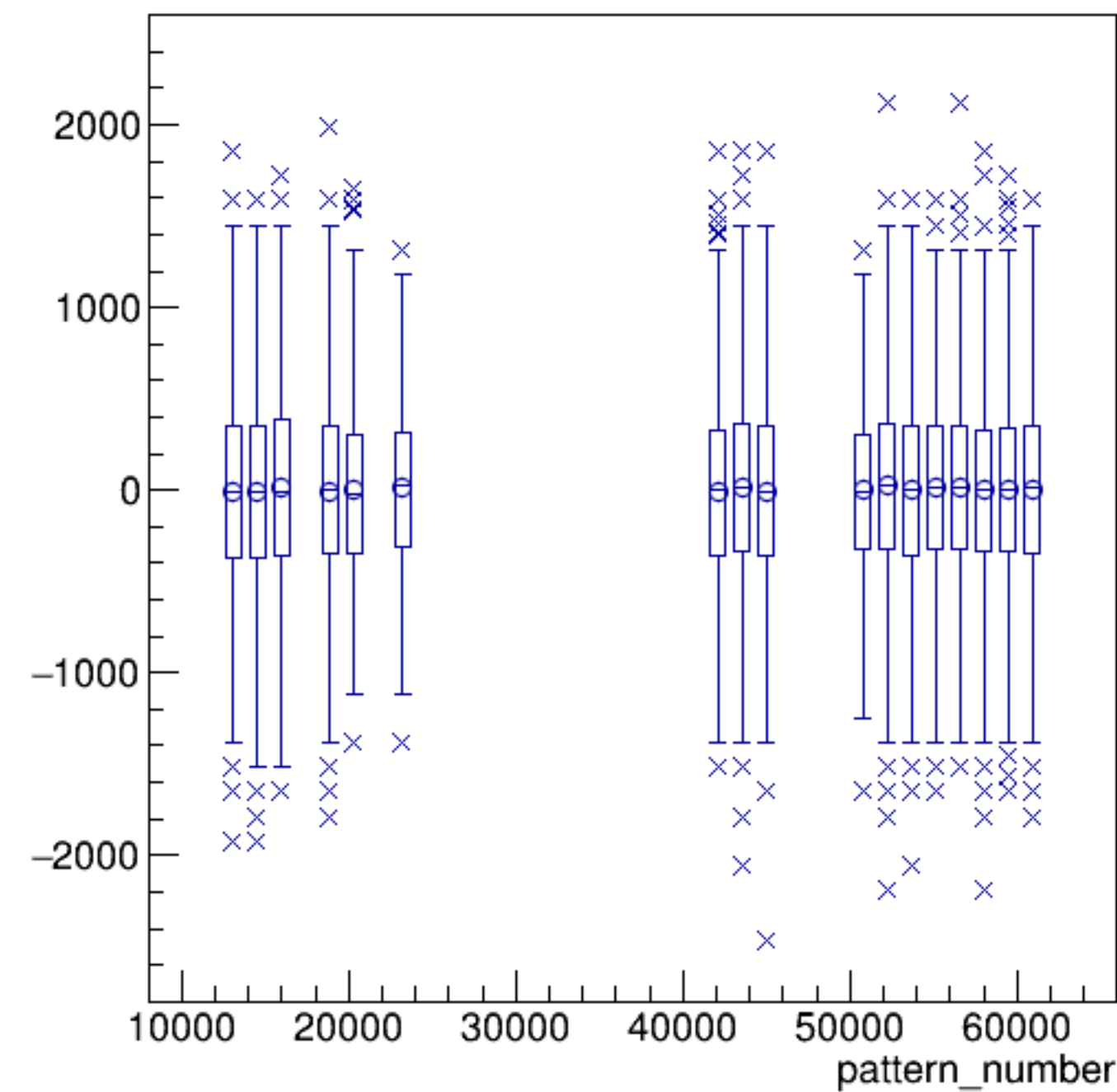


run5773\_000\_pairTree\_dsr.png

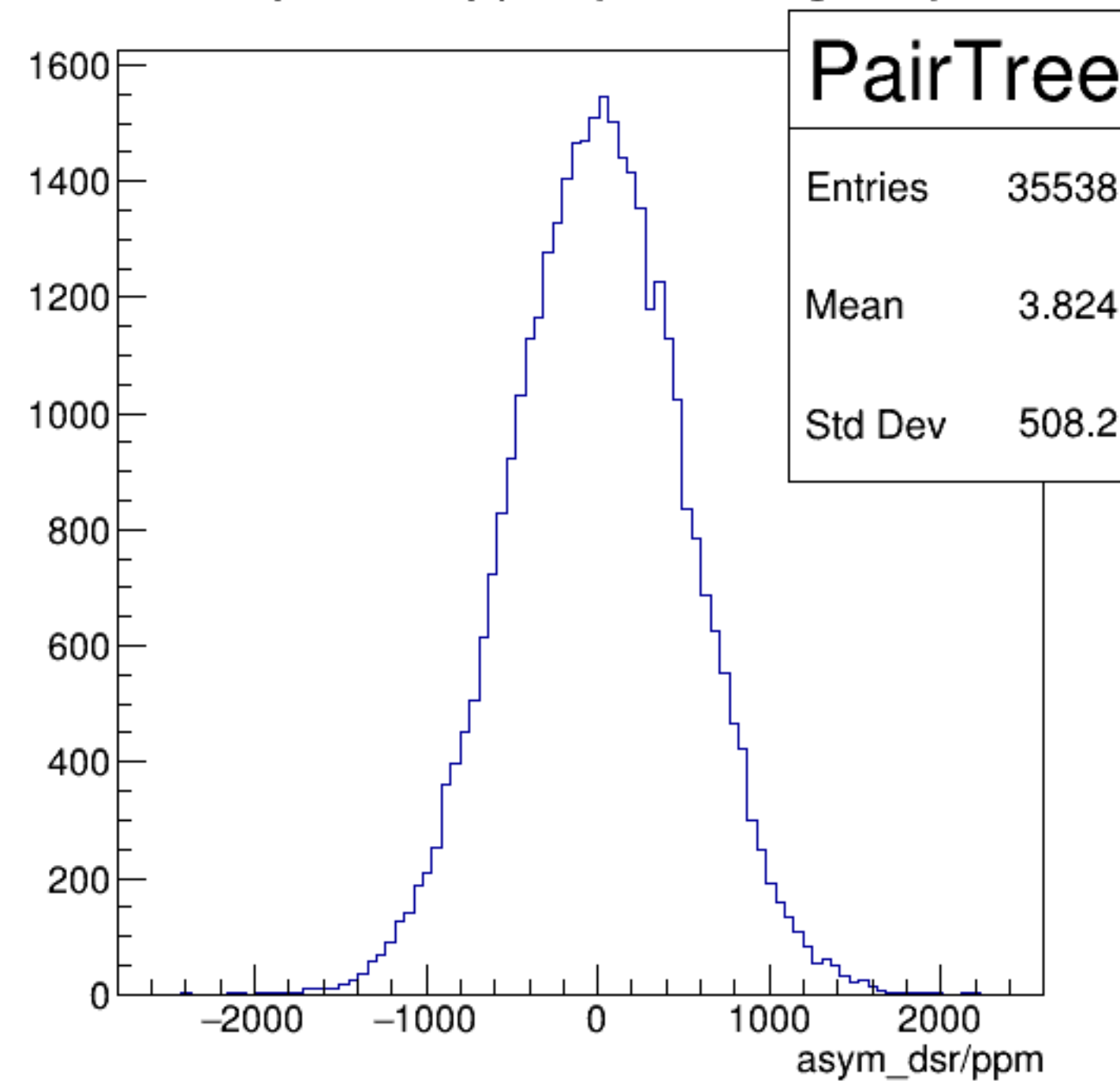
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asym\_dsr/ppm:pattern\_number {ErrorFlag==0}

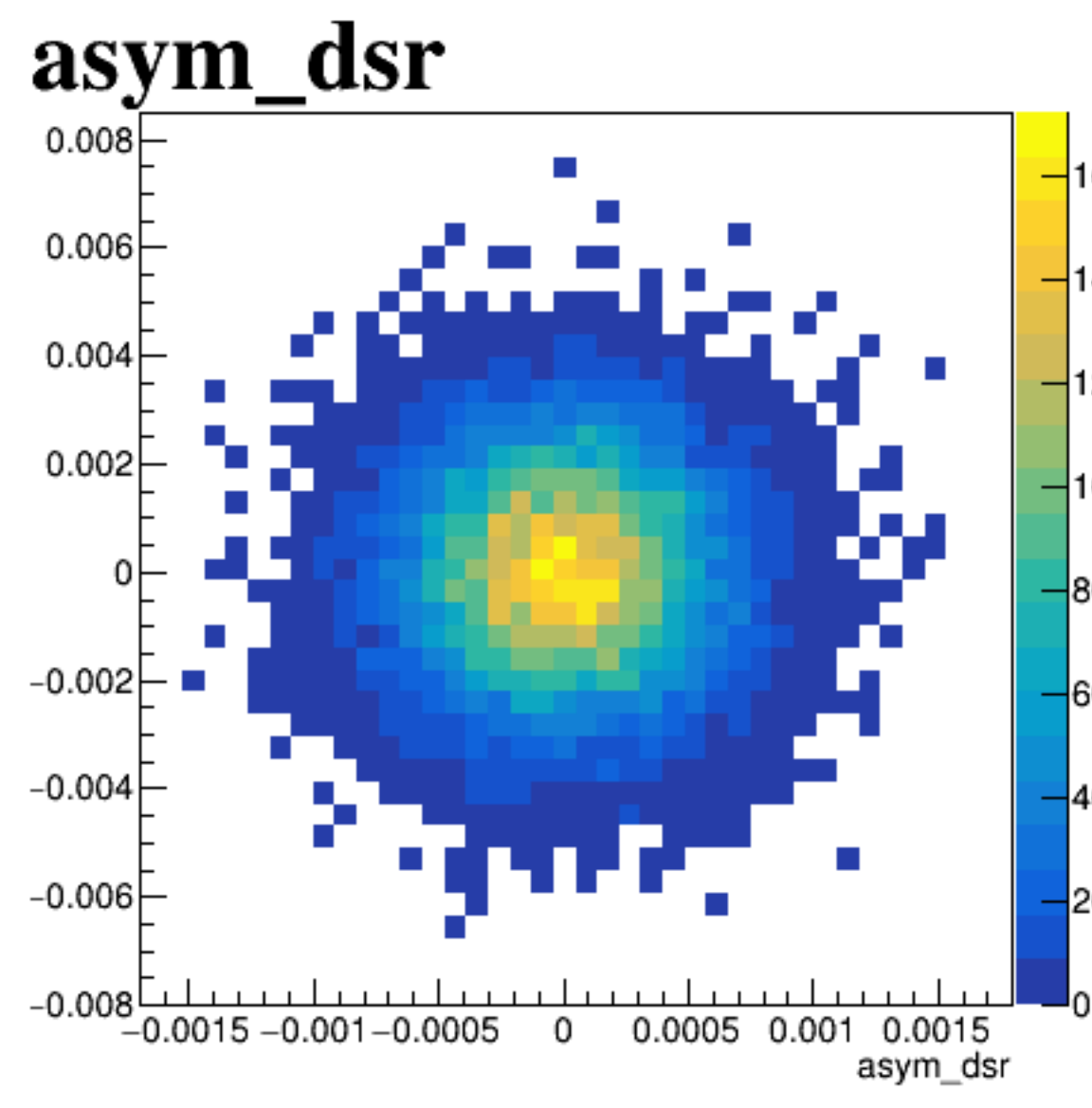
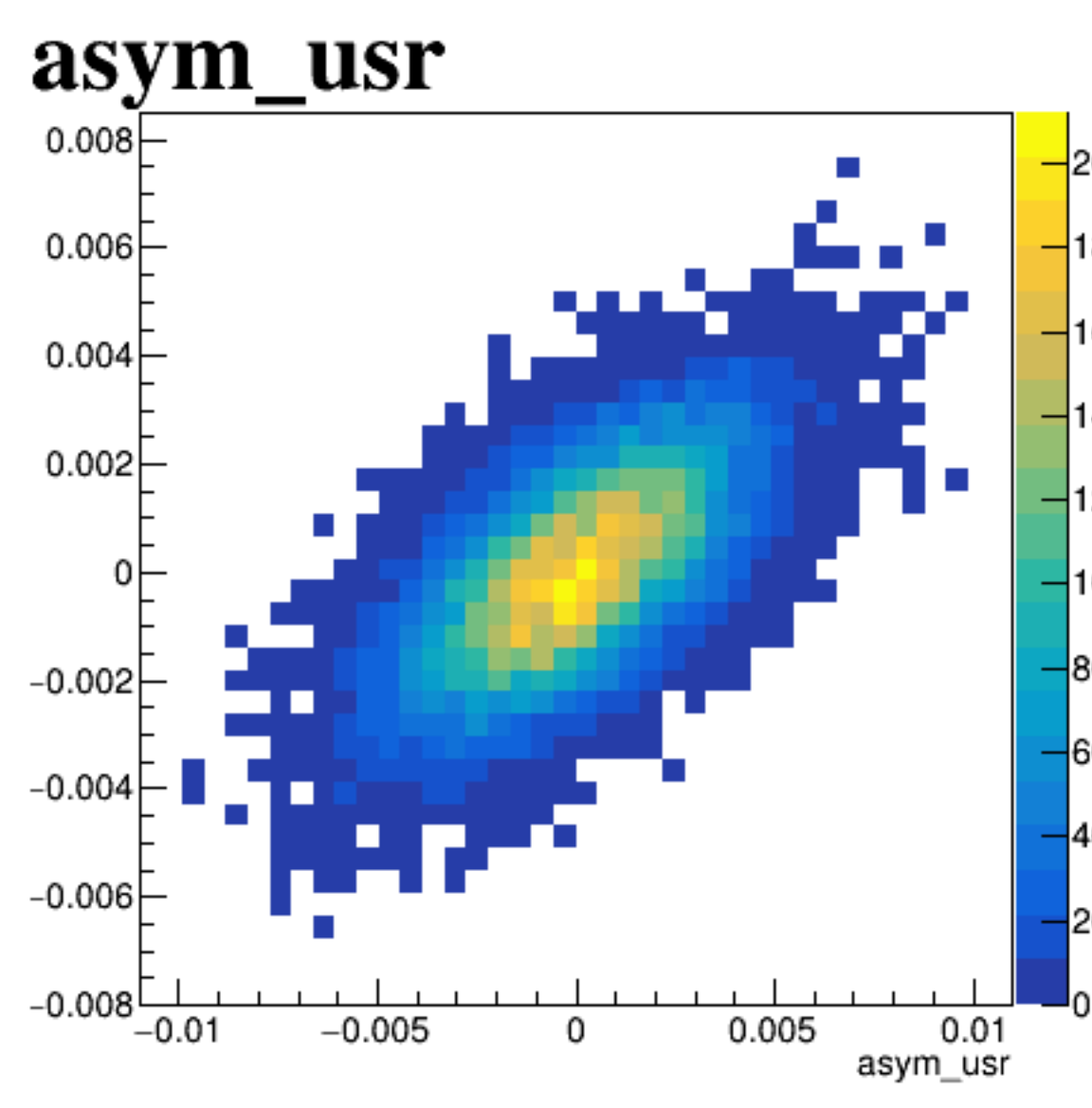
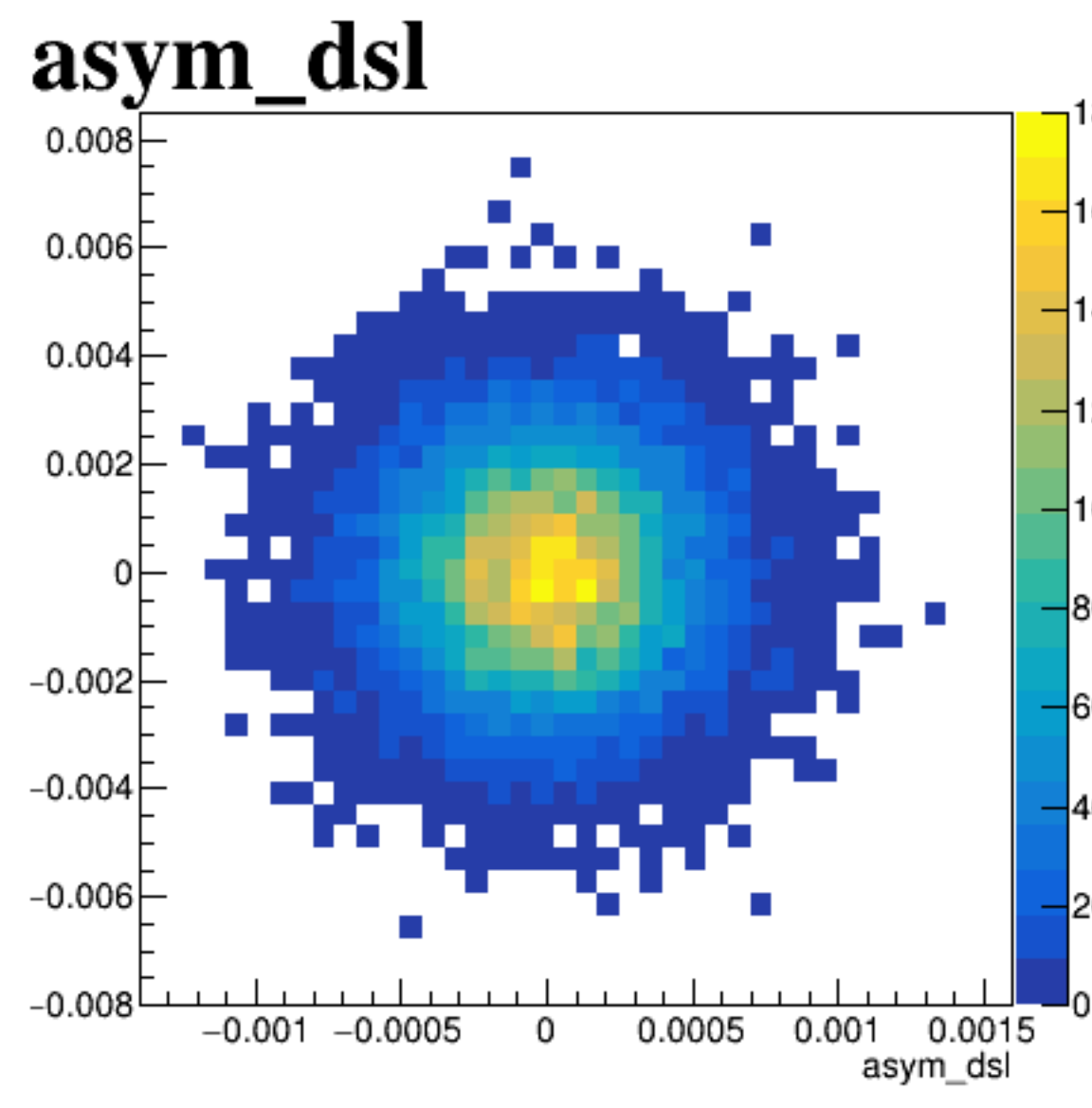
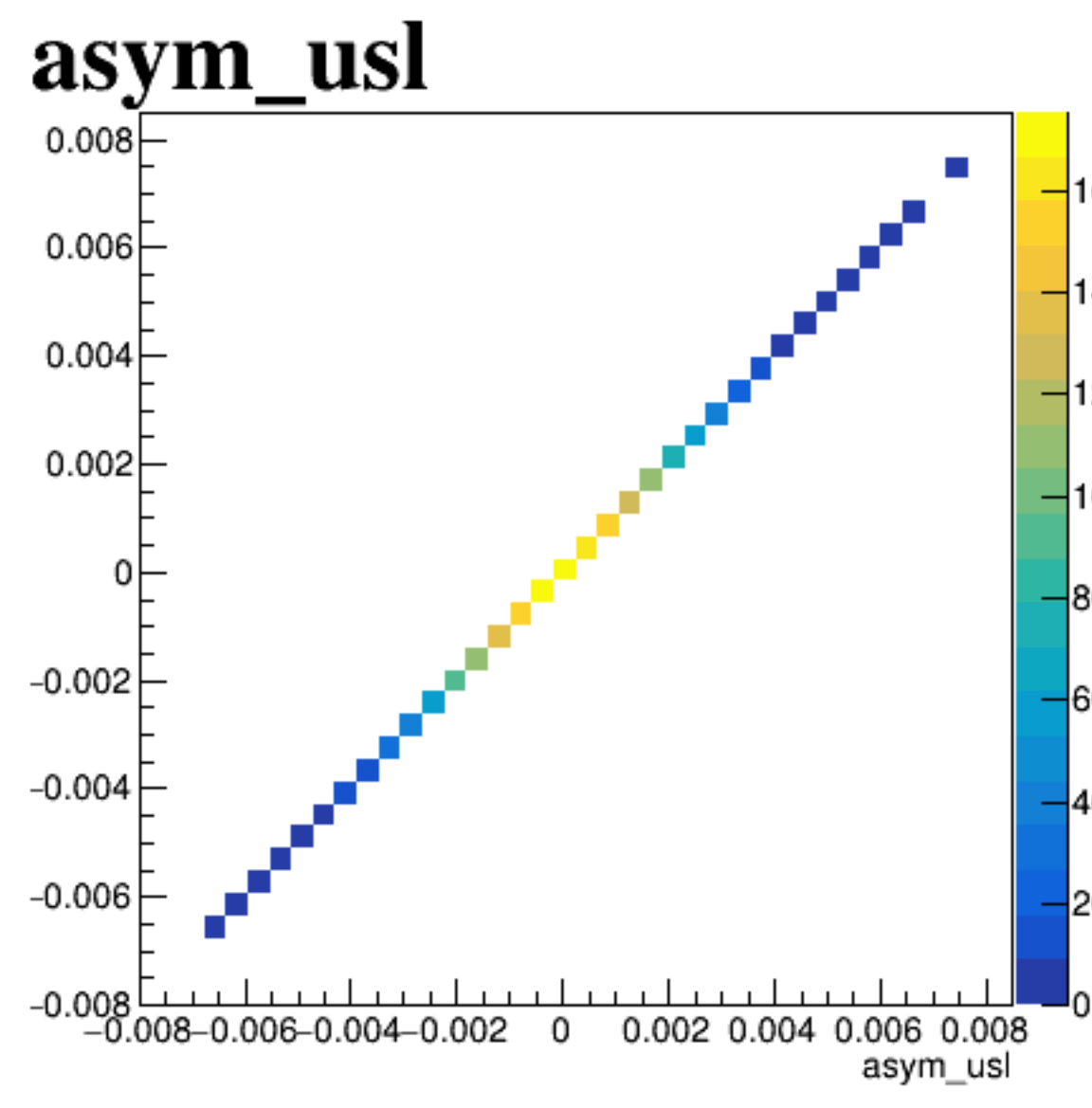


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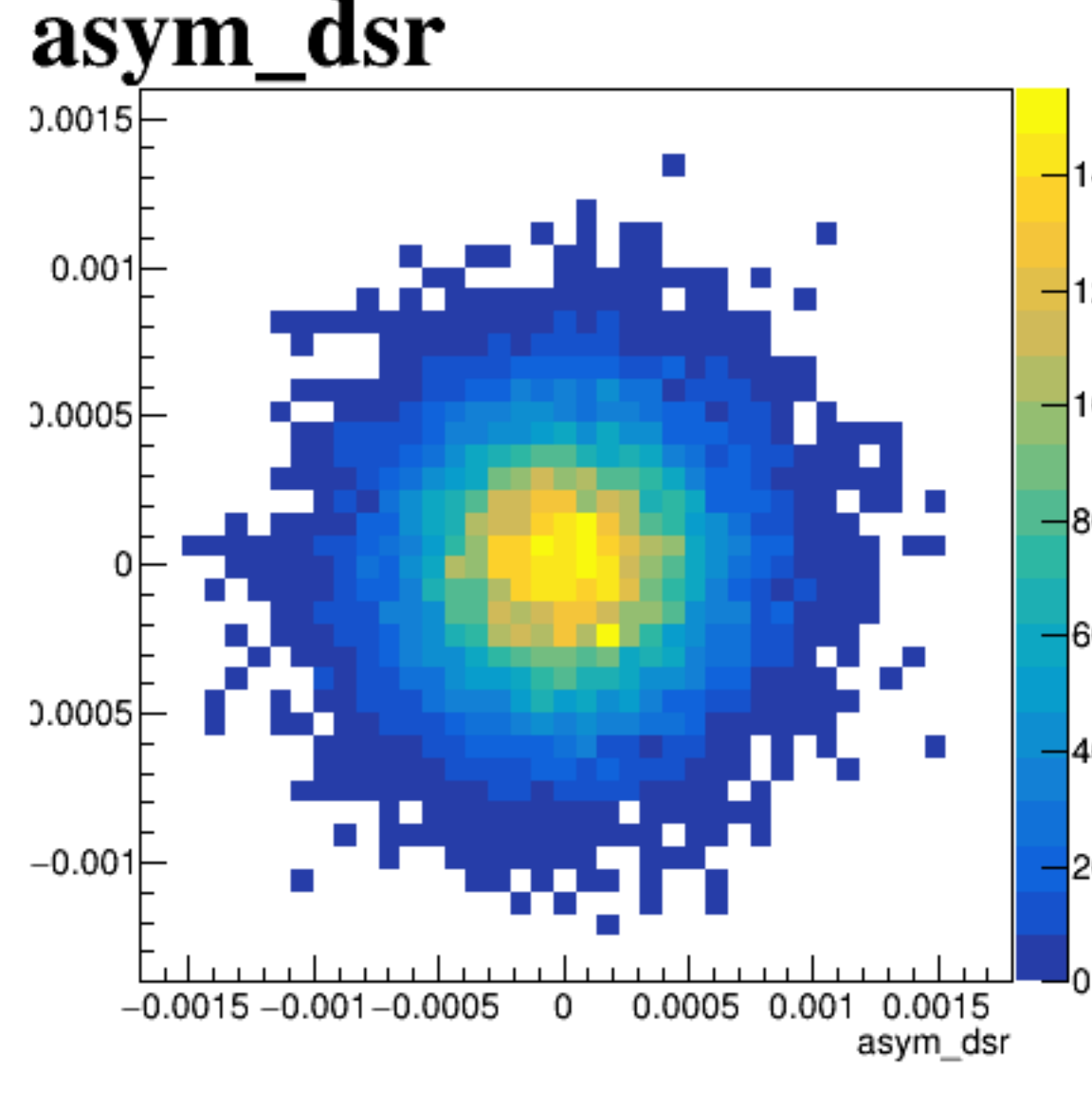
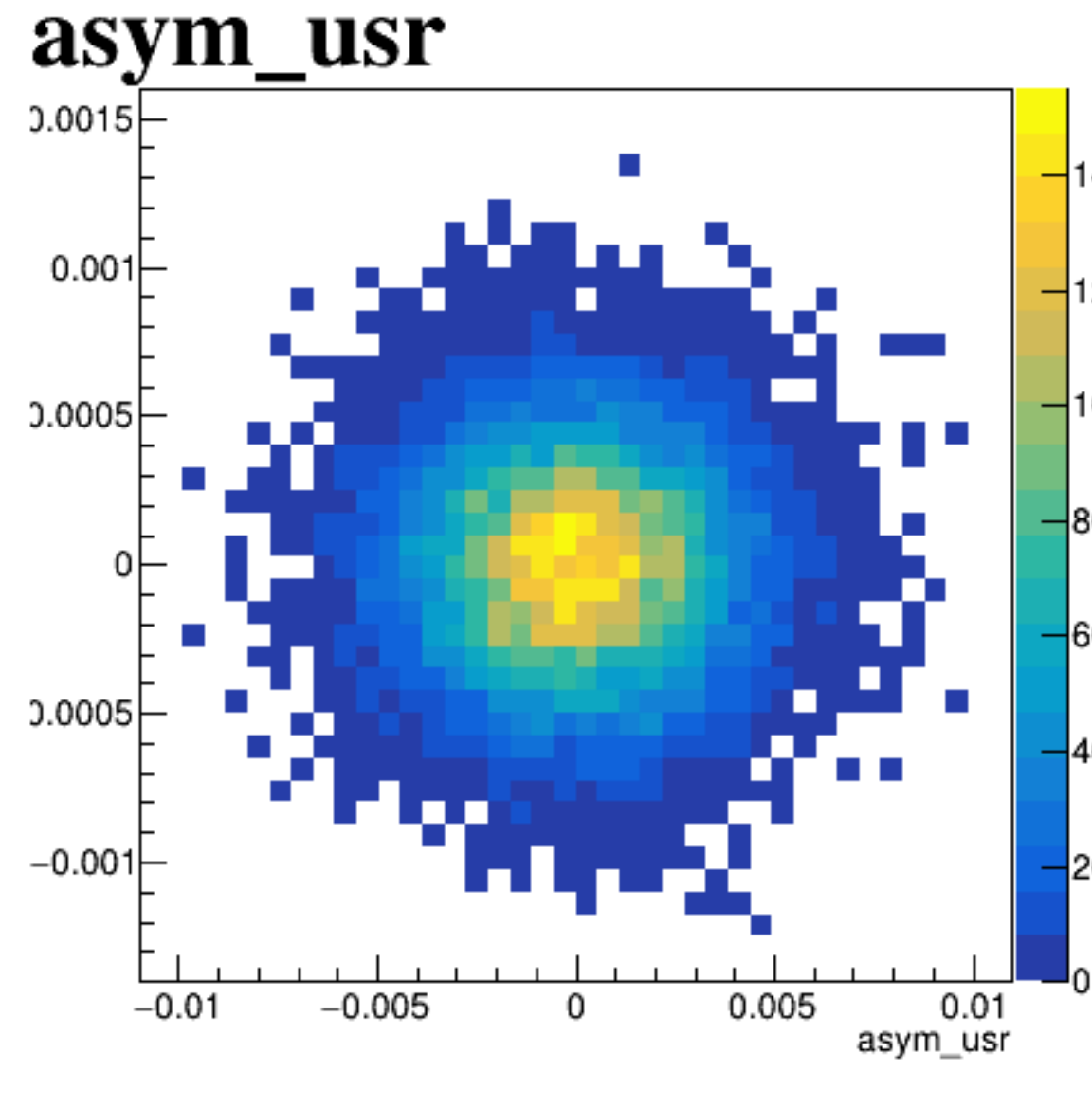
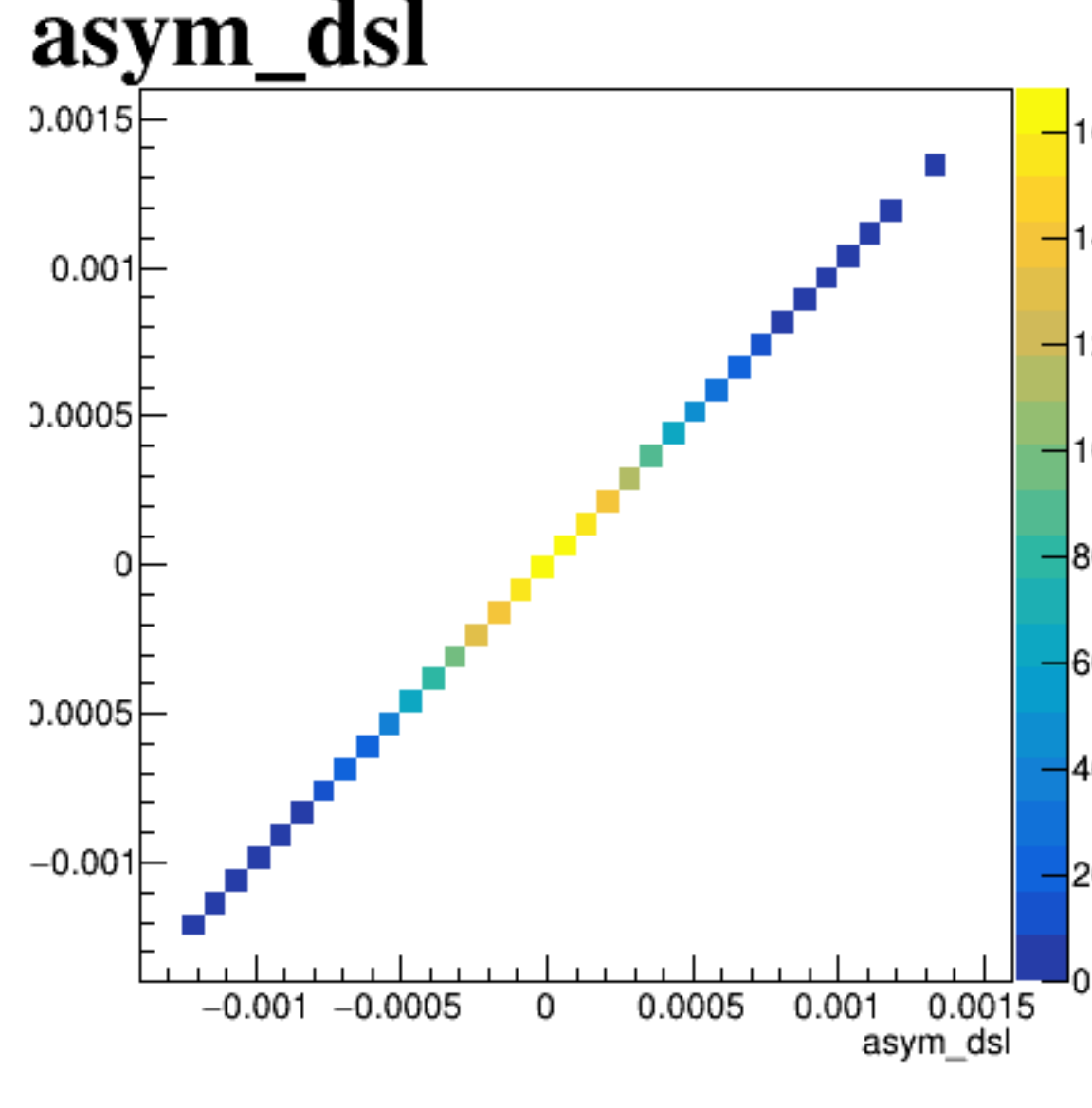
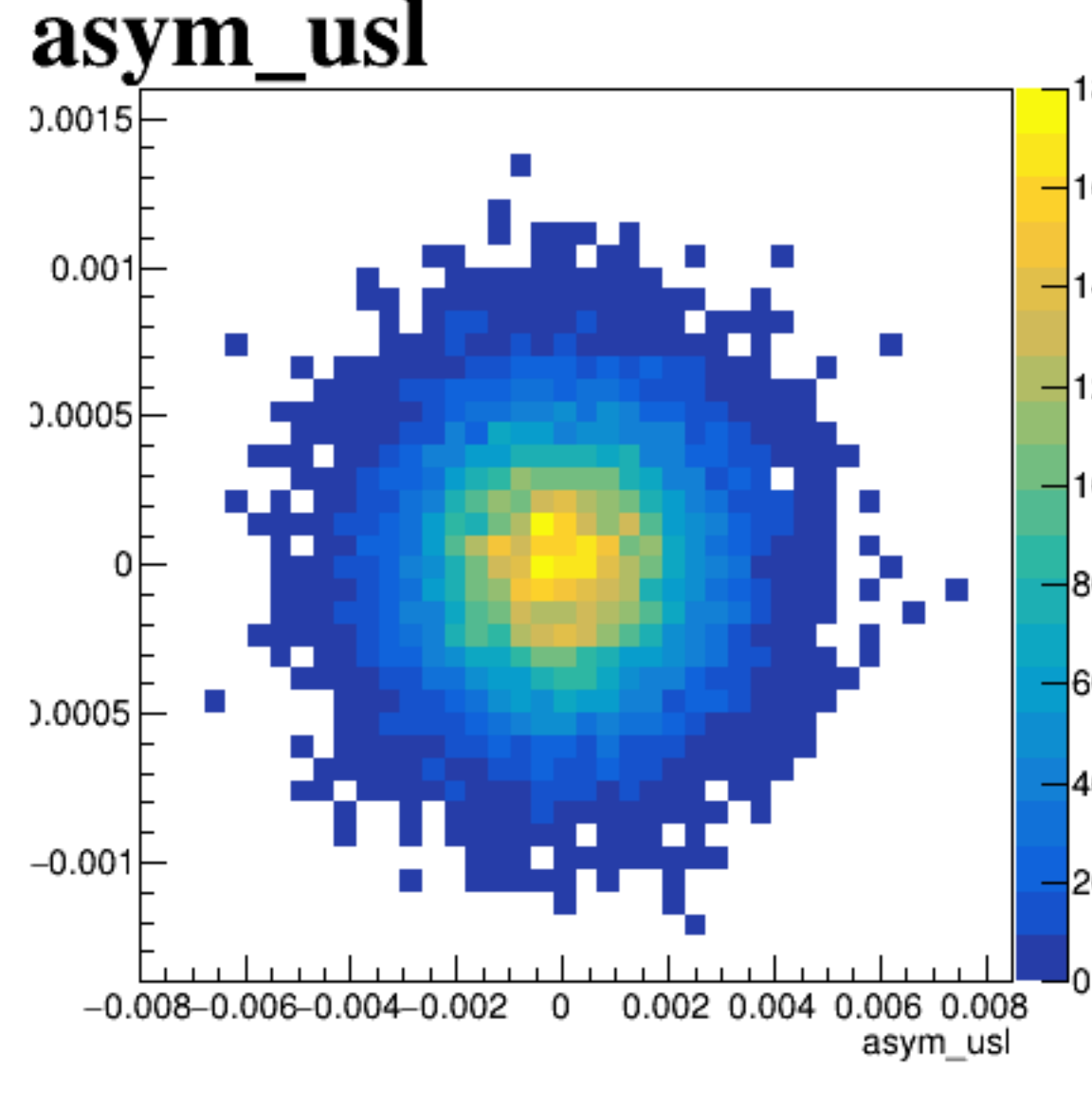




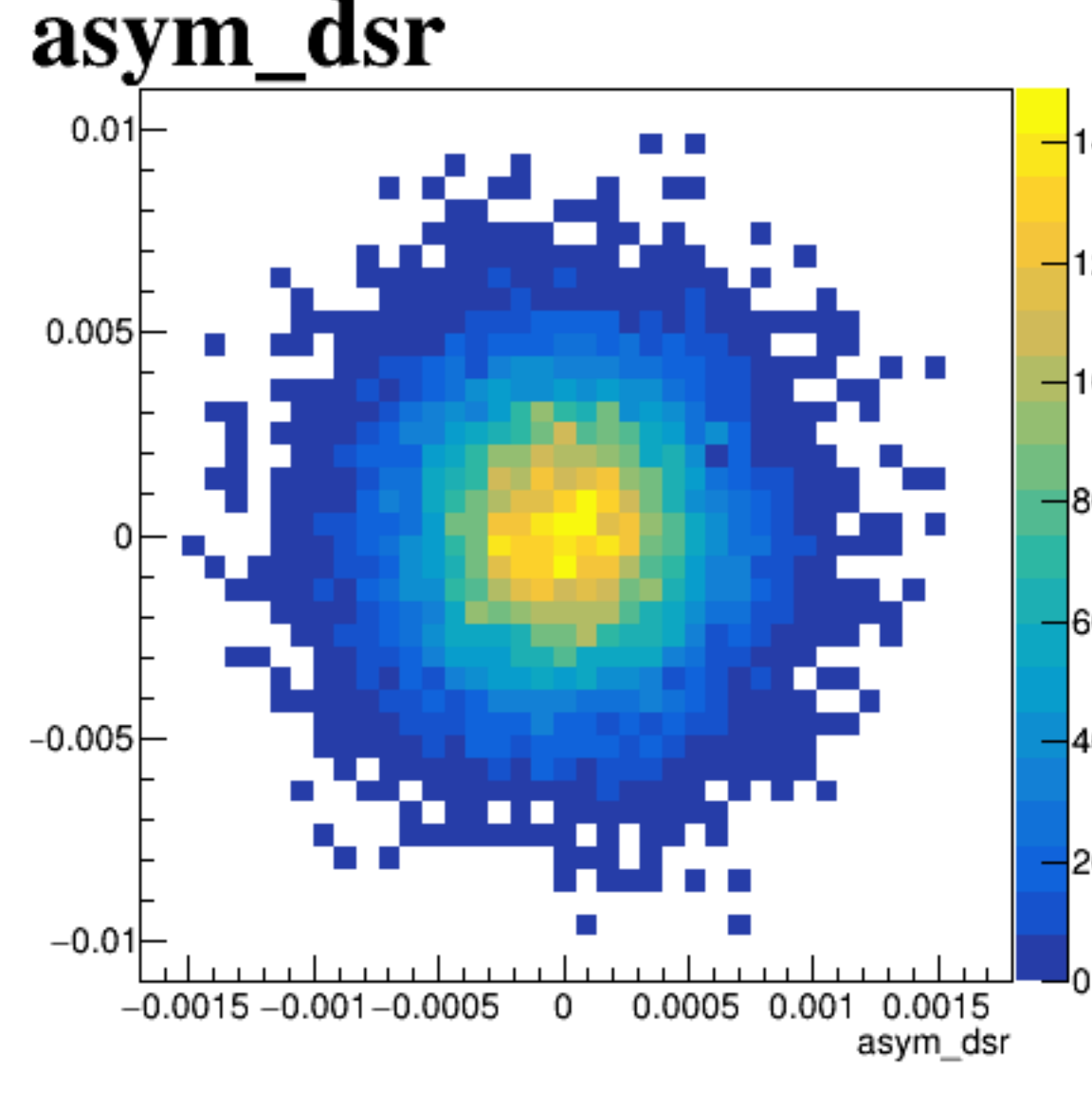
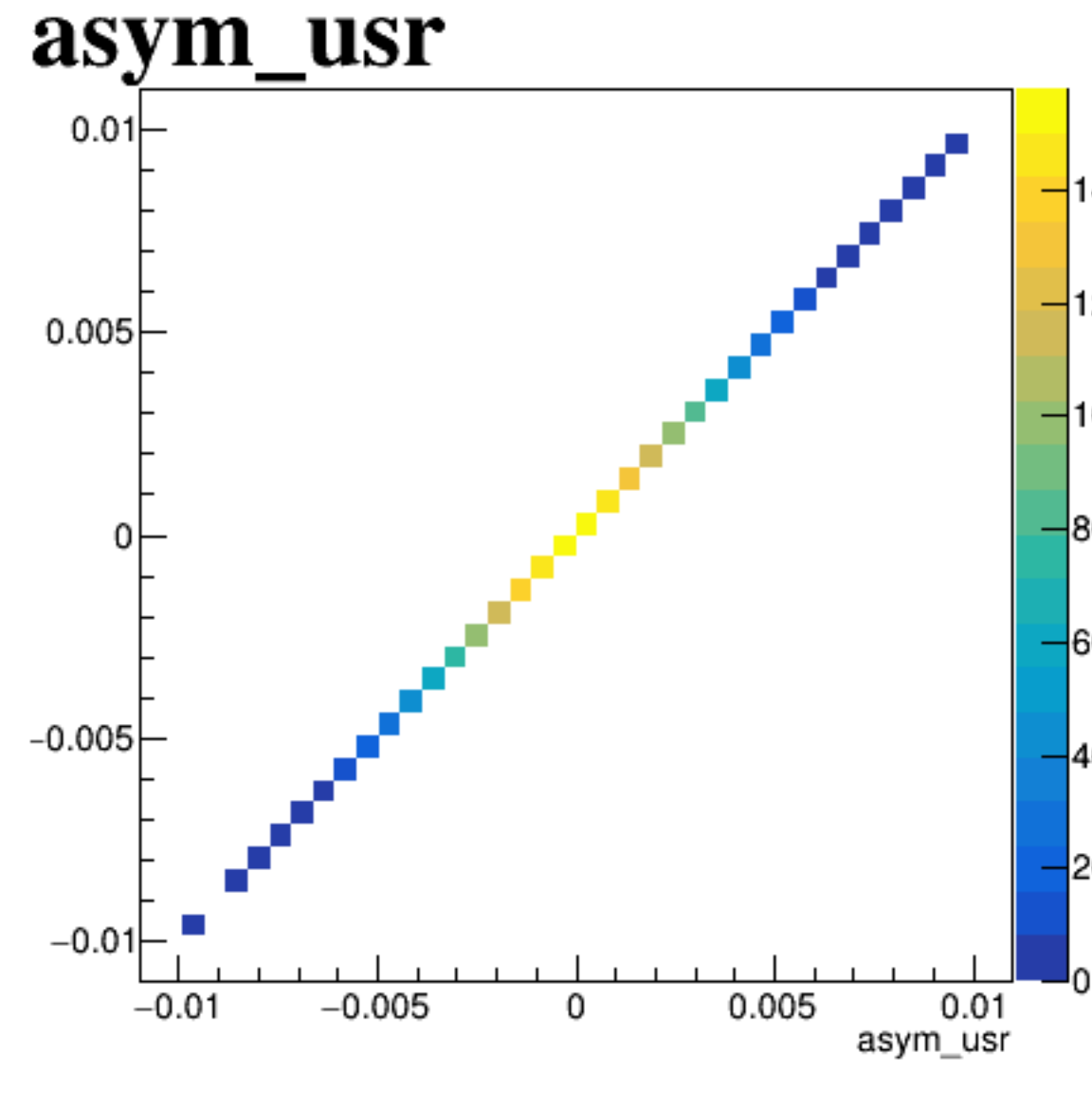
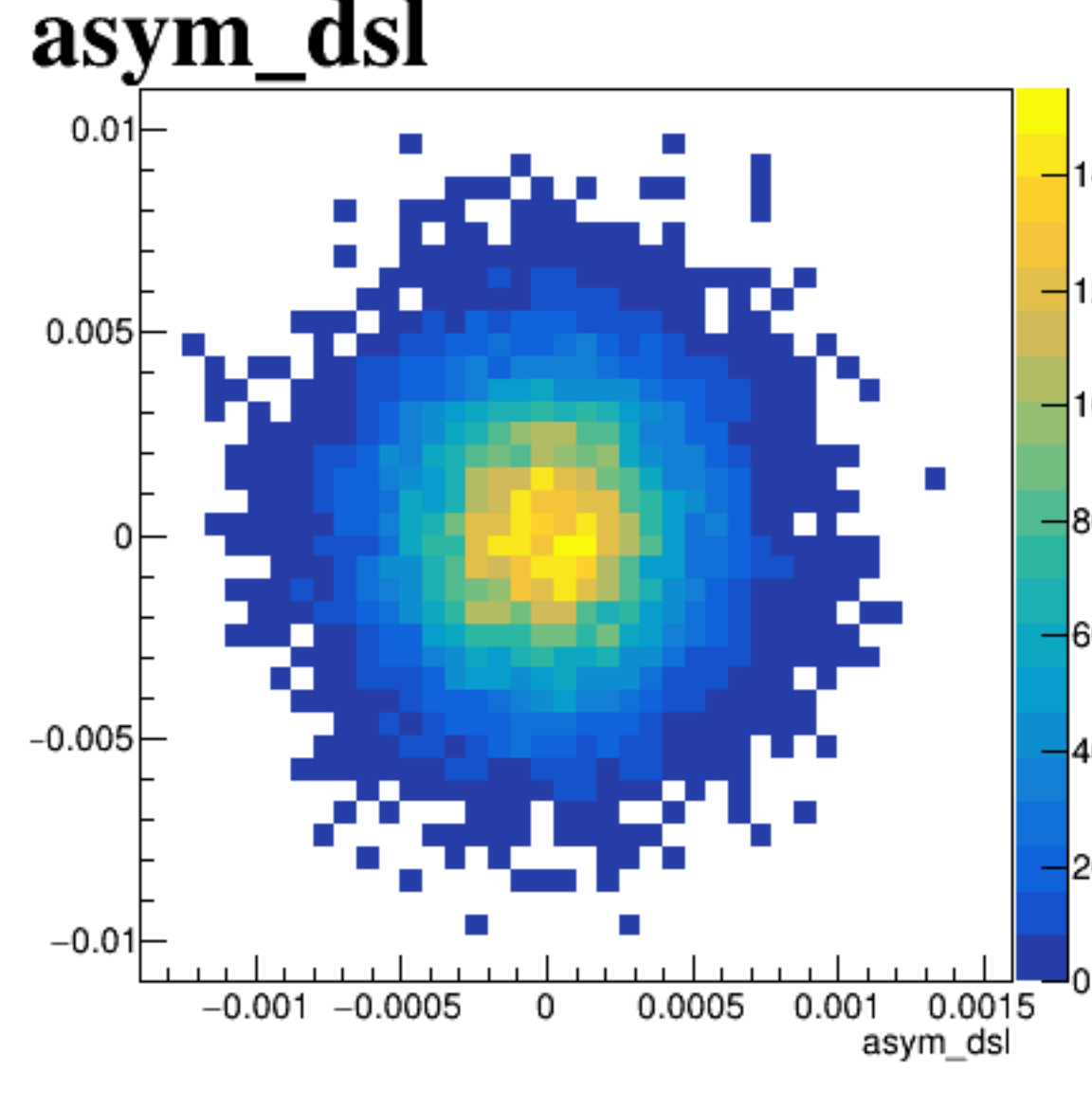
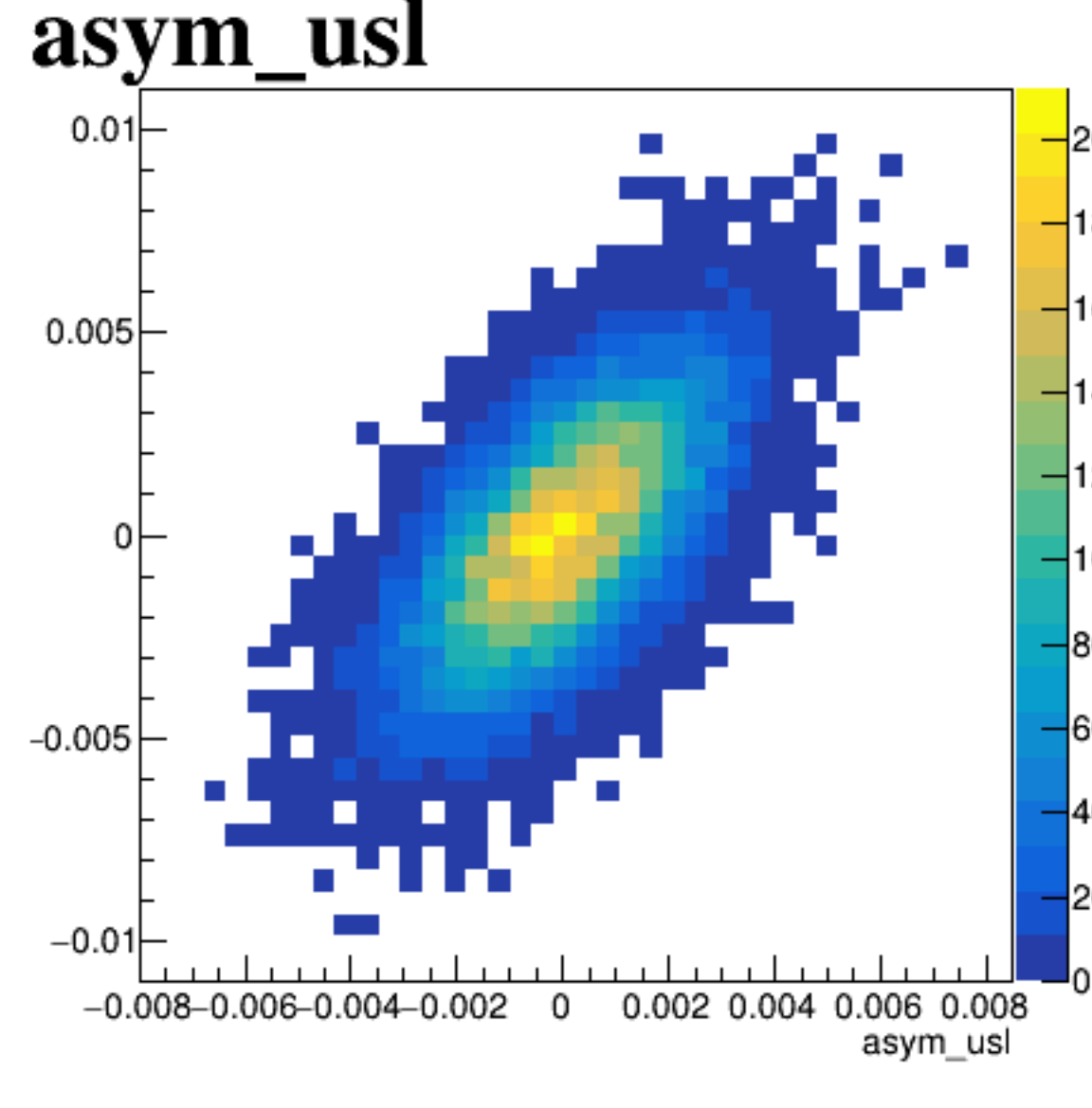
**asym\_usl**



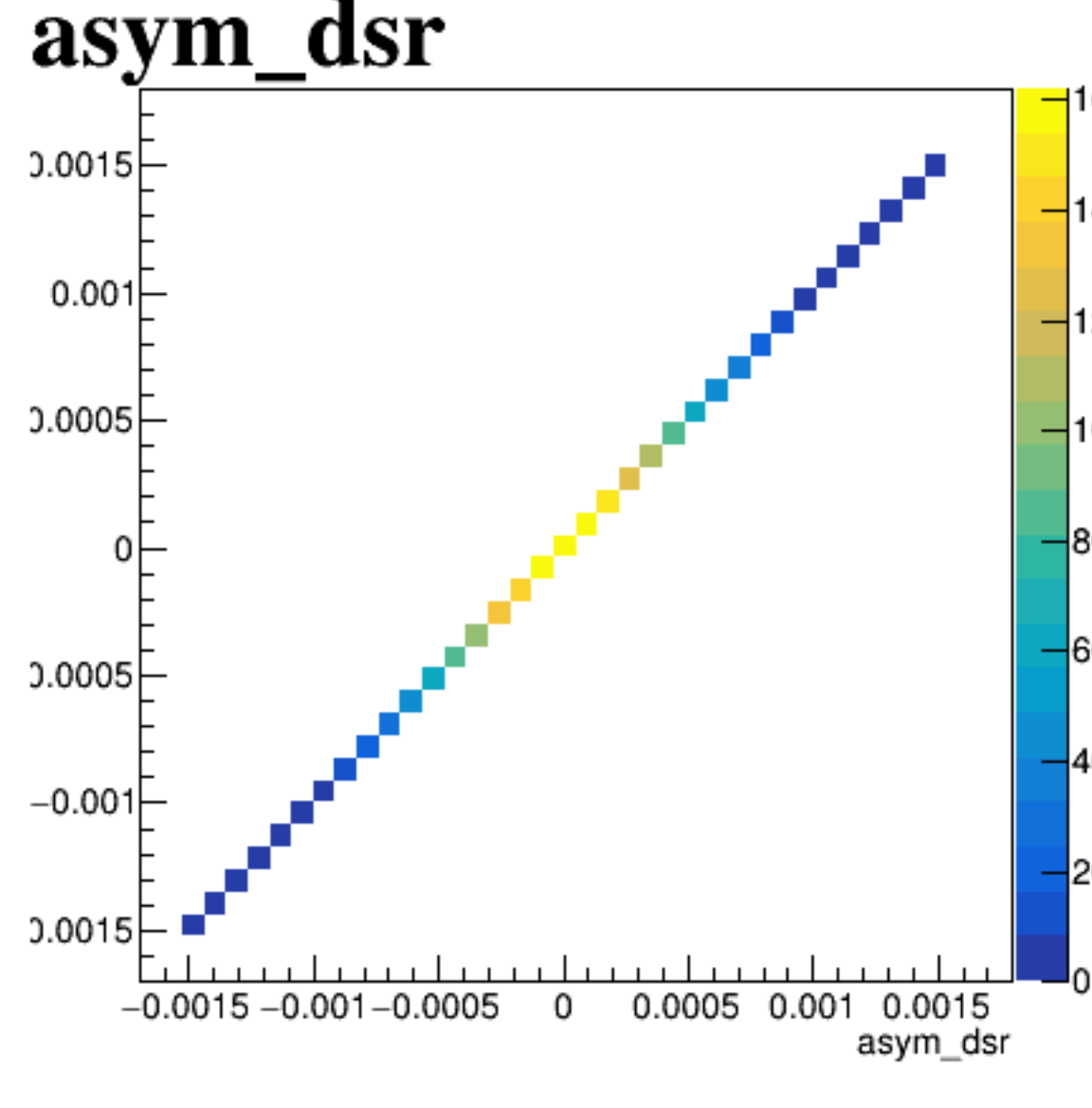
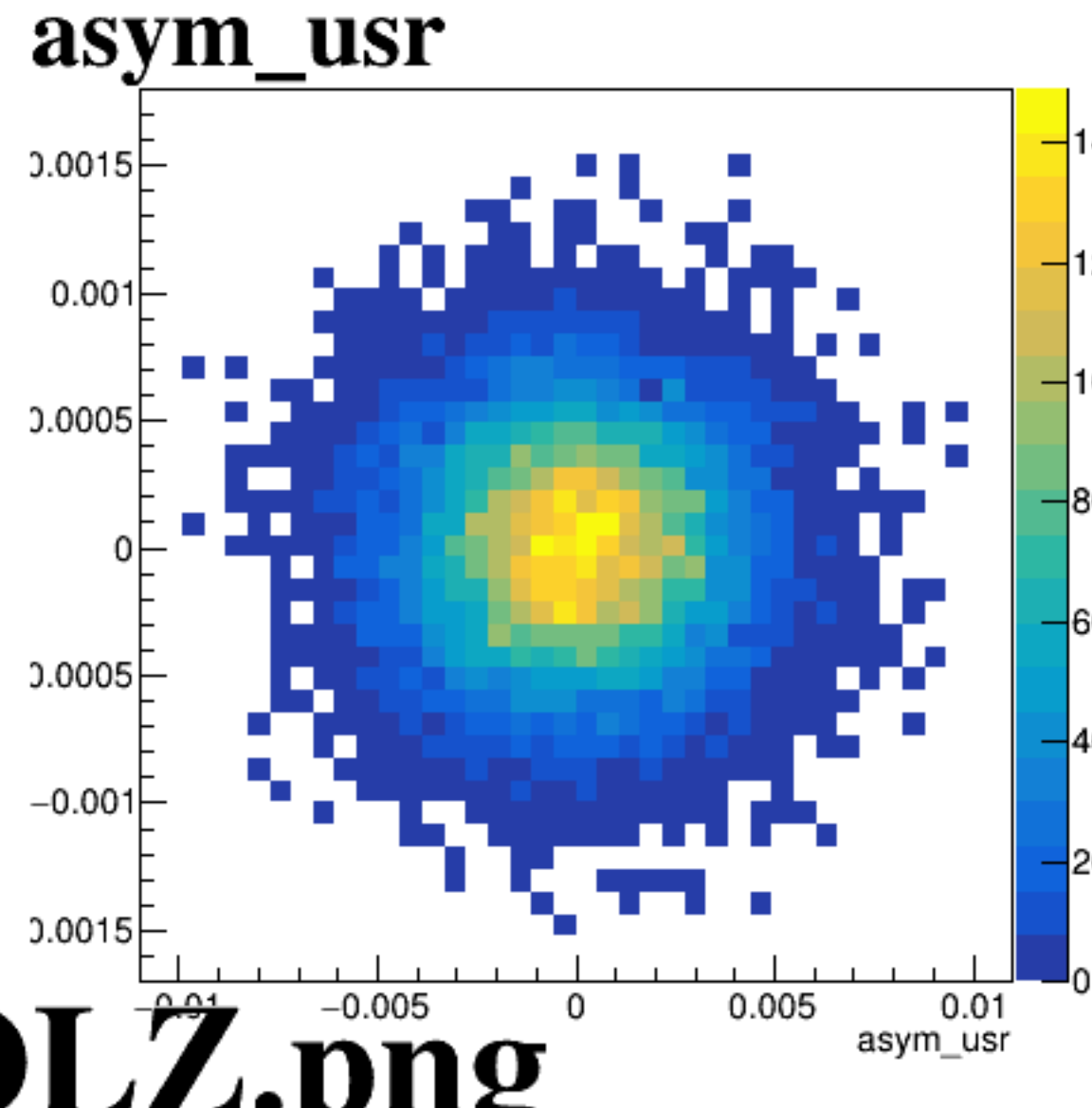
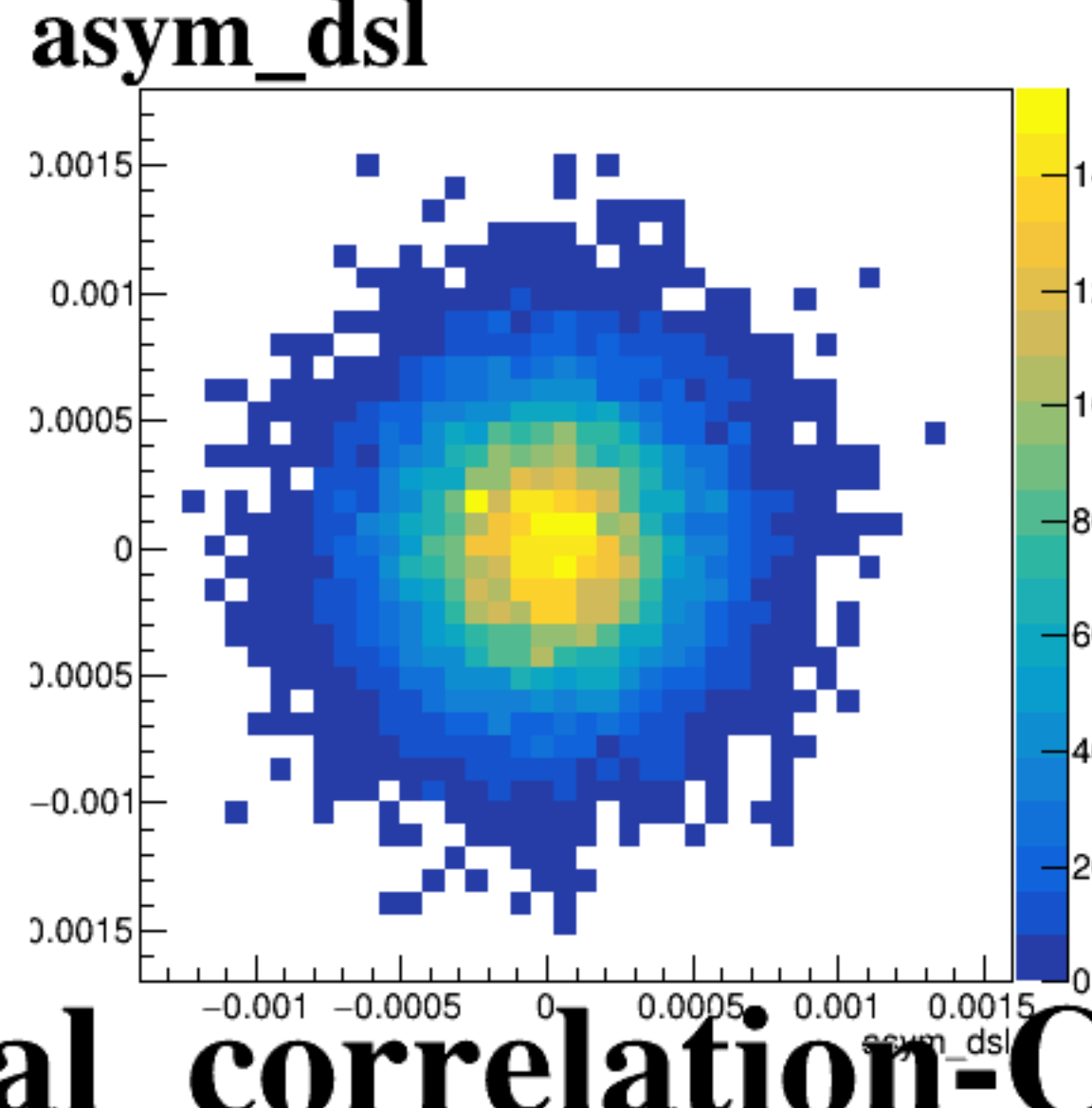
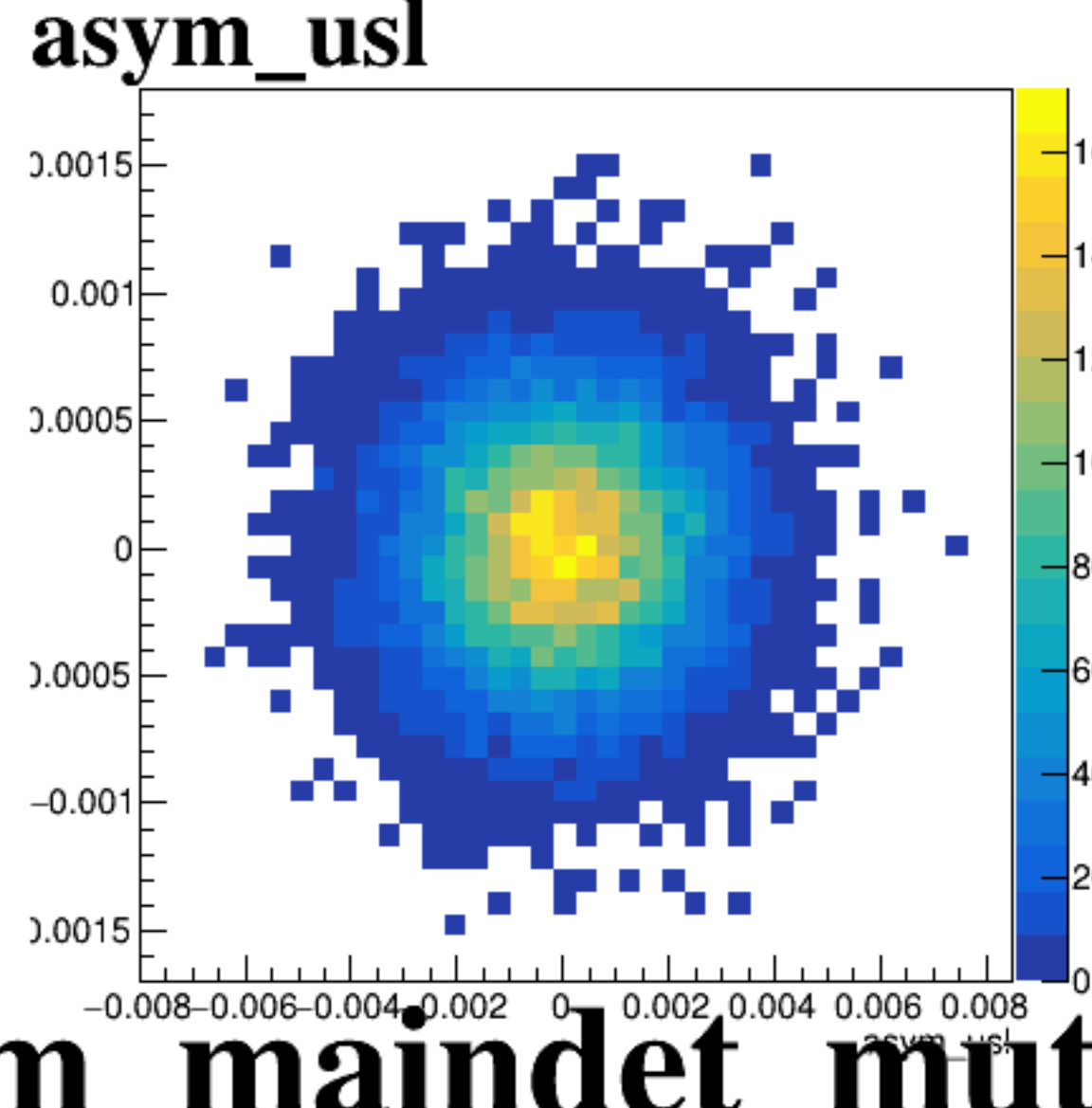
**asym\_dsl**



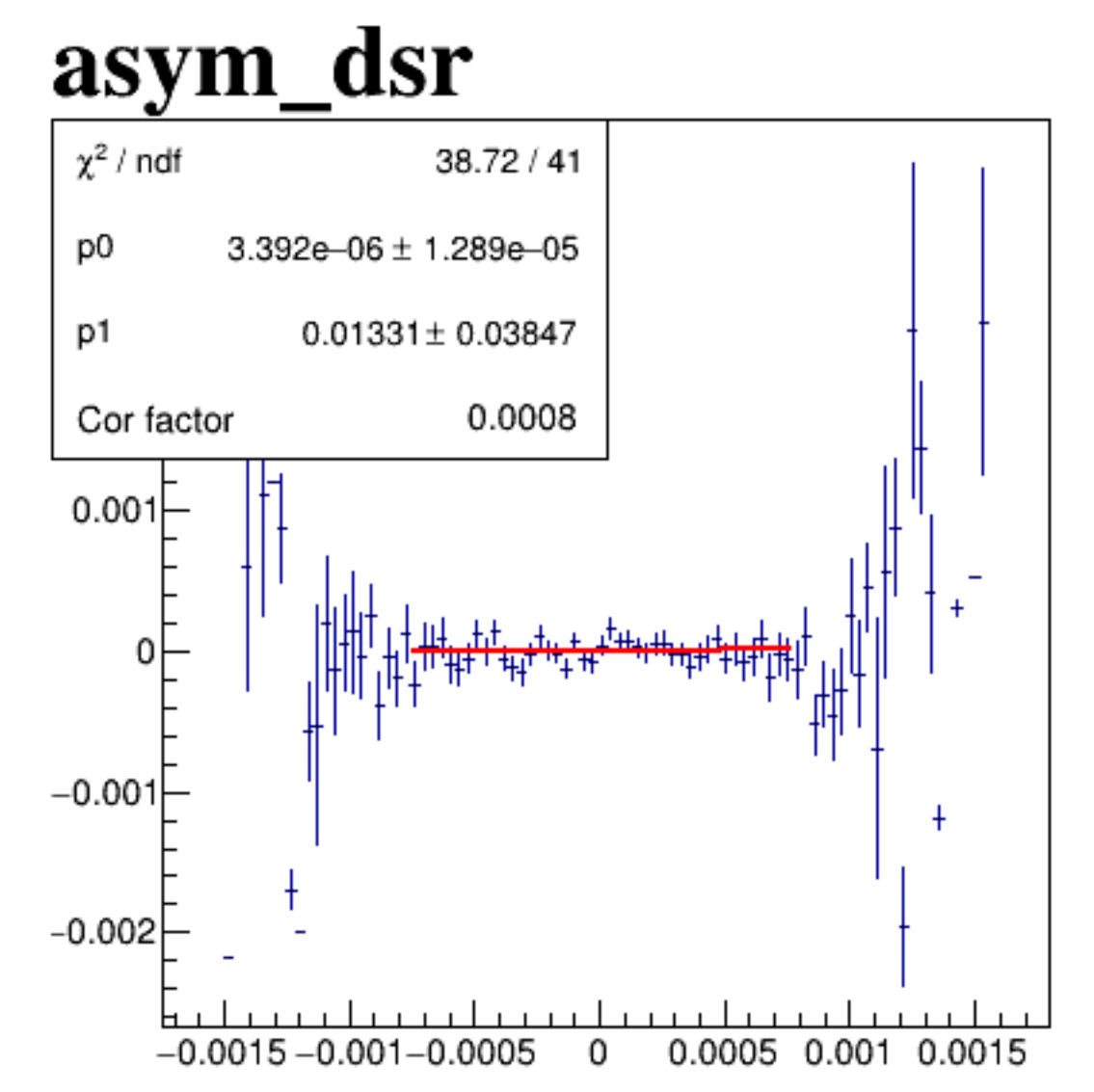
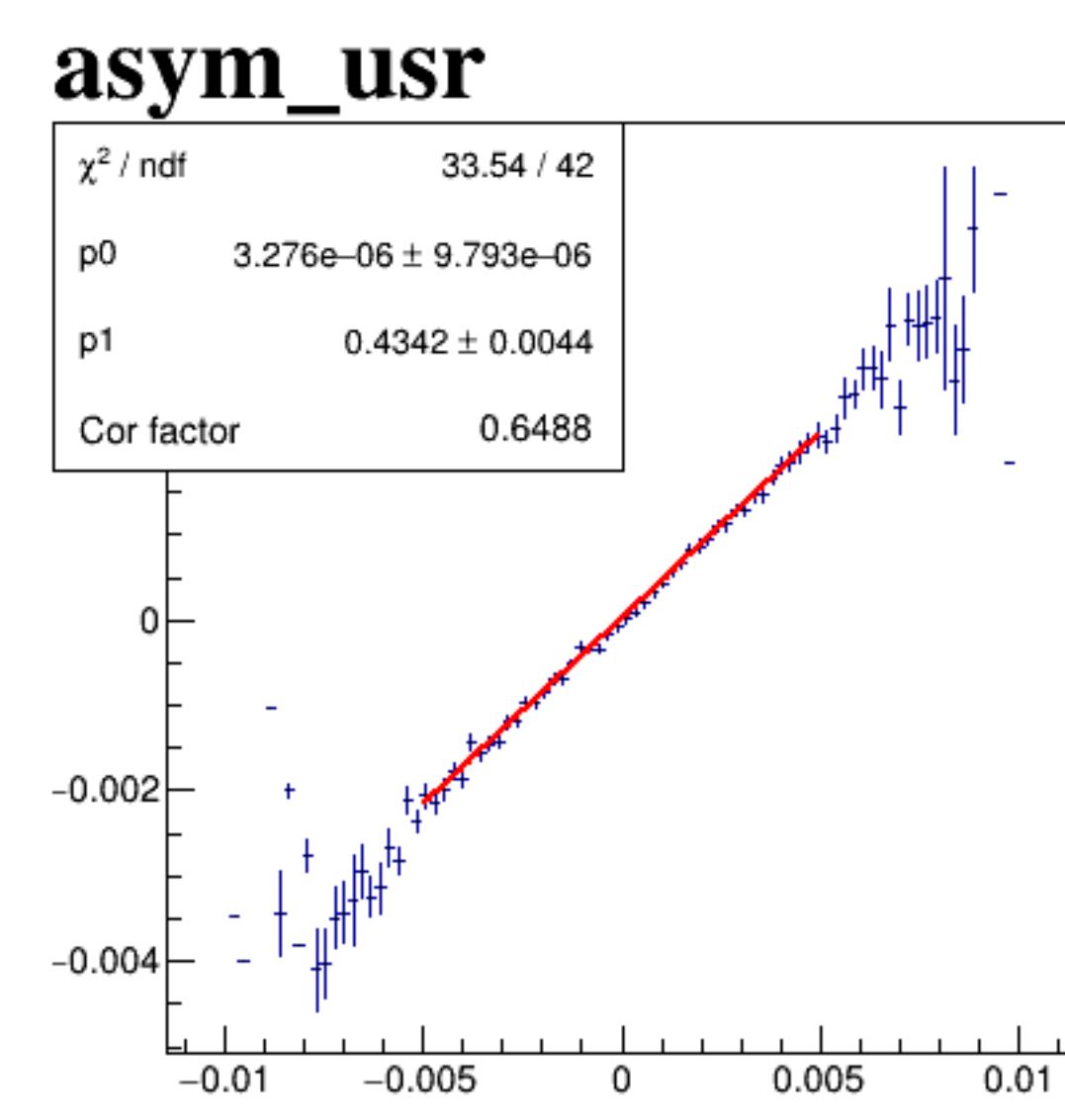
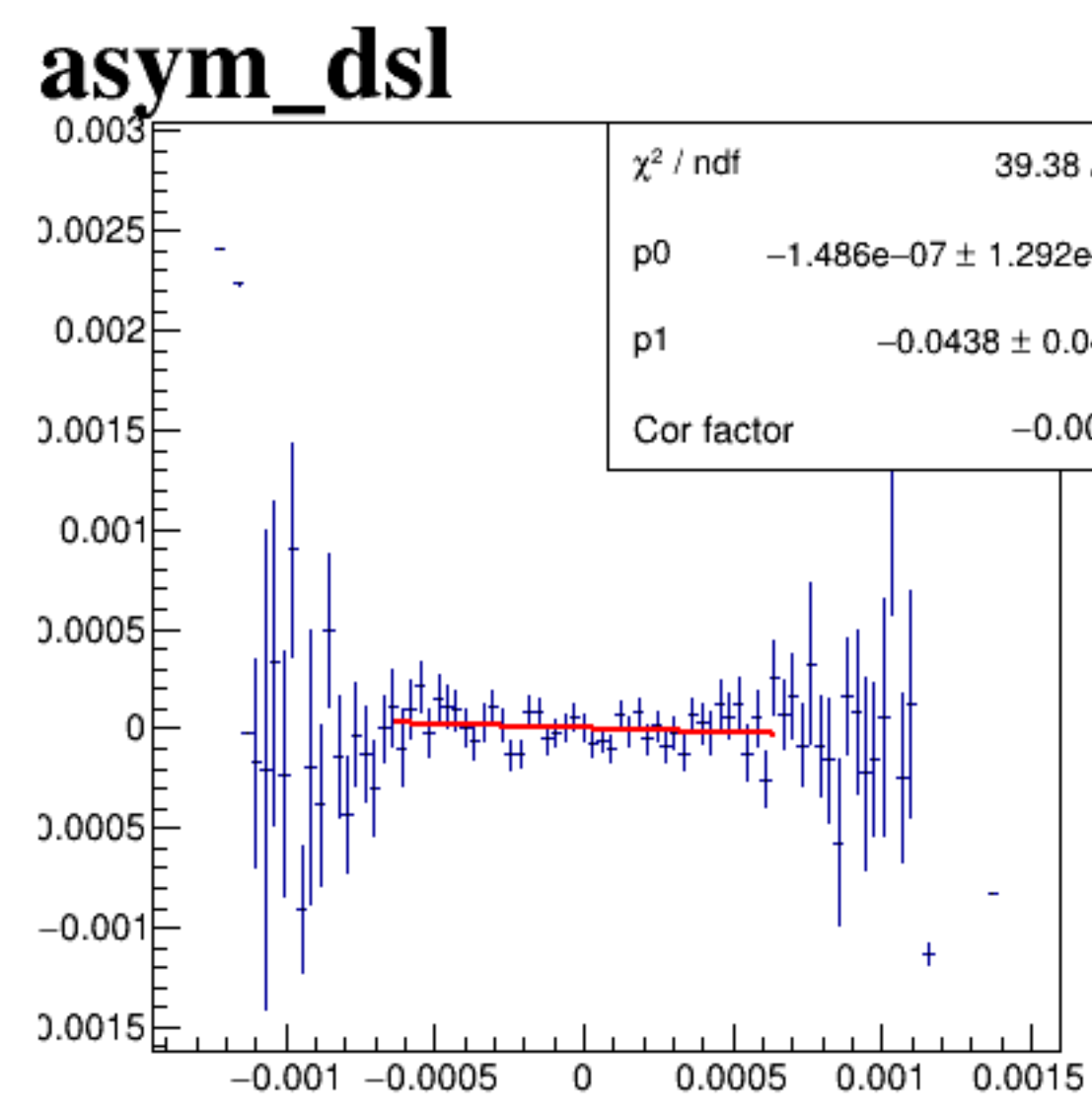
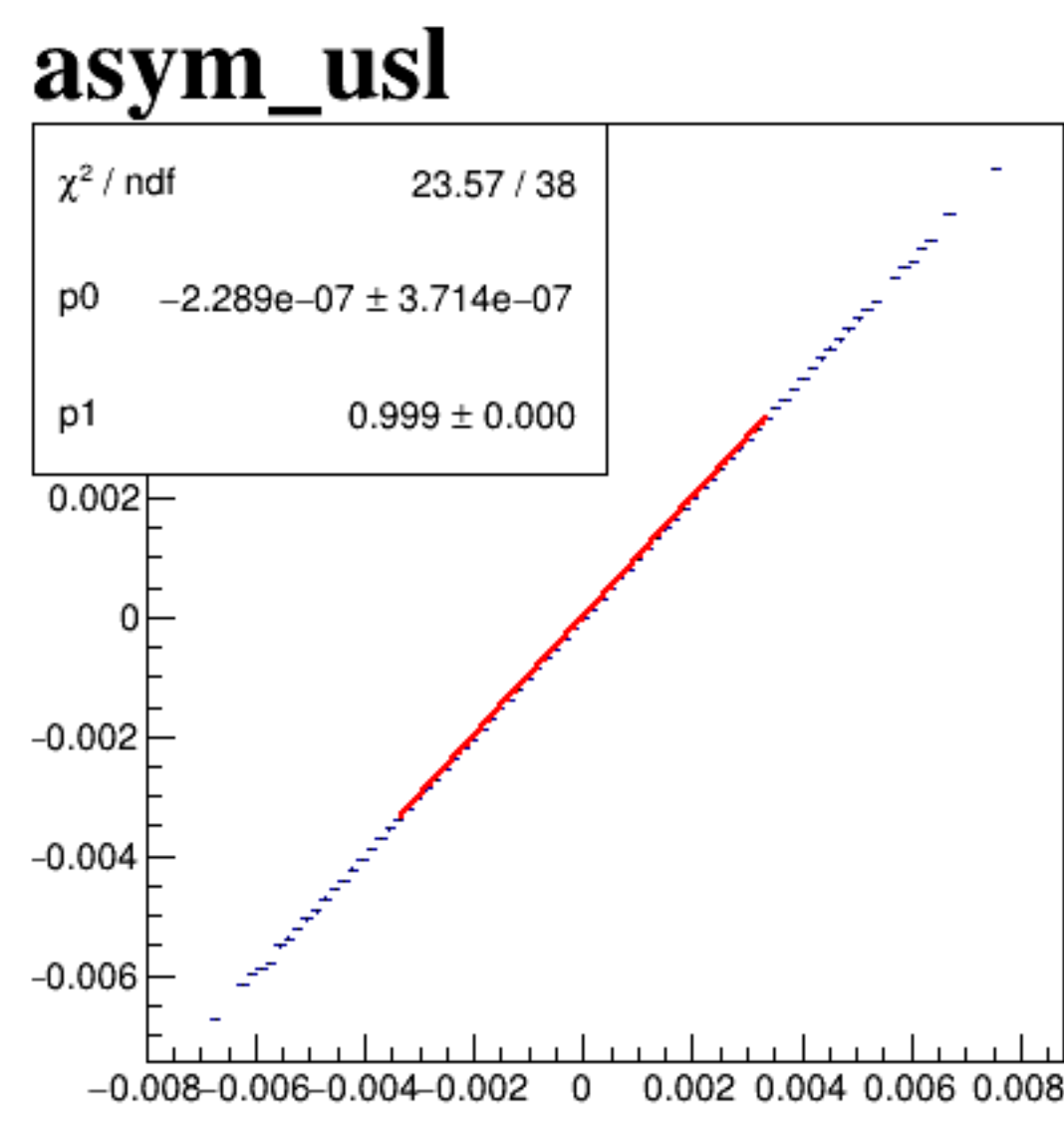
**asym\_usr**



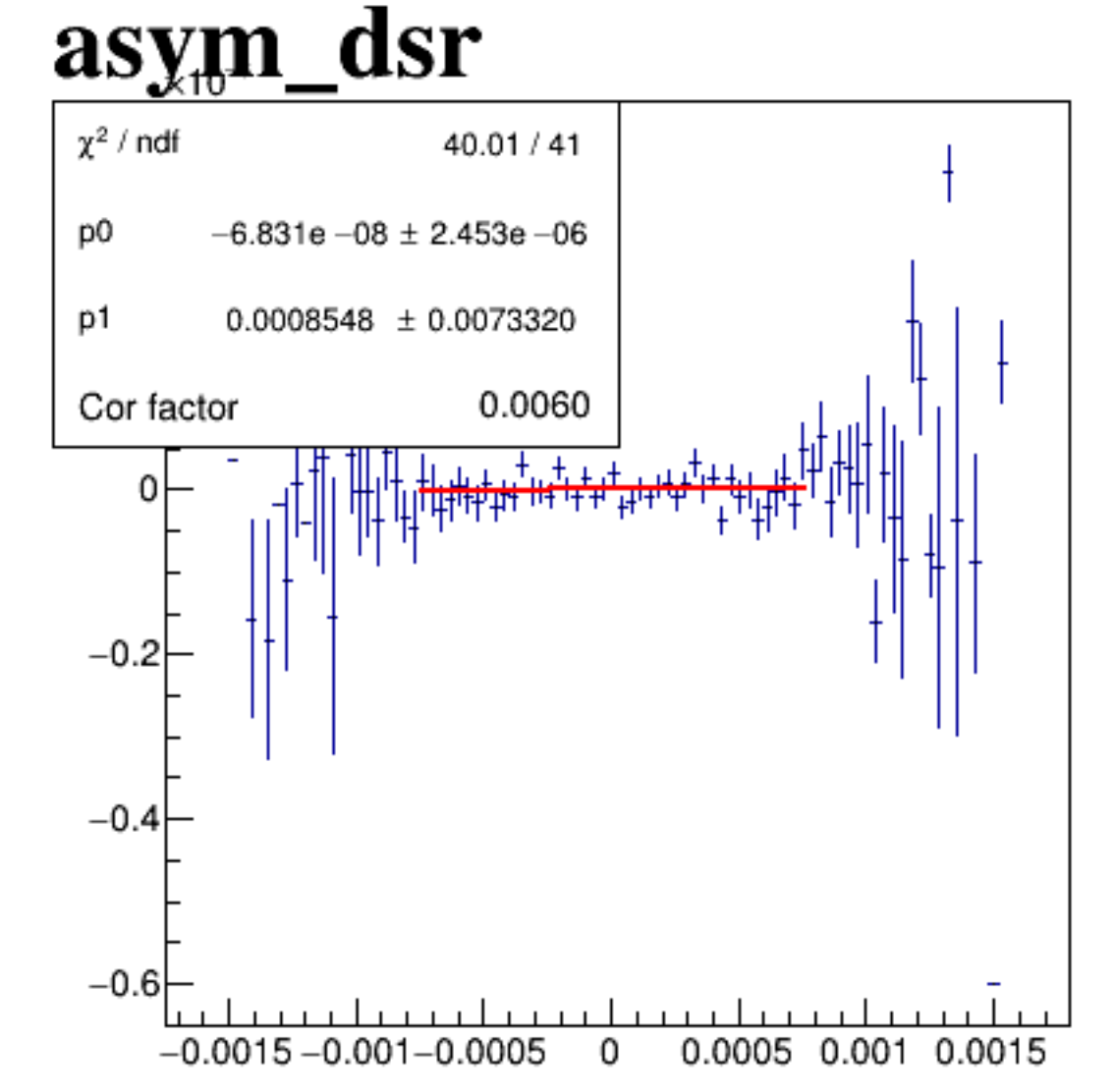
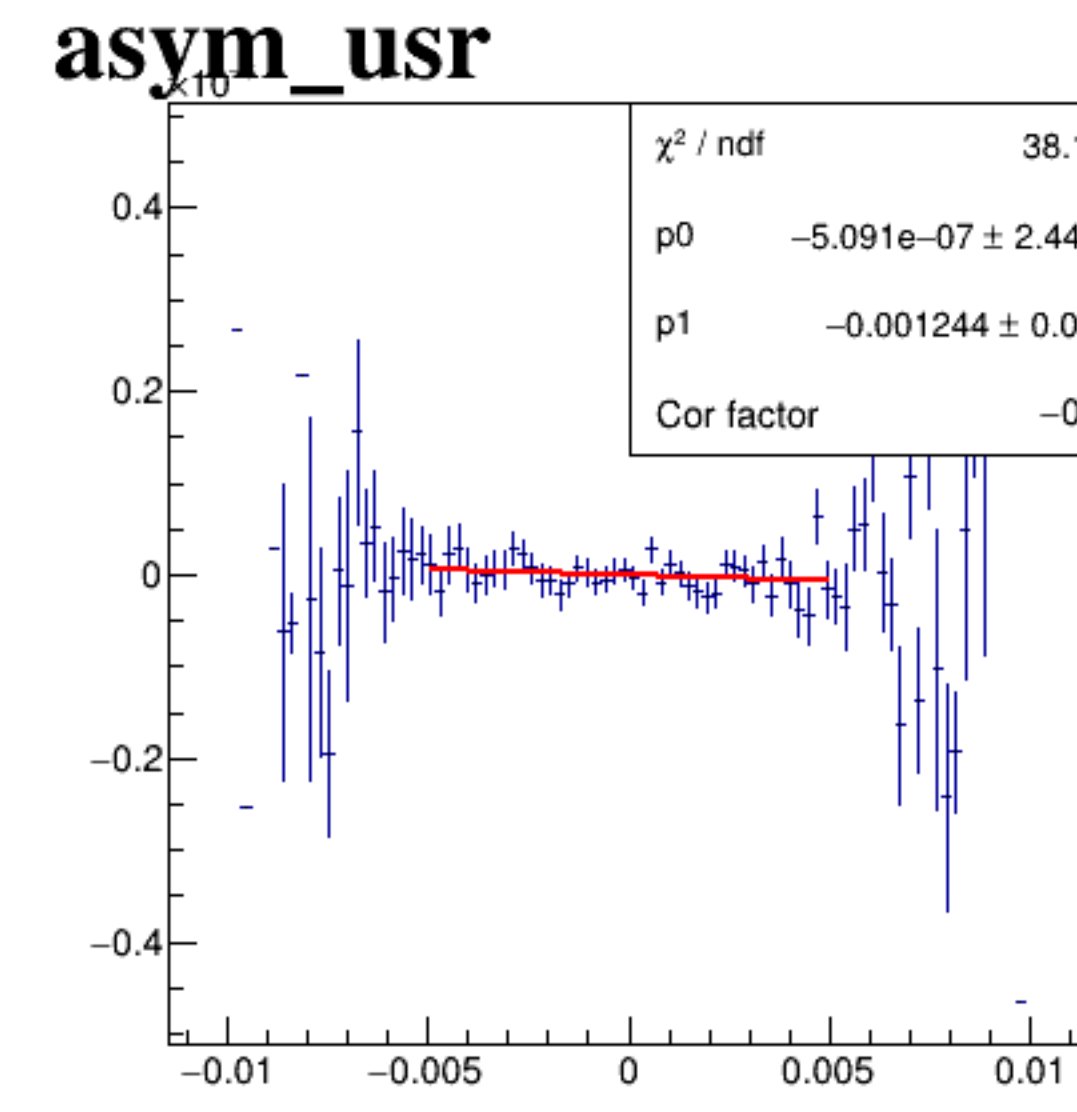
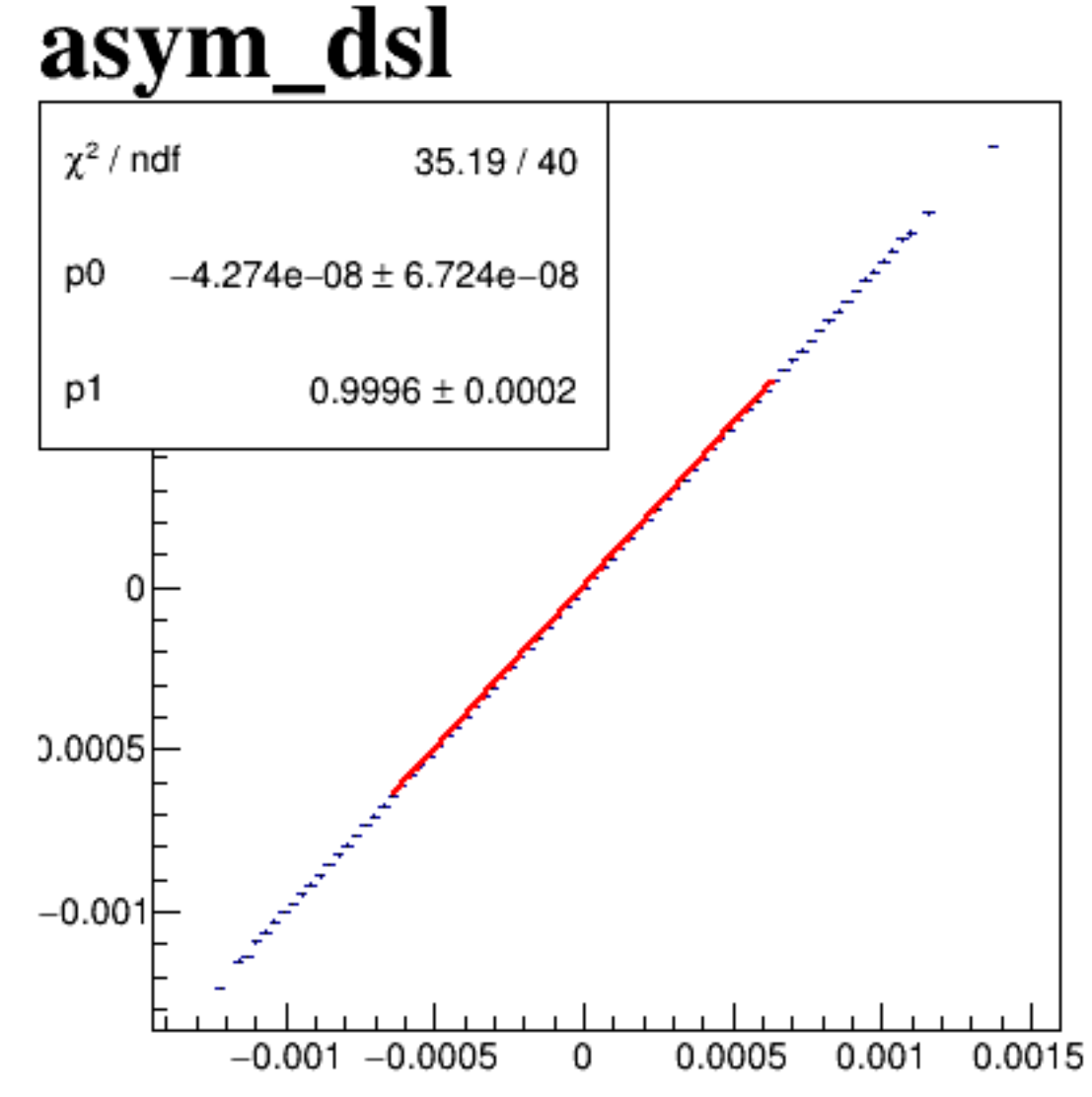
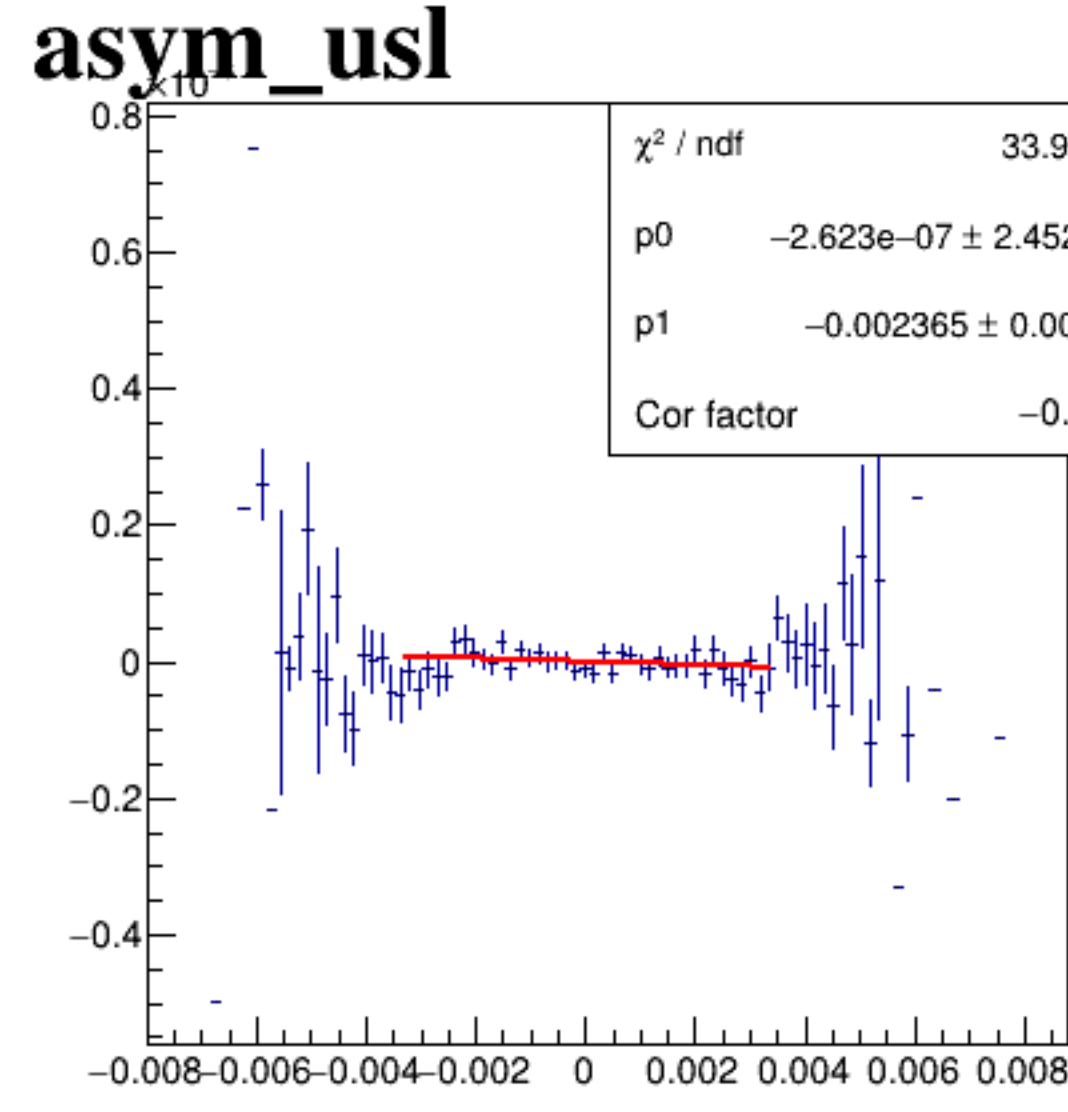
**asym\_dsr**



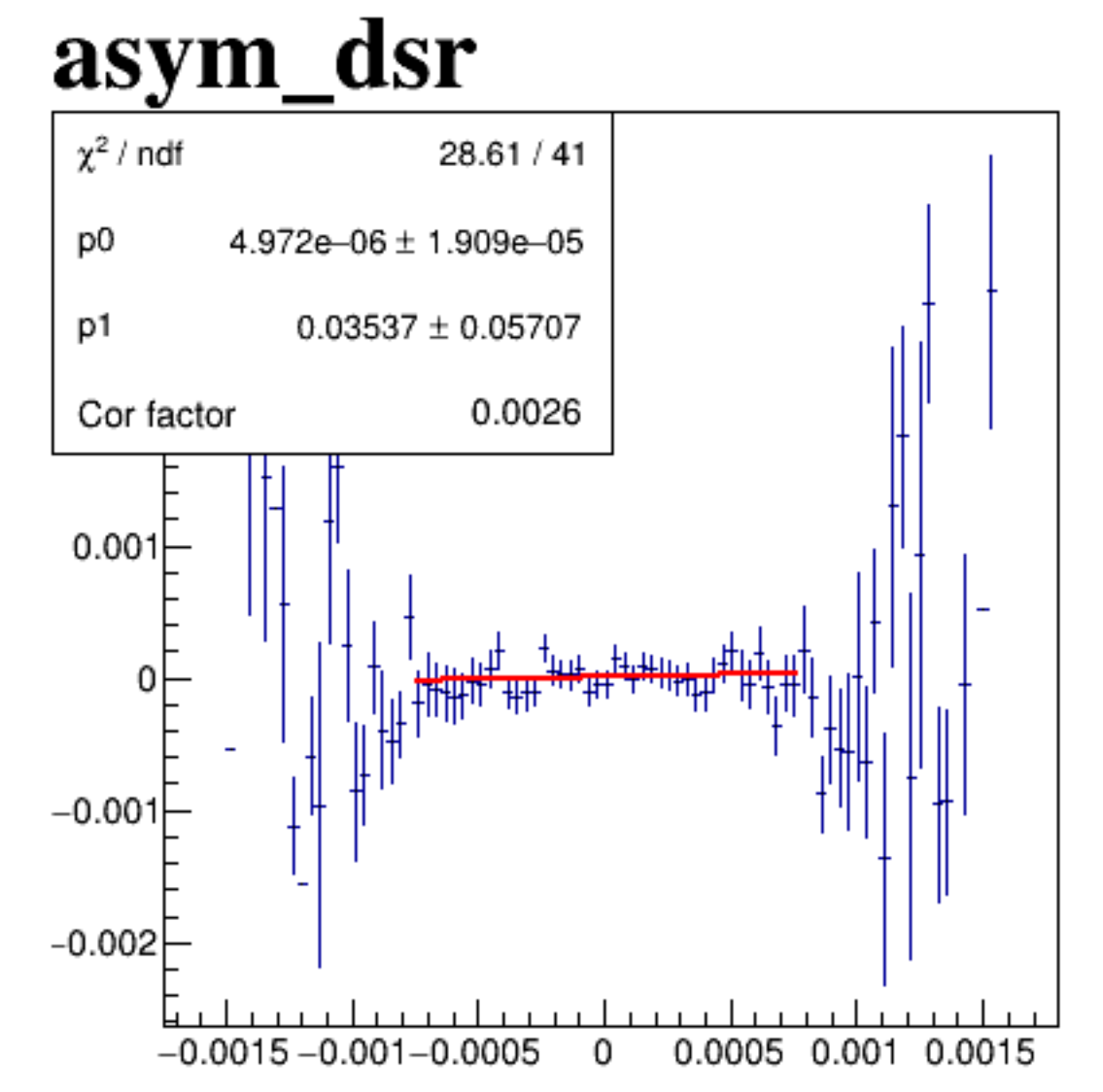
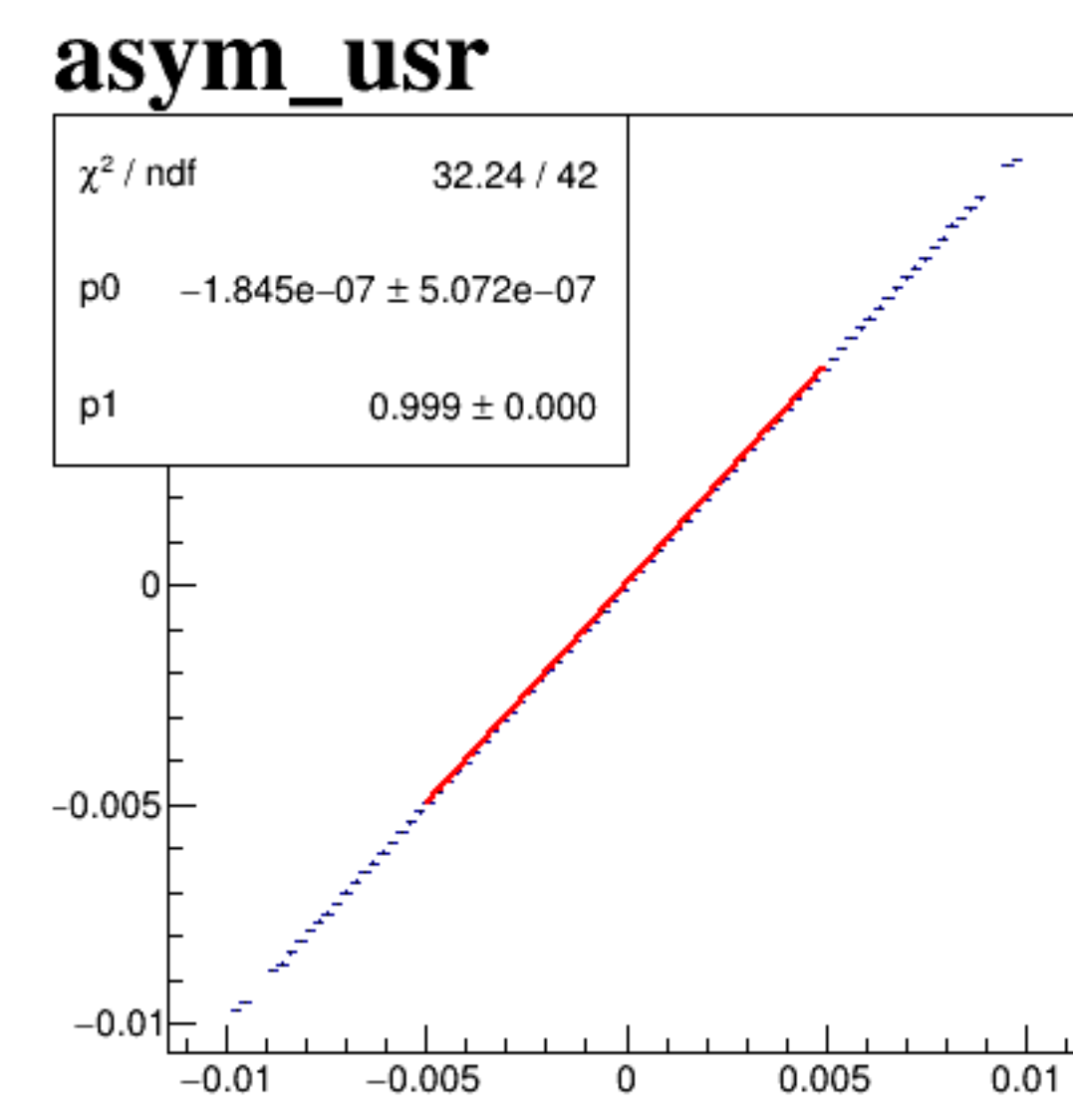
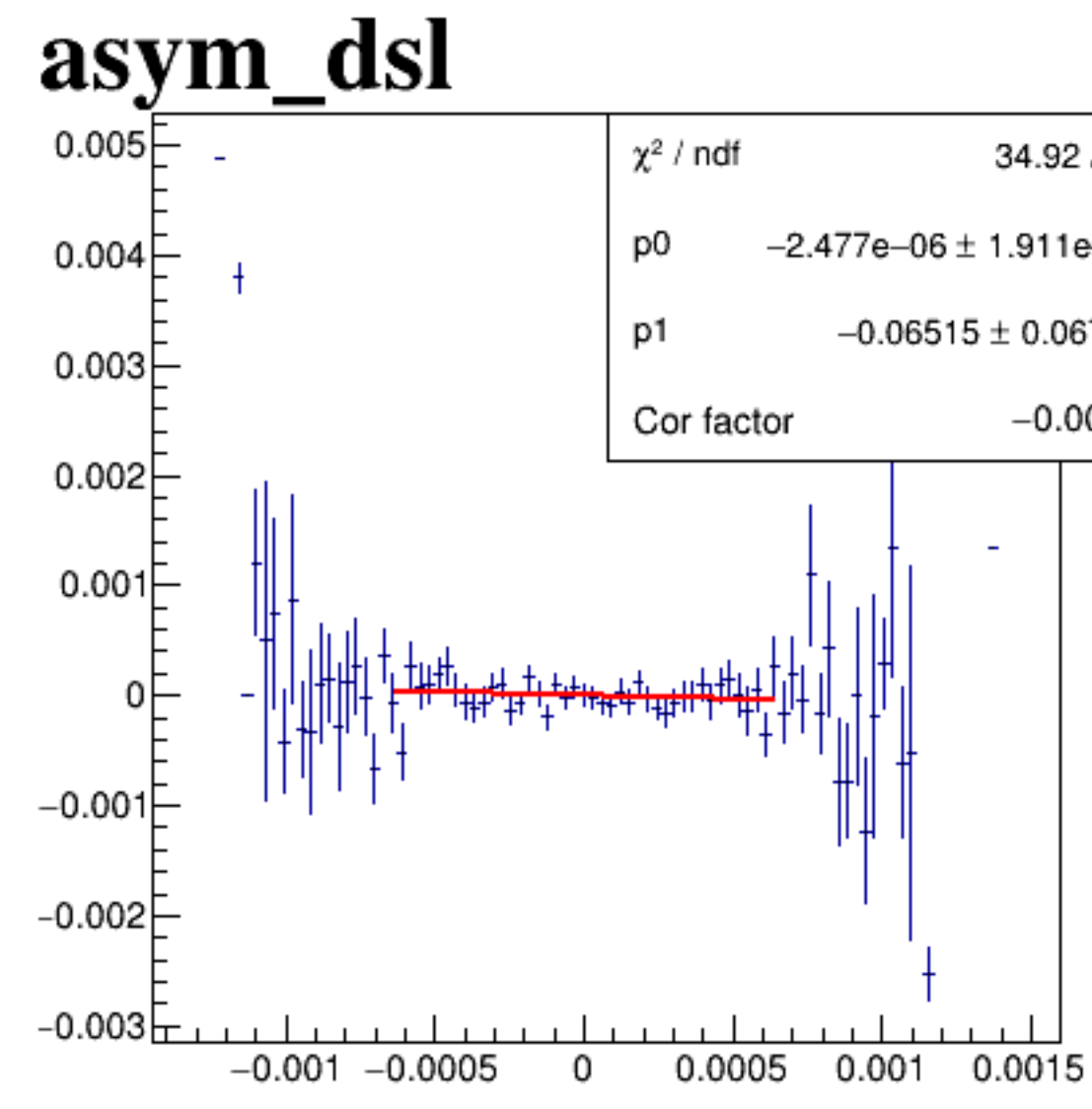
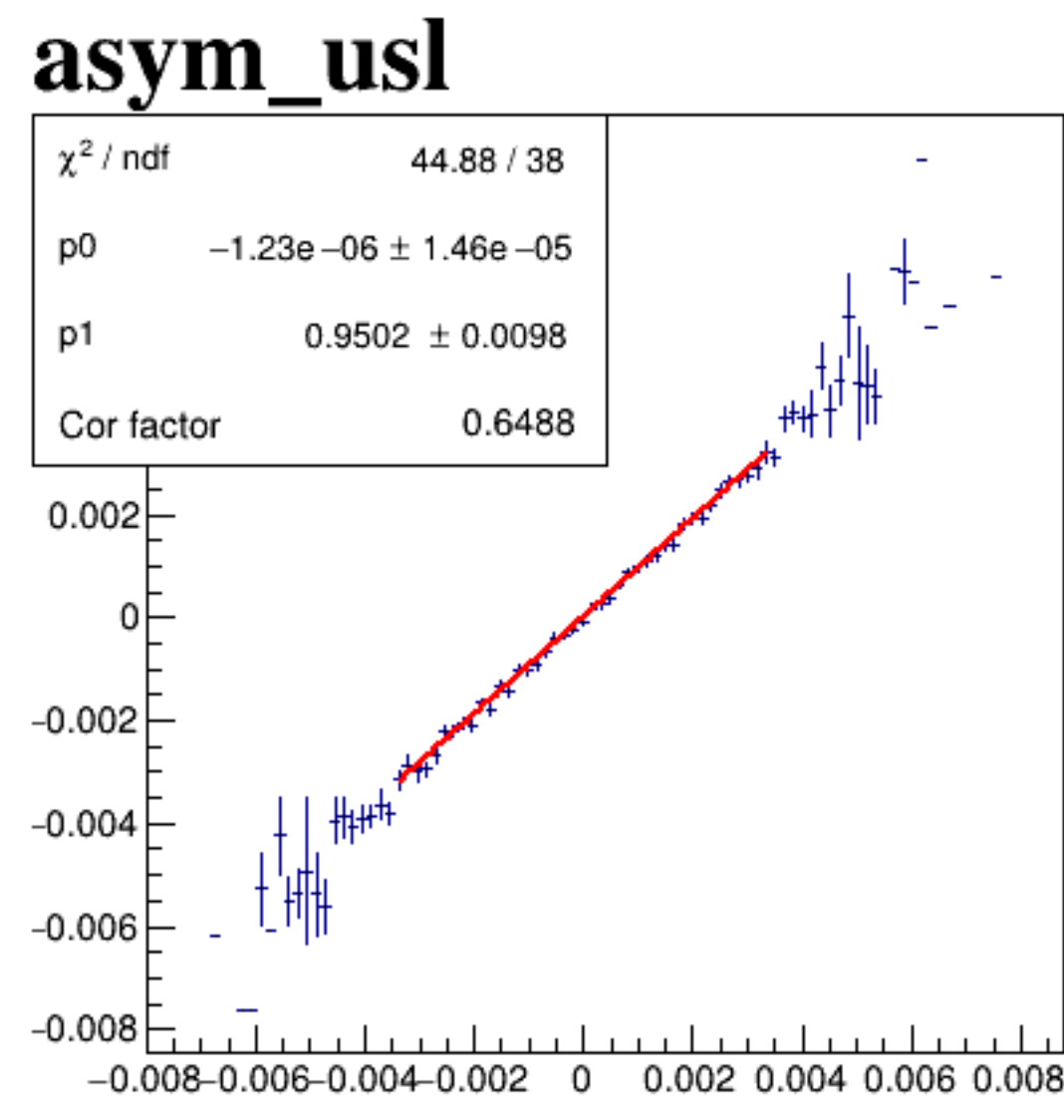
asym\_usl



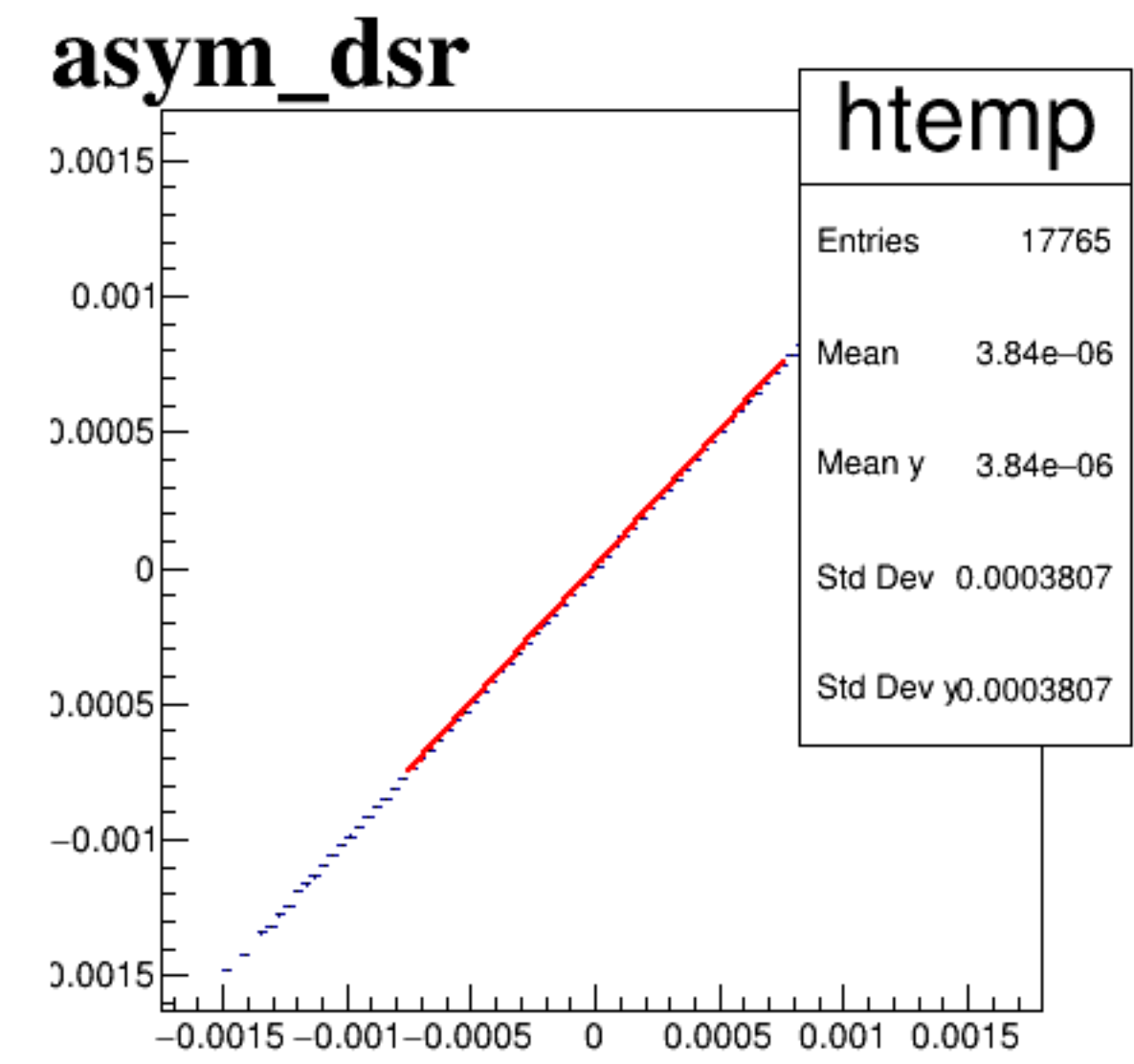
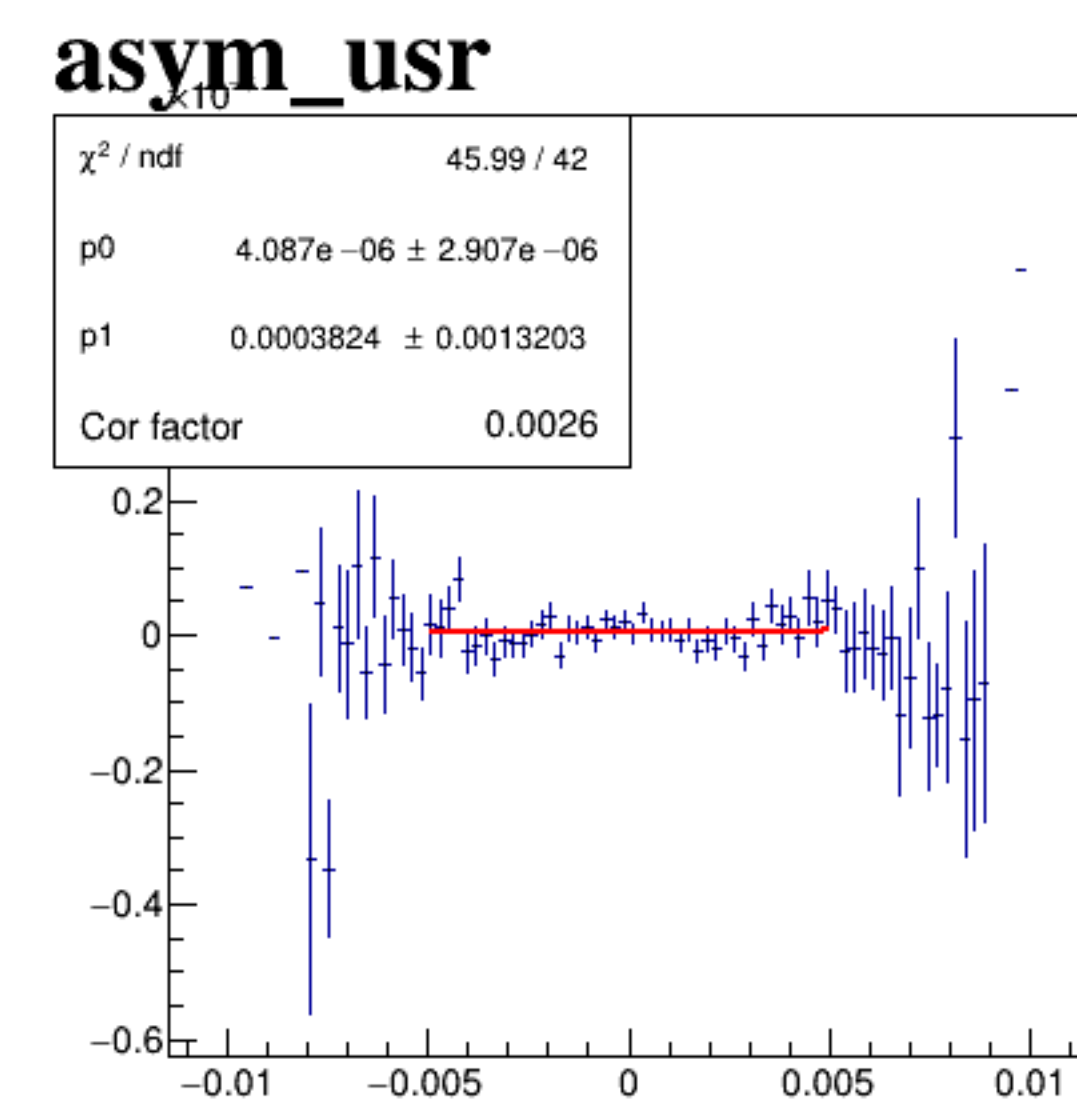
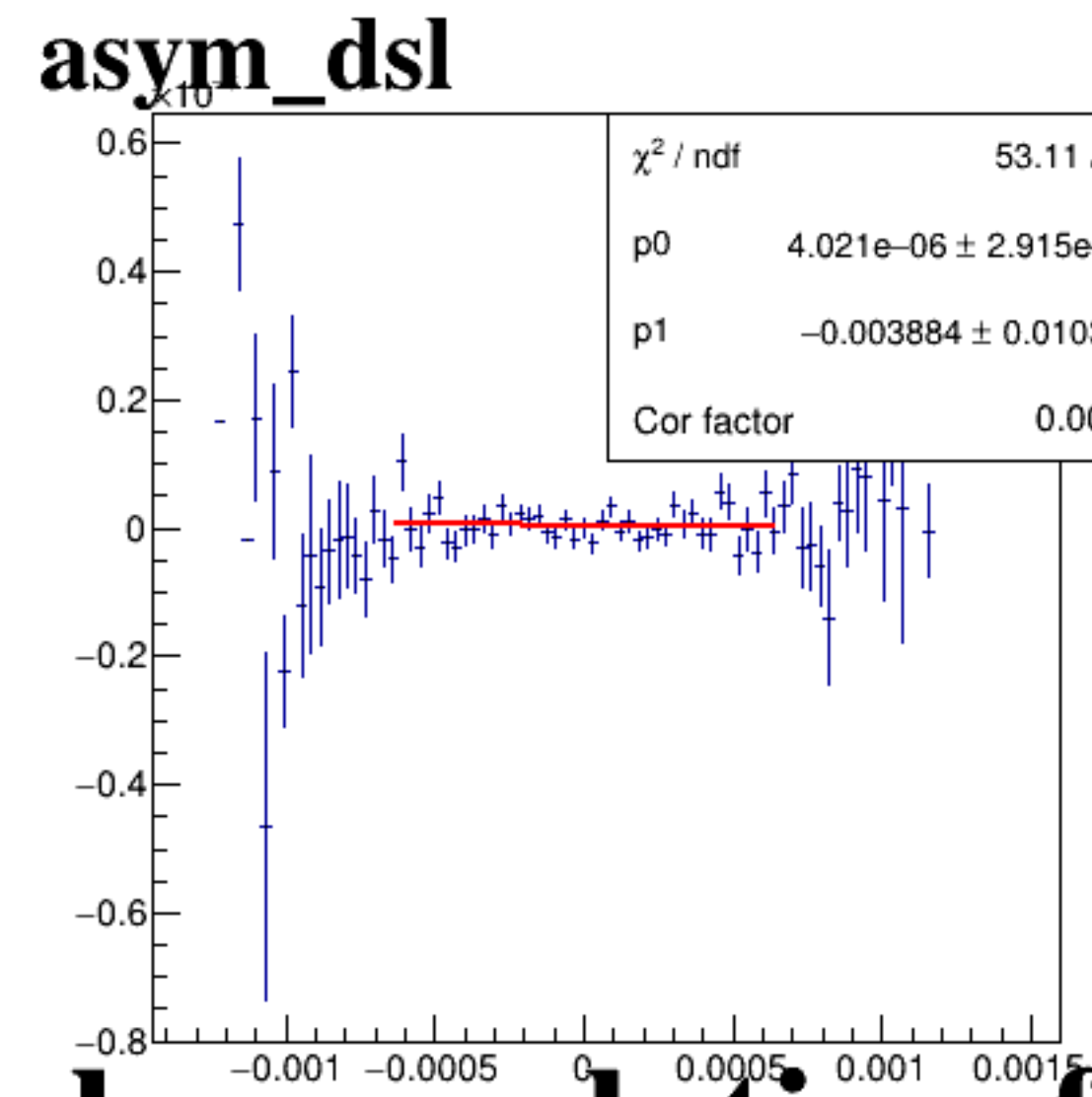
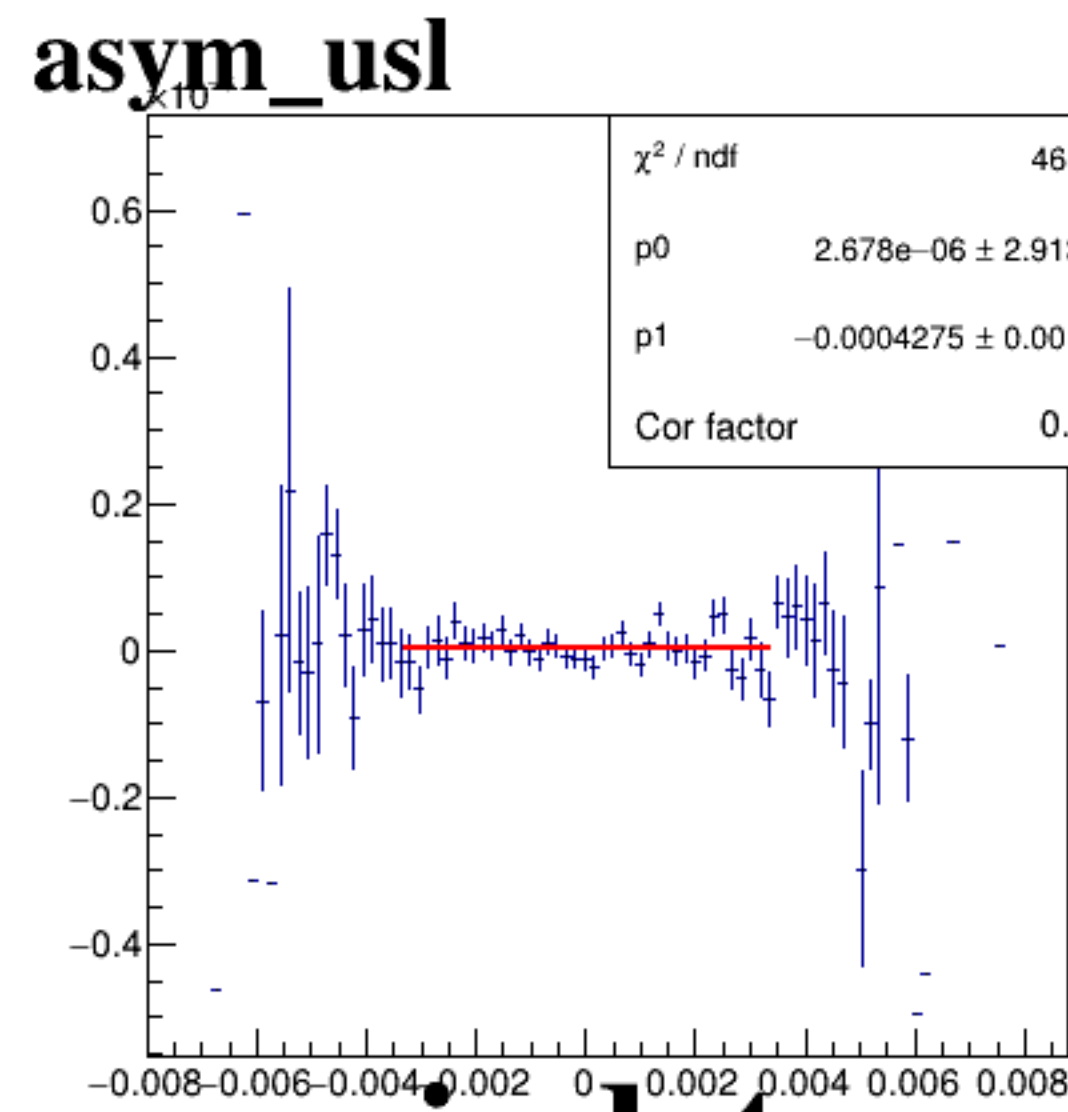
asym\_dsl



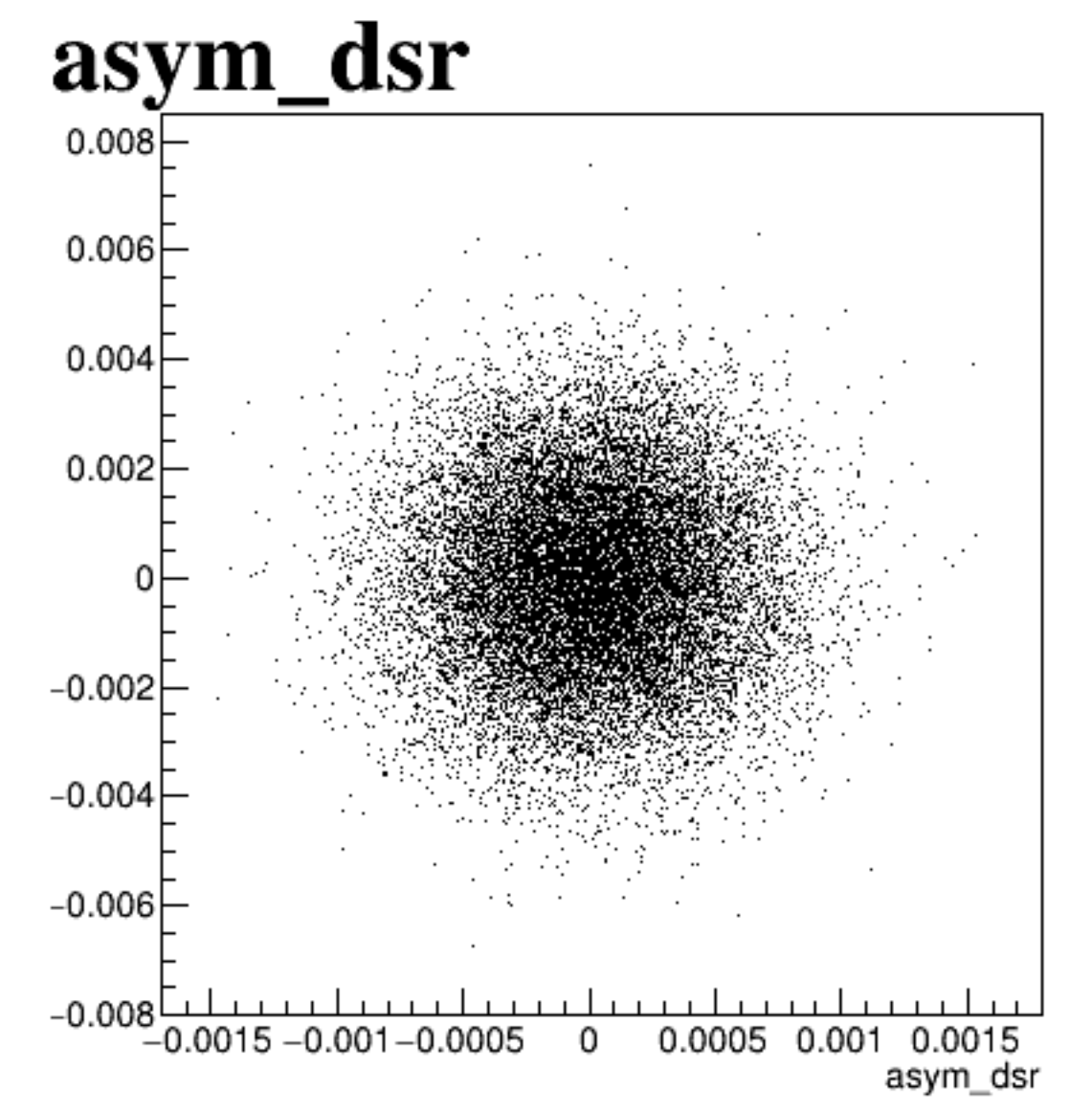
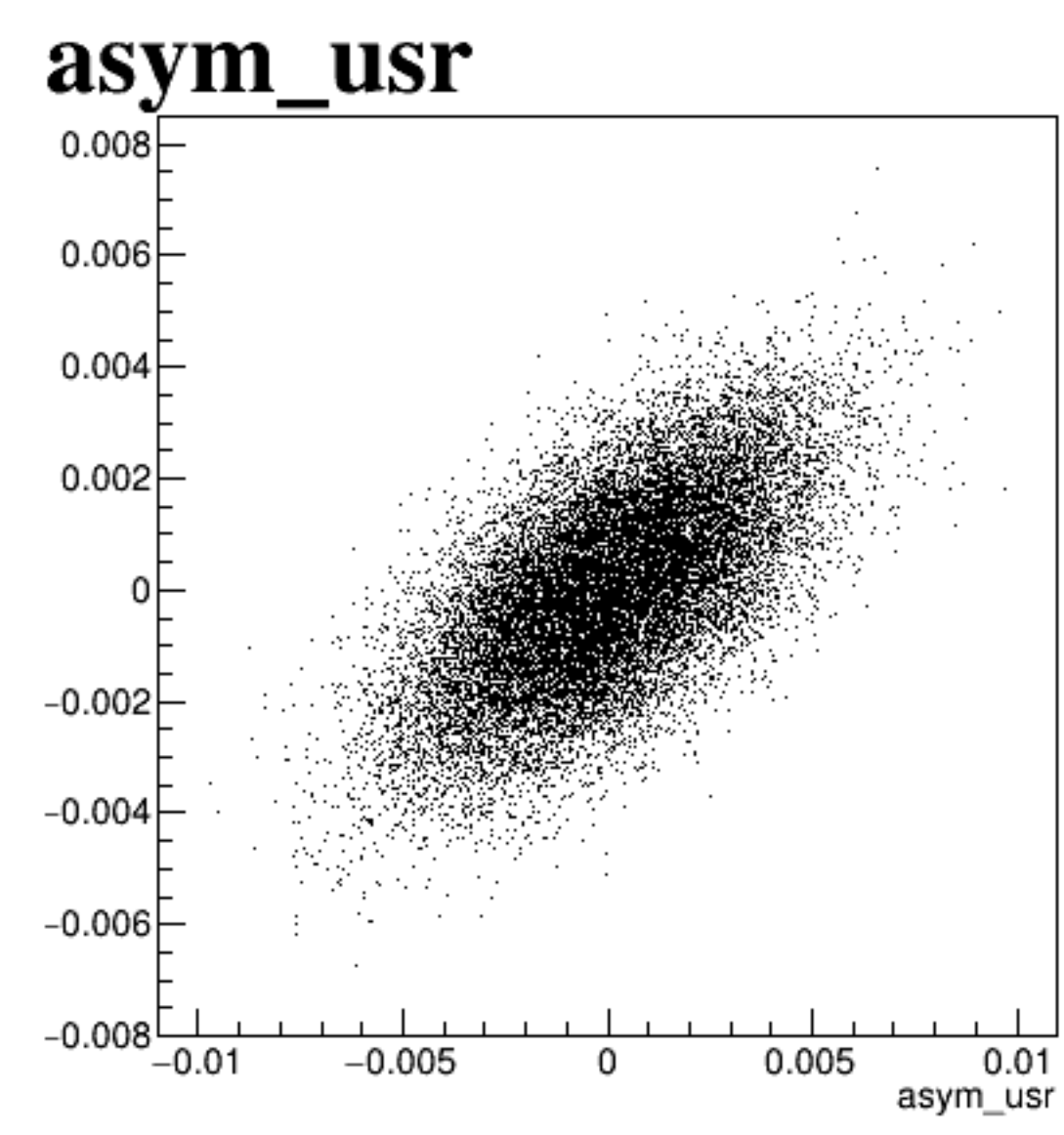
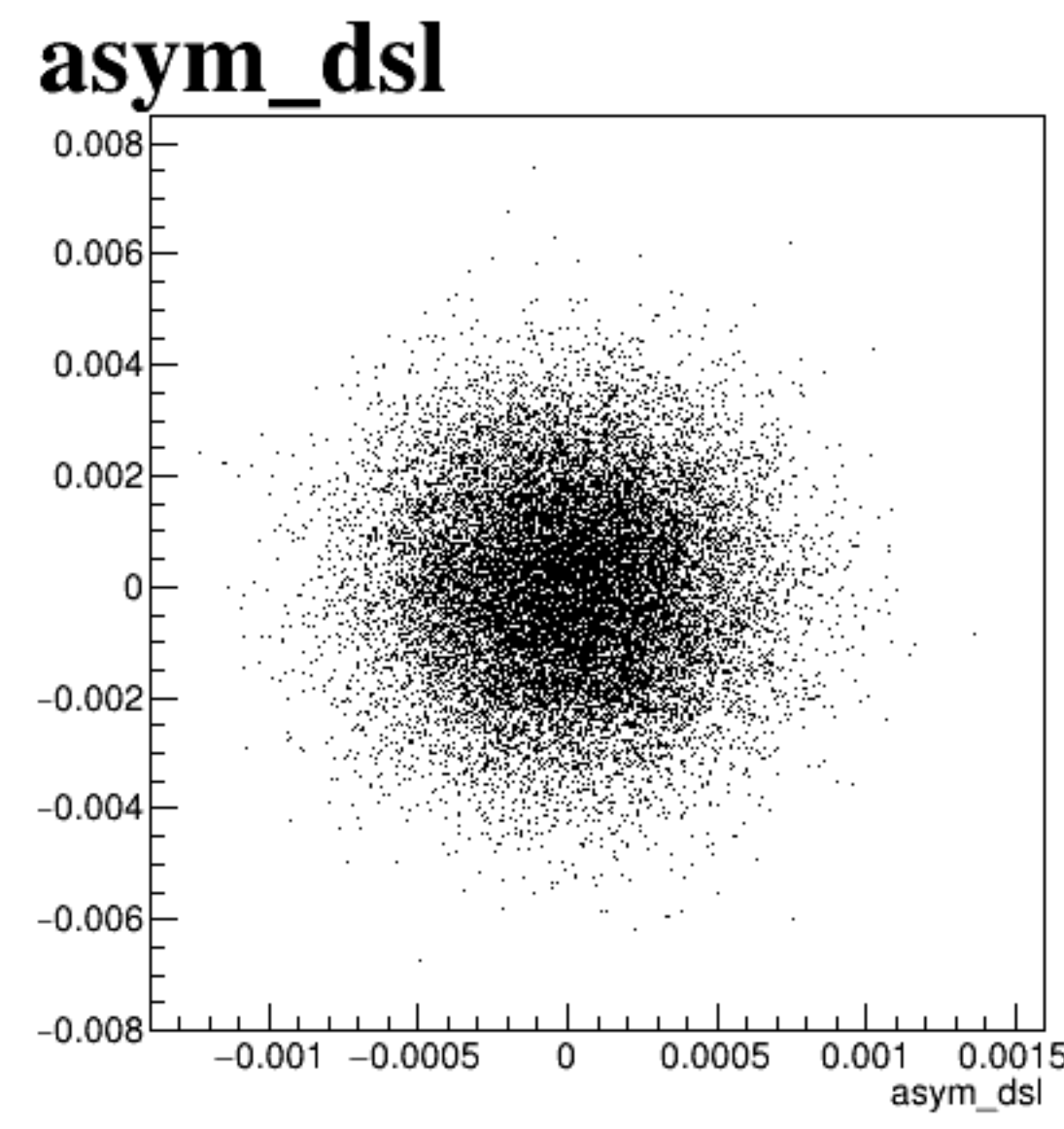
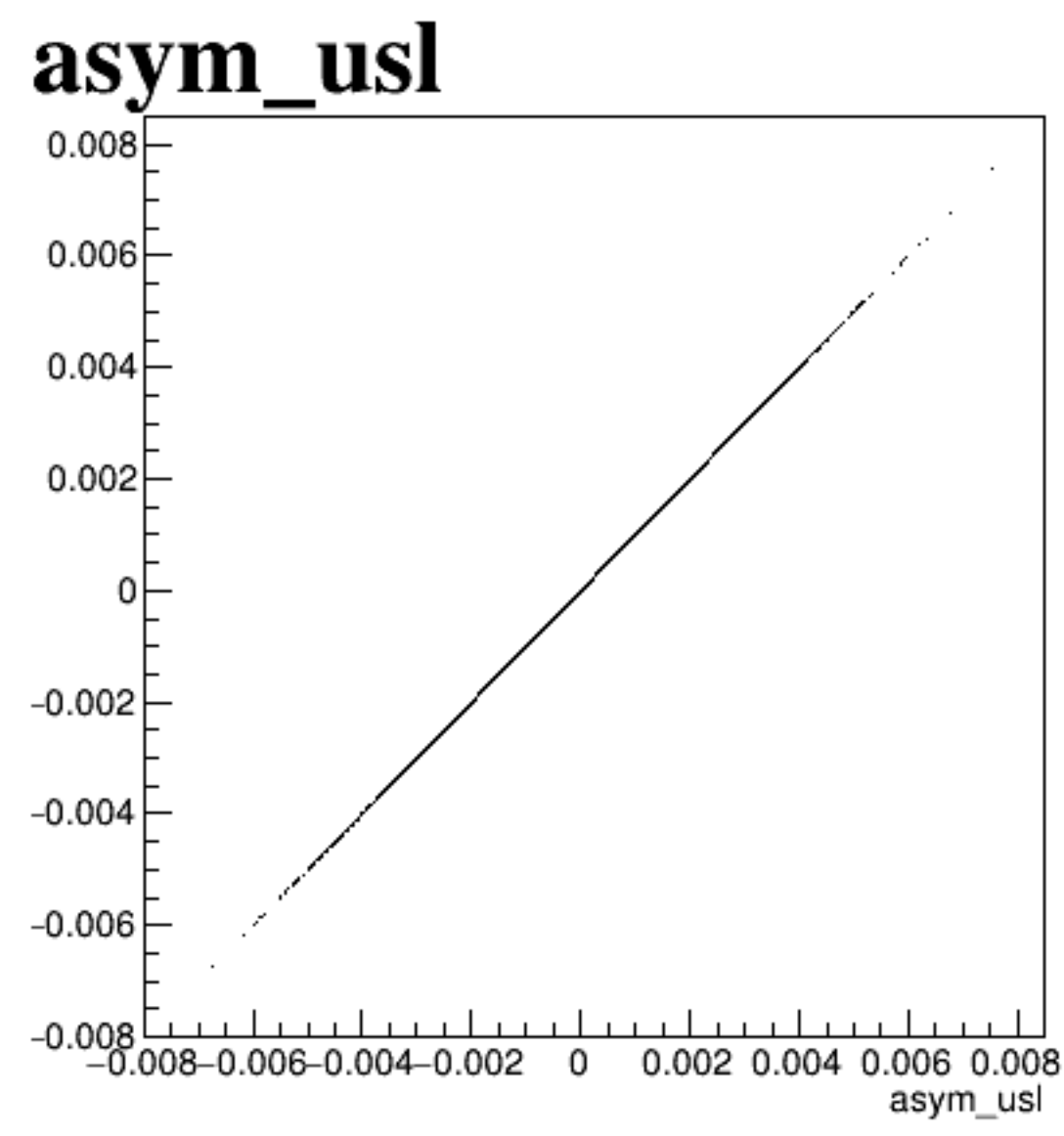
asym\_usr



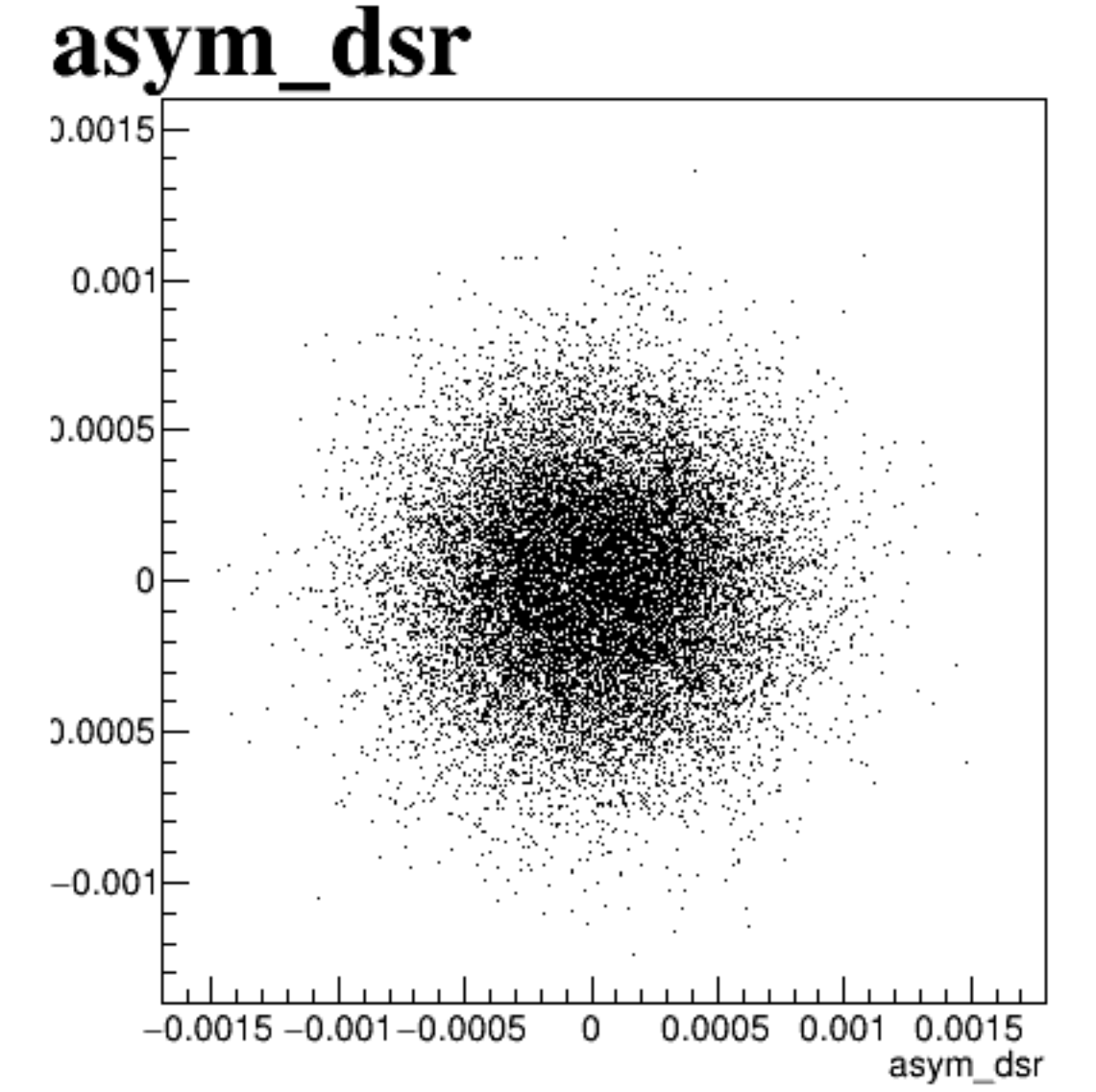
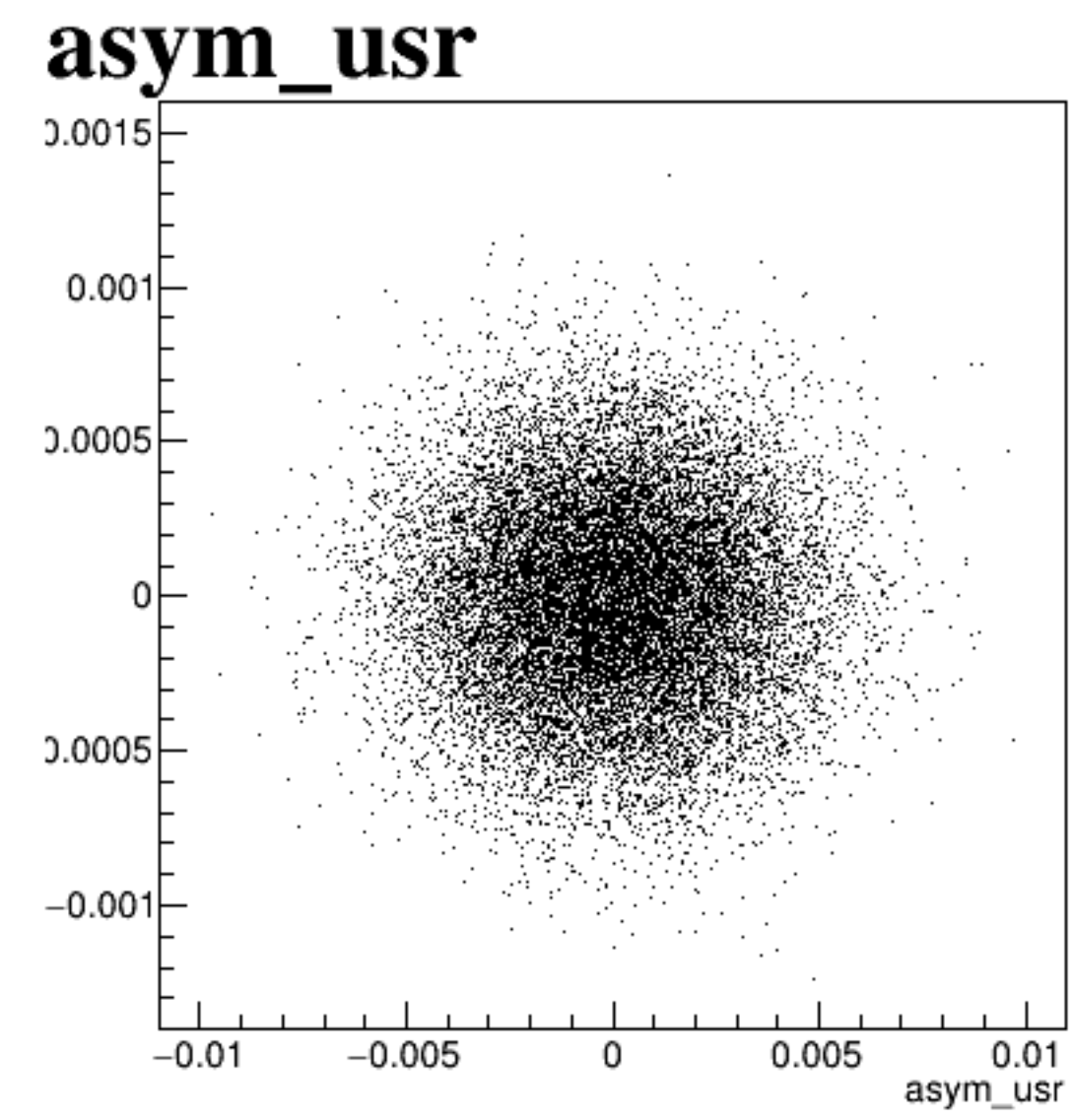
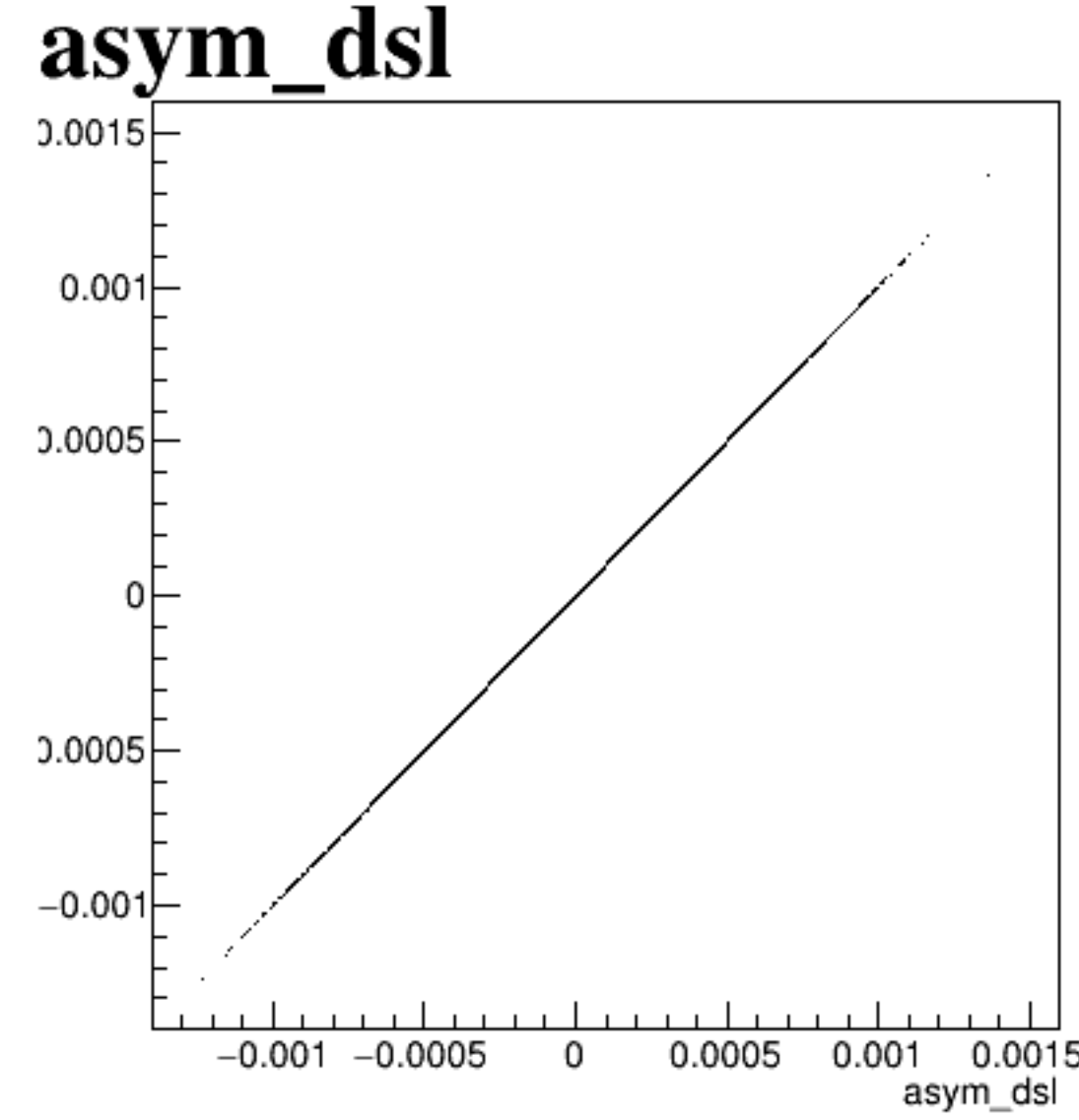
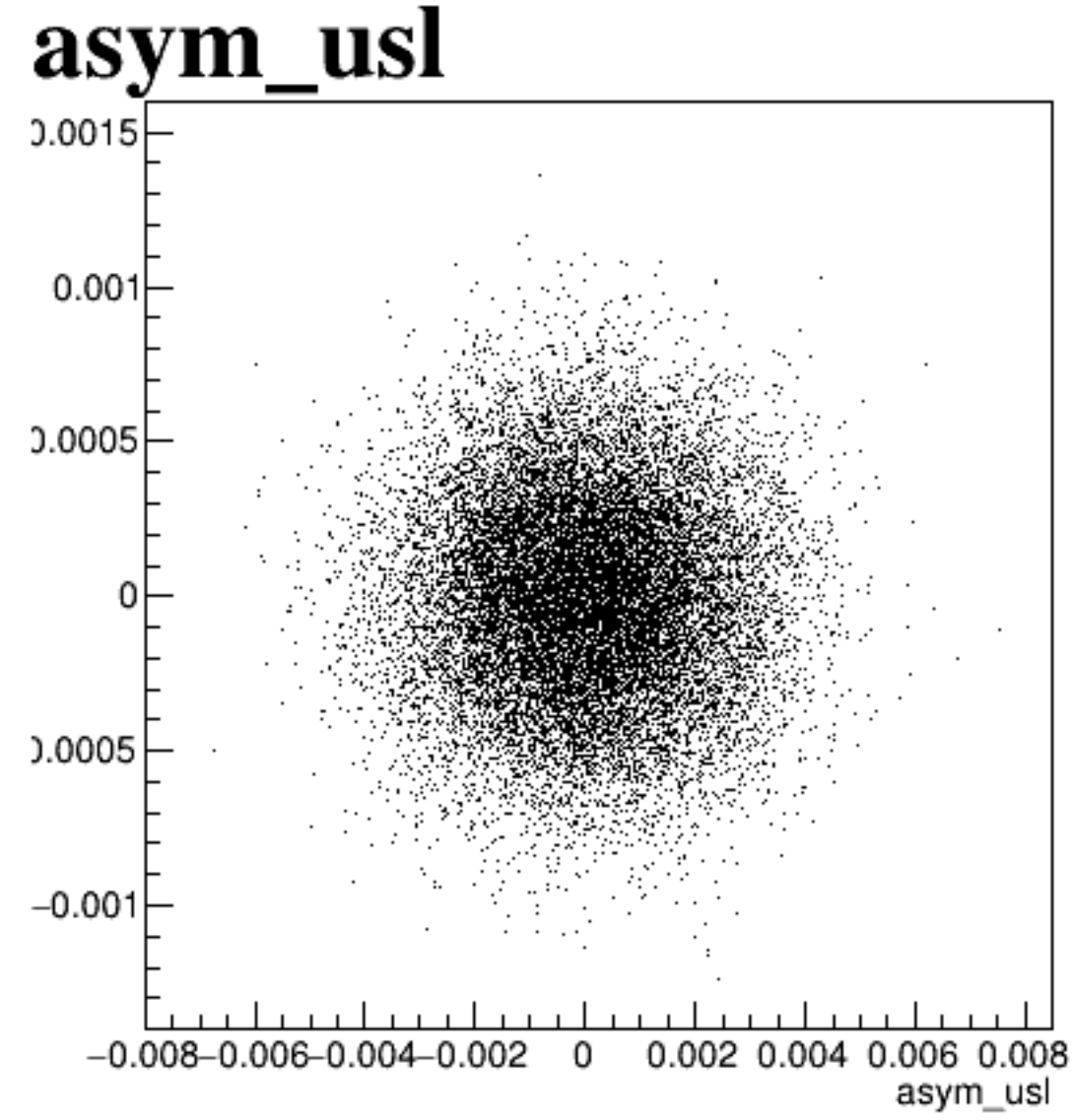
asym\_dsr



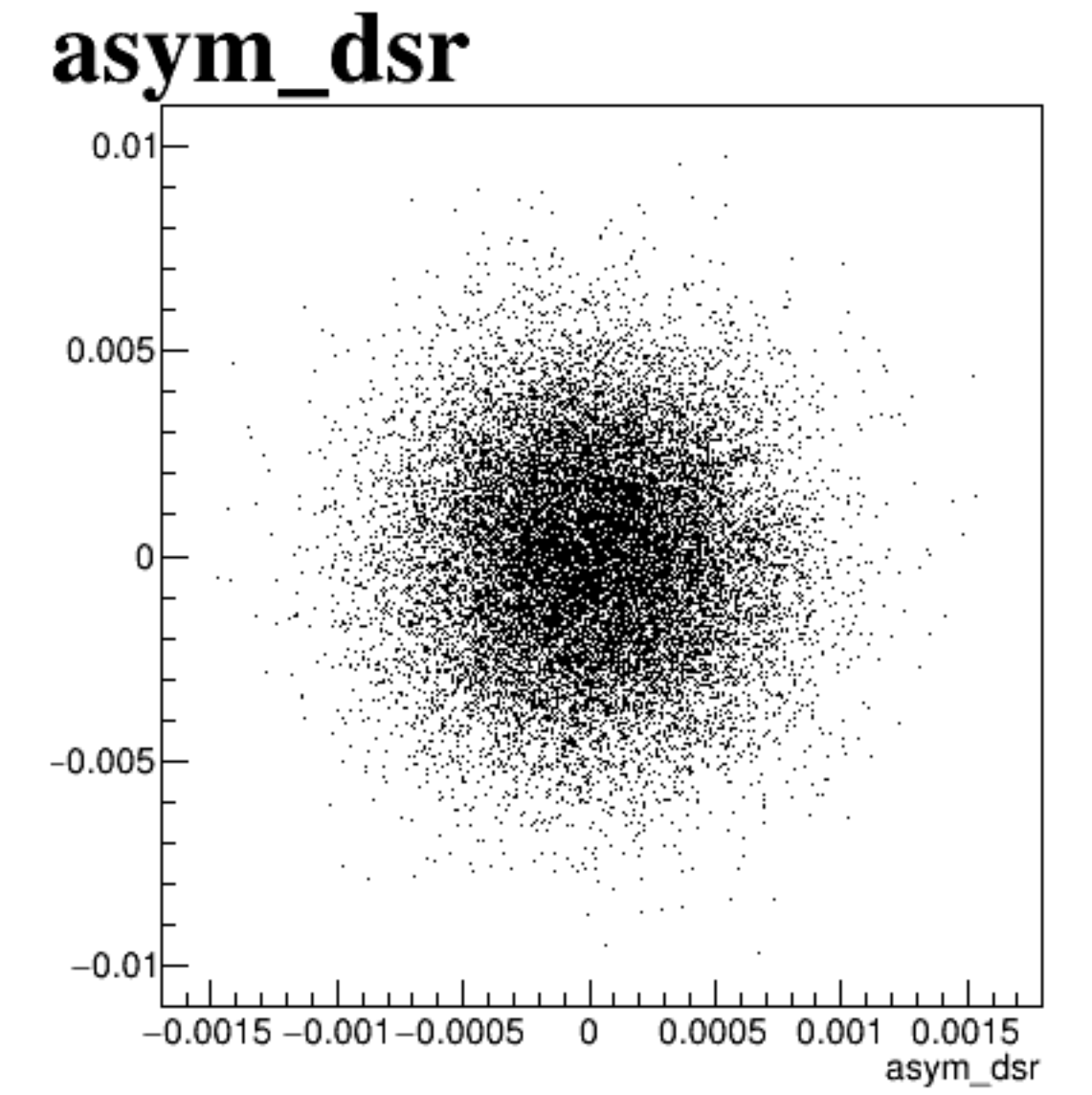
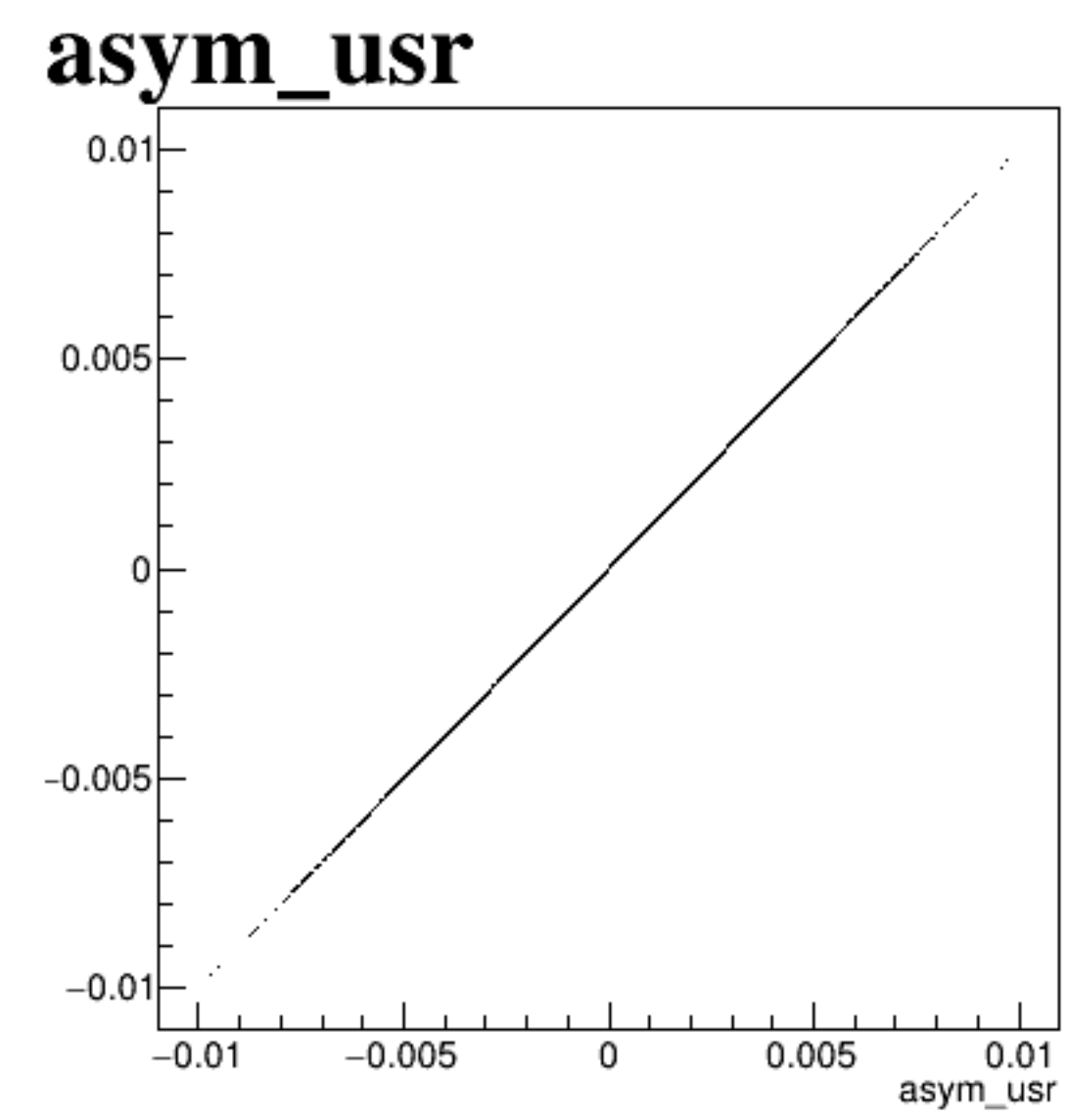
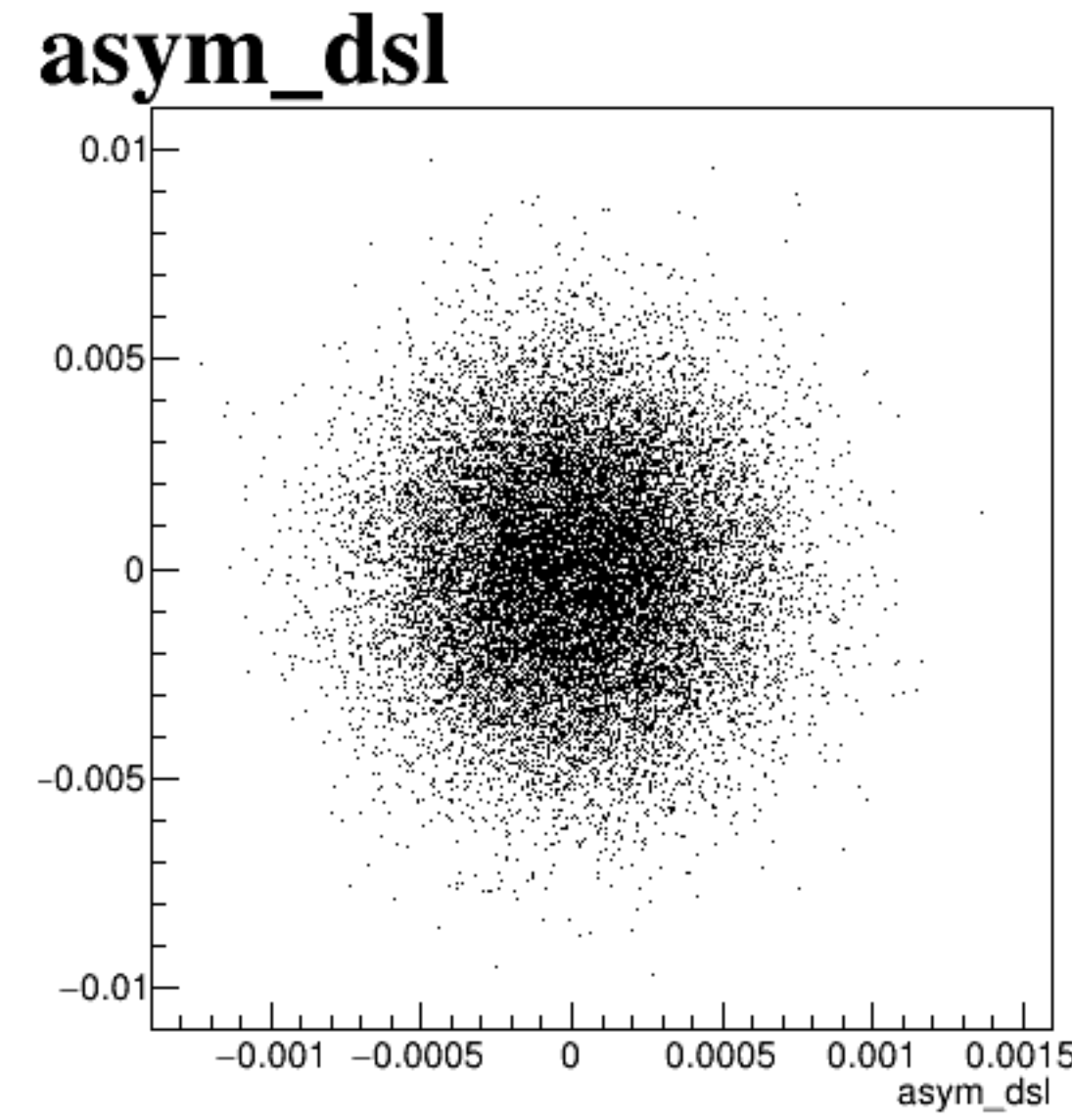
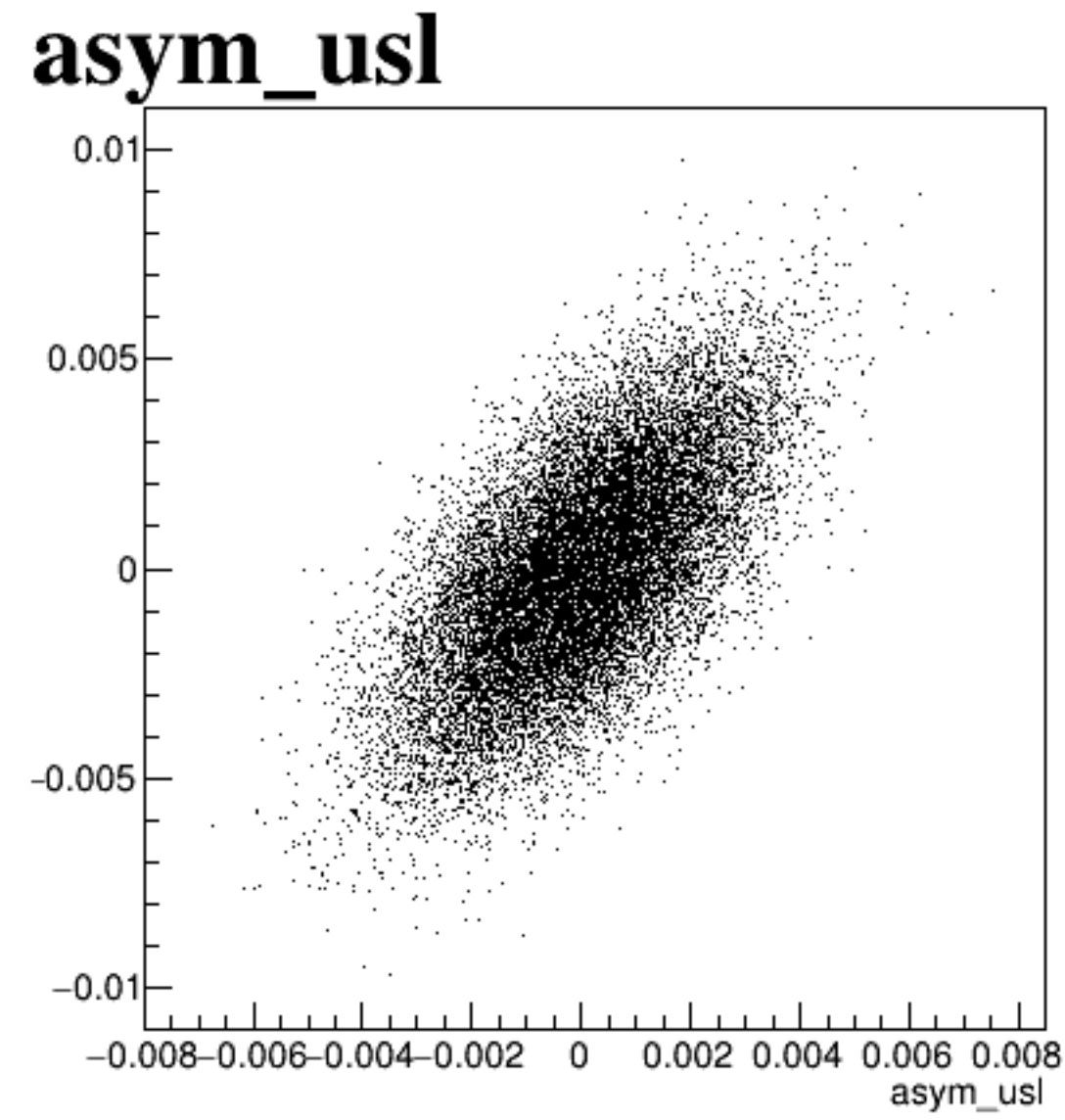
**asym\_usl**



**asym\_dsl**



**asym\_usr**



**asym\_dsr**

