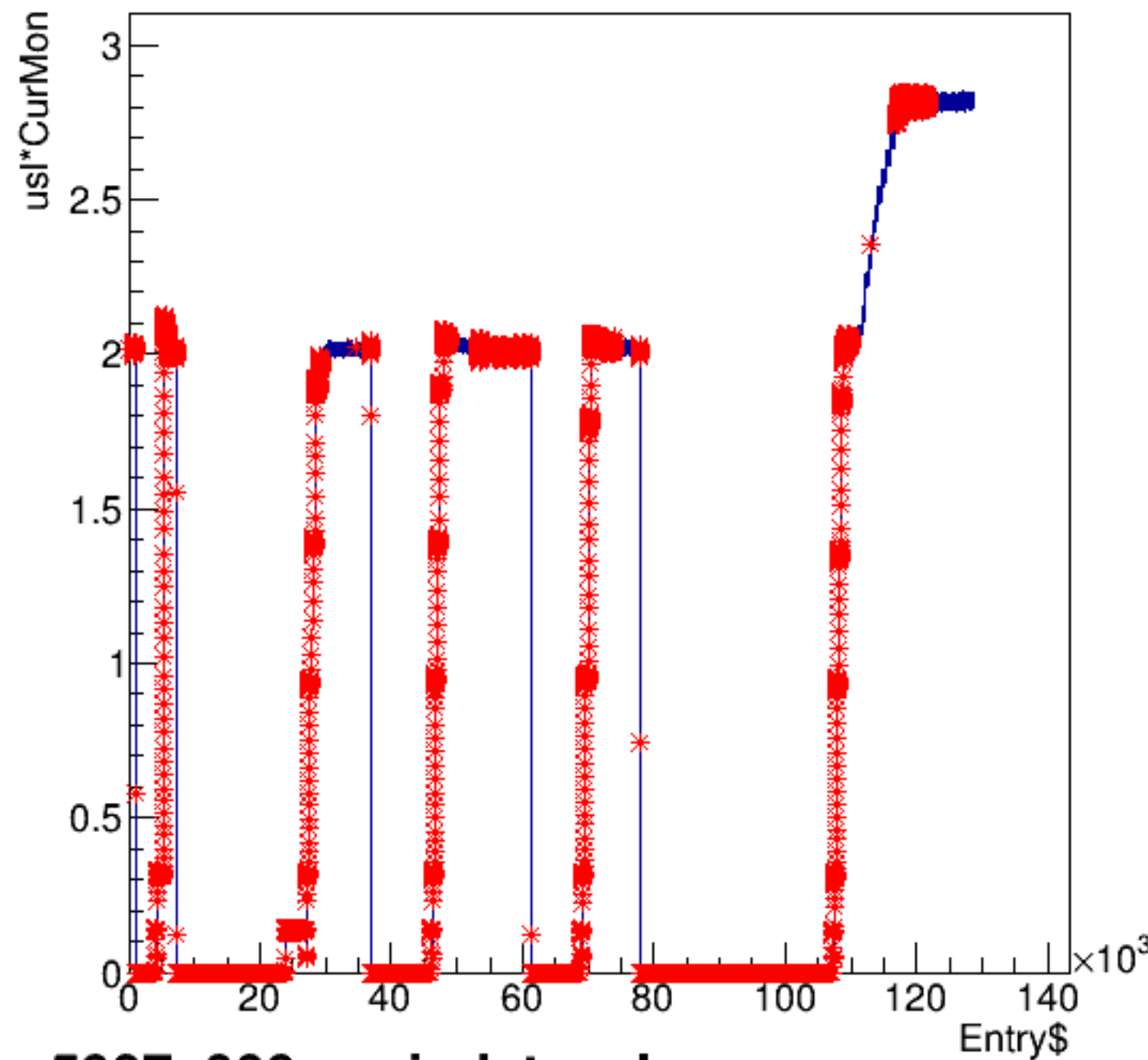
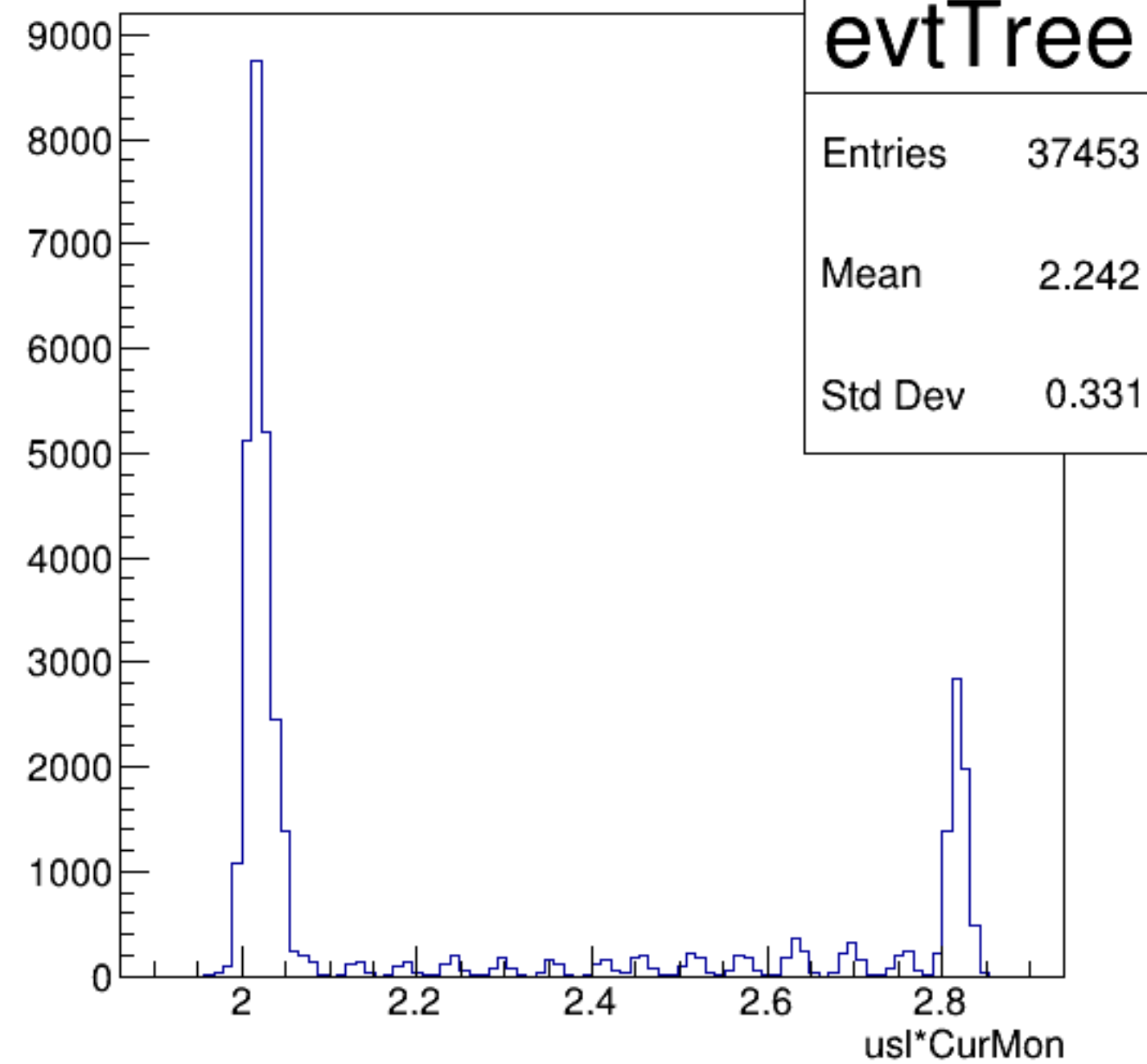


usl\*CurMon:Entry\$



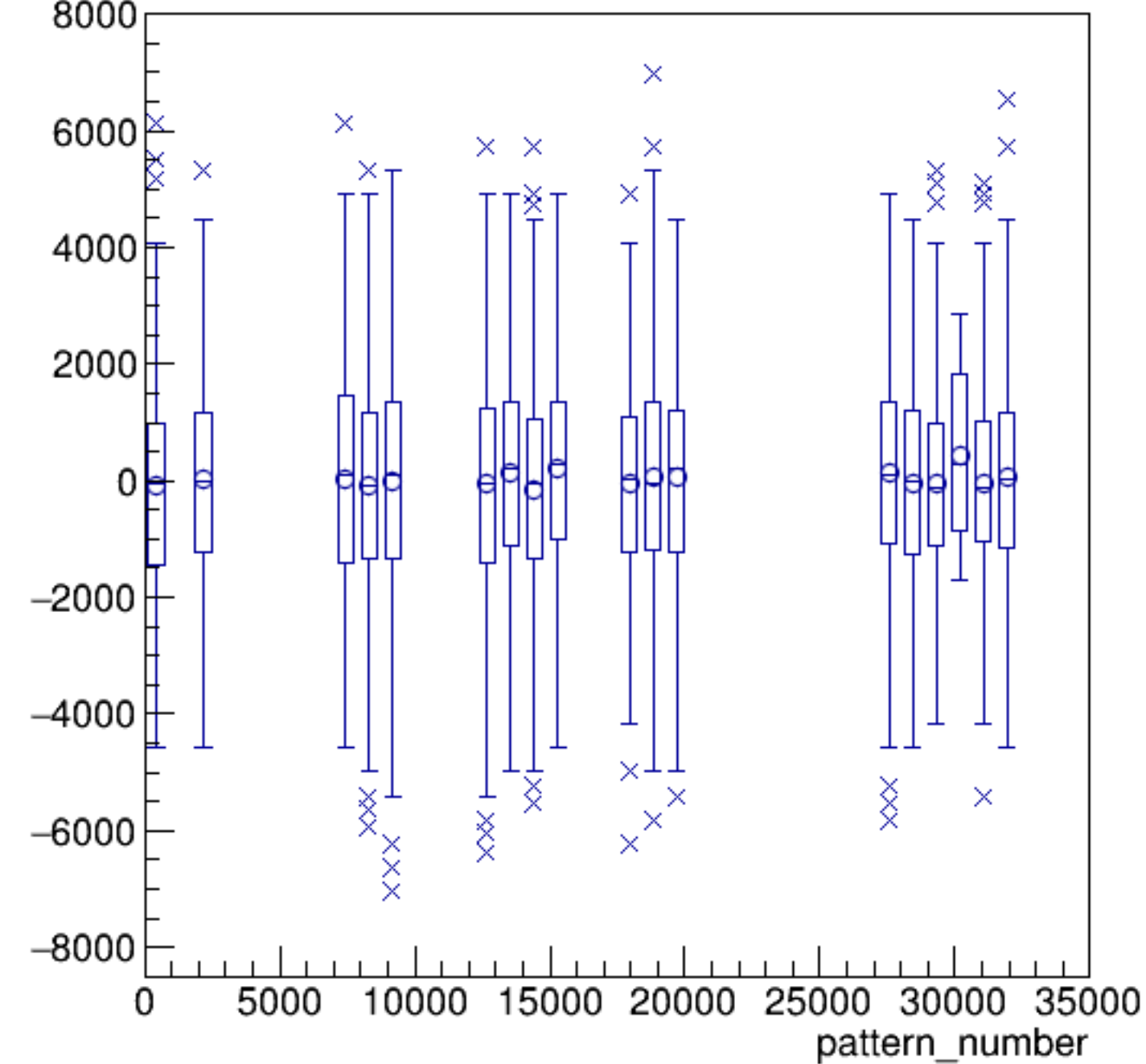
run5907\_000\_maindet\_usl.png

usl\*CurMon {ErrorFlag==0}

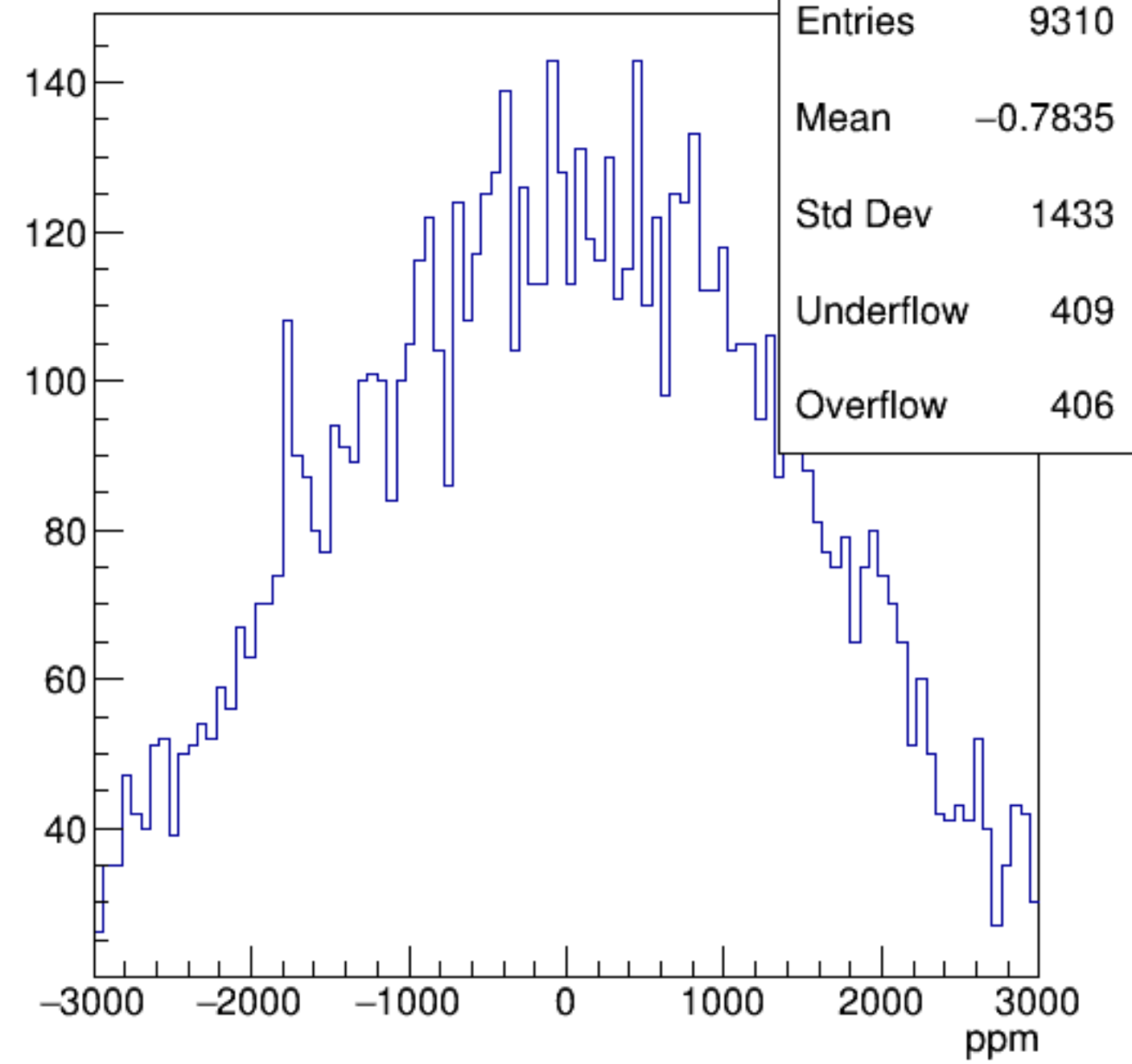


evtTree	
Entries	37453
Mean	2.242
Std Dev	0.331

asym\_usl/ppm:pattern\_number {ErrorFlag==0}

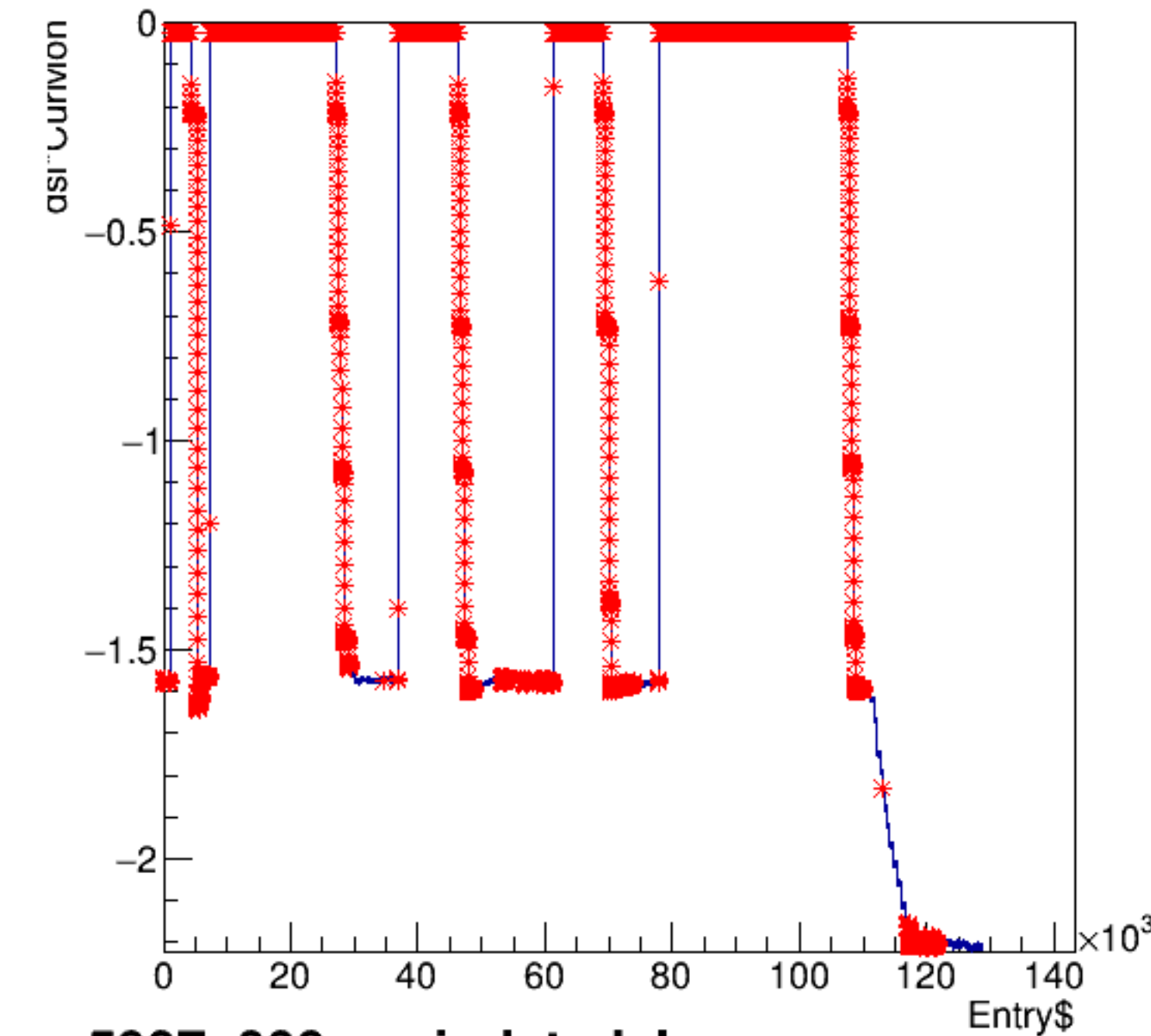


asym\_usl



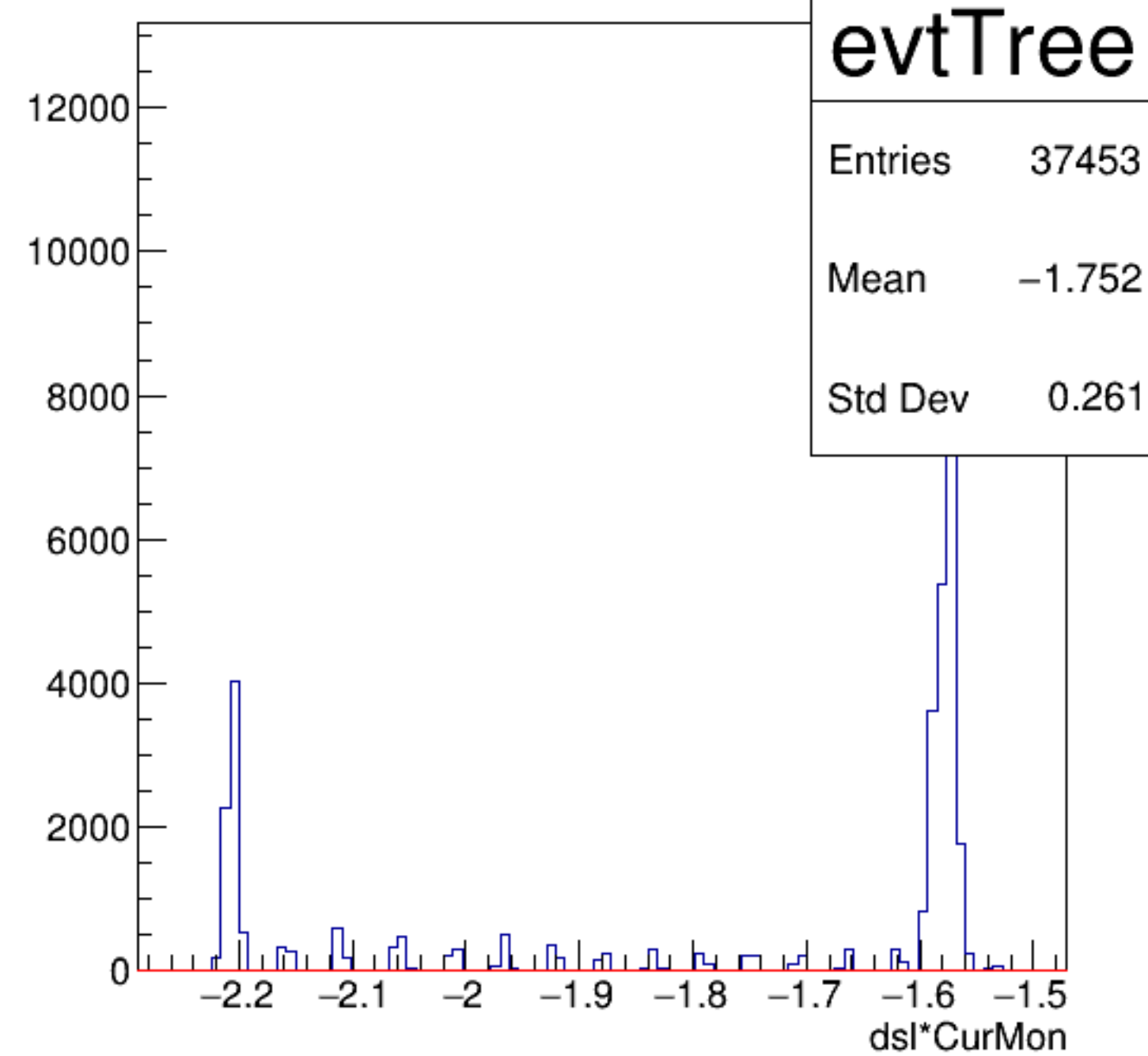
Entries	9310
Mean	-0.7835
Std Dev	1433
Underflow	409
Overflow	406

dsl\*CurMon:Entry\$

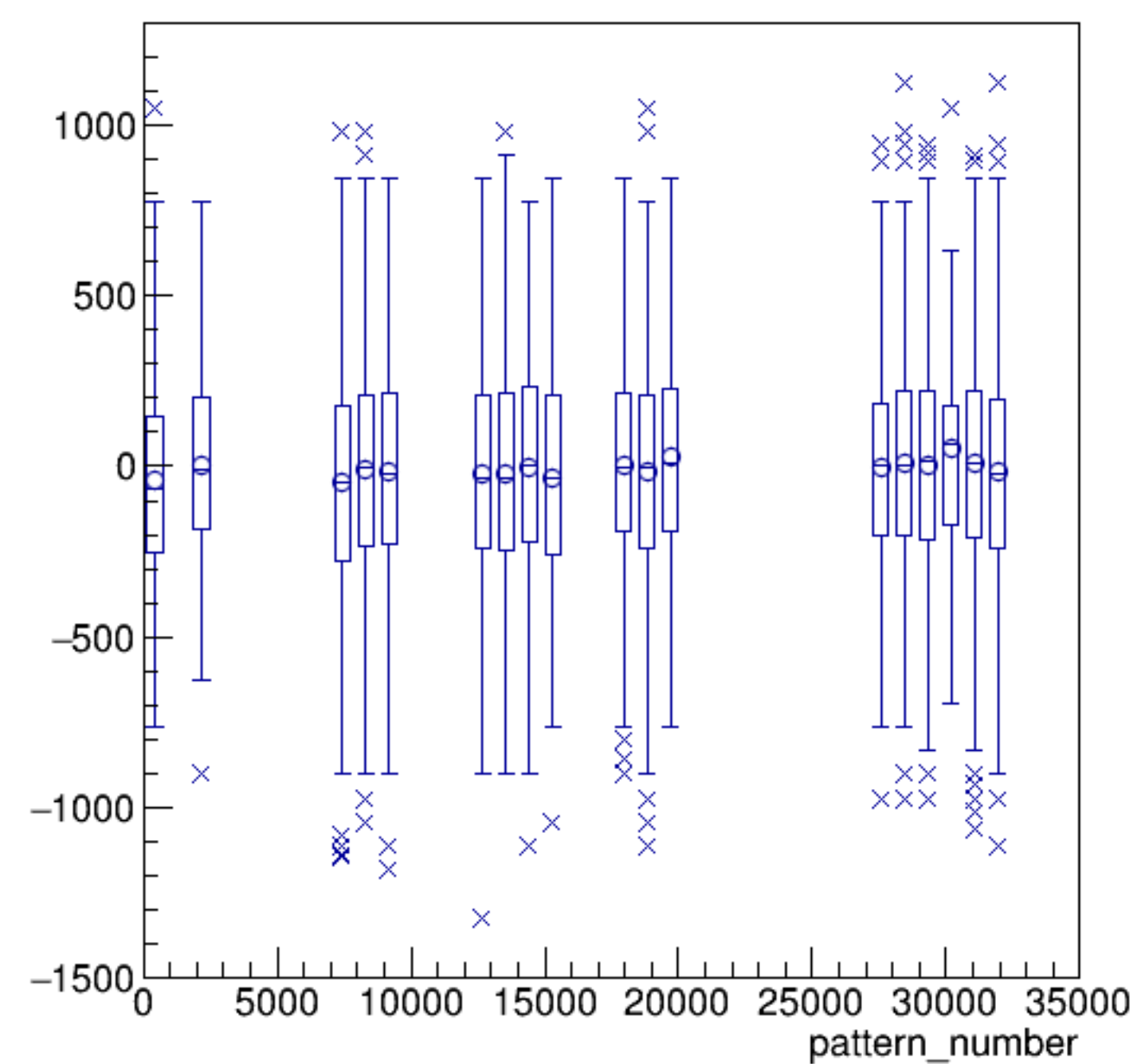


run5907\_000\_maindet\_dsl.png

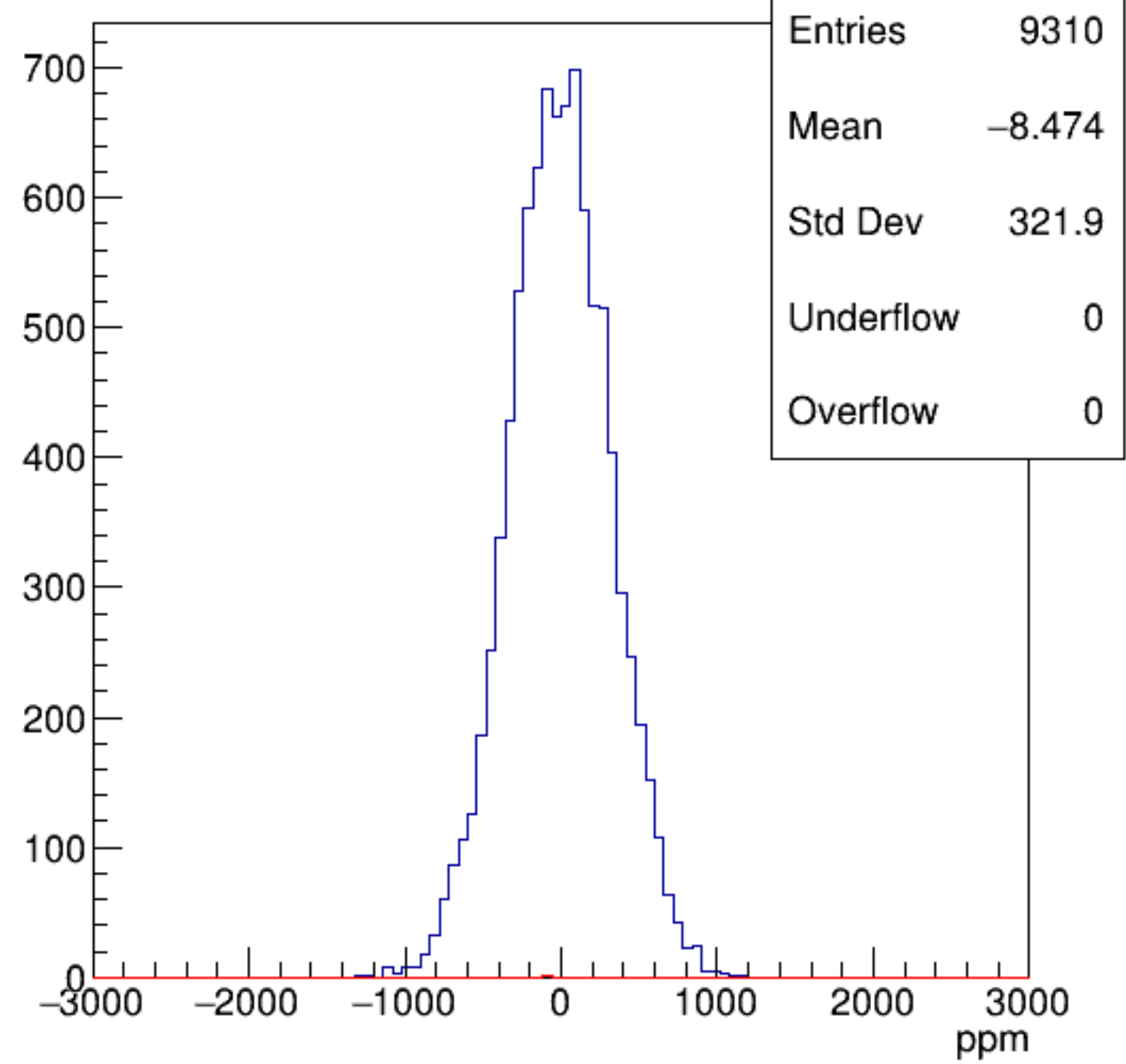
dsl\*CurMon {ErrorFlag==0}



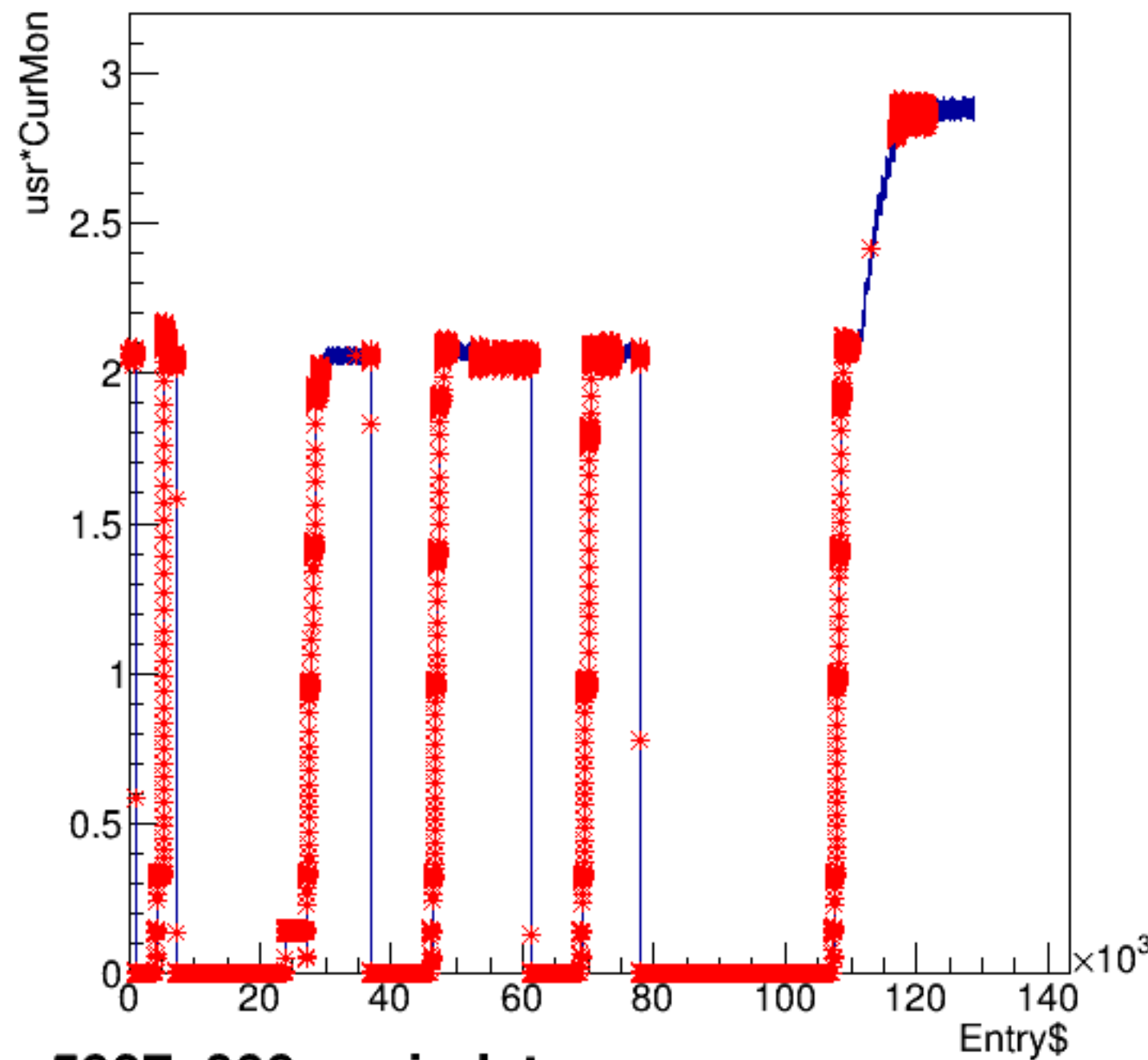
asym\_dsl/ppm:pattern\_number {ErrorFlag==0}



asym\_dsl

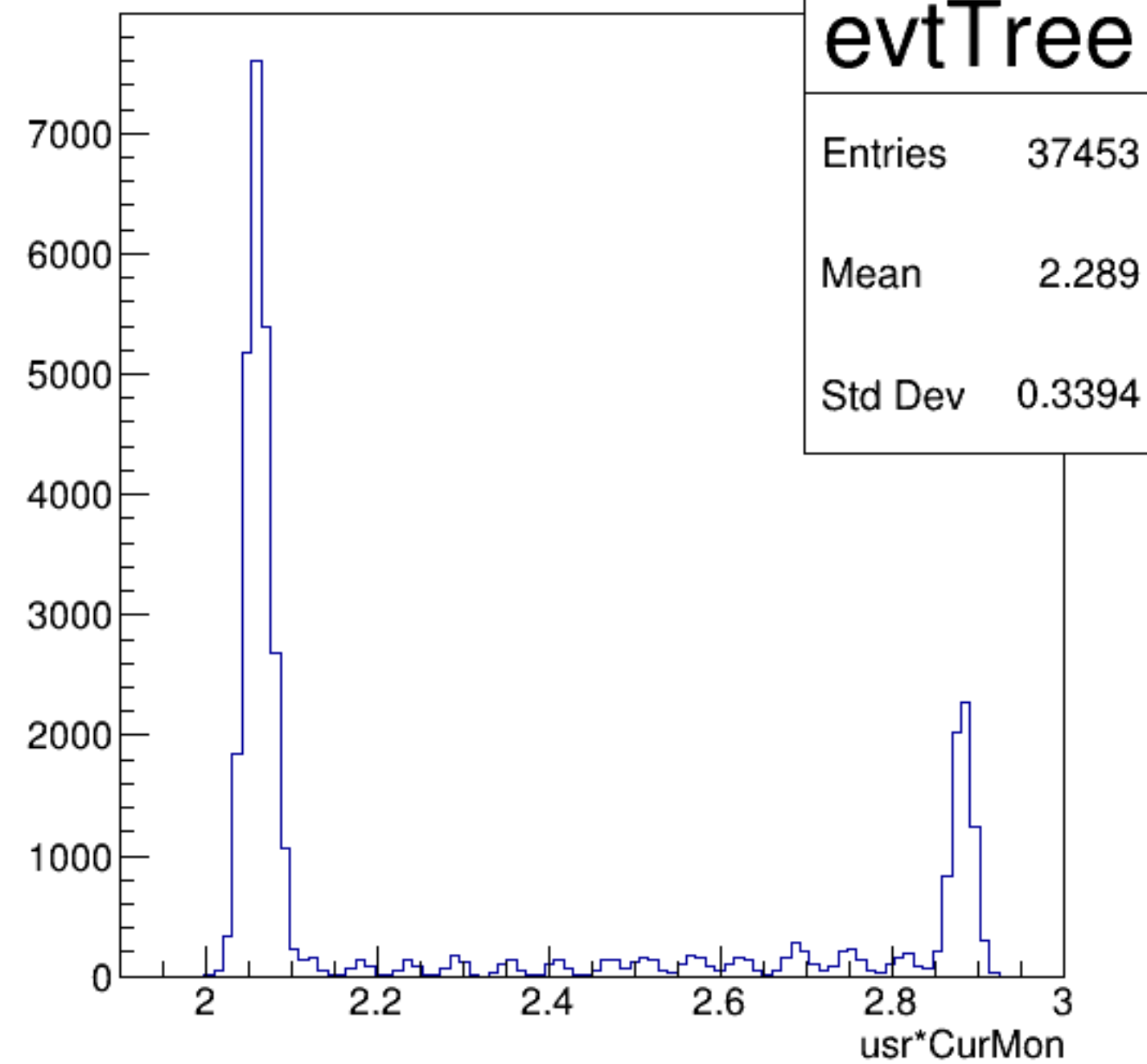


usr\*CurMon:Entry\$

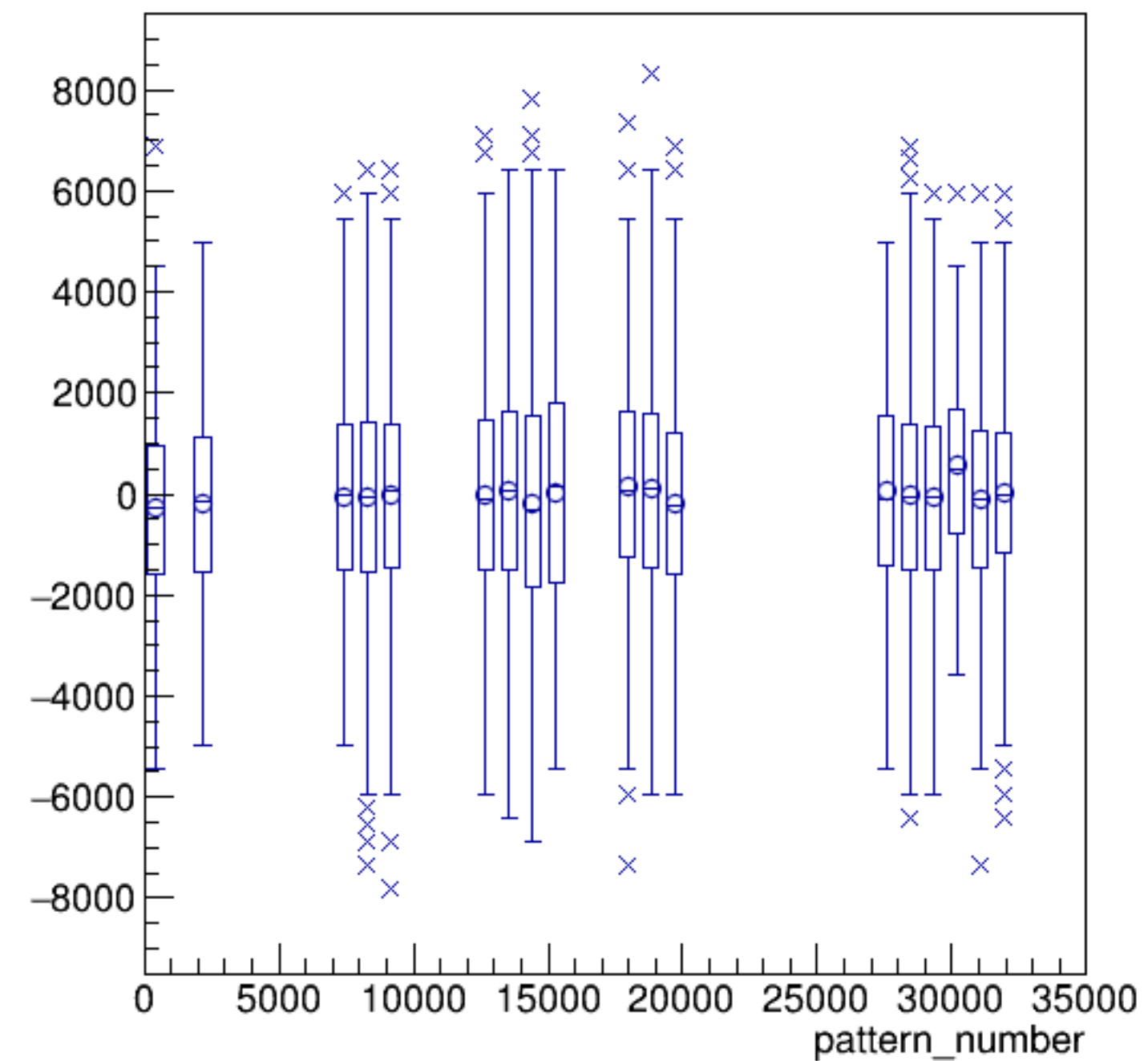


run5907\_000\_maindet\_usr.png

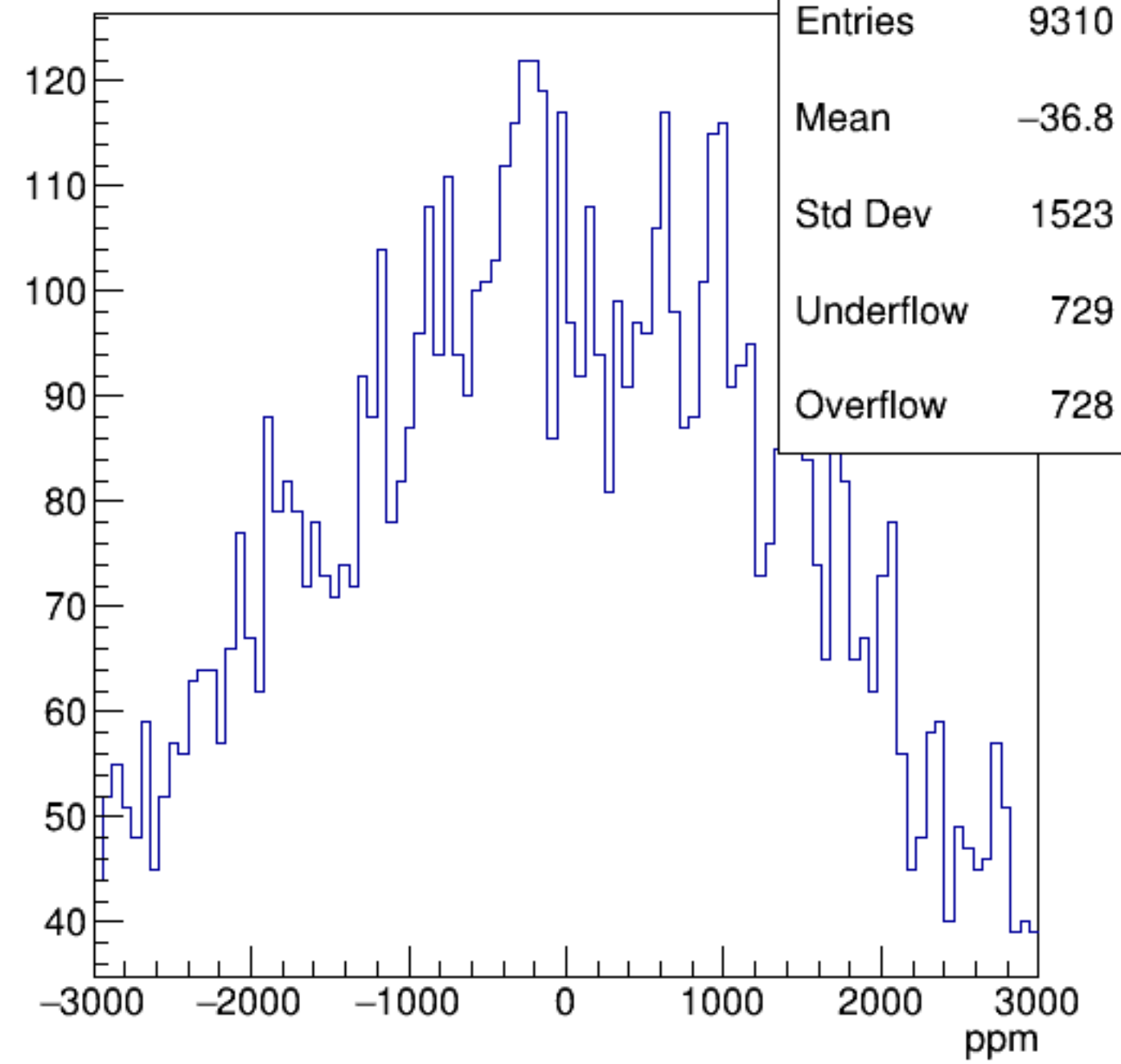
usr\*CurMon {ErrorFlag==0}



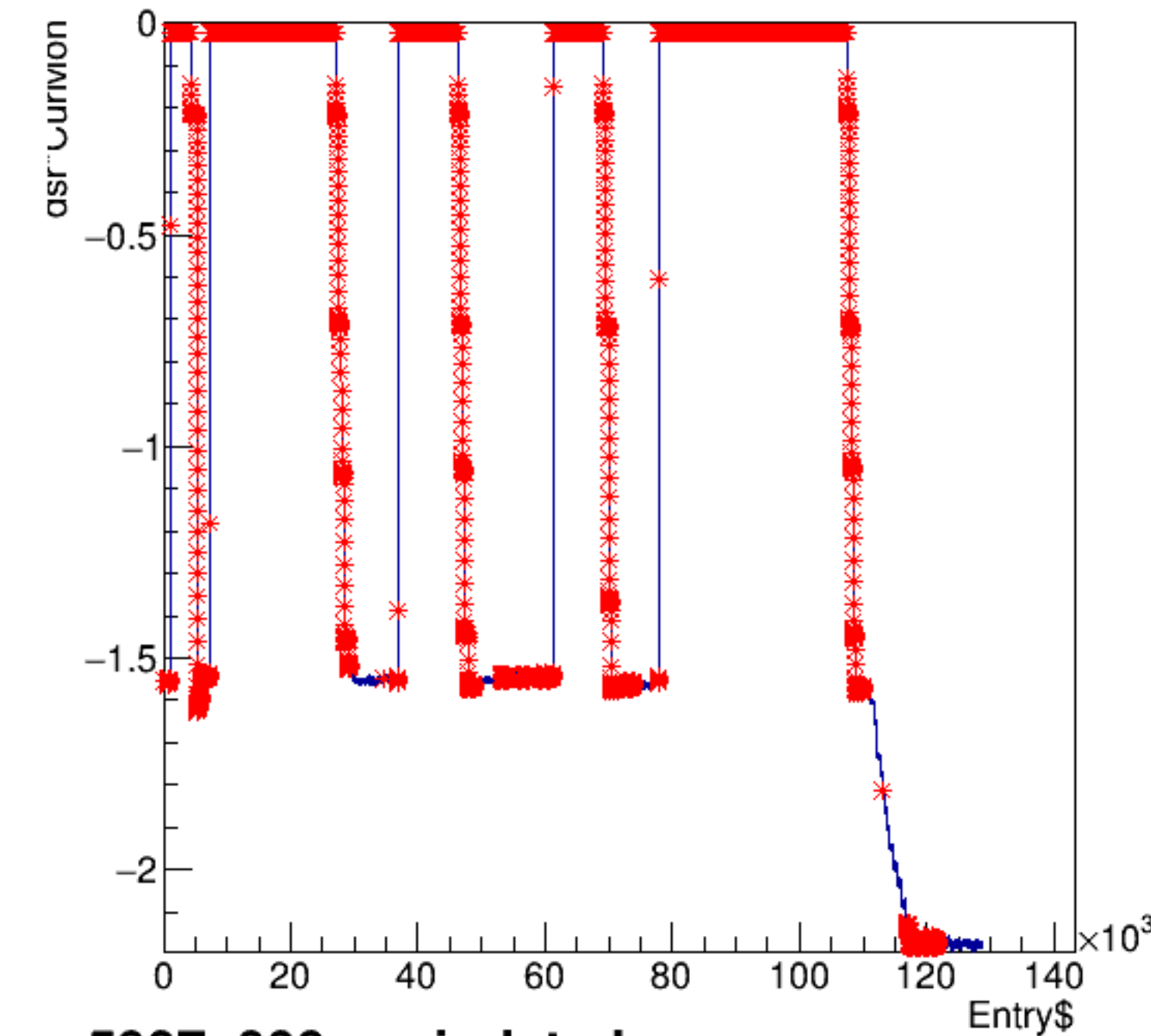
asym\_usr/ppm:pattern\_number {ErrorFlag==0}



asym\_usr

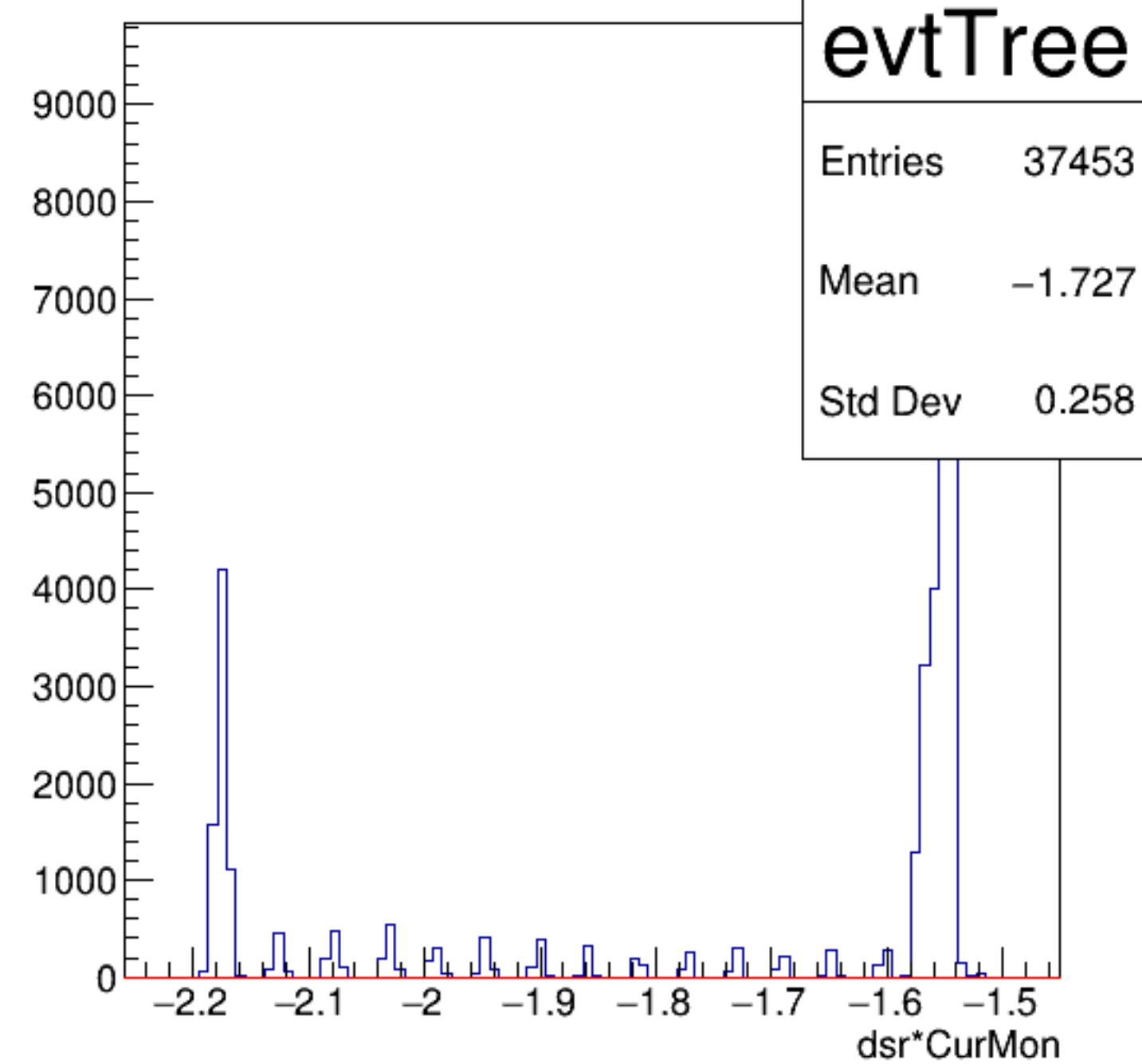


dsr\*CurMon:Entry\$

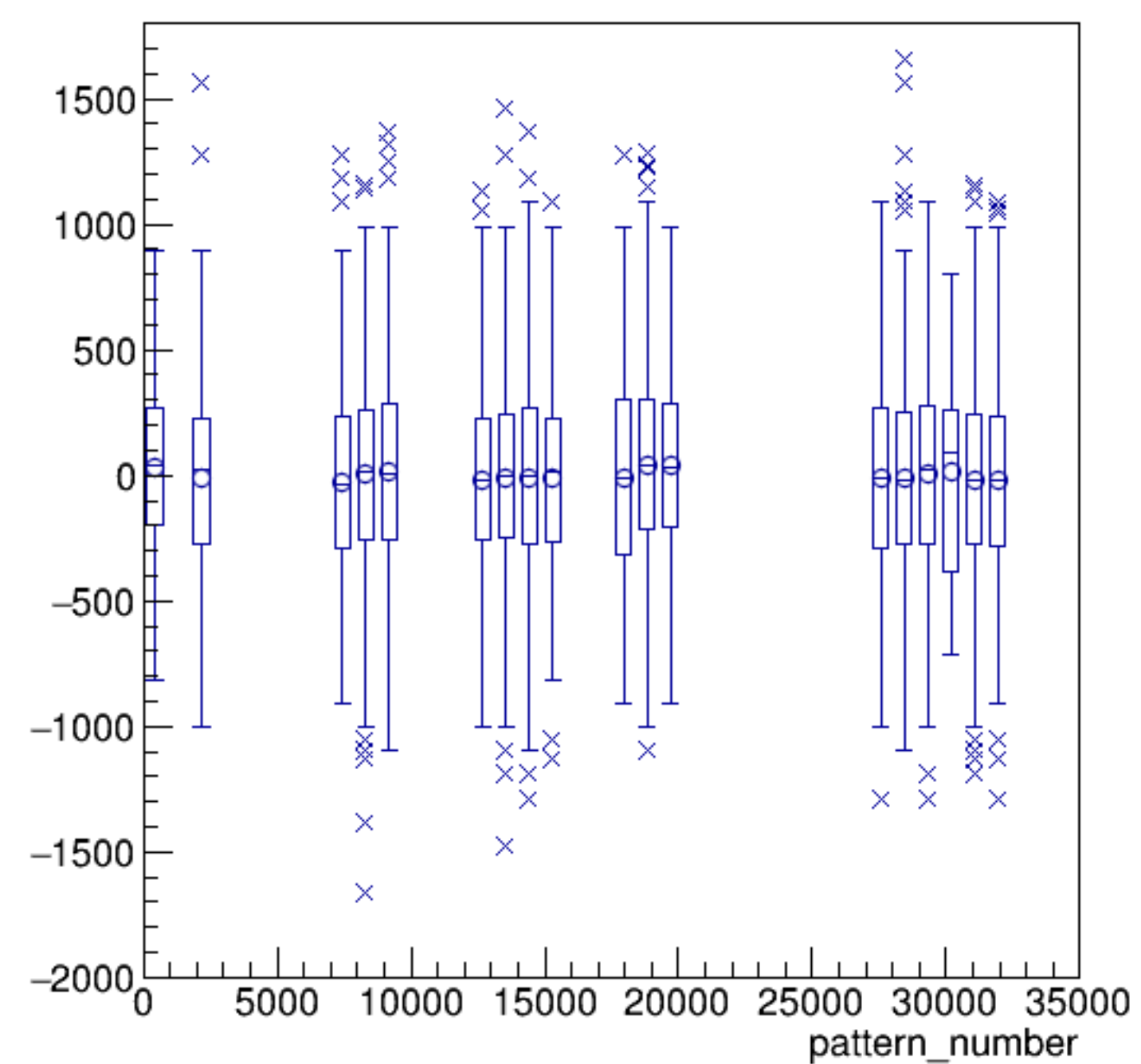


run5907\_000\_maindet\_dsr.png

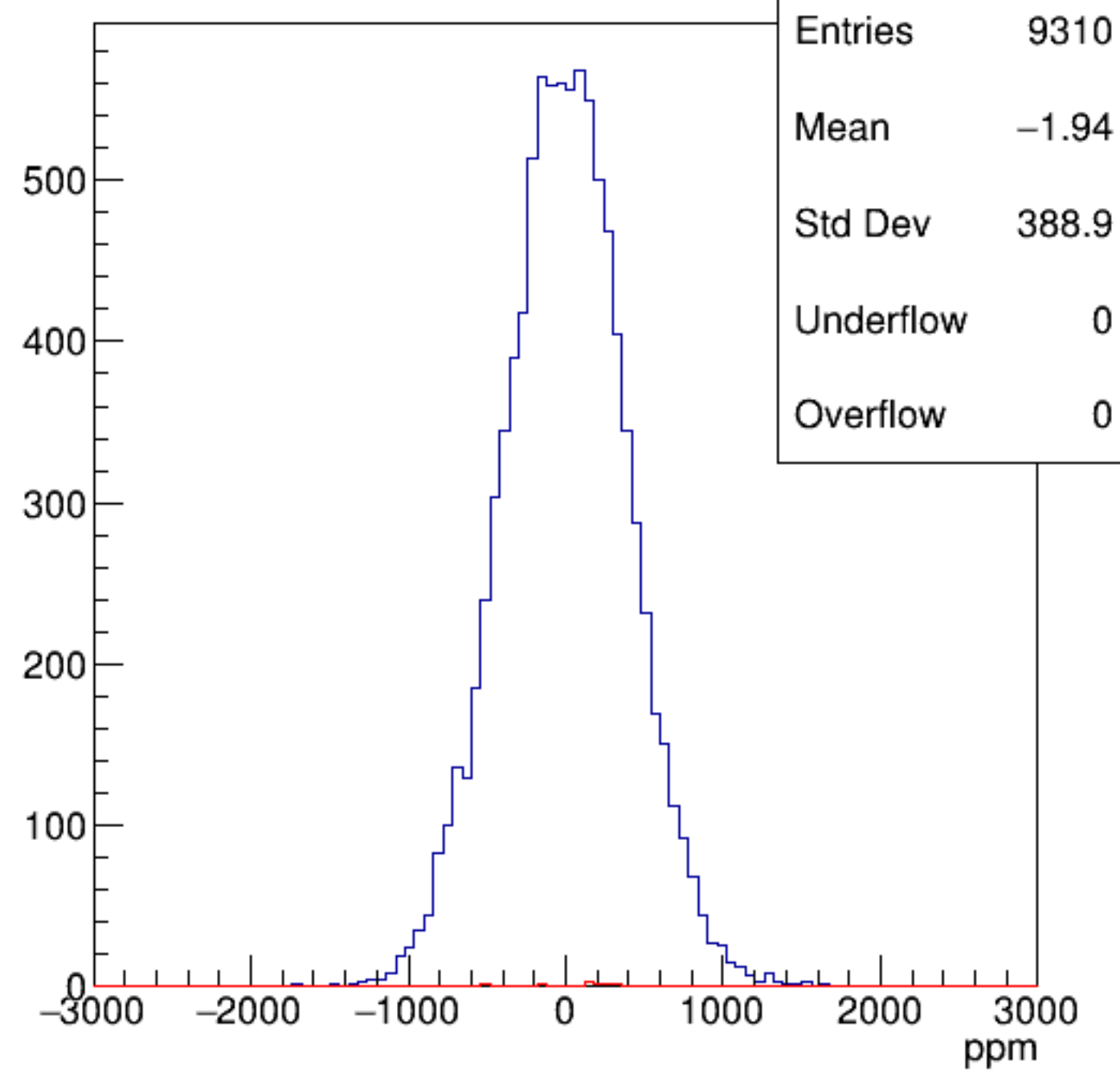
dsr\*CurMon {ErrorFlag==0}



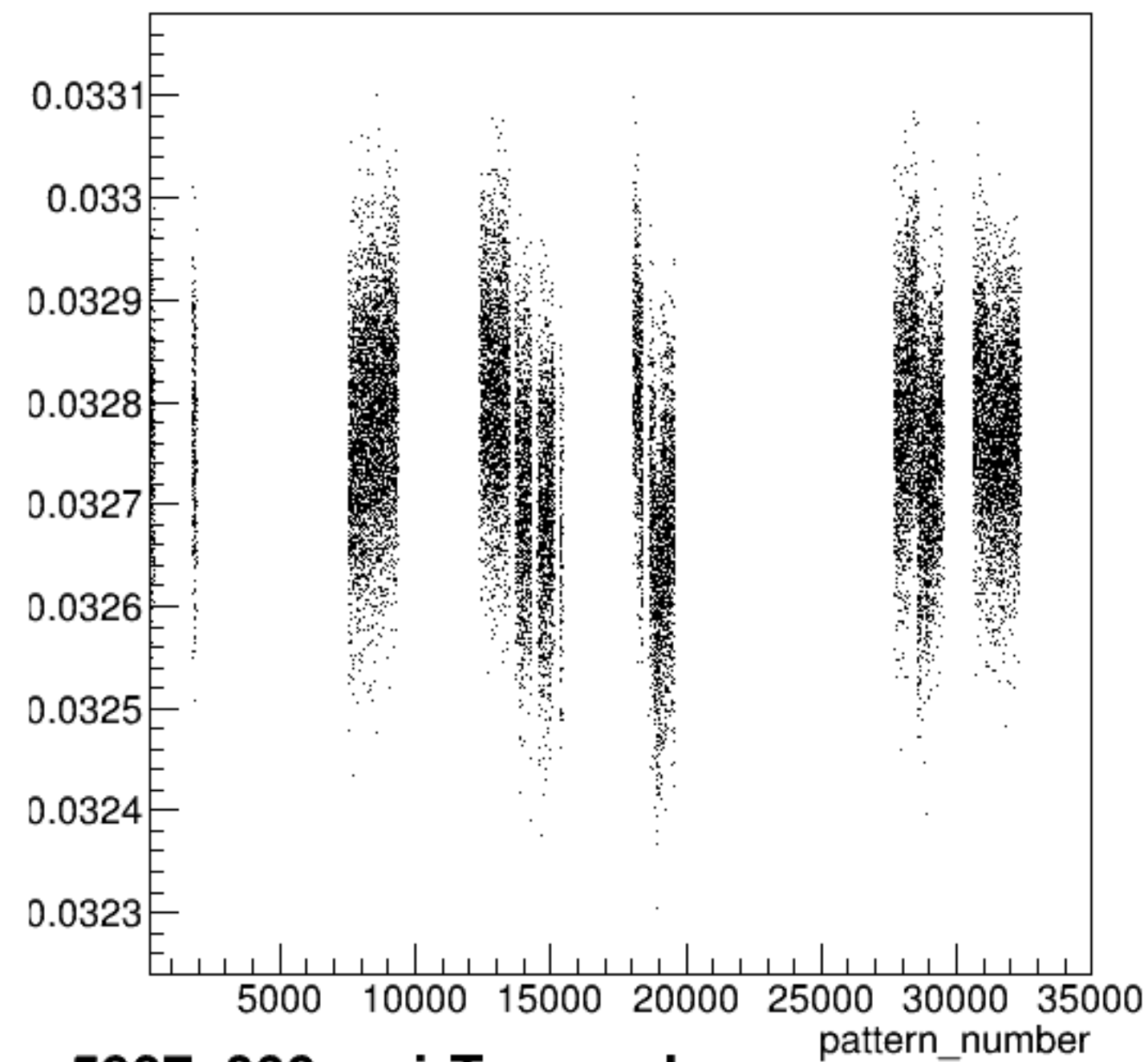
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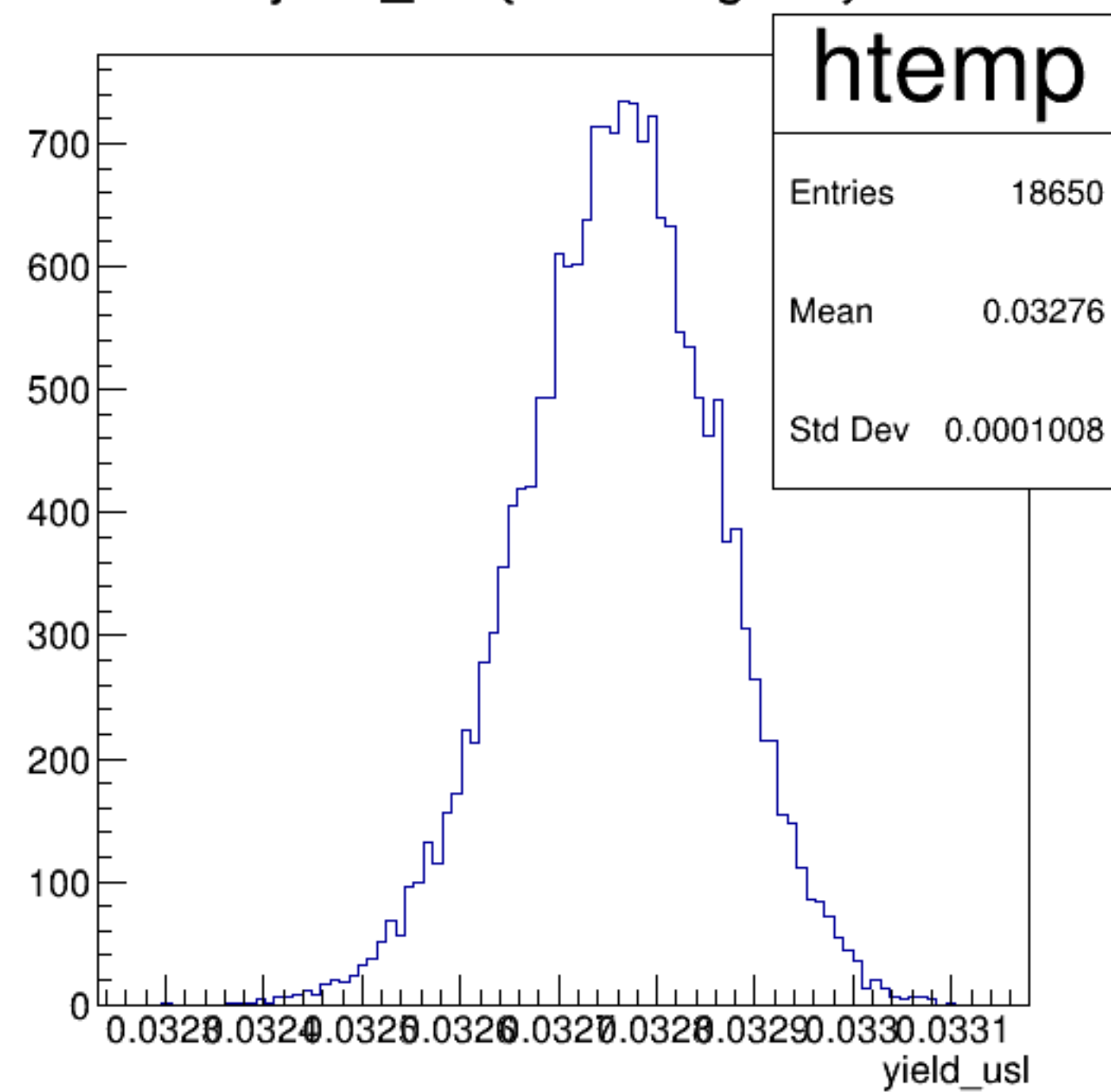
asym\_dsr



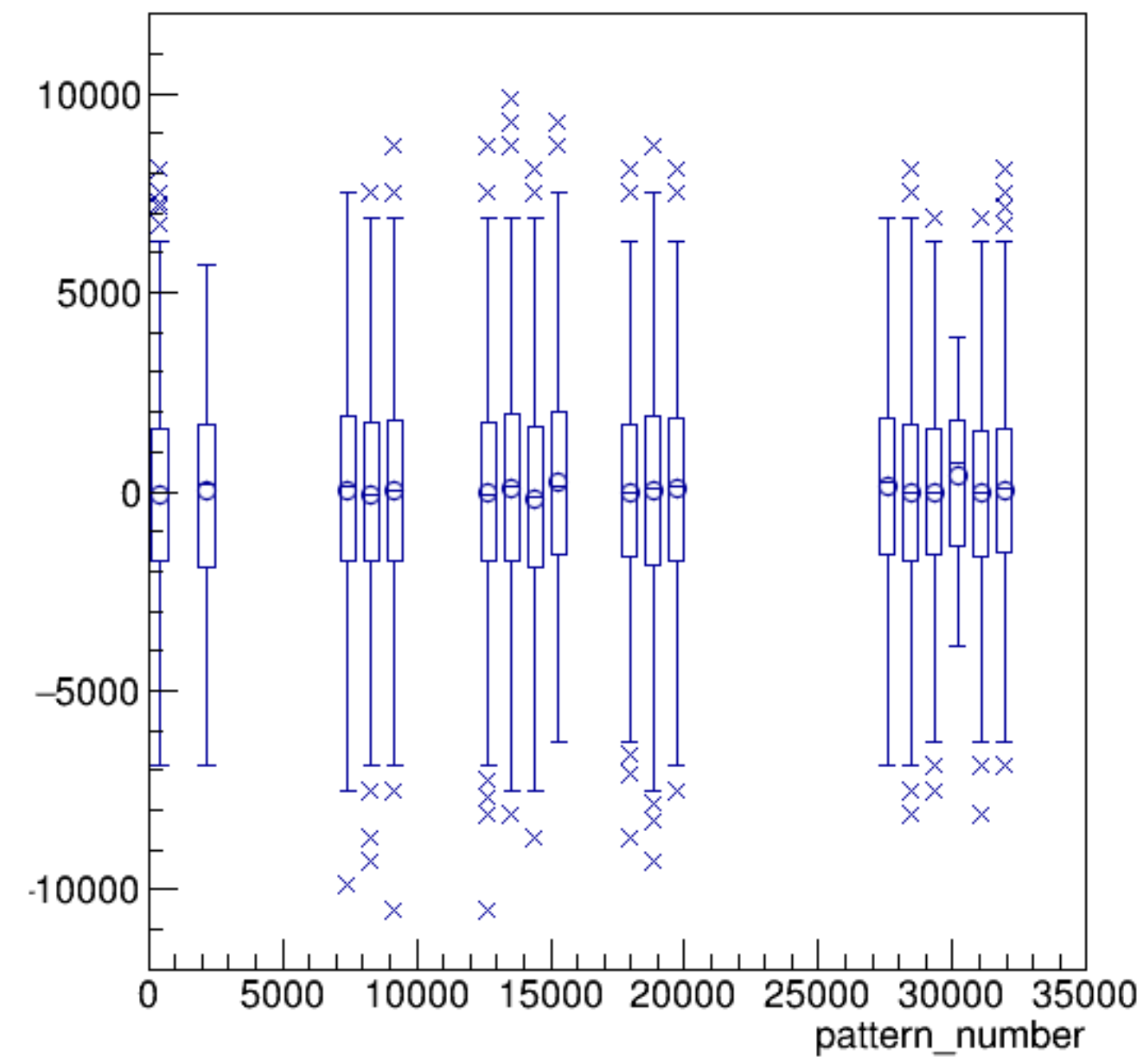
yield\_usl:pattern\_number {ErrorFlag==0}



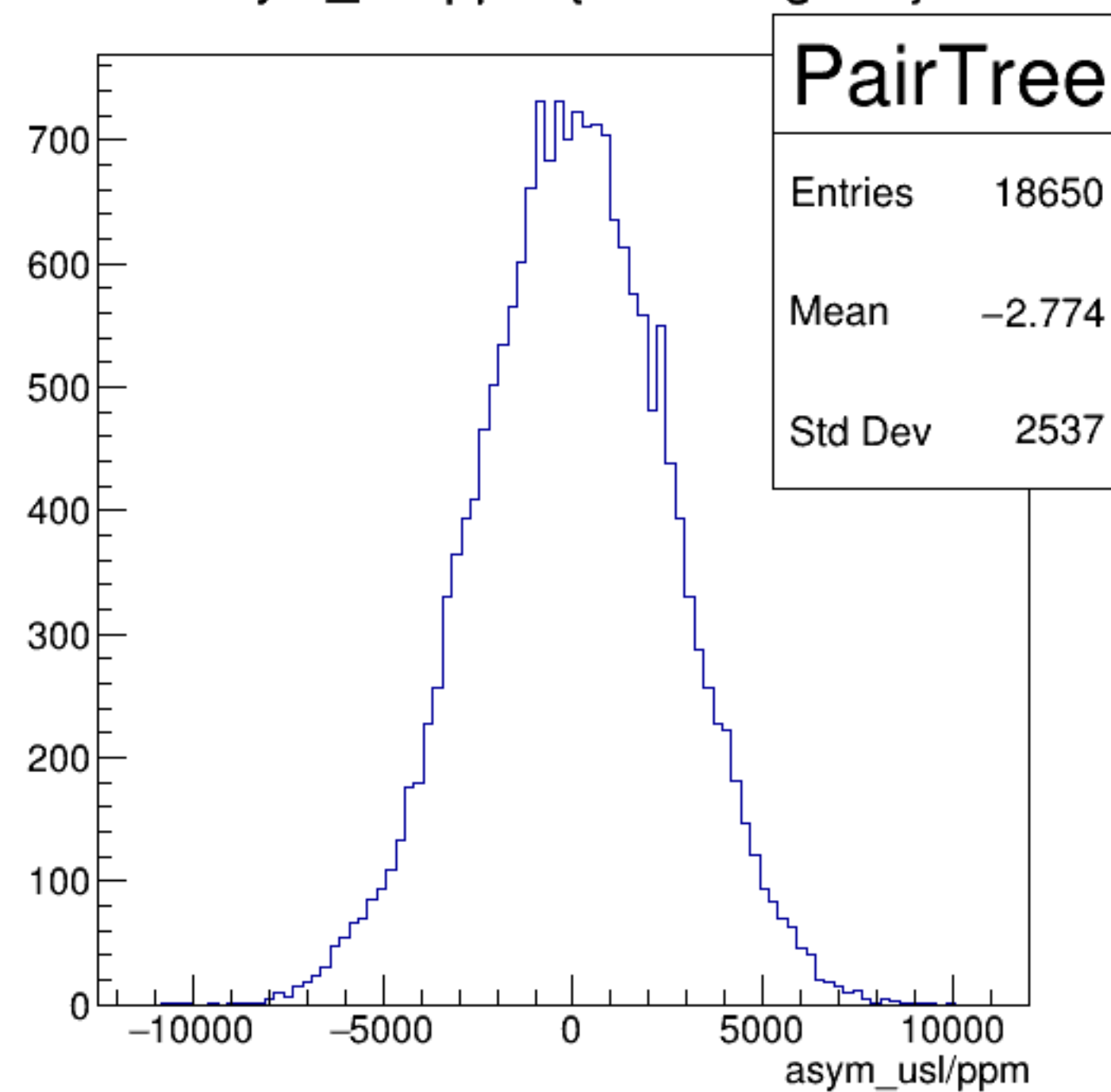
yield\_usl {ErrorFlag==0}



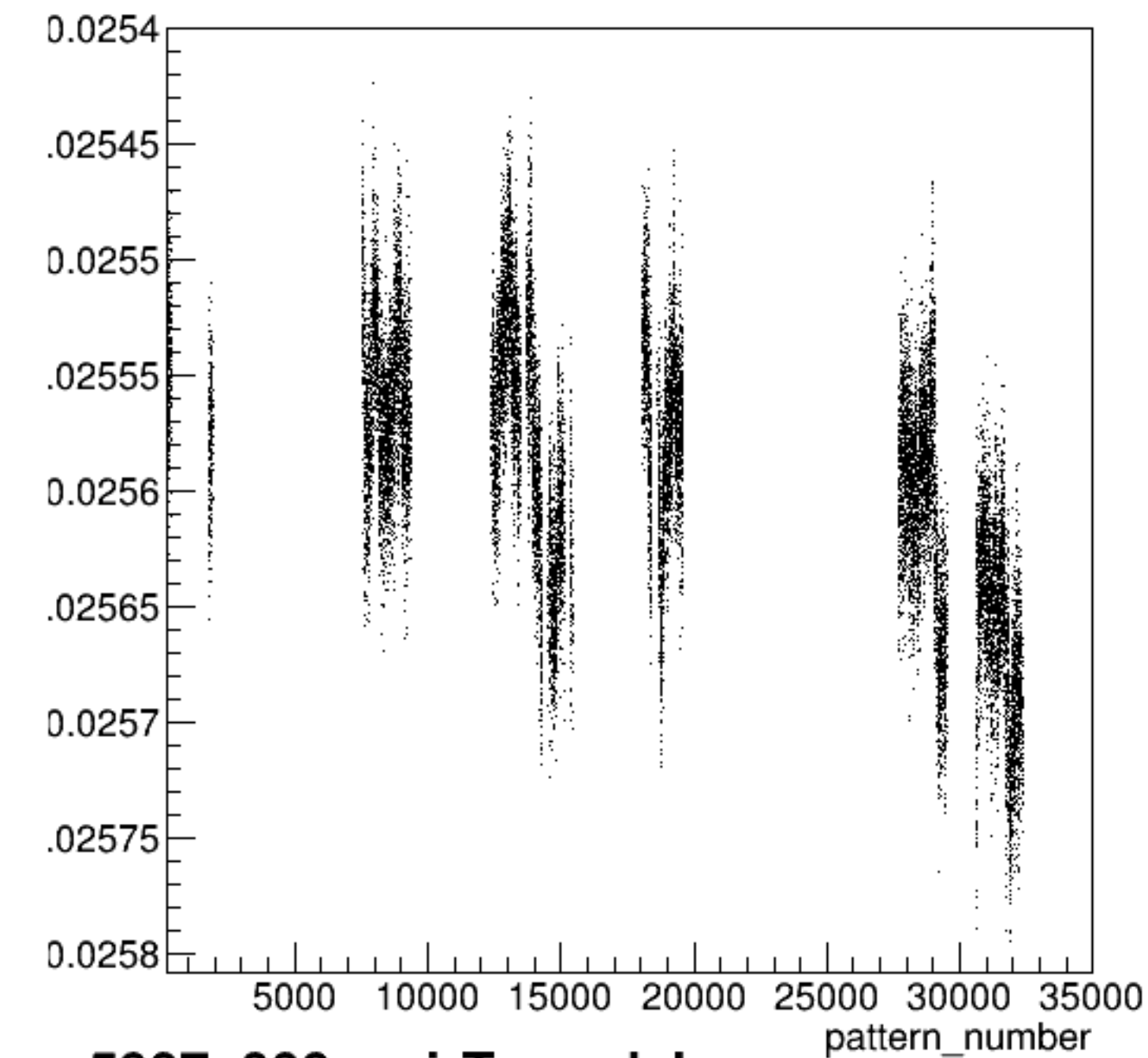
asym\_usl/ppm:pattern\_number {ErrorFlag==0}



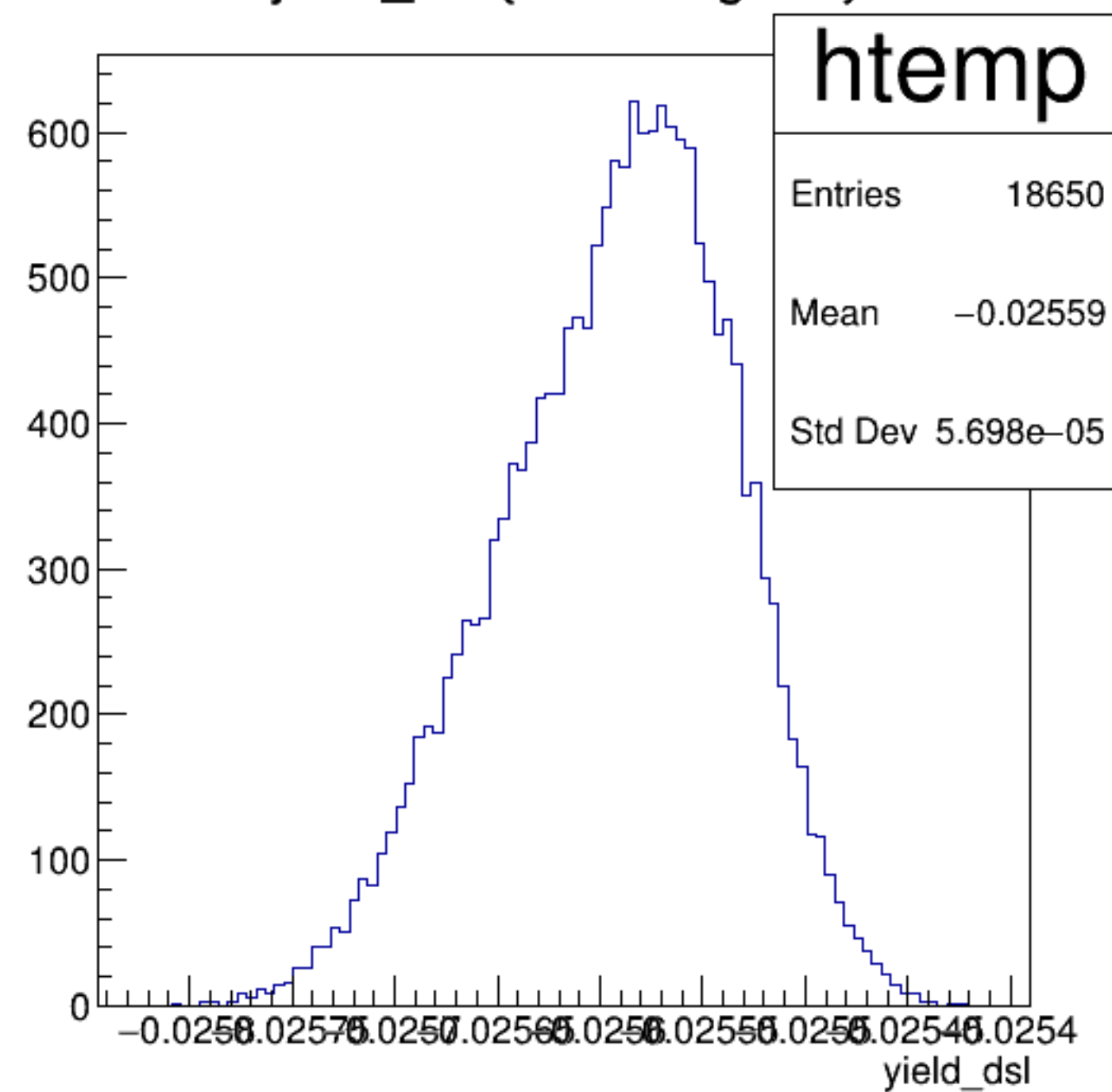
asym\_usl/ppm {ErrorFlag==0}



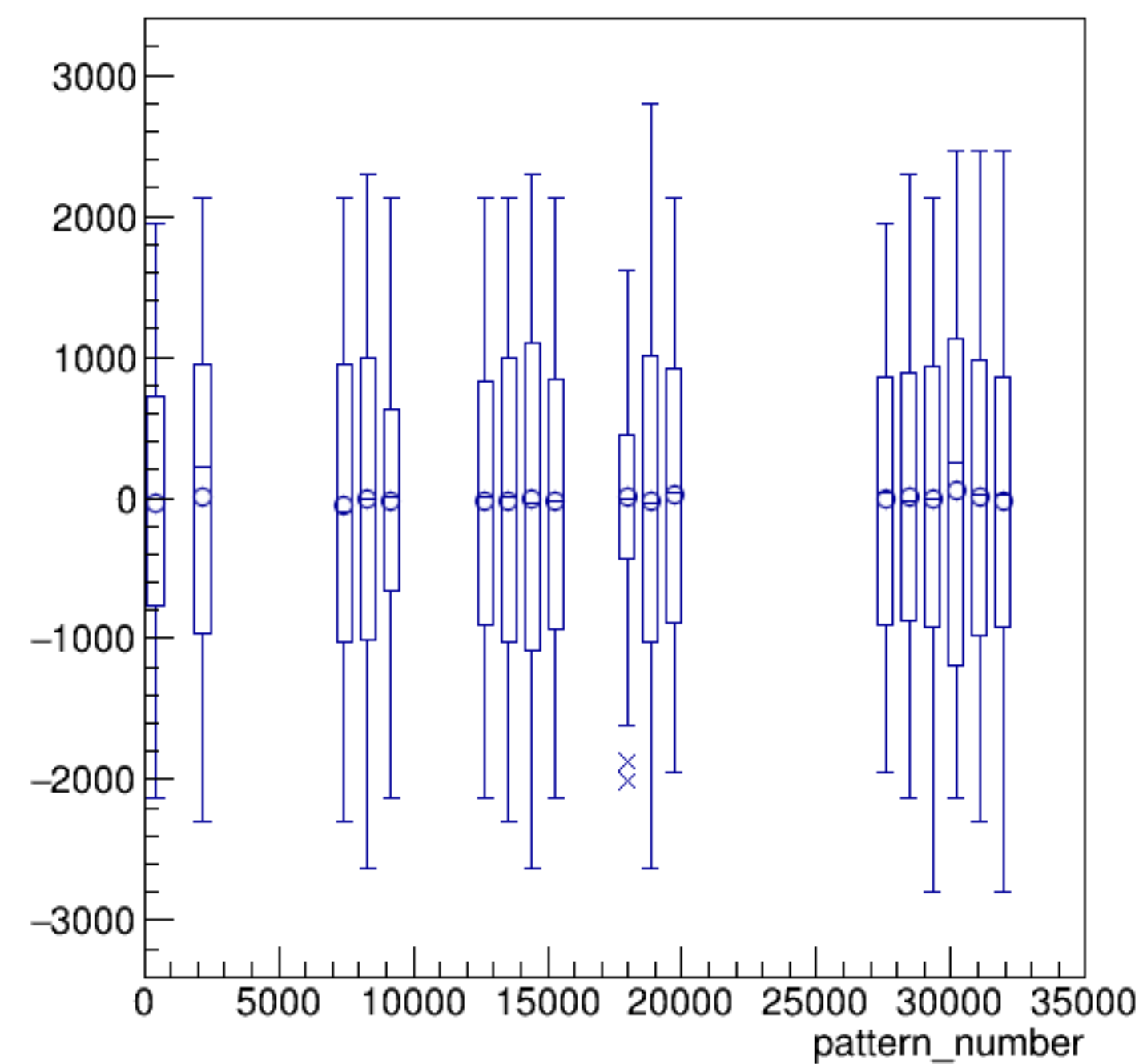
yield\_dsl:pattern\_number {ErrorFlag==0}



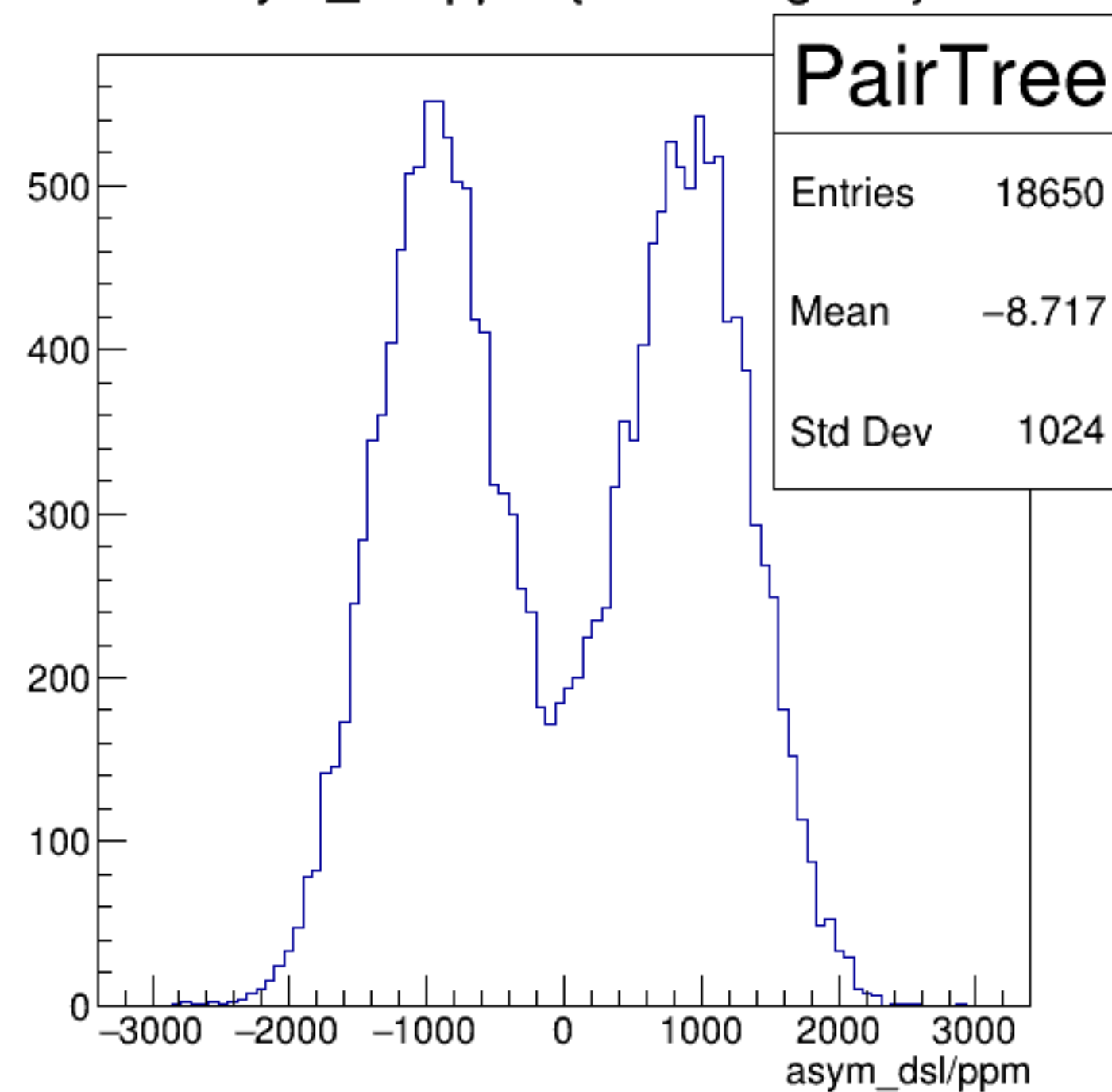
yield\_dsl {ErrorFlag==0}



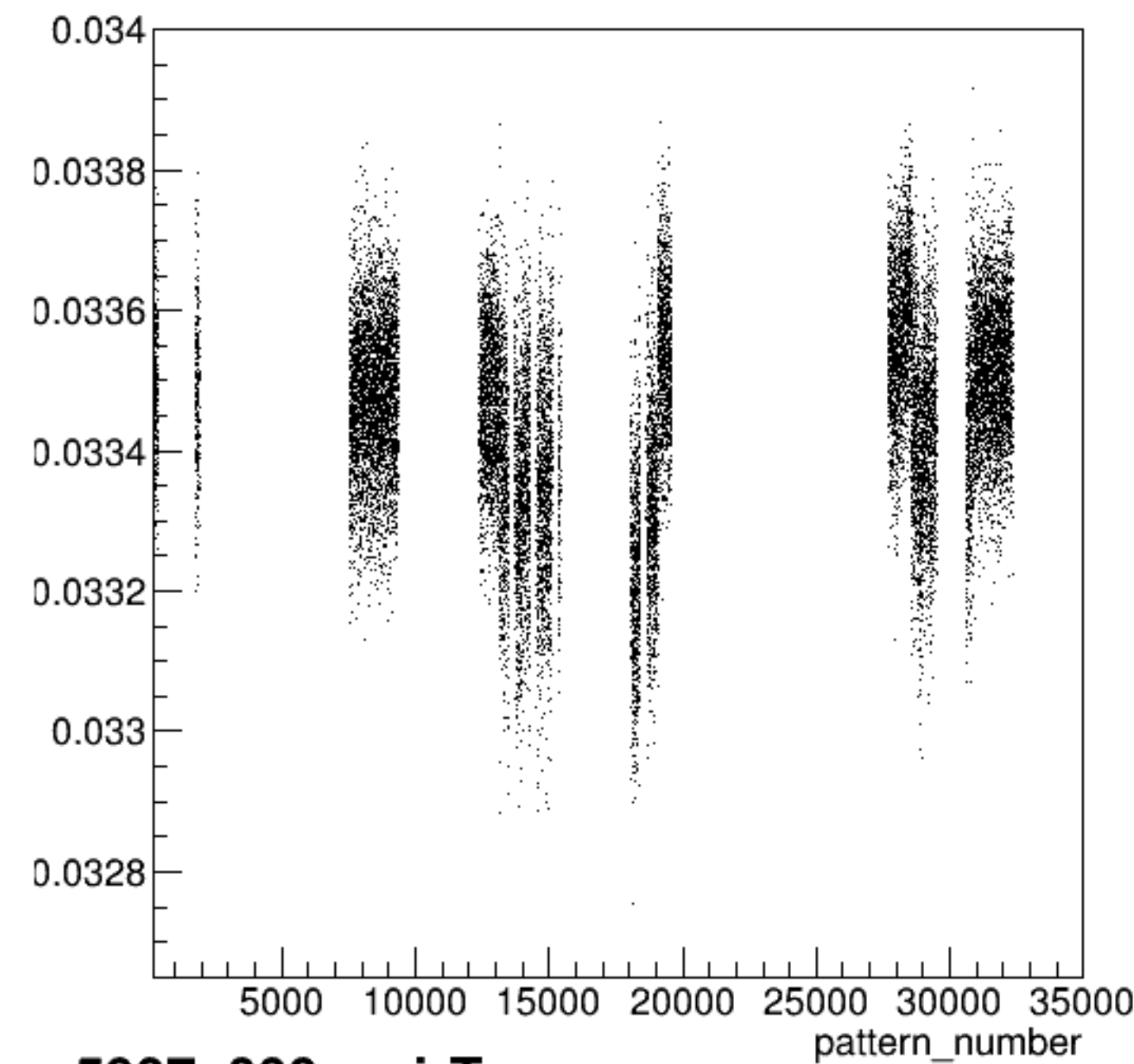
asym\_dsl/ppm:pattern\_number {ErrorFlag==0}



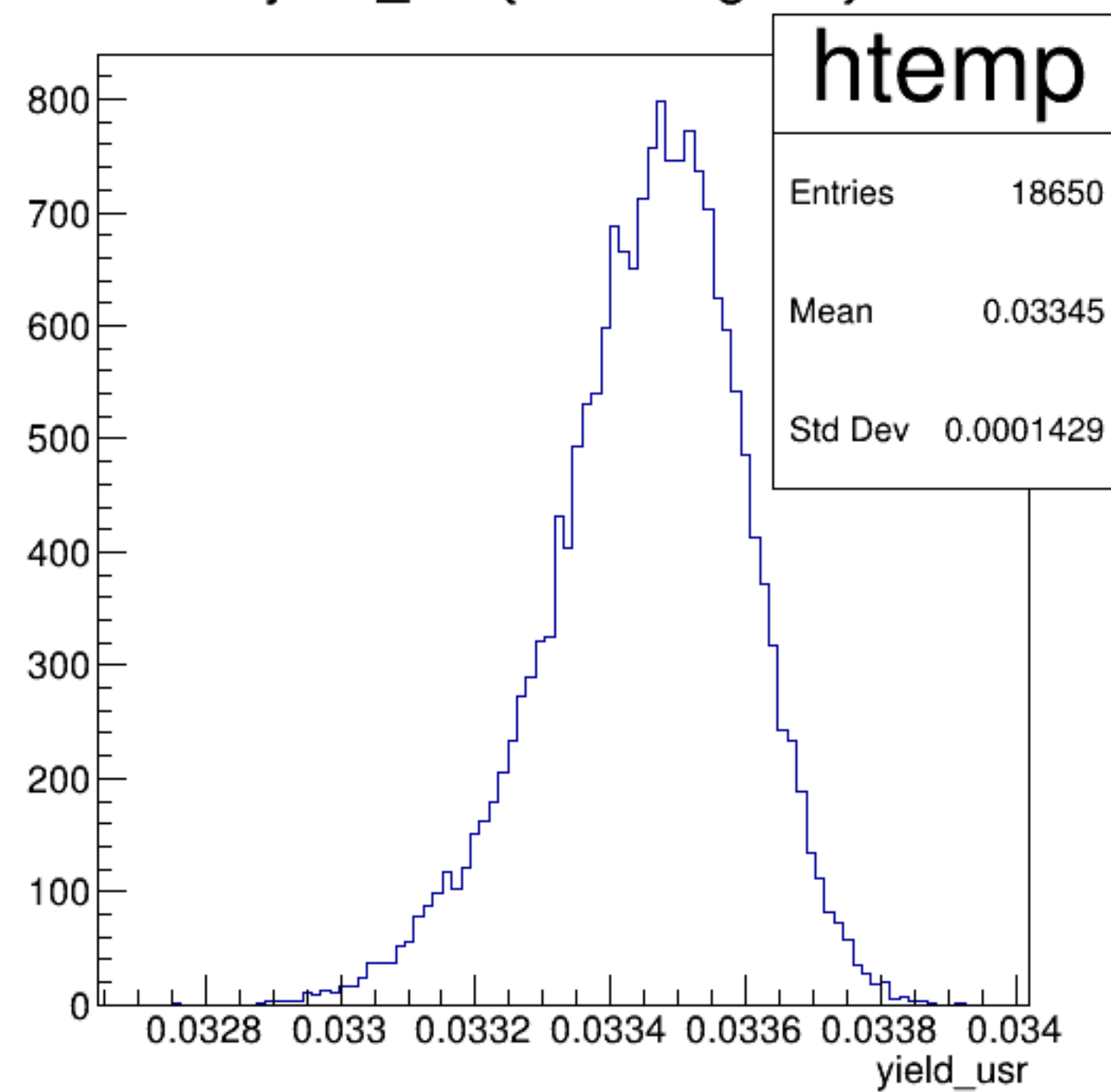
asym\_dsl/ppm {ErrorFlag==0}



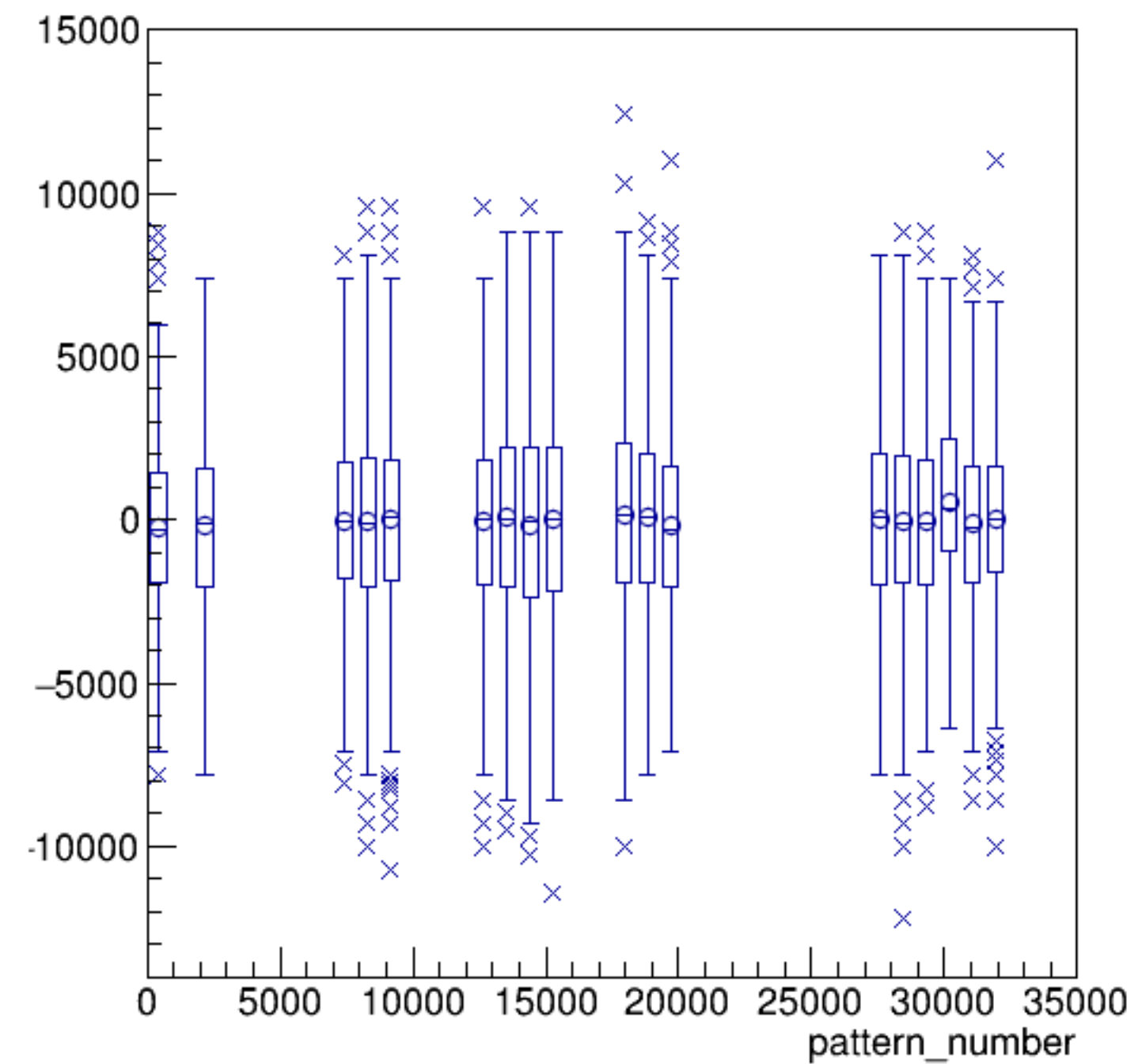
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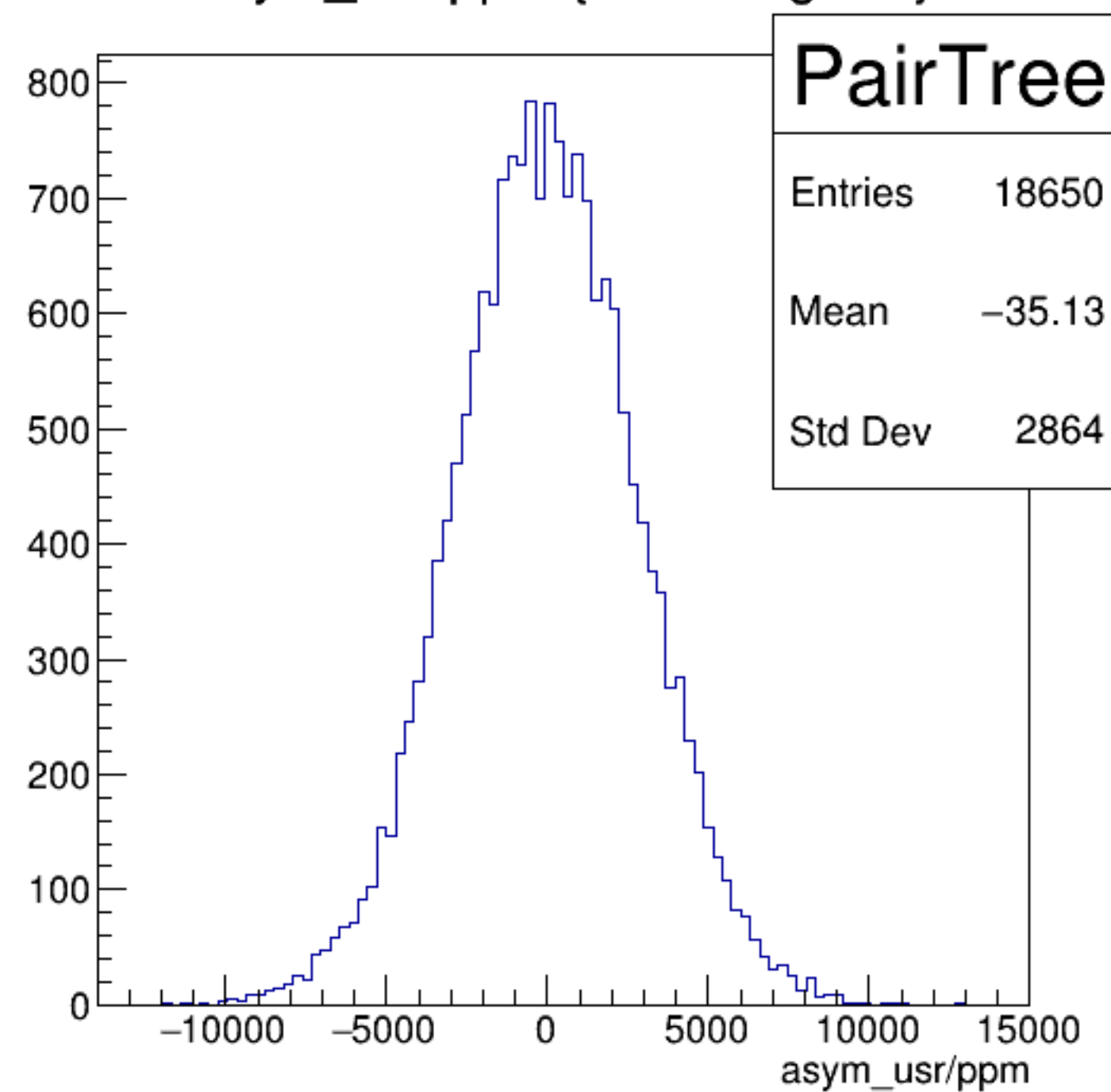
yield\_usr {ErrorFlag==0}



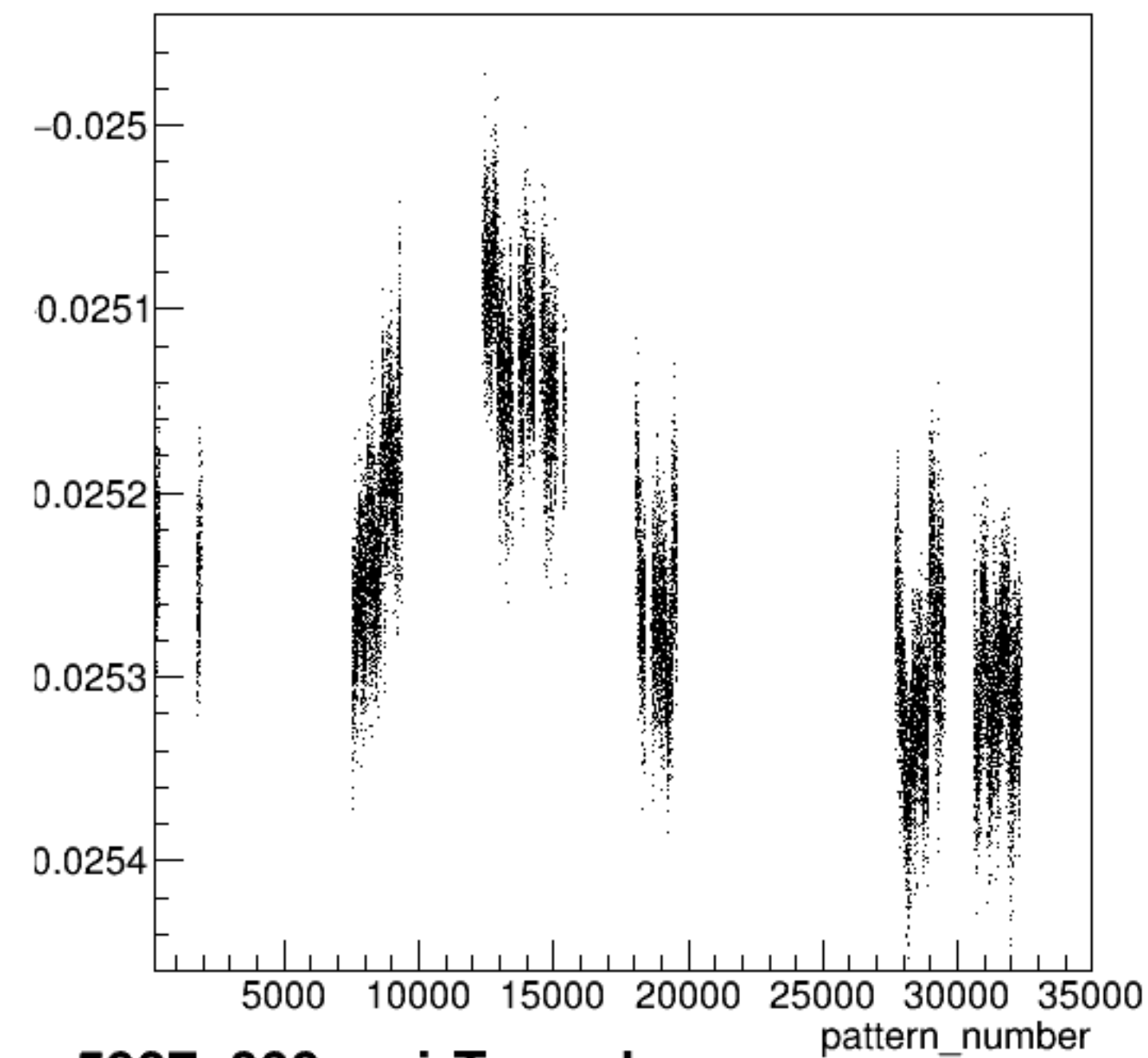
asym\_usr/ppm:pattern\_number {ErrorFlag==0}



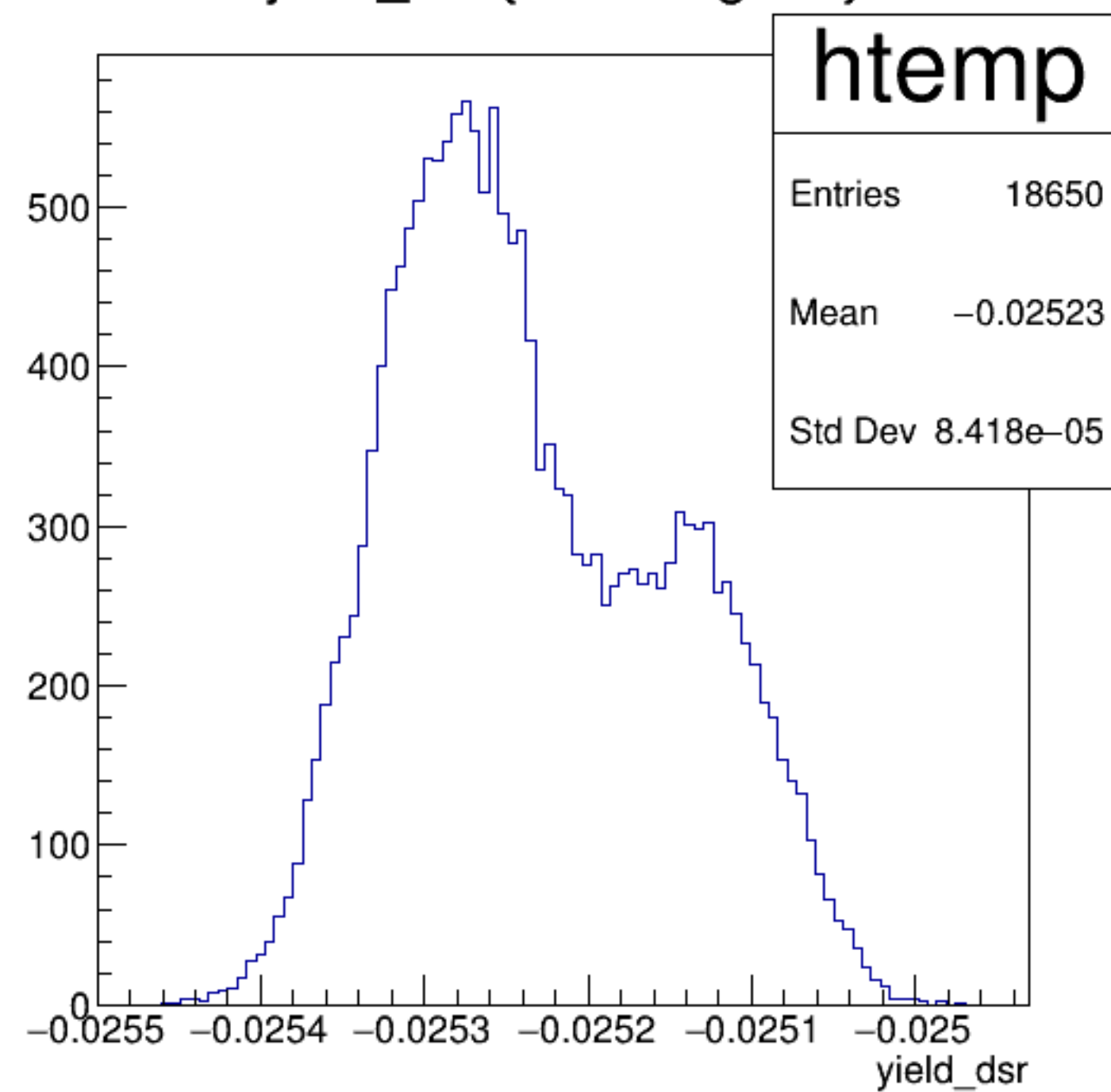
asym\_usr/ppm {ErrorFlag==0}



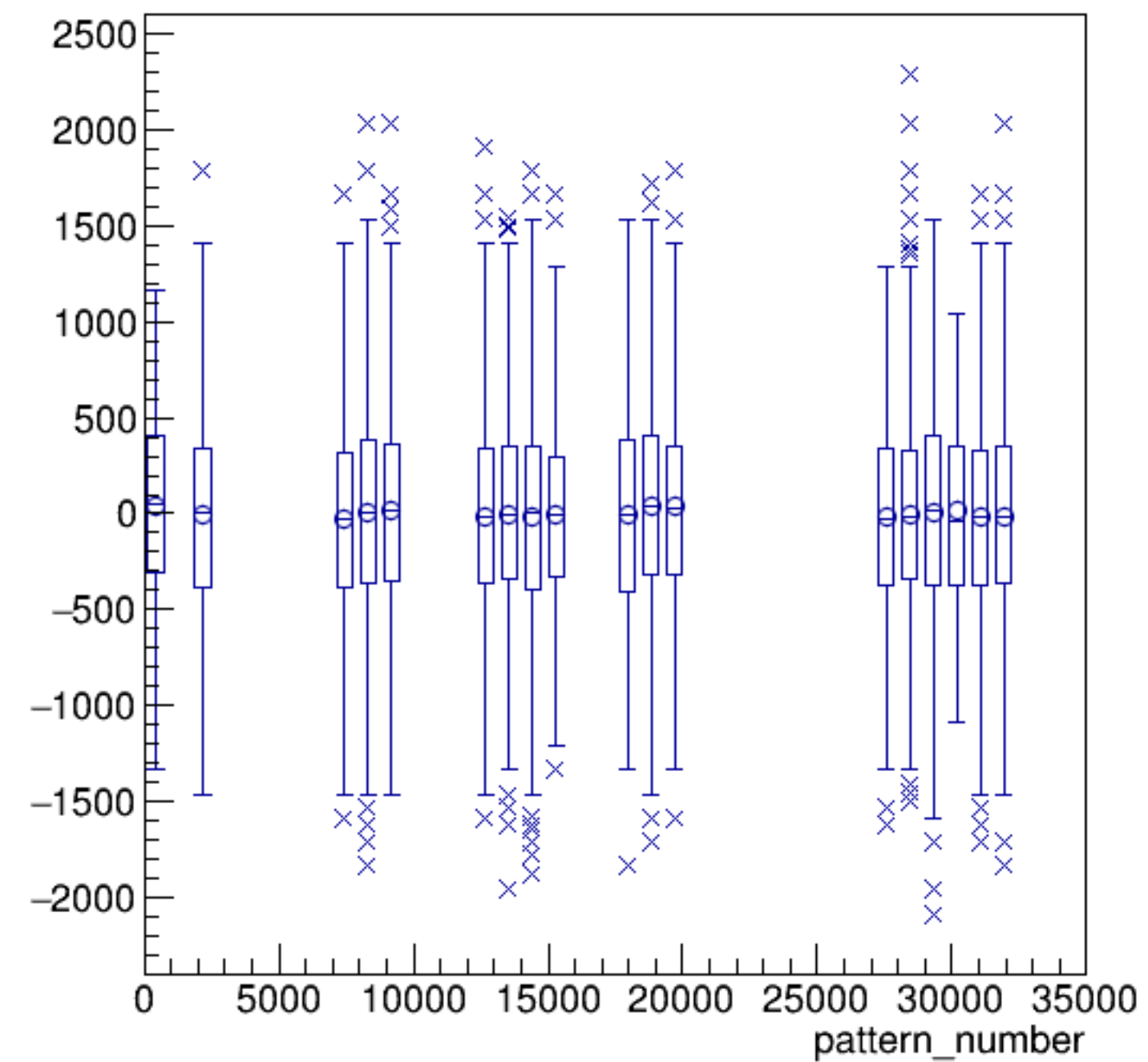
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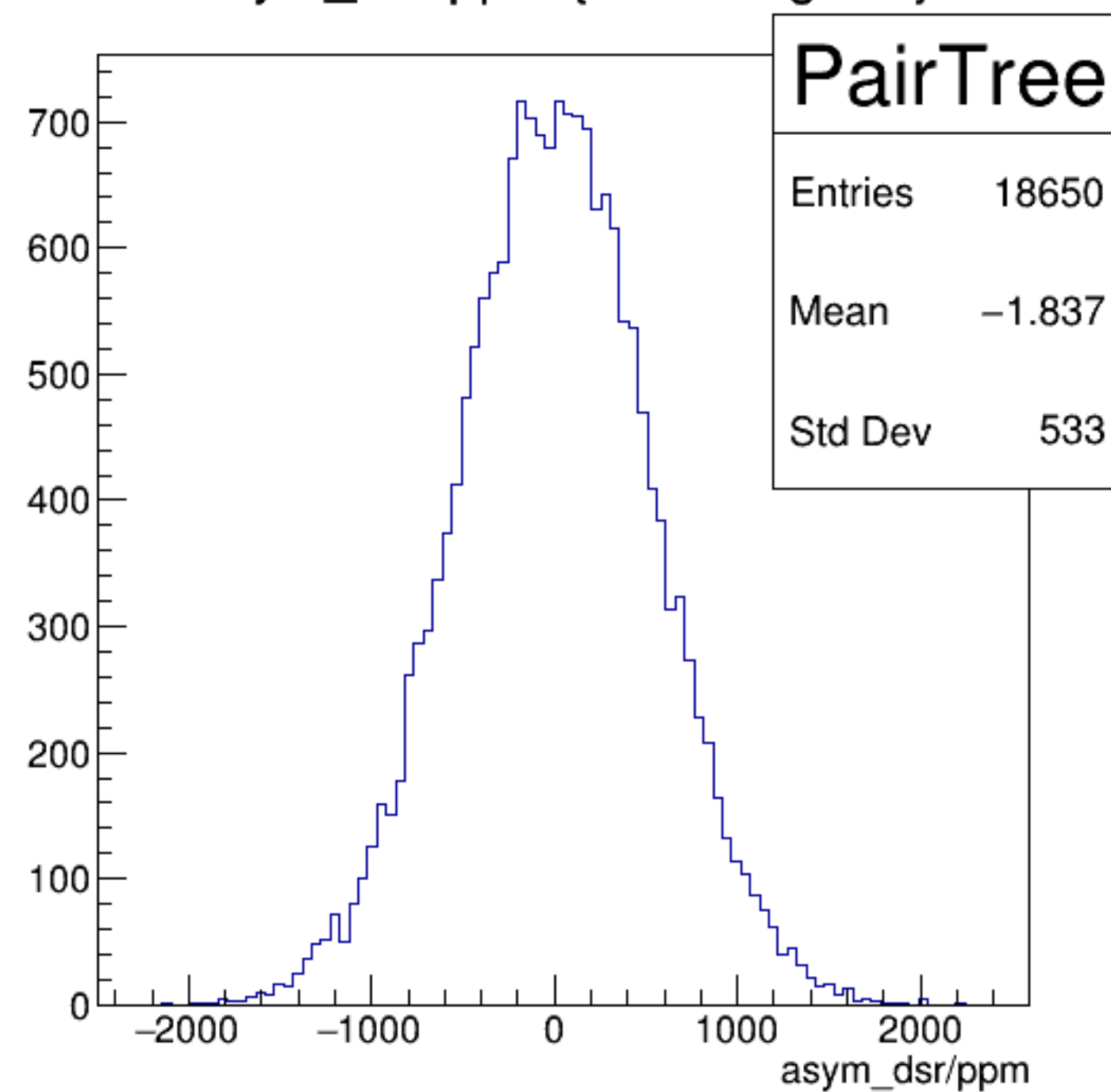
yield\_dsr {ErrorFlag==0}



asym\_dsr/ppm:pattern\_number {ErrorFlag==0}



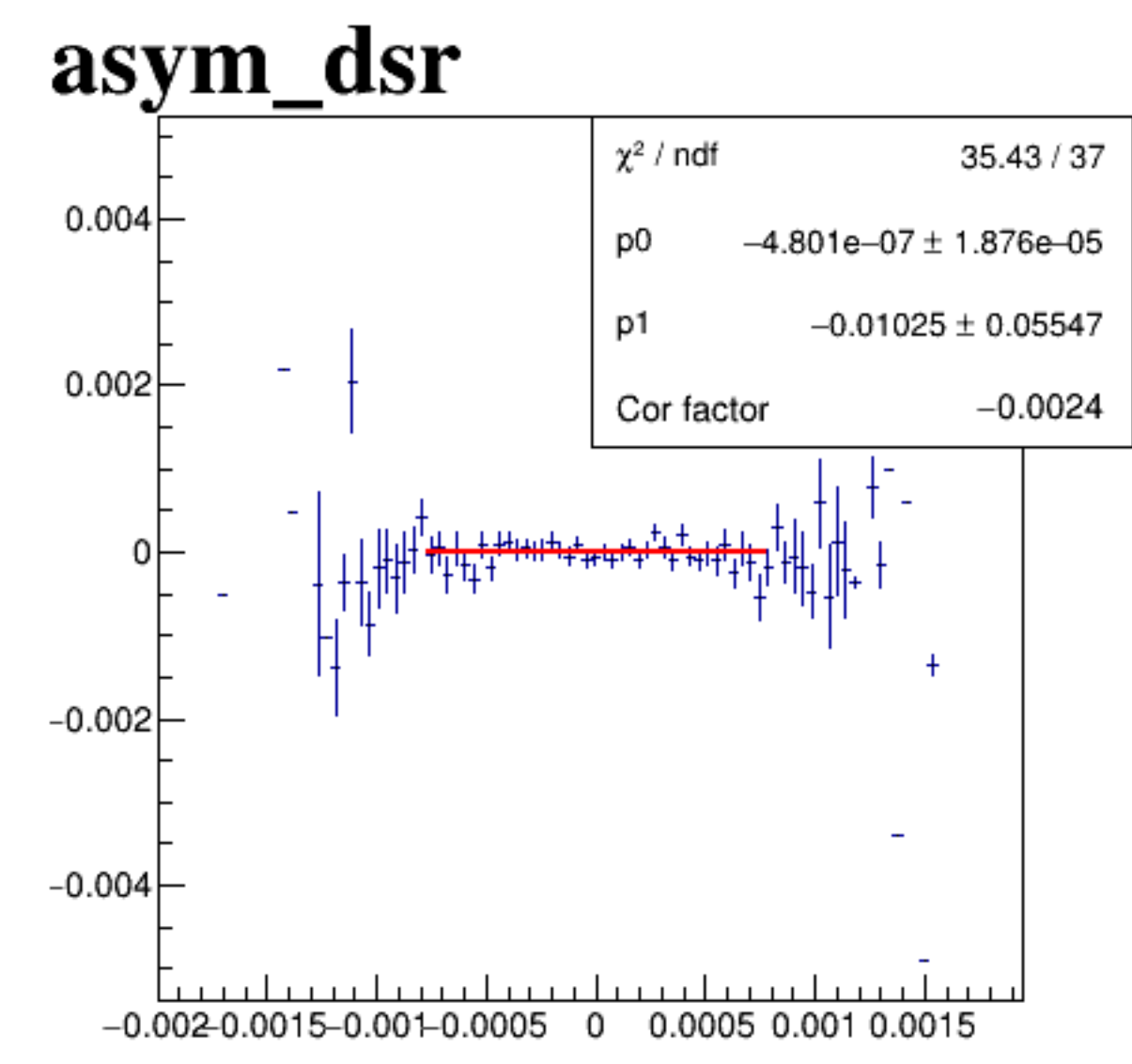
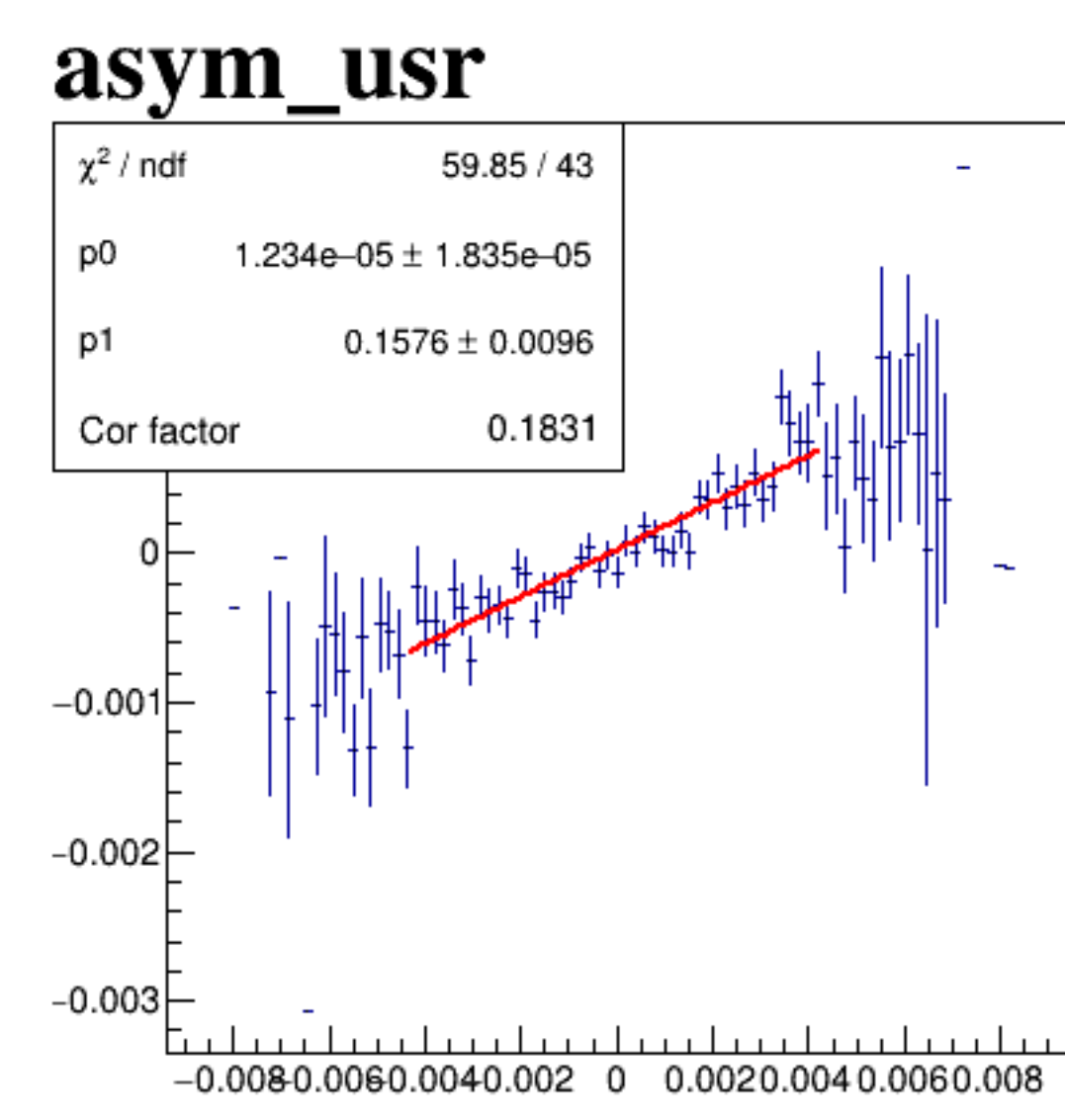
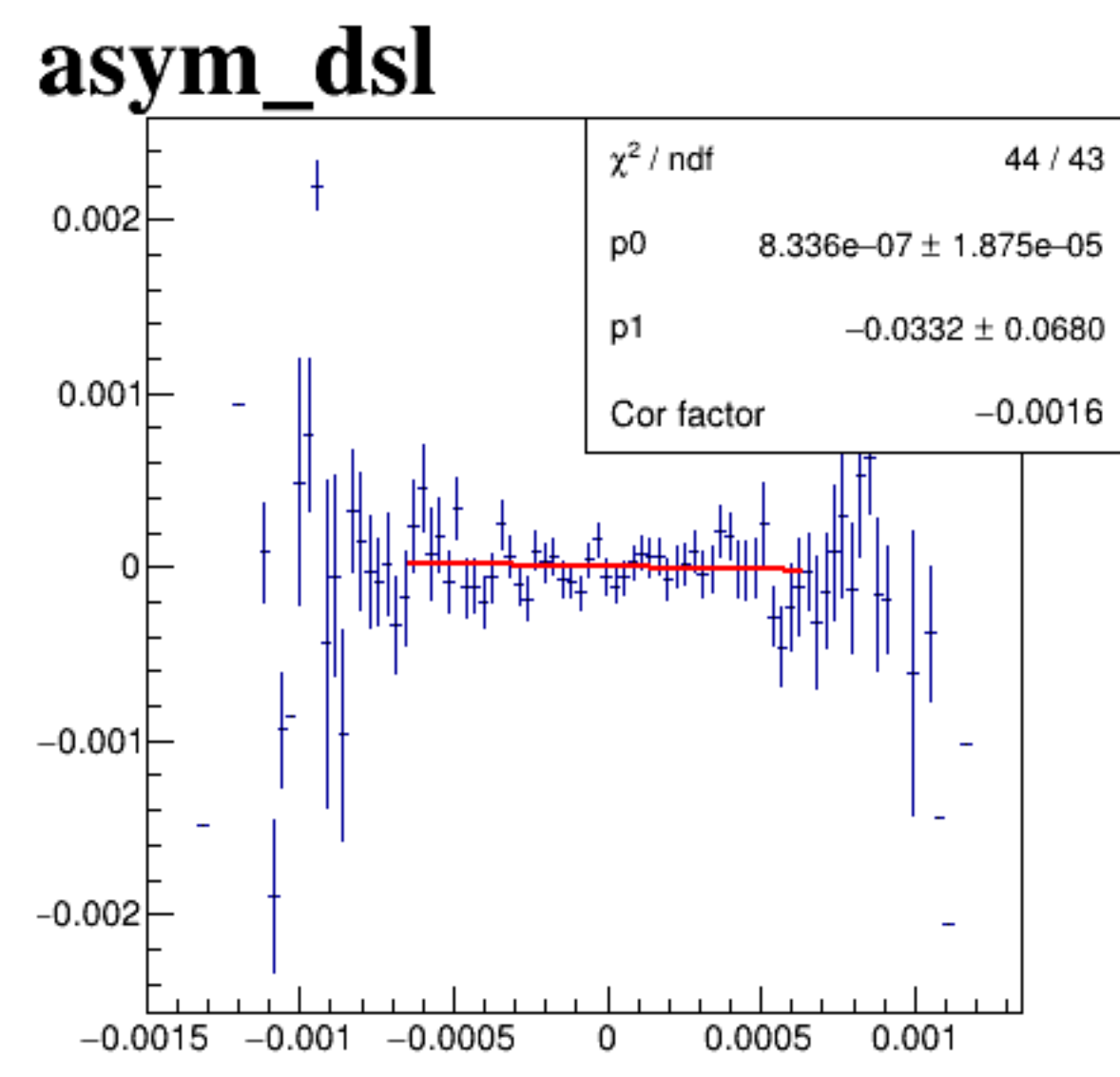
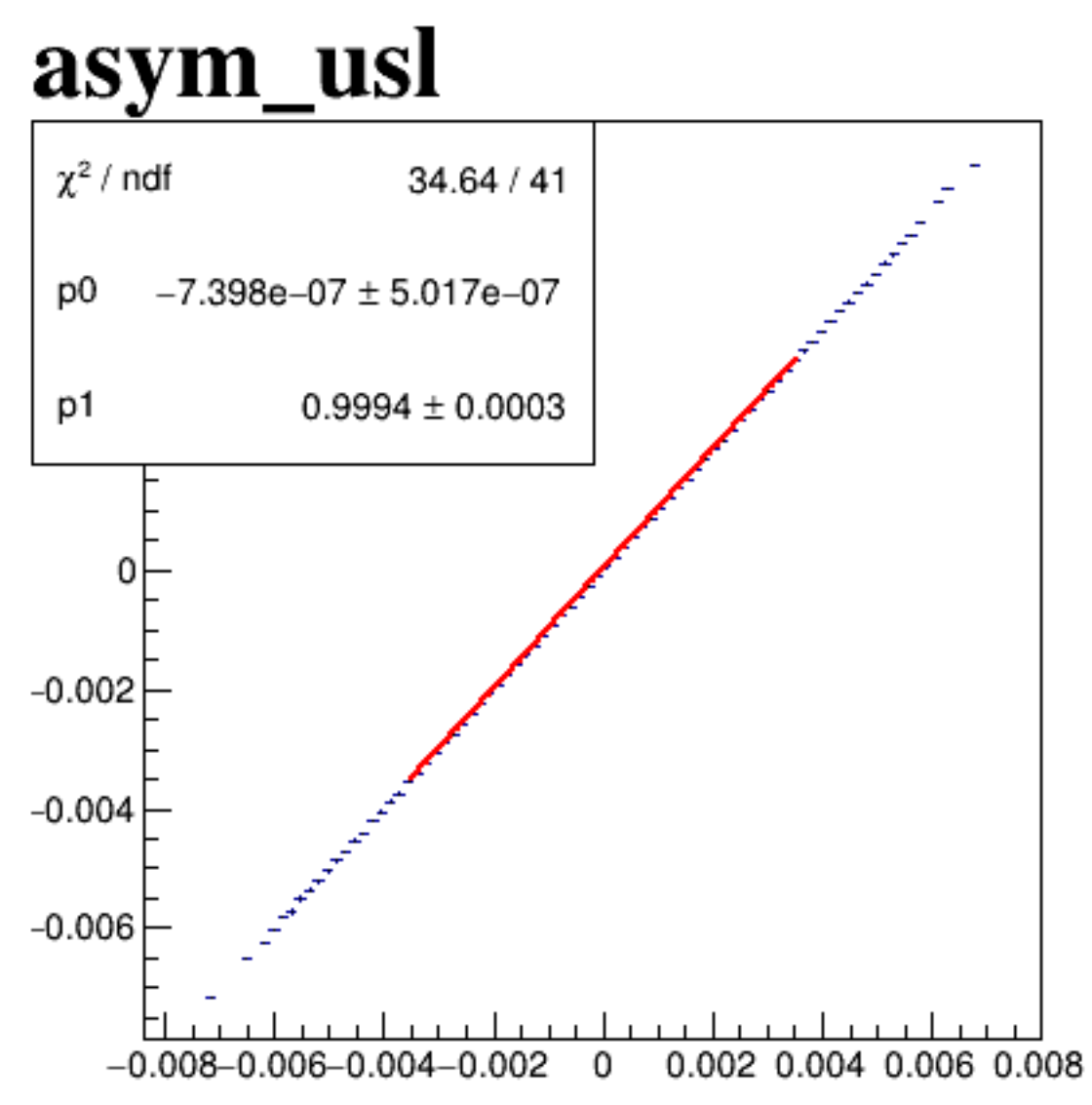
asym\_dsr/ppm {ErrorFlag==0}



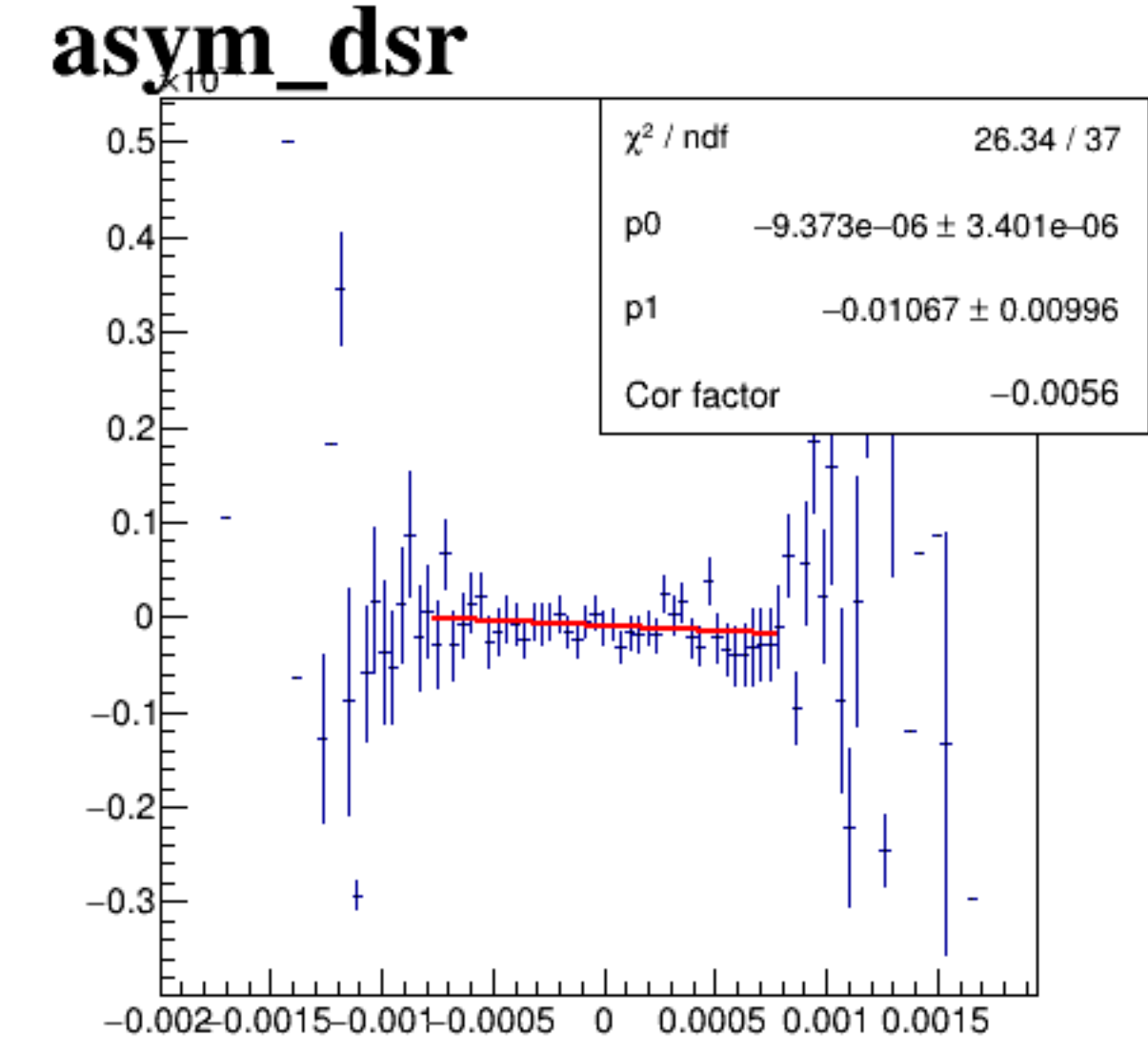
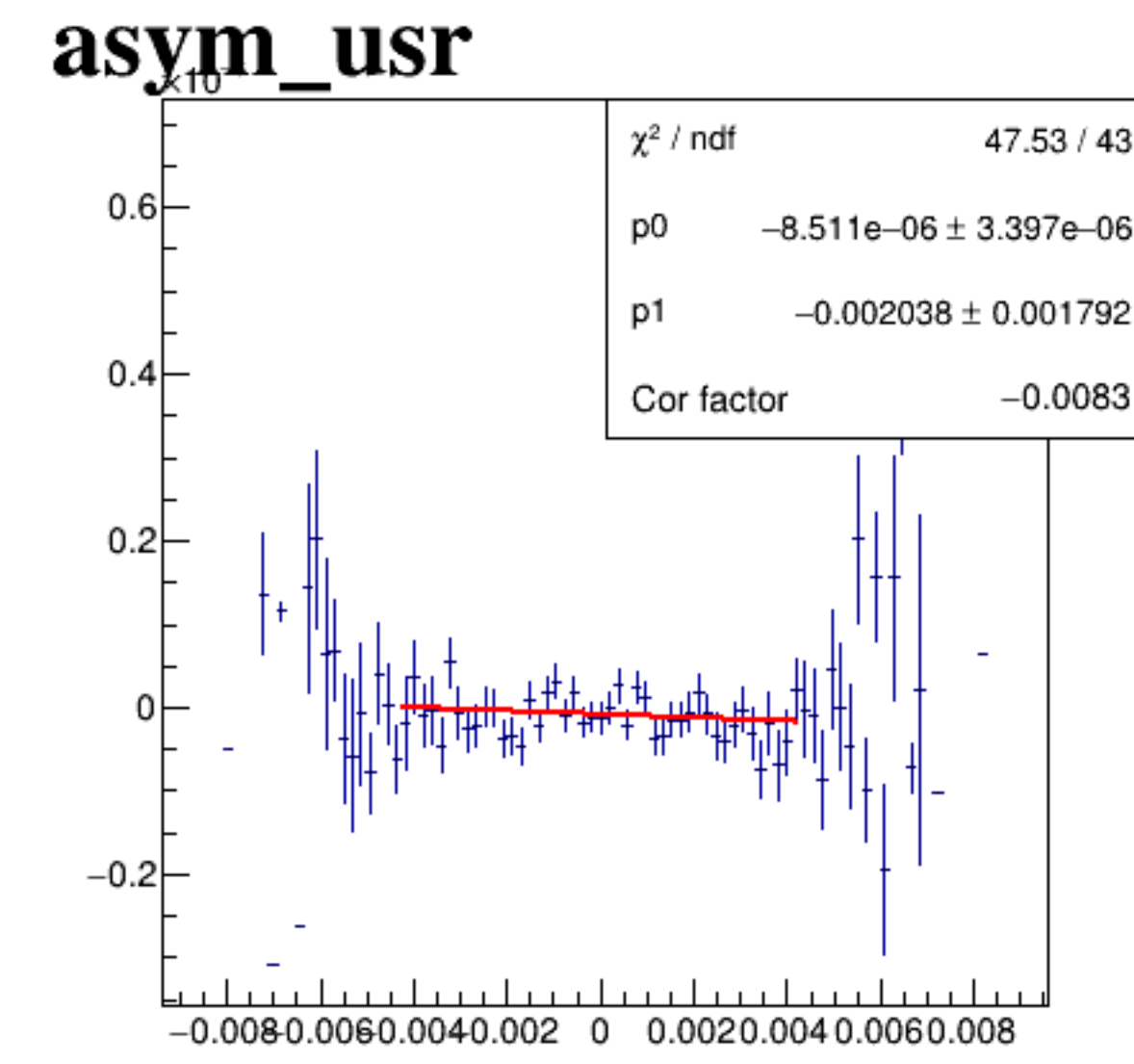
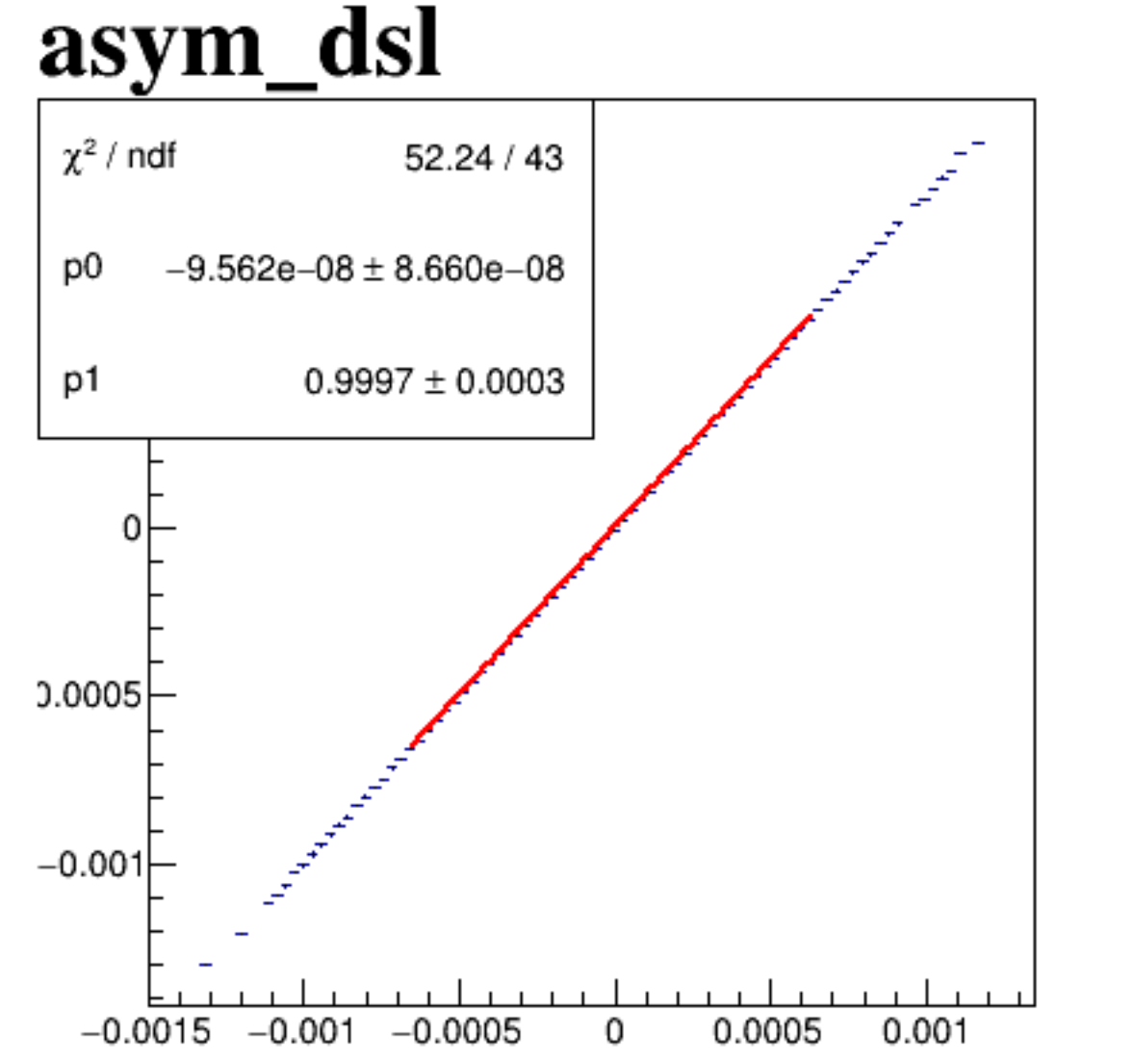
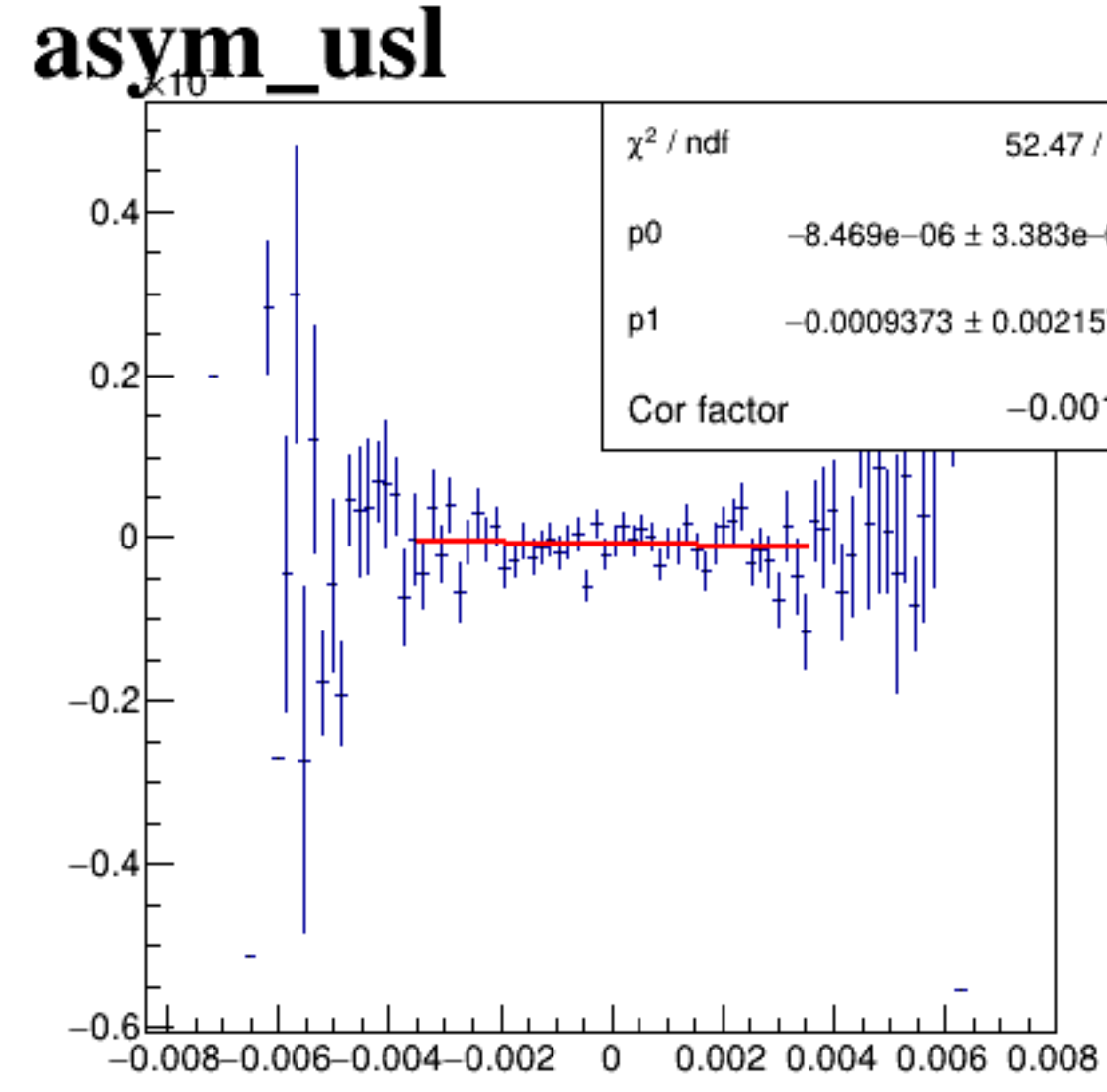




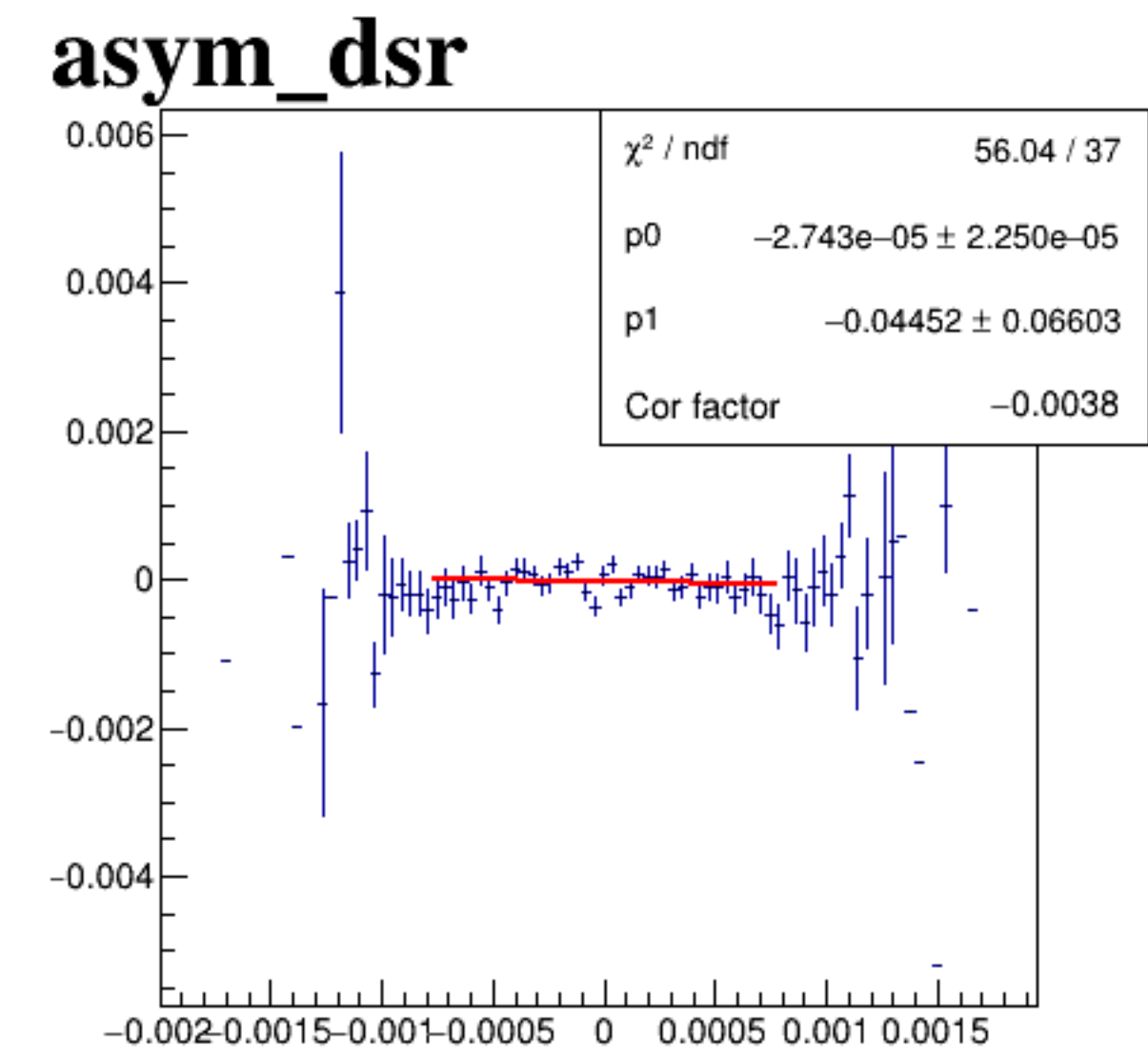
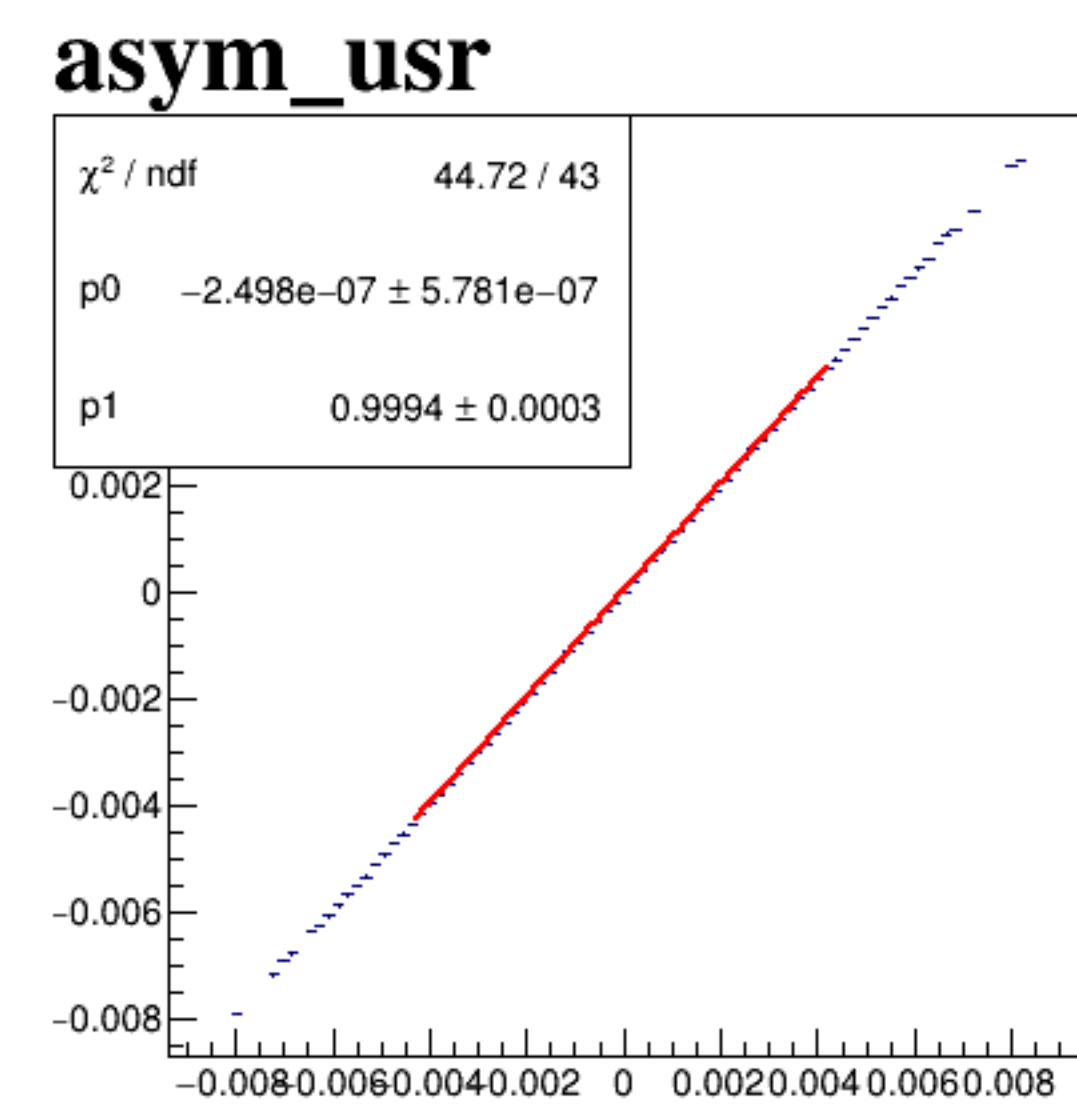
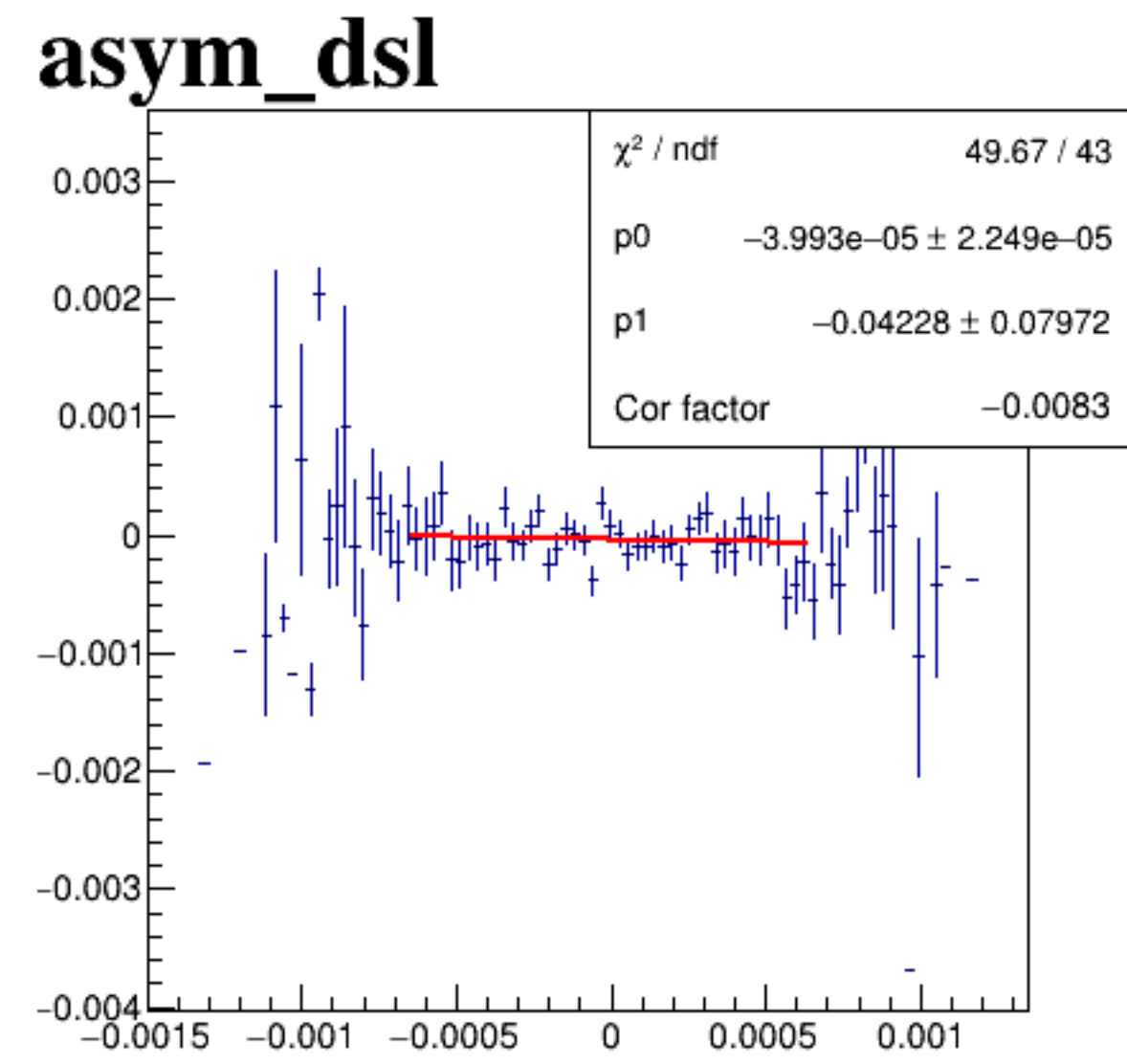
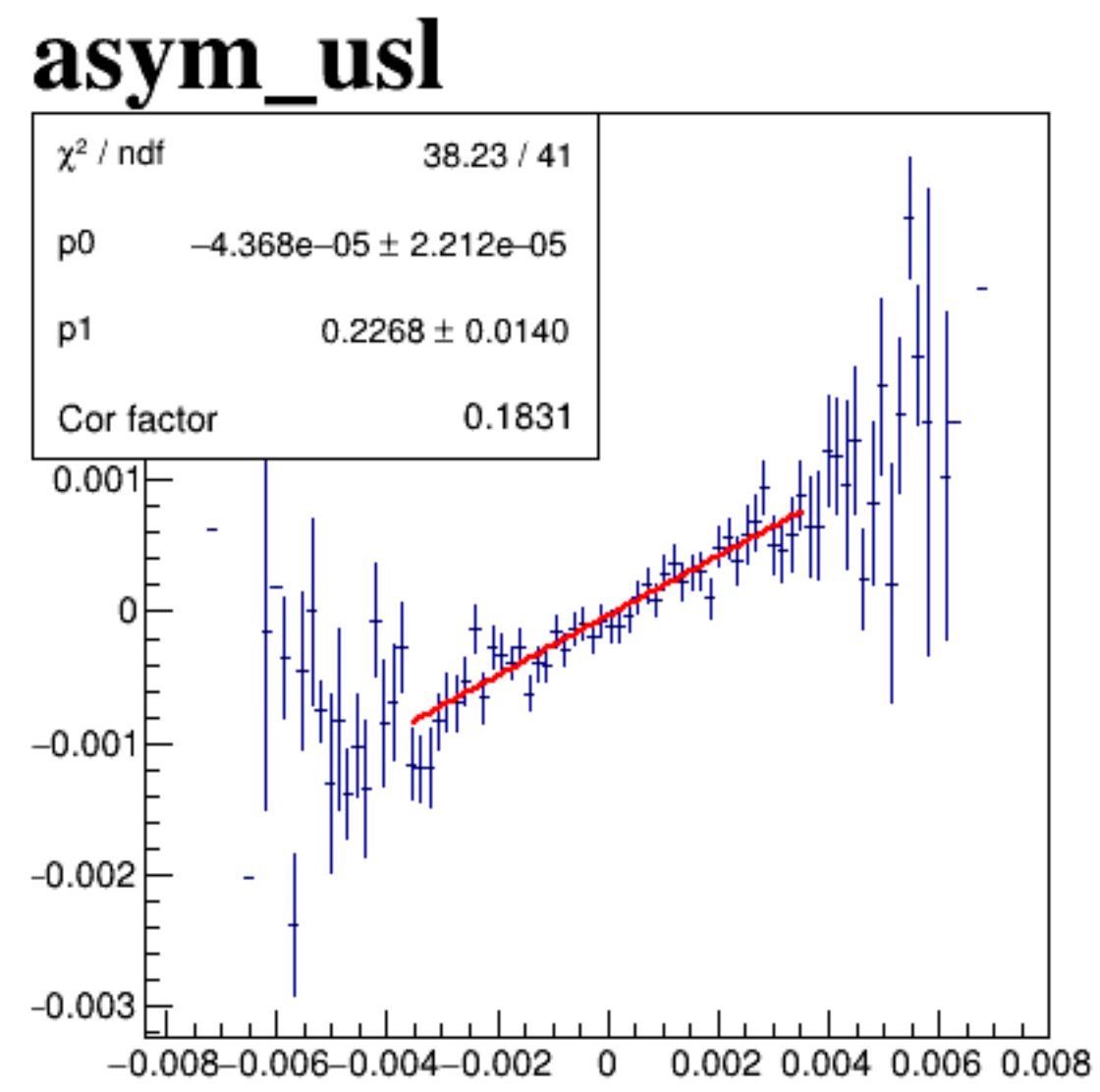
asym\_usl



asym\_dsl



asym\_usr



asym\_dsr

