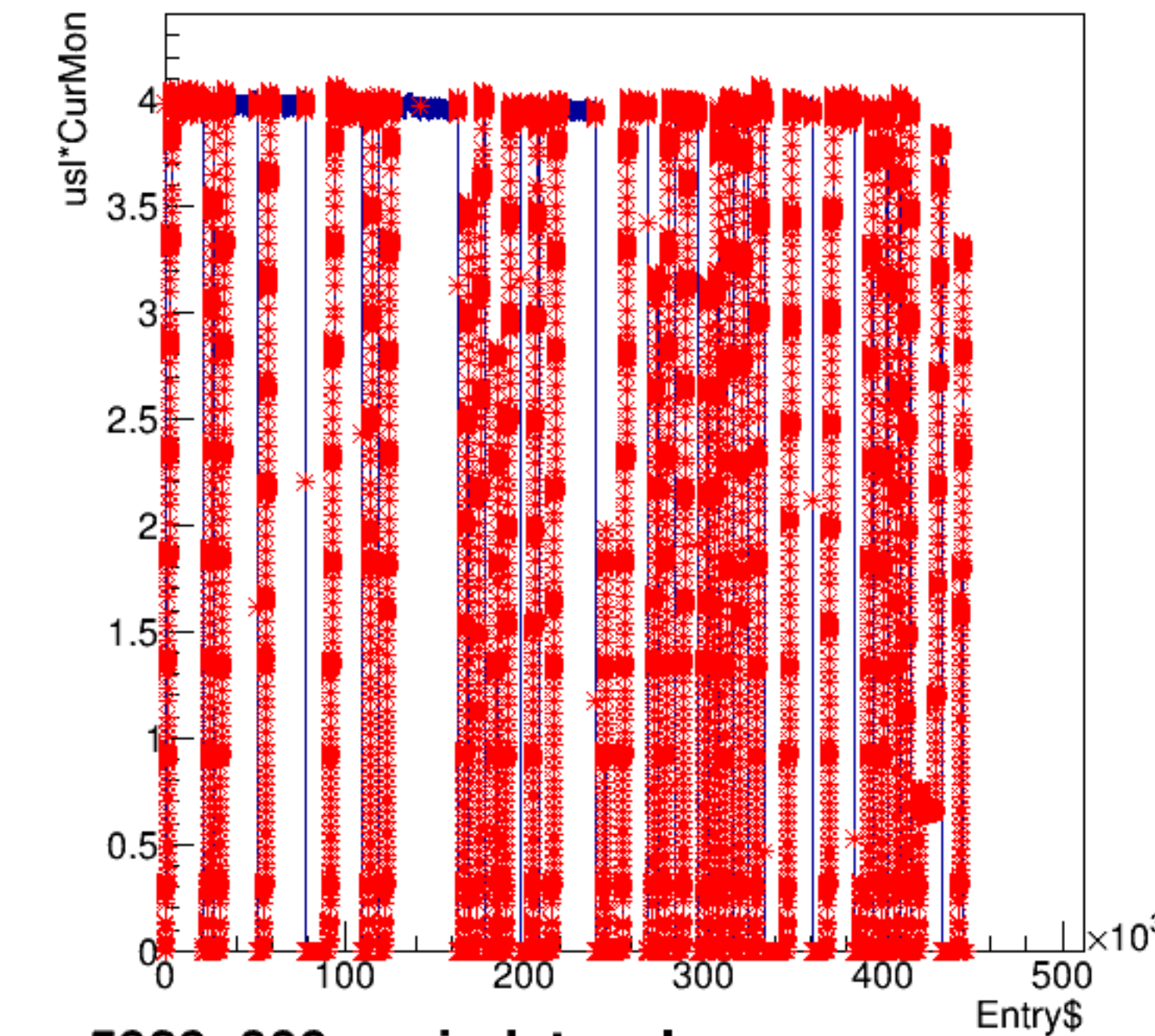
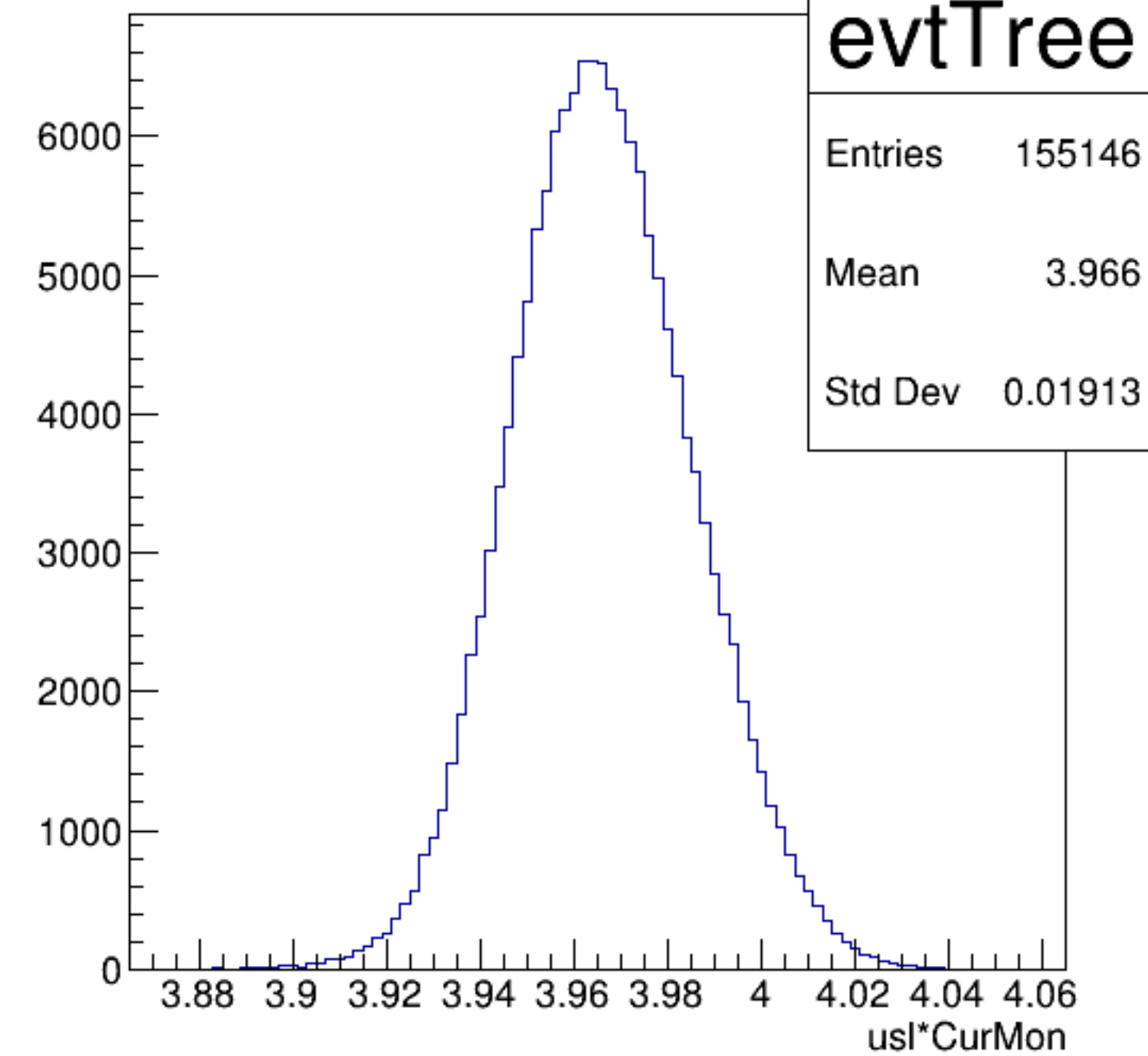


usl*CurMon:Entry\$

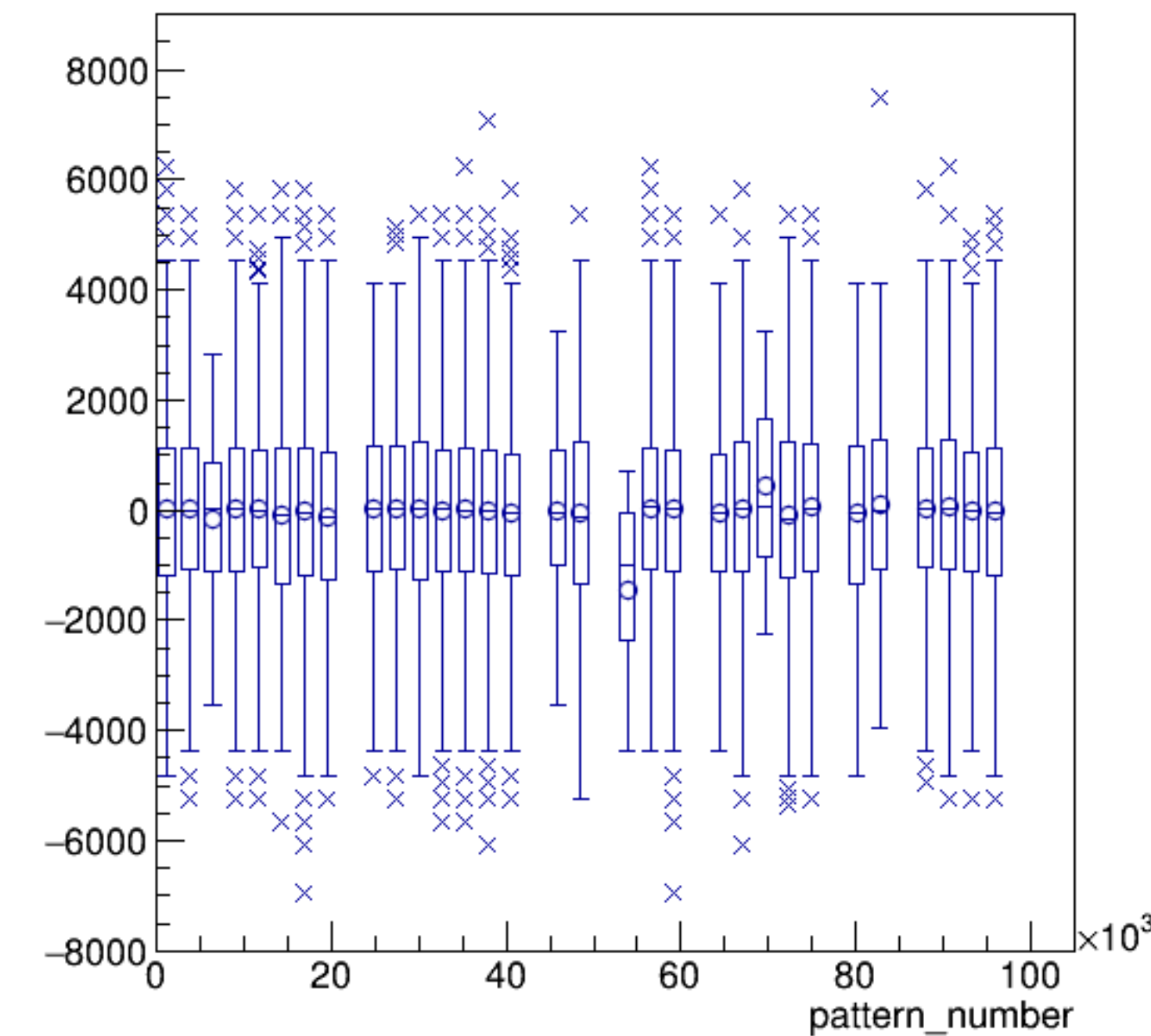


run5929_000_maindet_usl.png

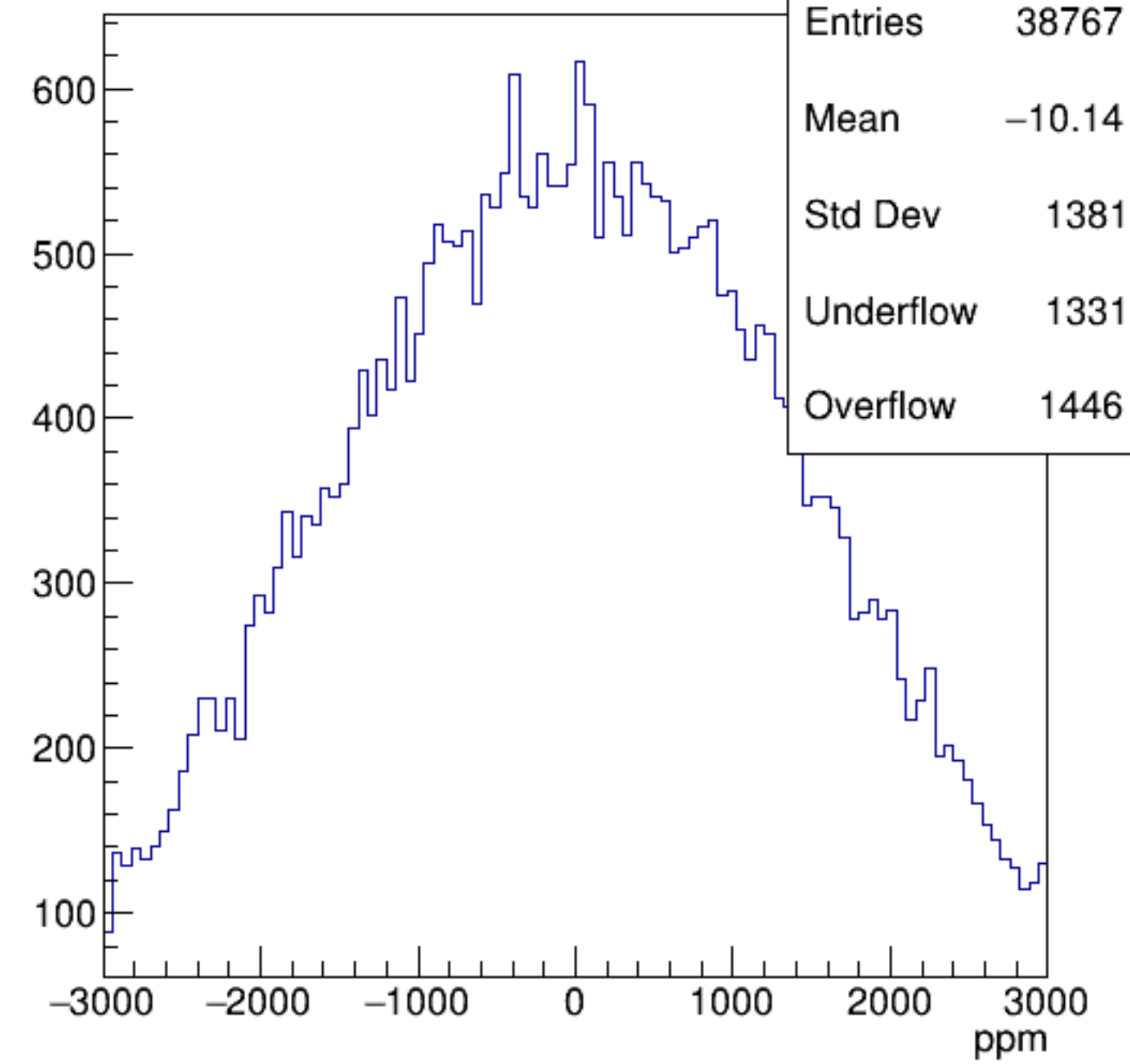
usl*CurMon {ErrorFlag==0}



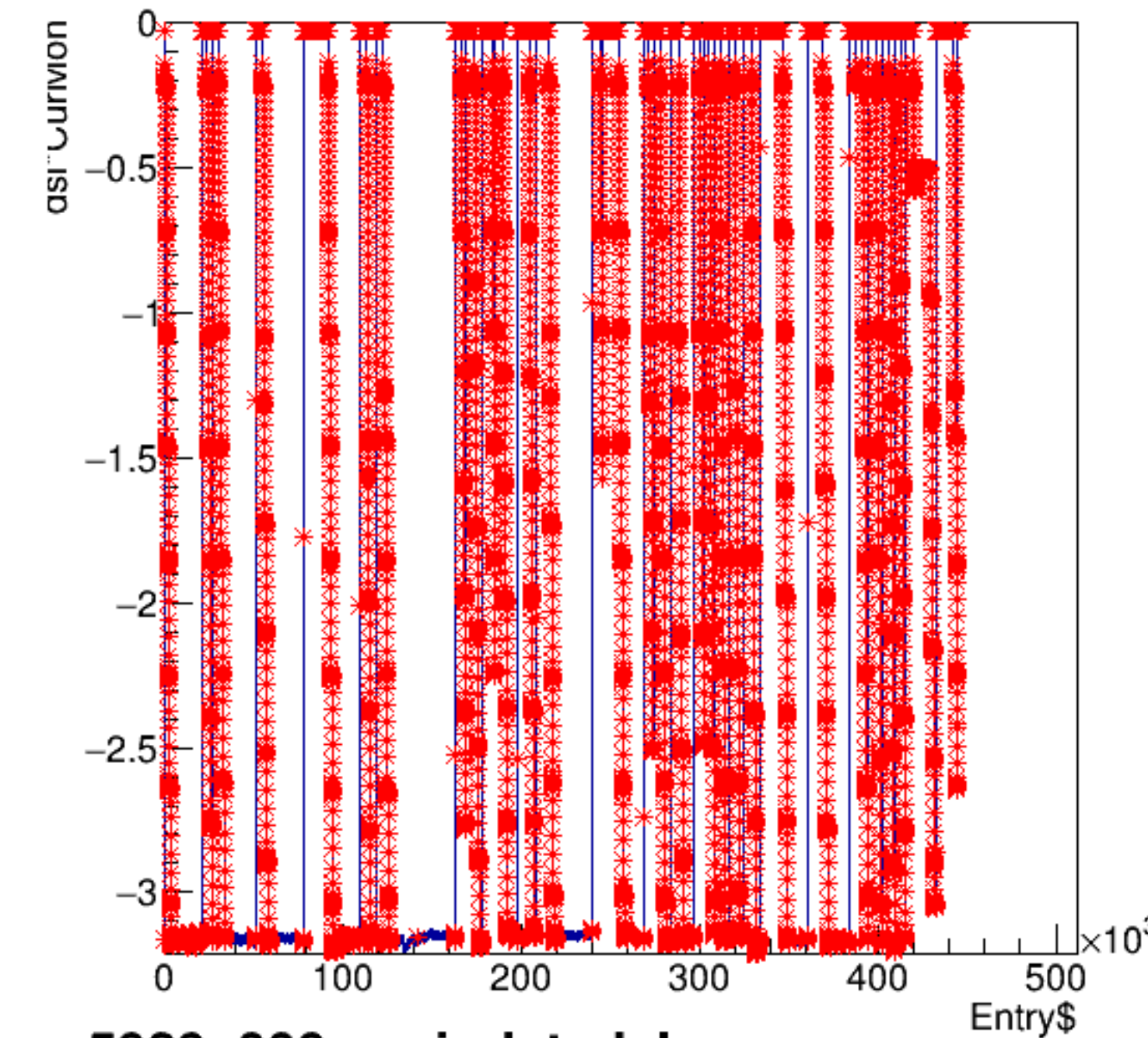
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl

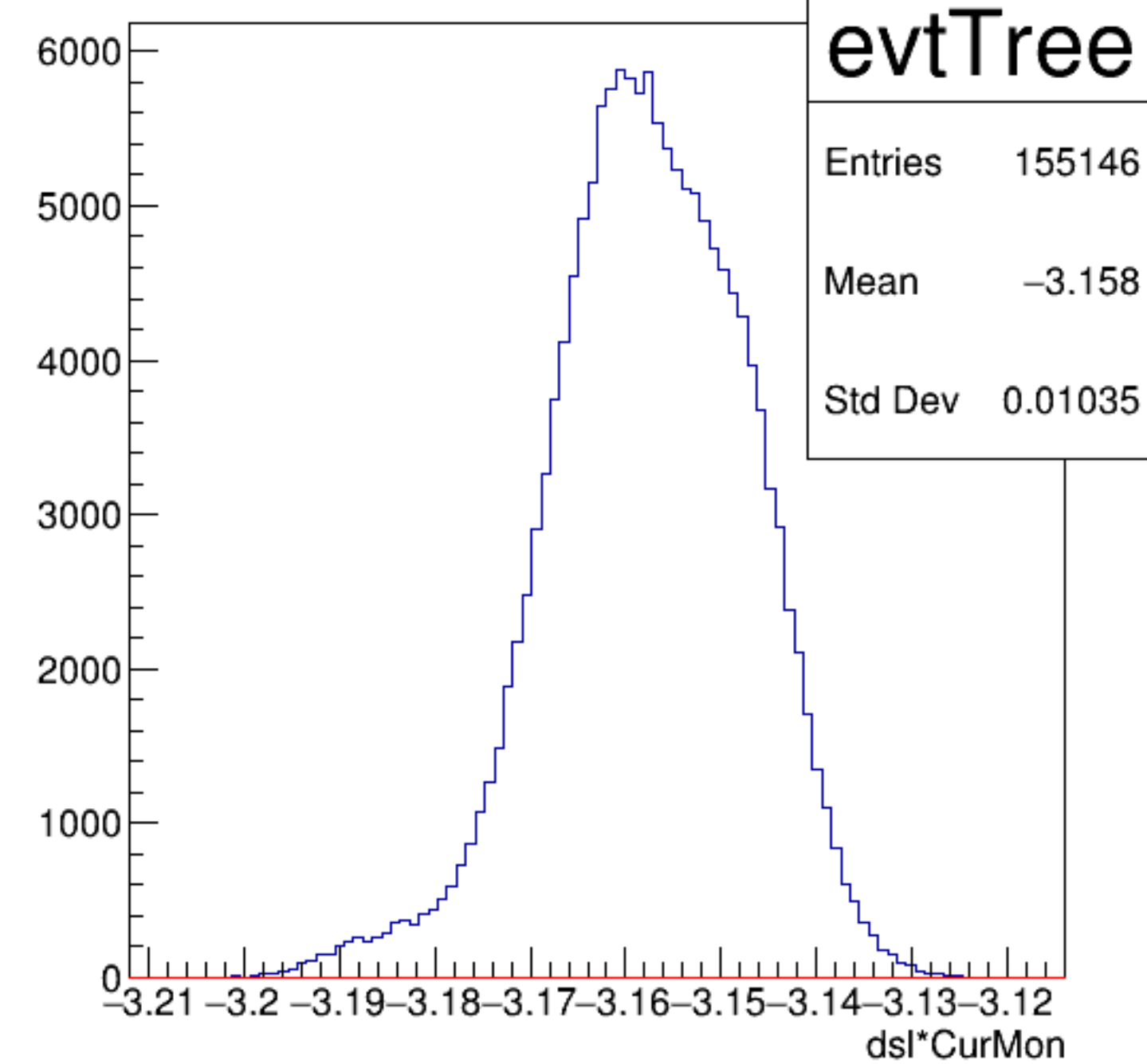


dsl*CurMon:Entry\$



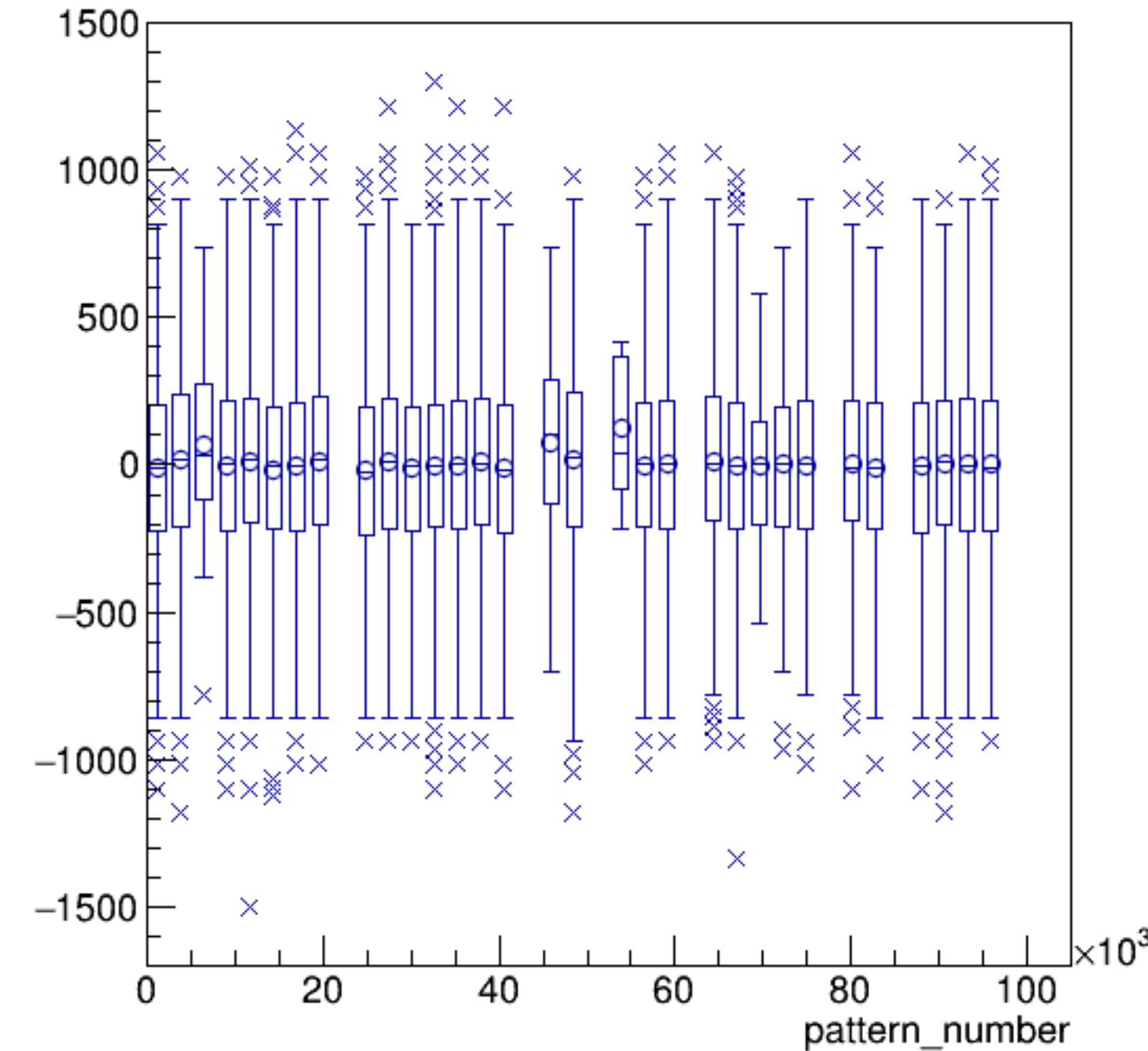
run5929_000_maindet_dsl.png

dsl*CurMon {ErrorFlag==0}

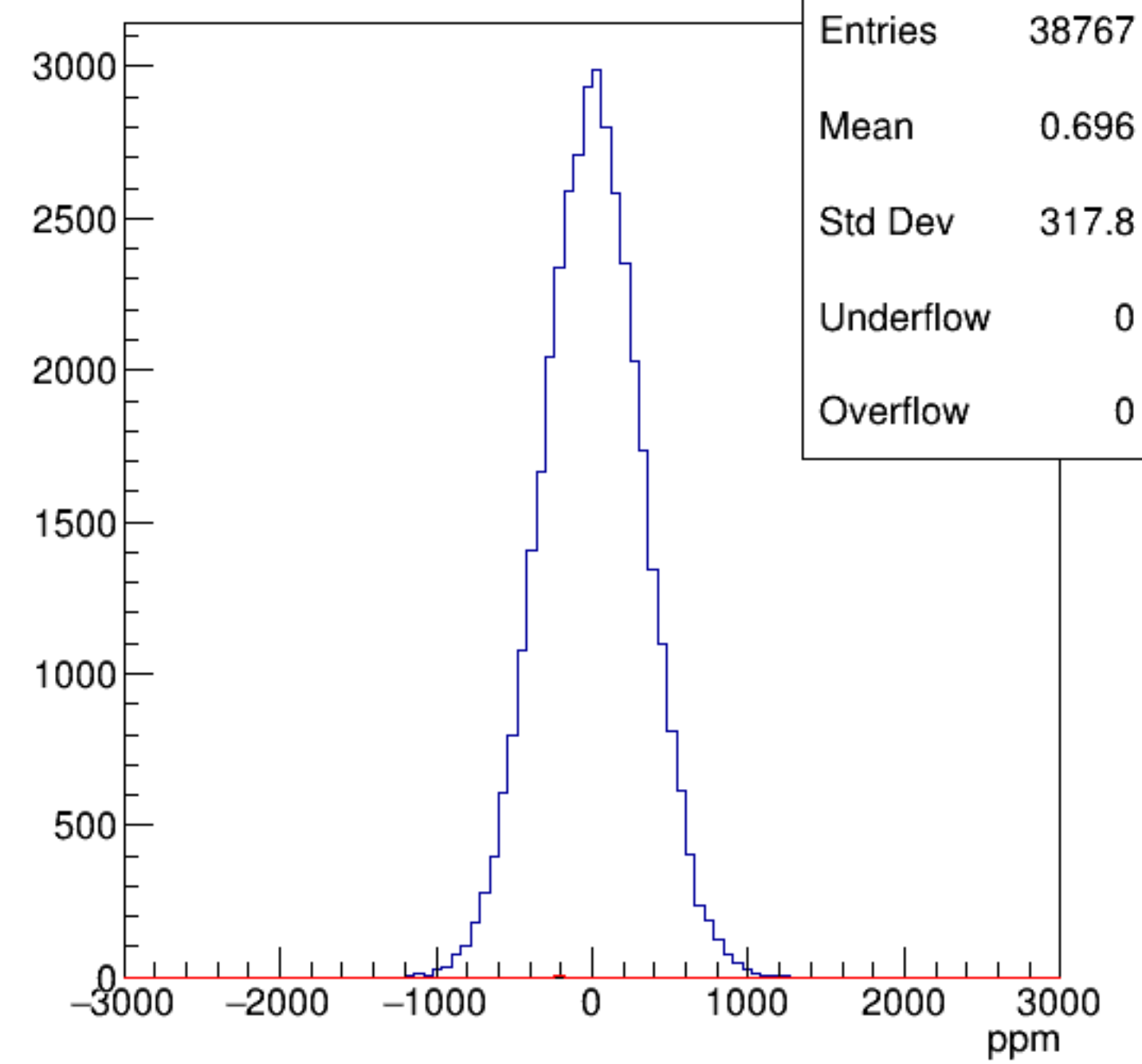


evtTree	
Entries	155146
Mean	-3.158
Std Dev	0.01035

asym_dsl/ppm:pattern_number {ErrorFlag==0}

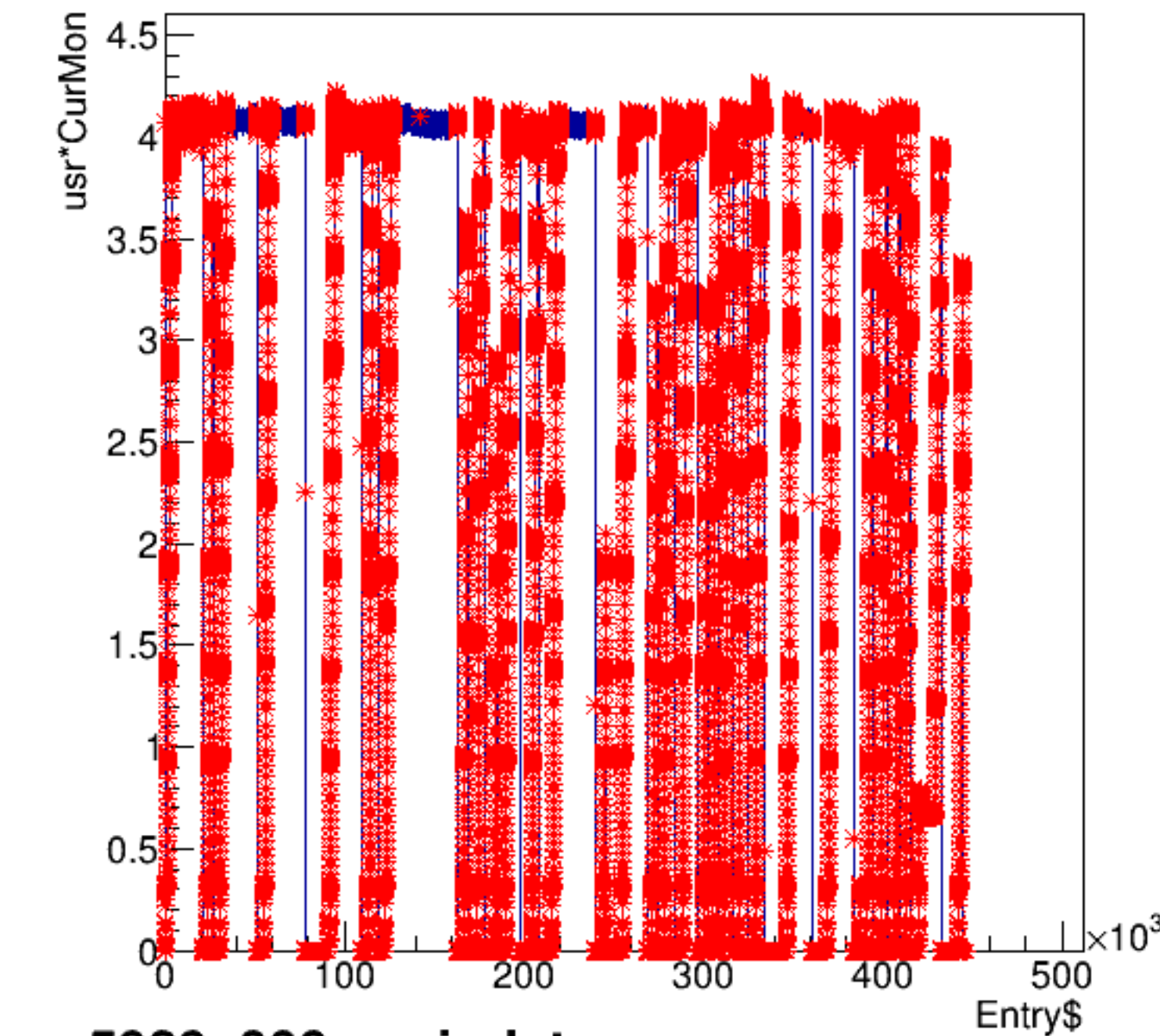


asym_dsl



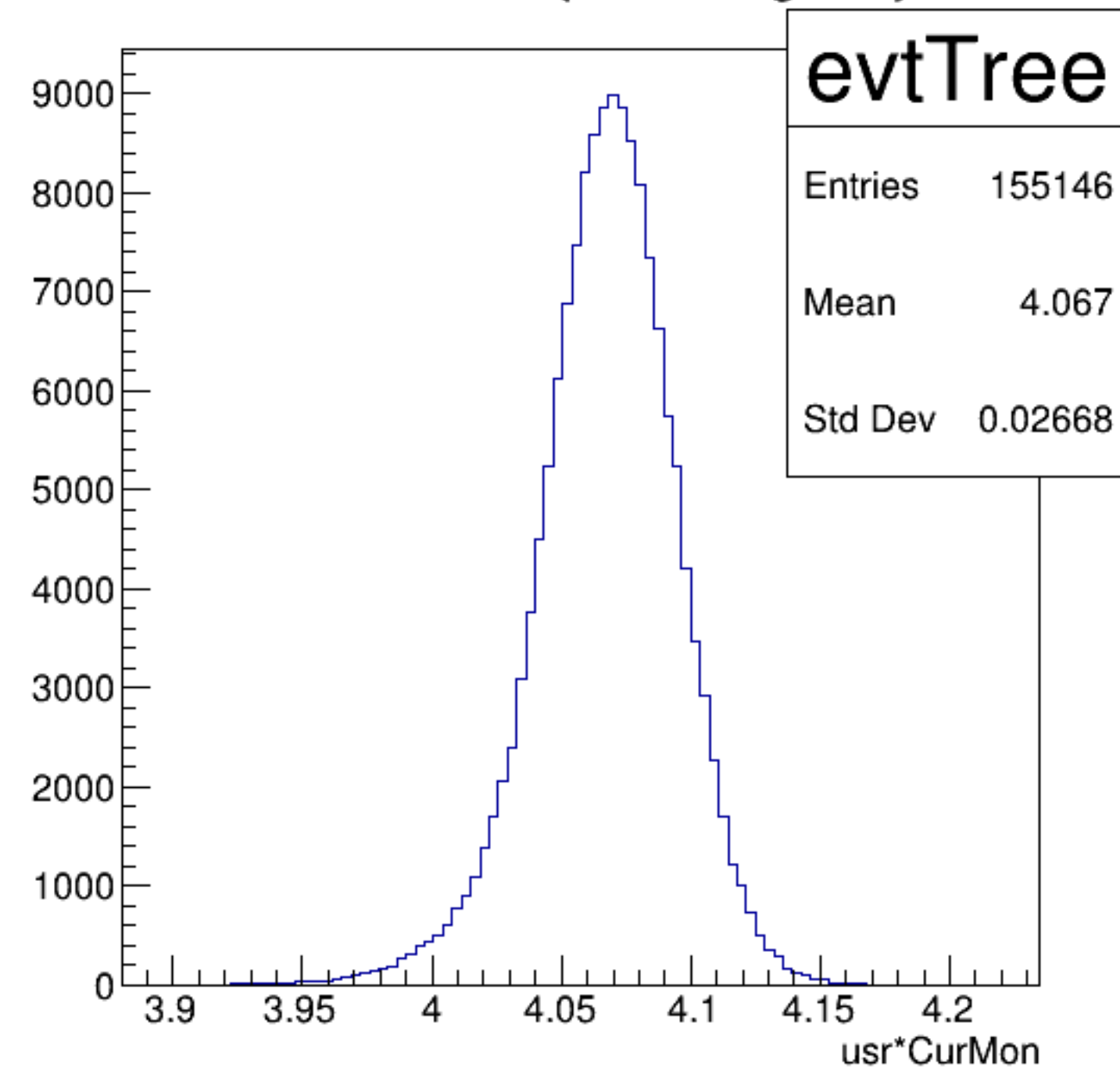
Entries	38767
Mean	0.696
Std Dev	317.8
Underflow	0
Overflow	0

usr*CurMon:Entry\$

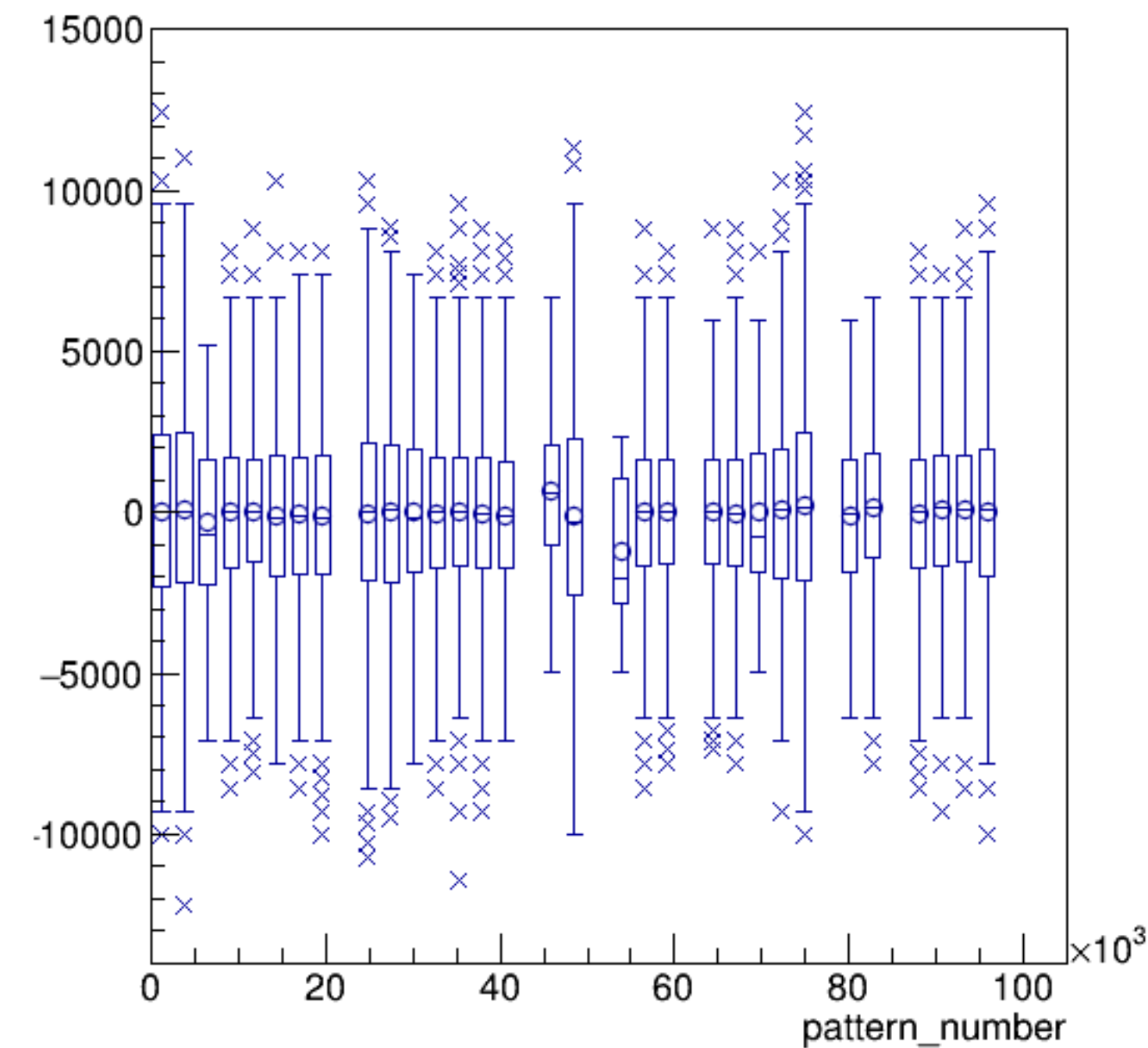


run5929_000_maindet_usr.png

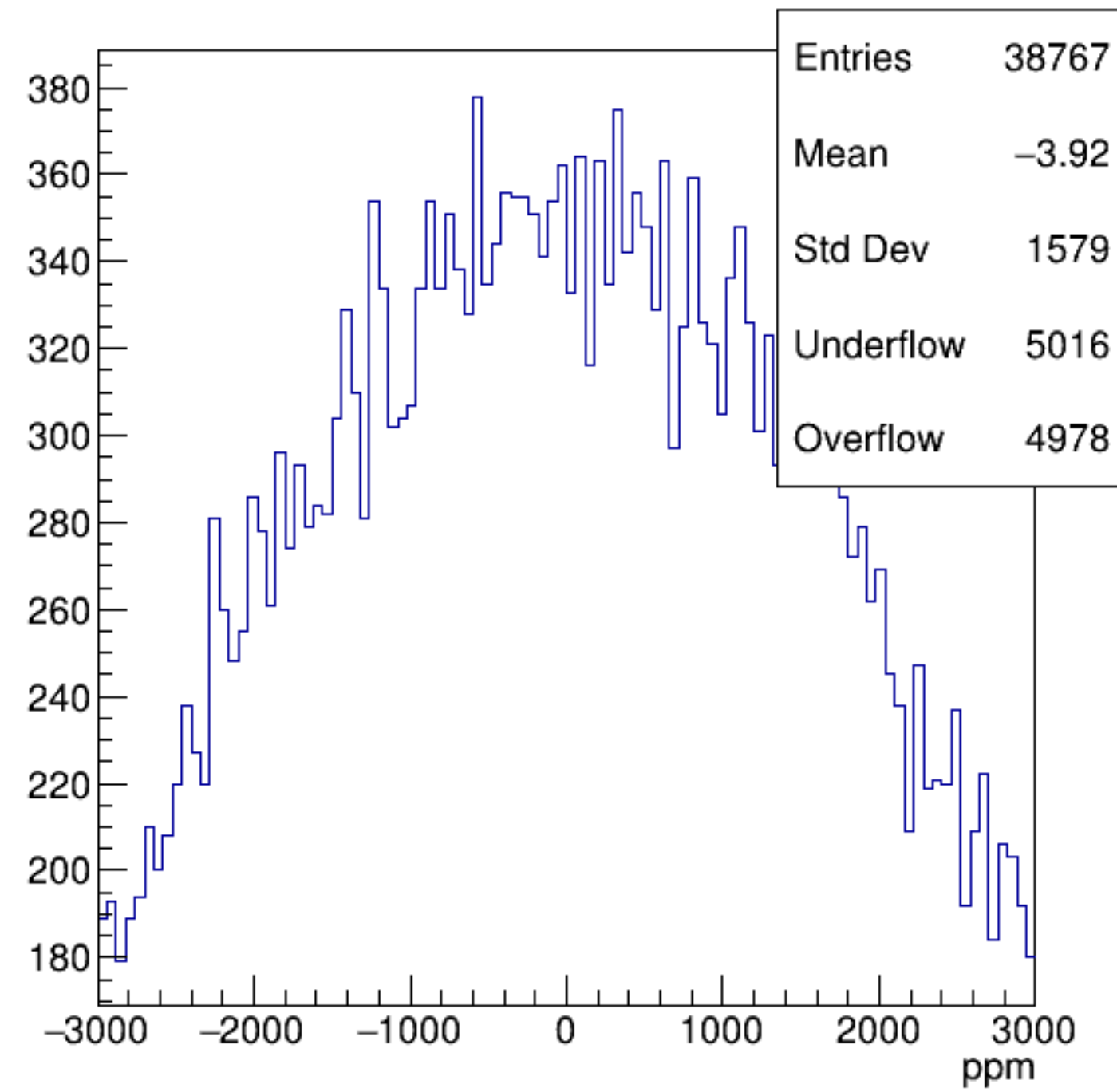
usr*CurMon {ErrorFlag==0}



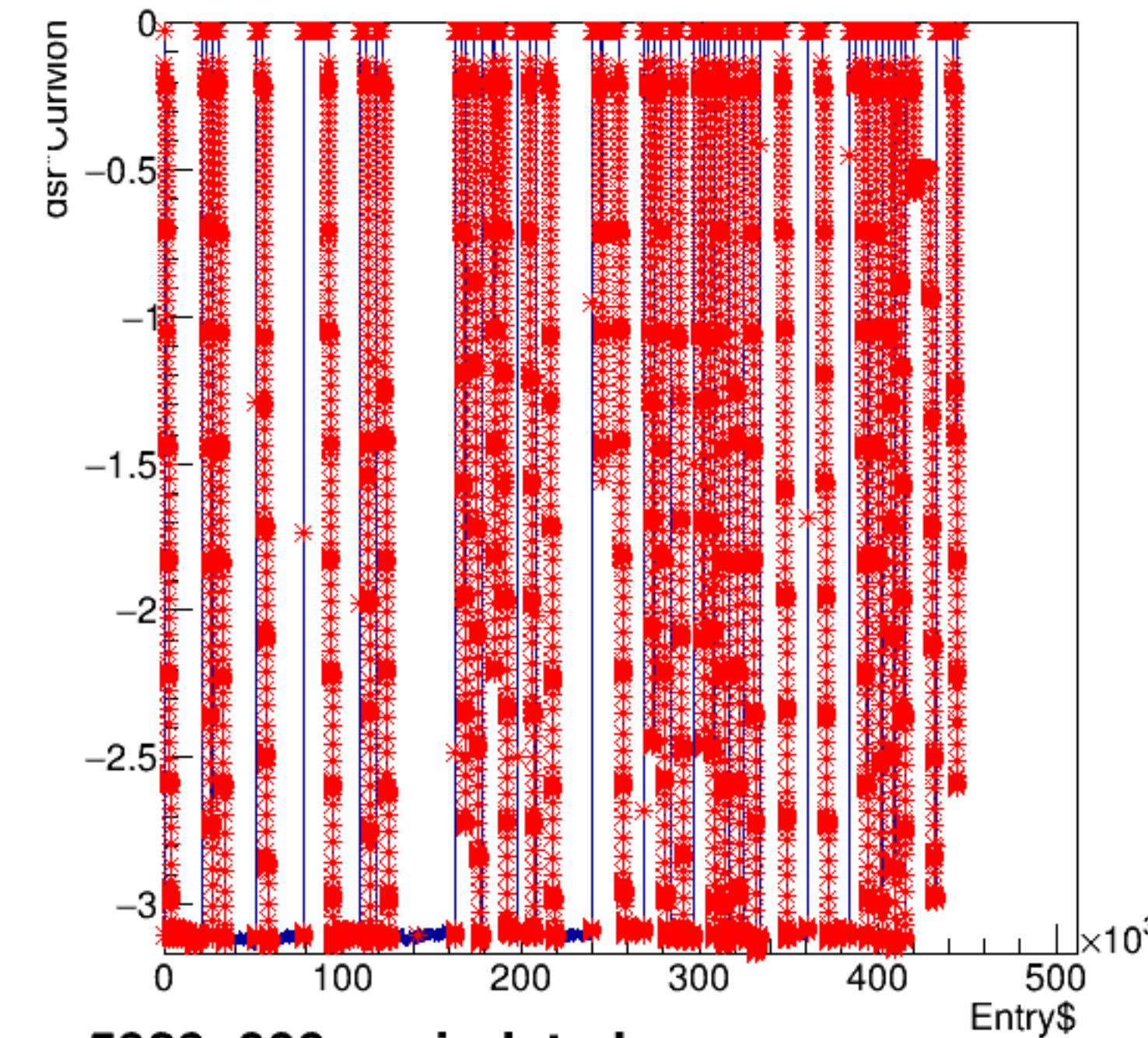
asym_usr/ppm:pattern_number {ErrorFlag==0}



asym_usr

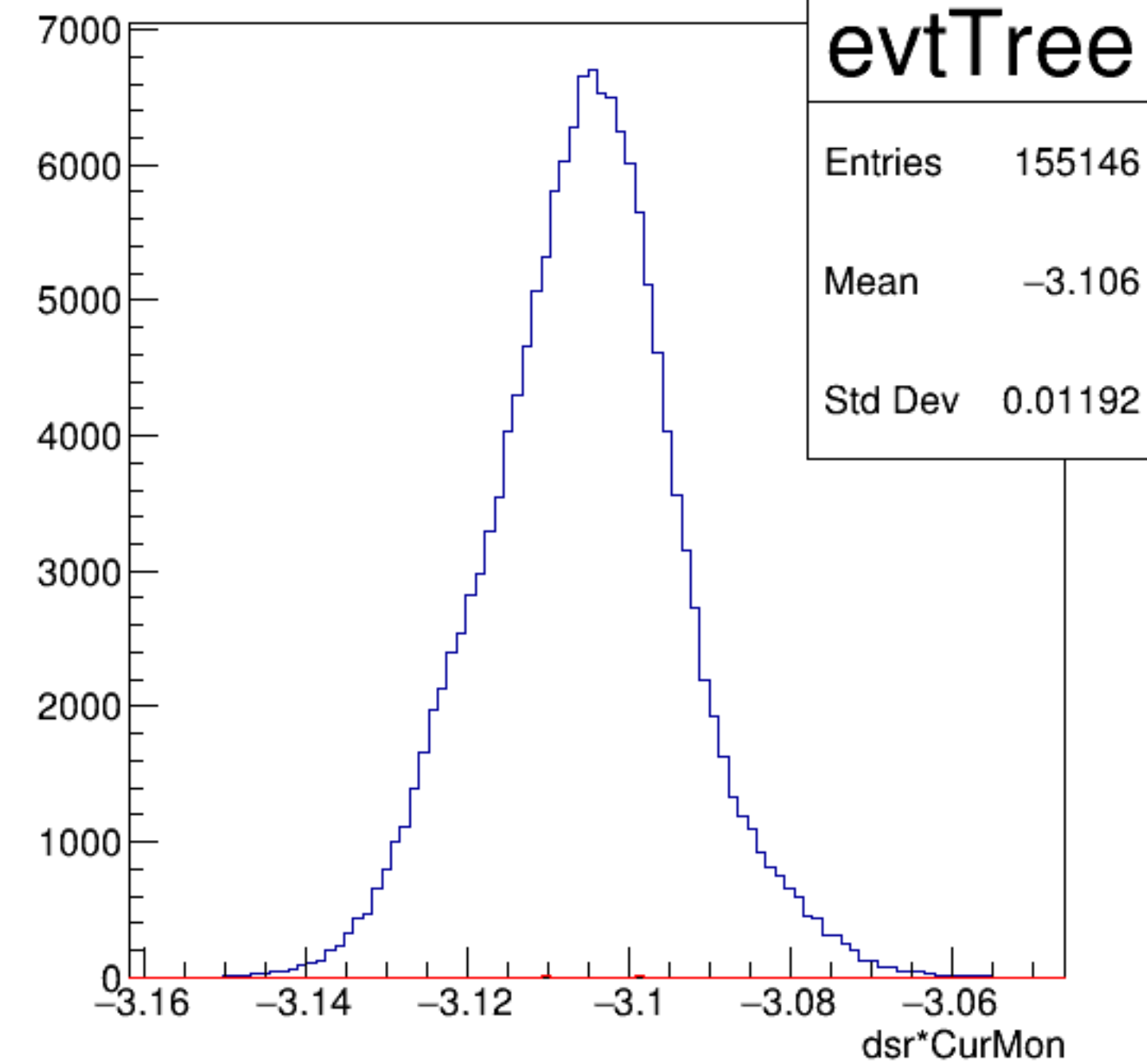


dsr*CurMon:Entry\$



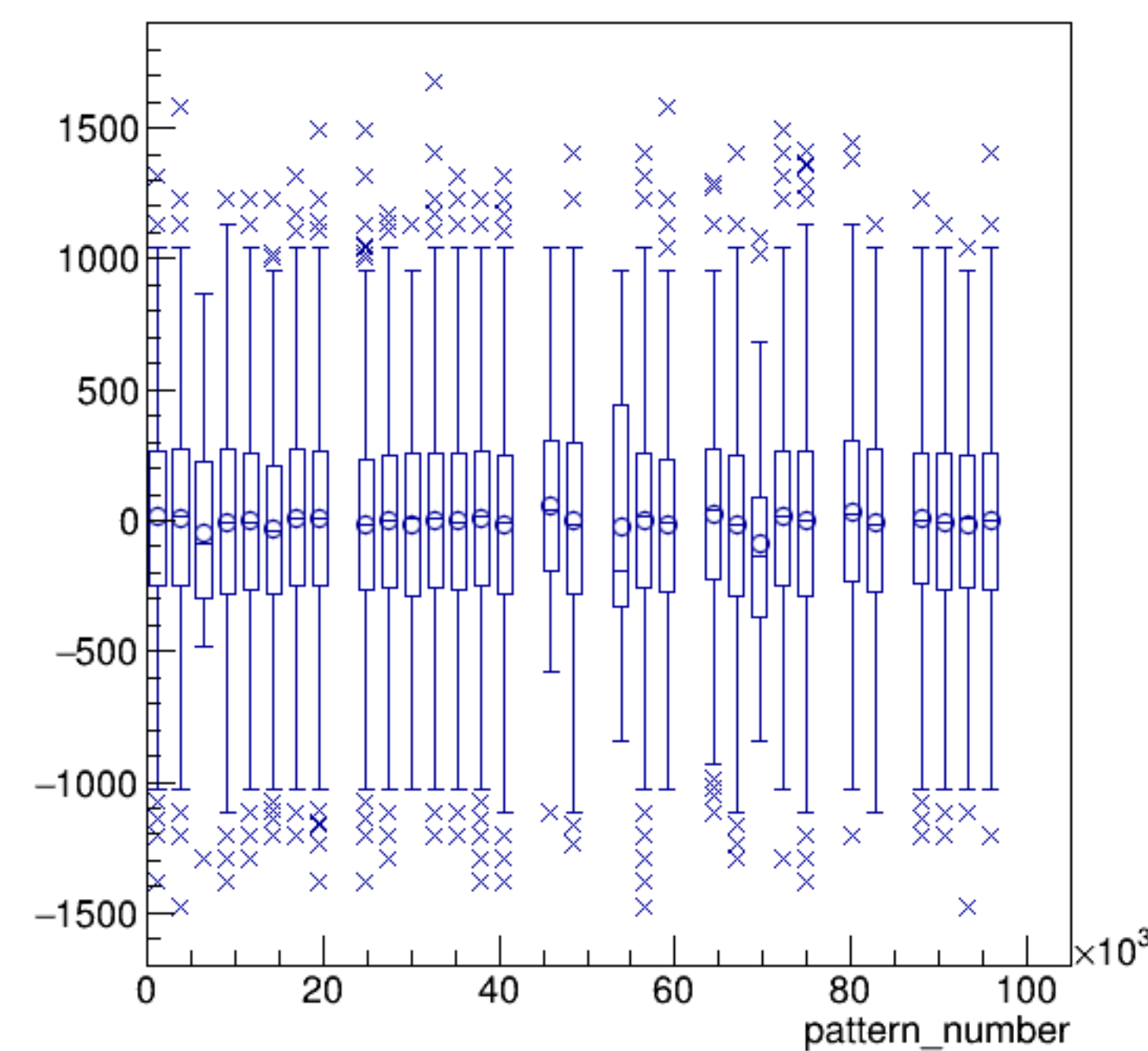
run5929_000_maindet_dsr.png

dsr*CurMon {ErrorFlag==0}

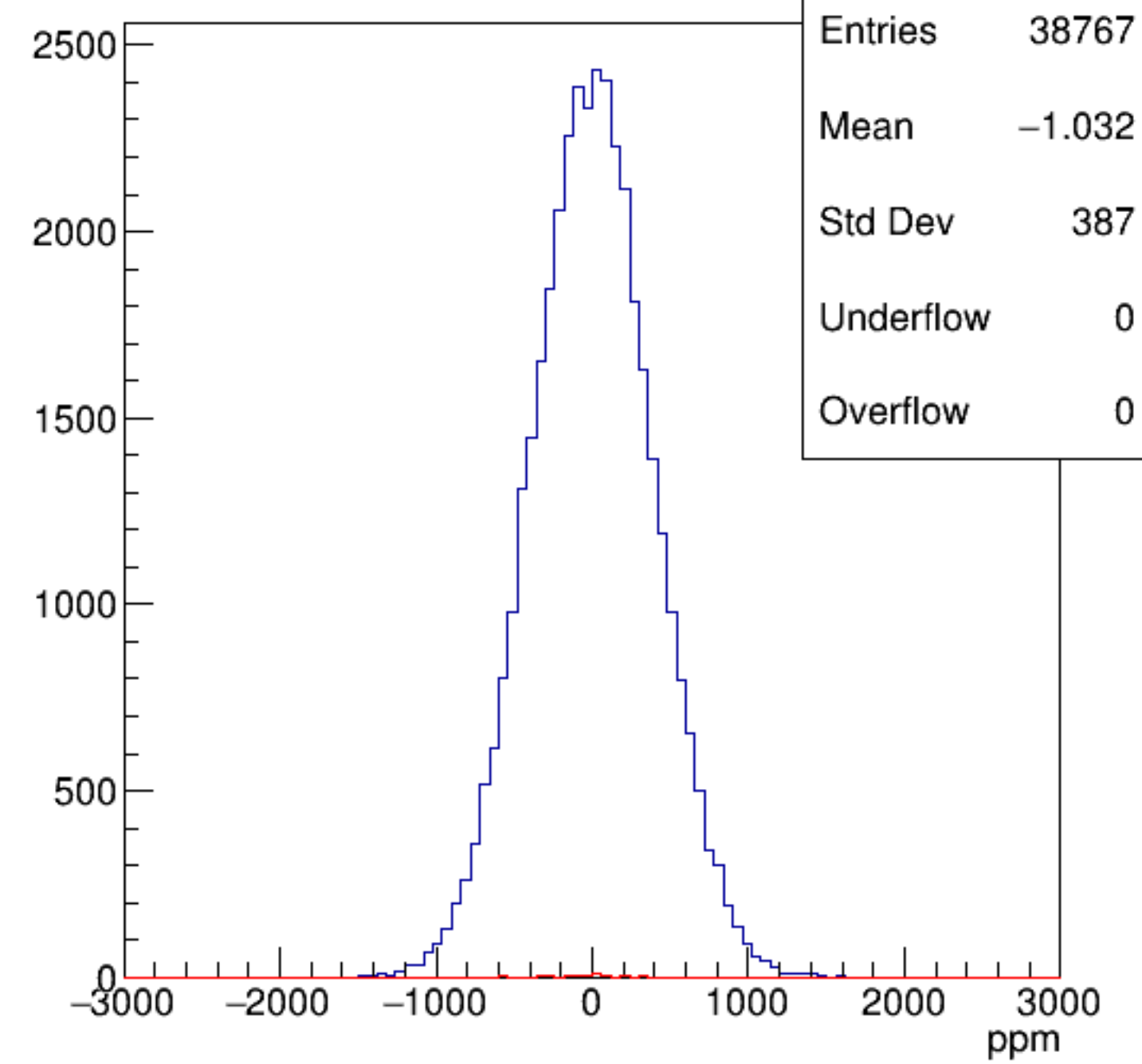


evtTree	
Entries	155146
Mean	-3.106
Std Dev	0.01192

asym_dsr/ppm:pattern_number {ErrorFlag==0}

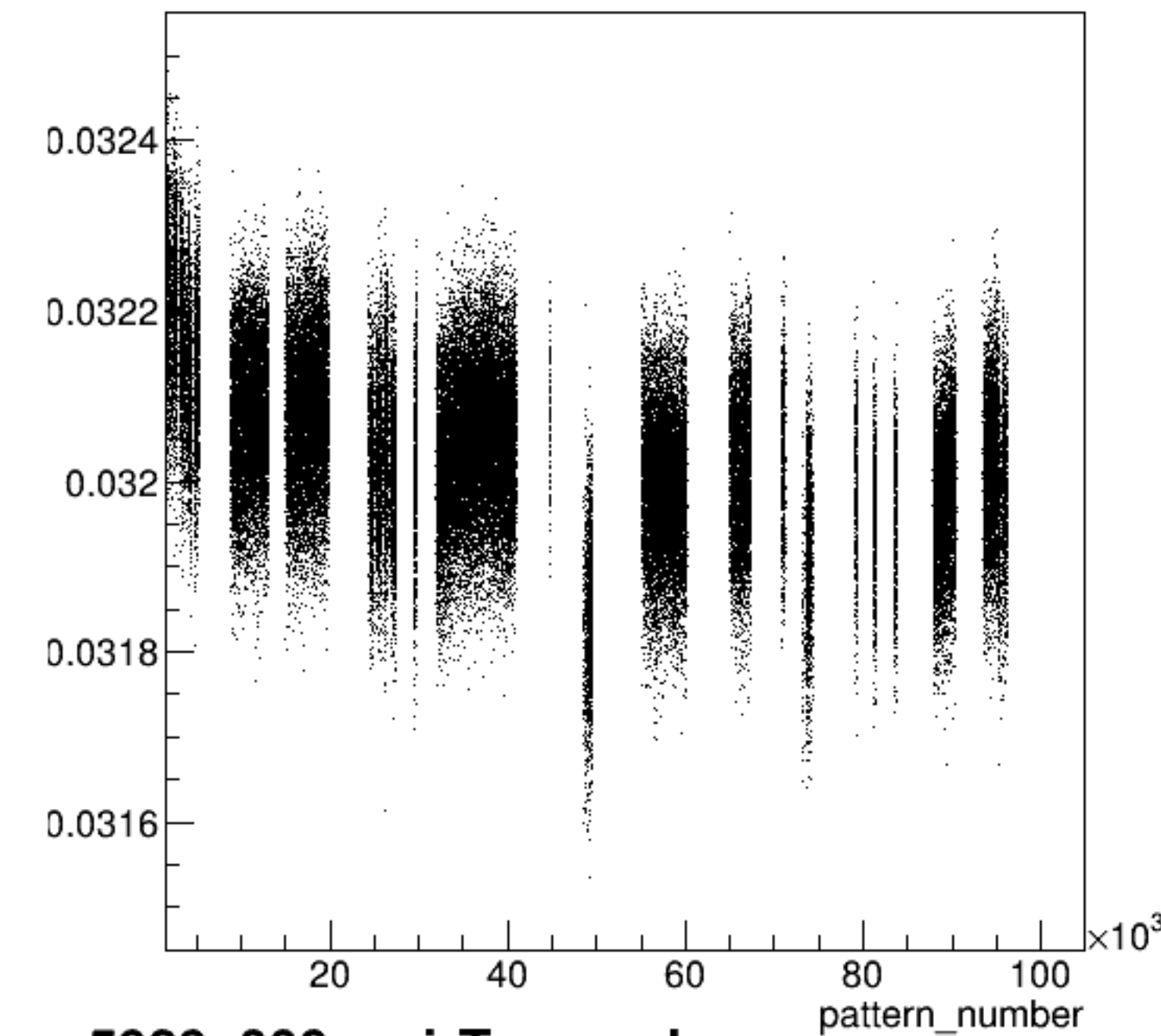


asym_dsr



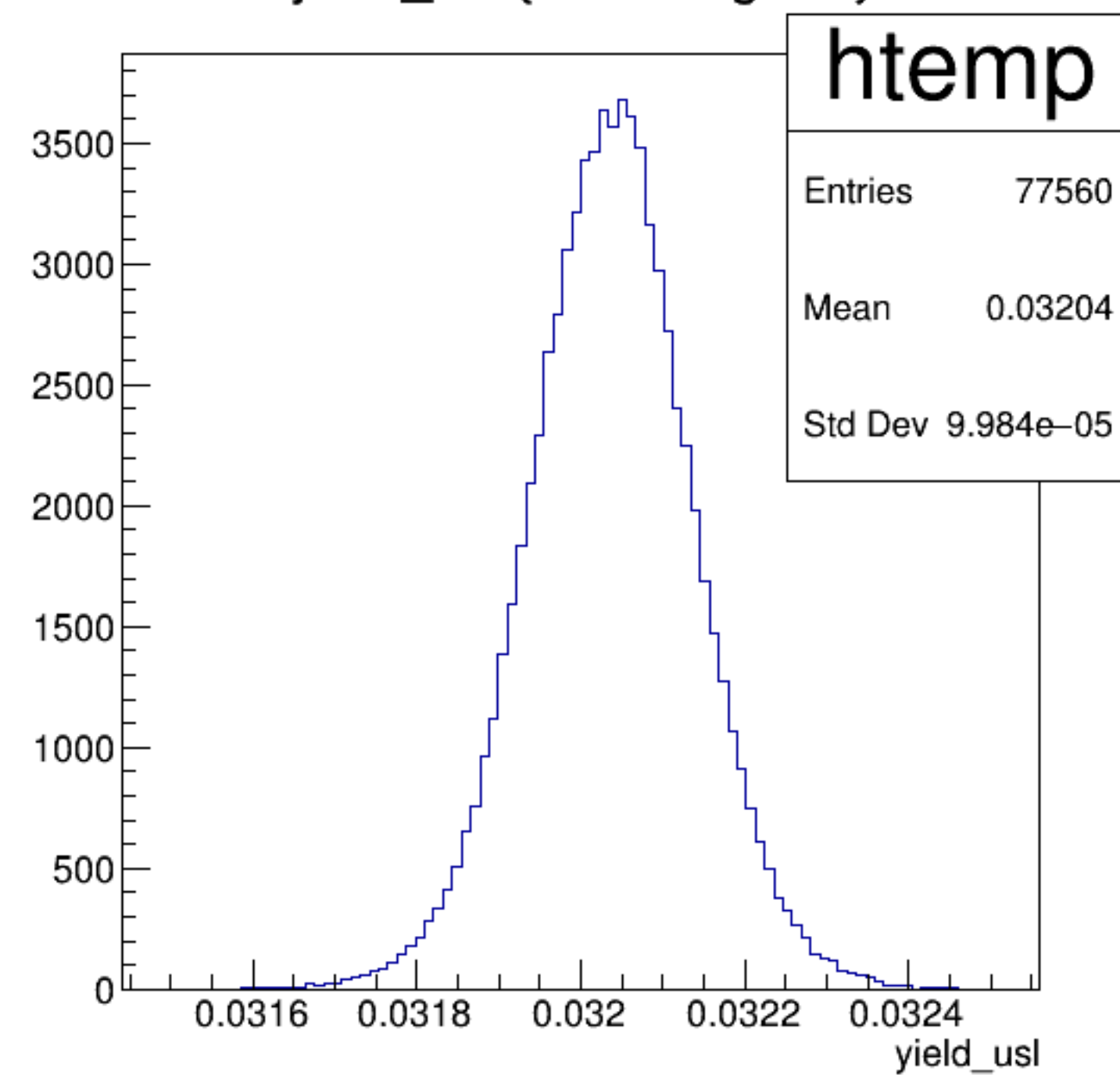
Entries	38767
Mean	-1.032
Std Dev	387
Underflow	0
Overflow	0

yield_usl:pattern_number {ErrorFlag==0}

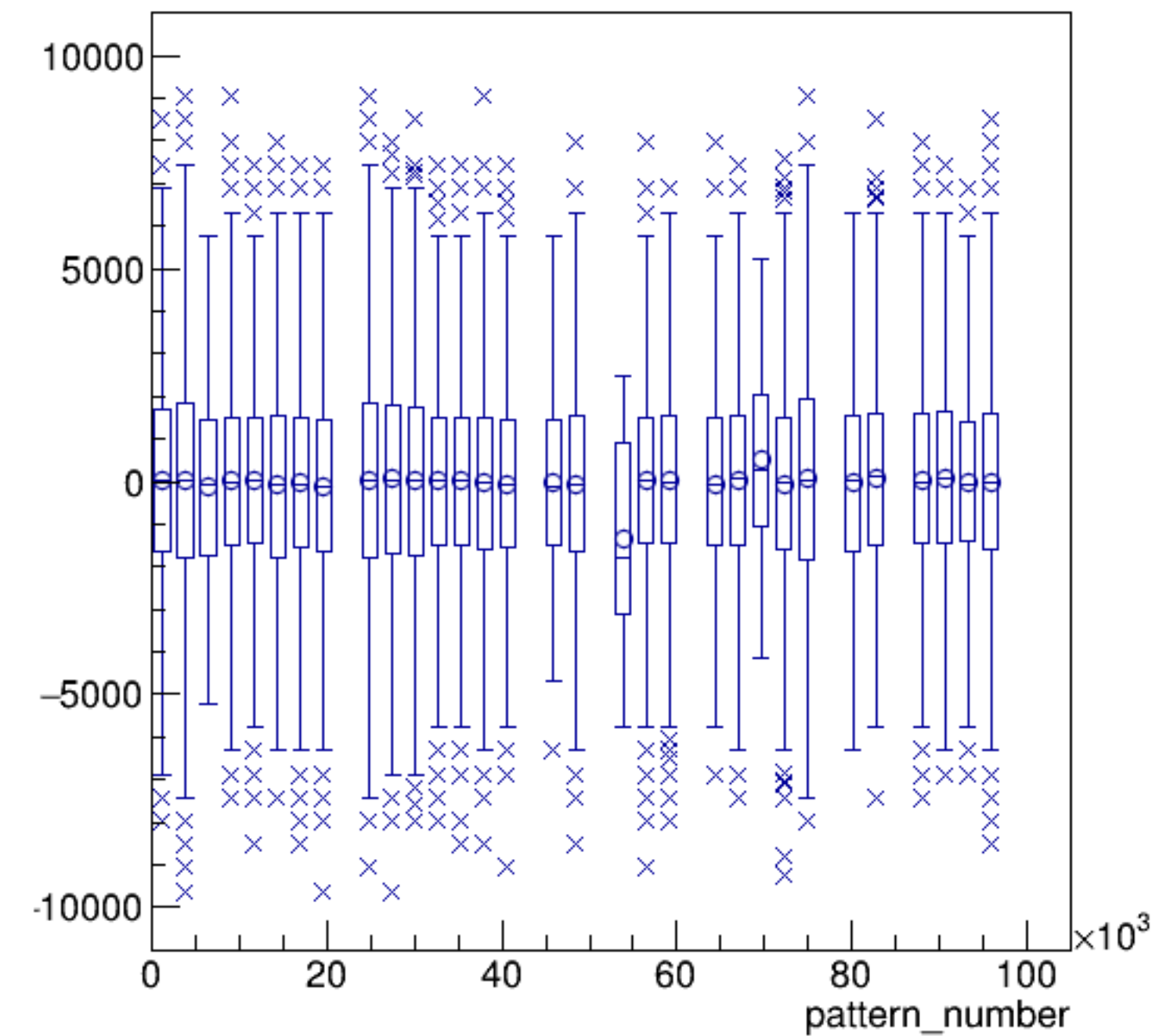


run5929_000_pairTree_usl.png

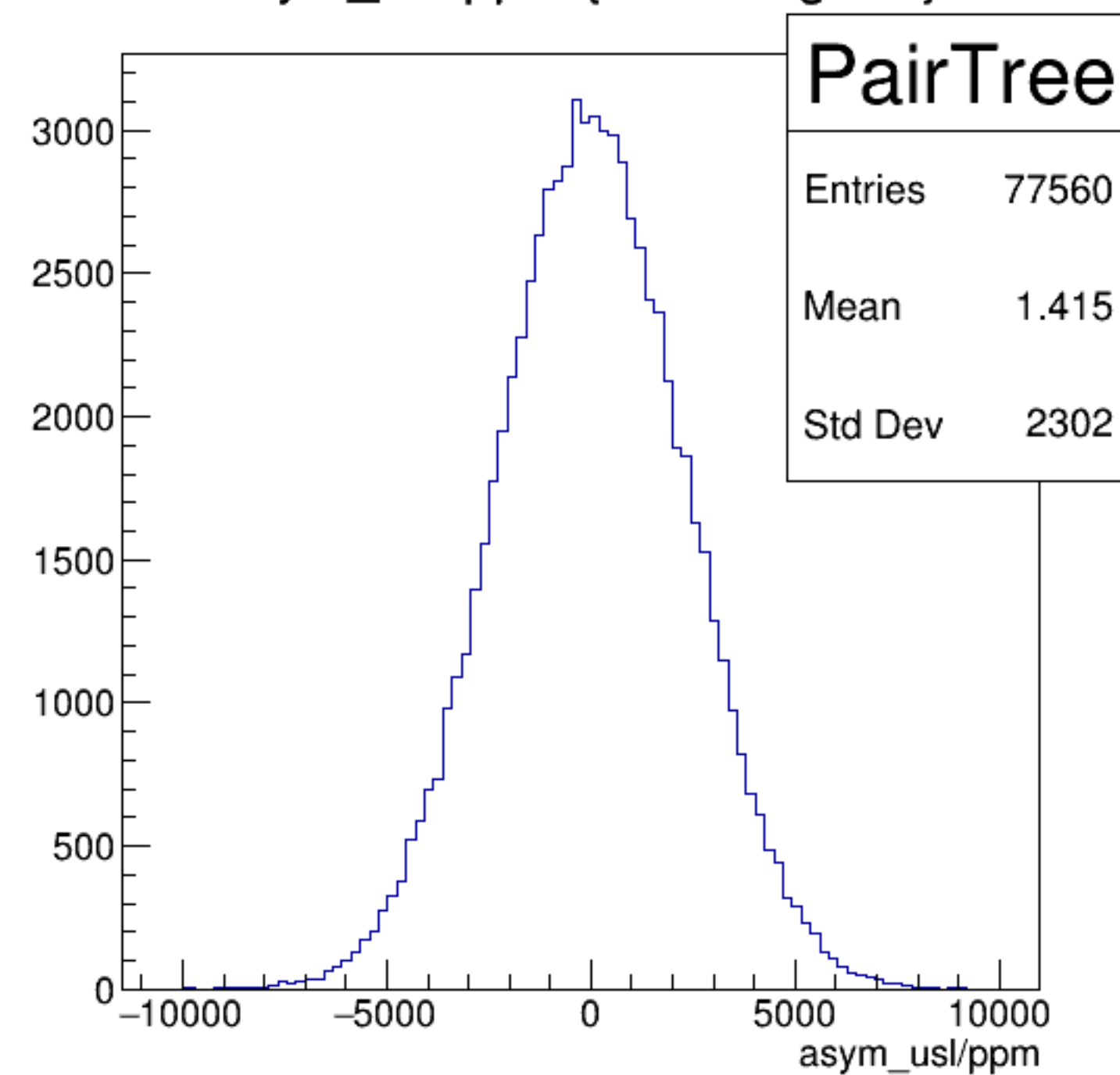
yield_usl {ErrorFlag==0}



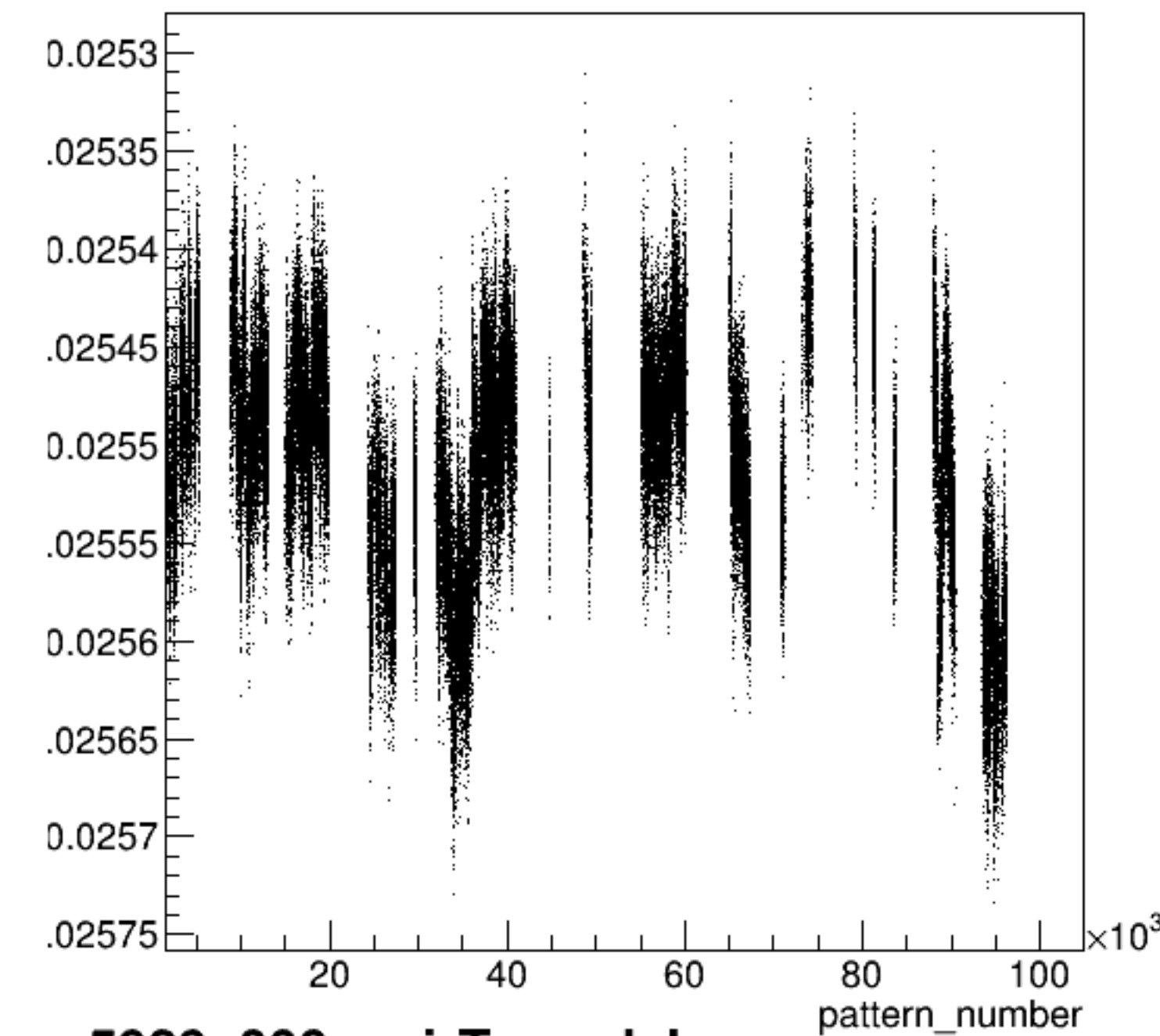
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl/ppm {ErrorFlag==0}

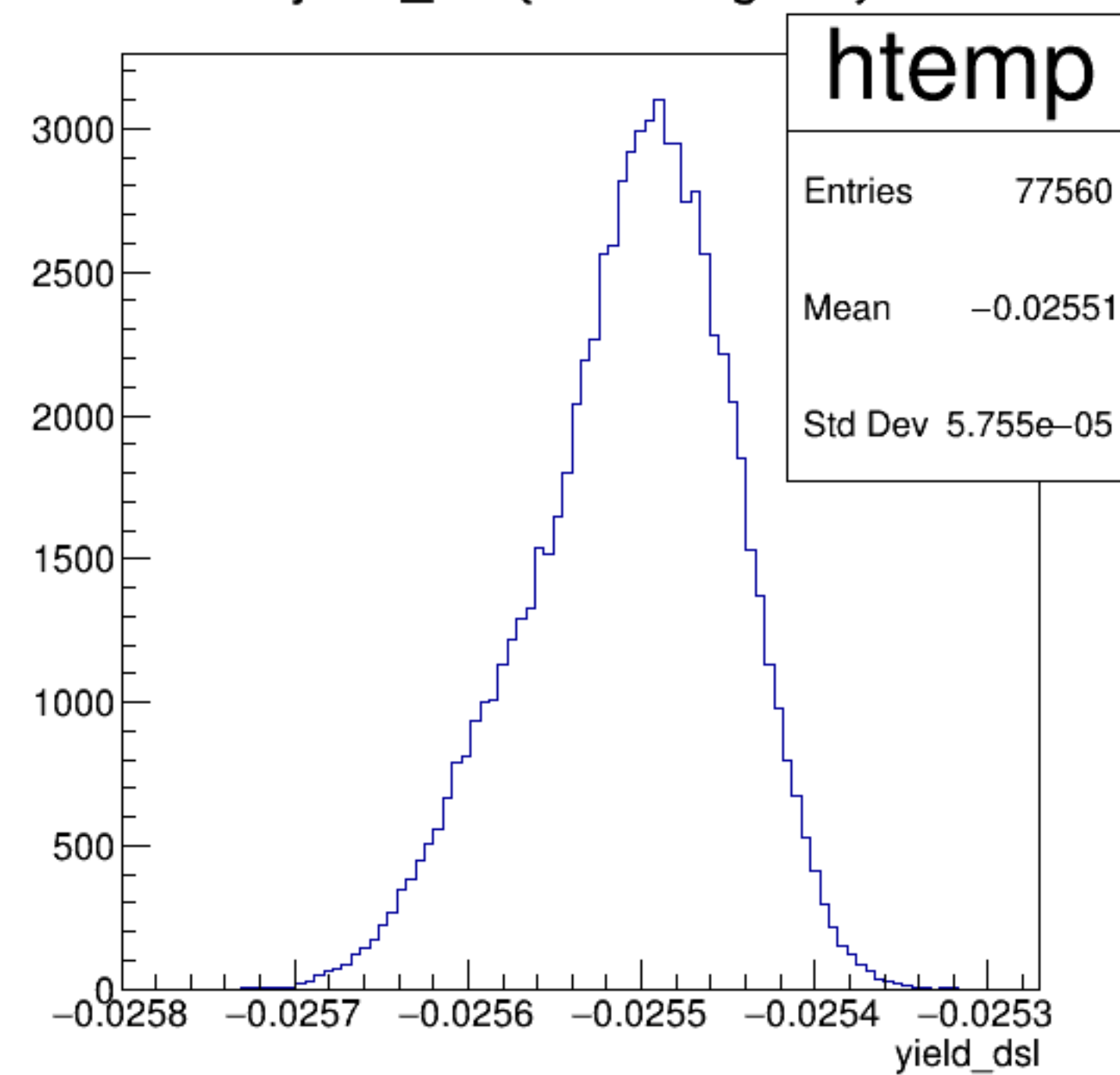


yield_dsl:pattern_number {ErrorFlag==0}

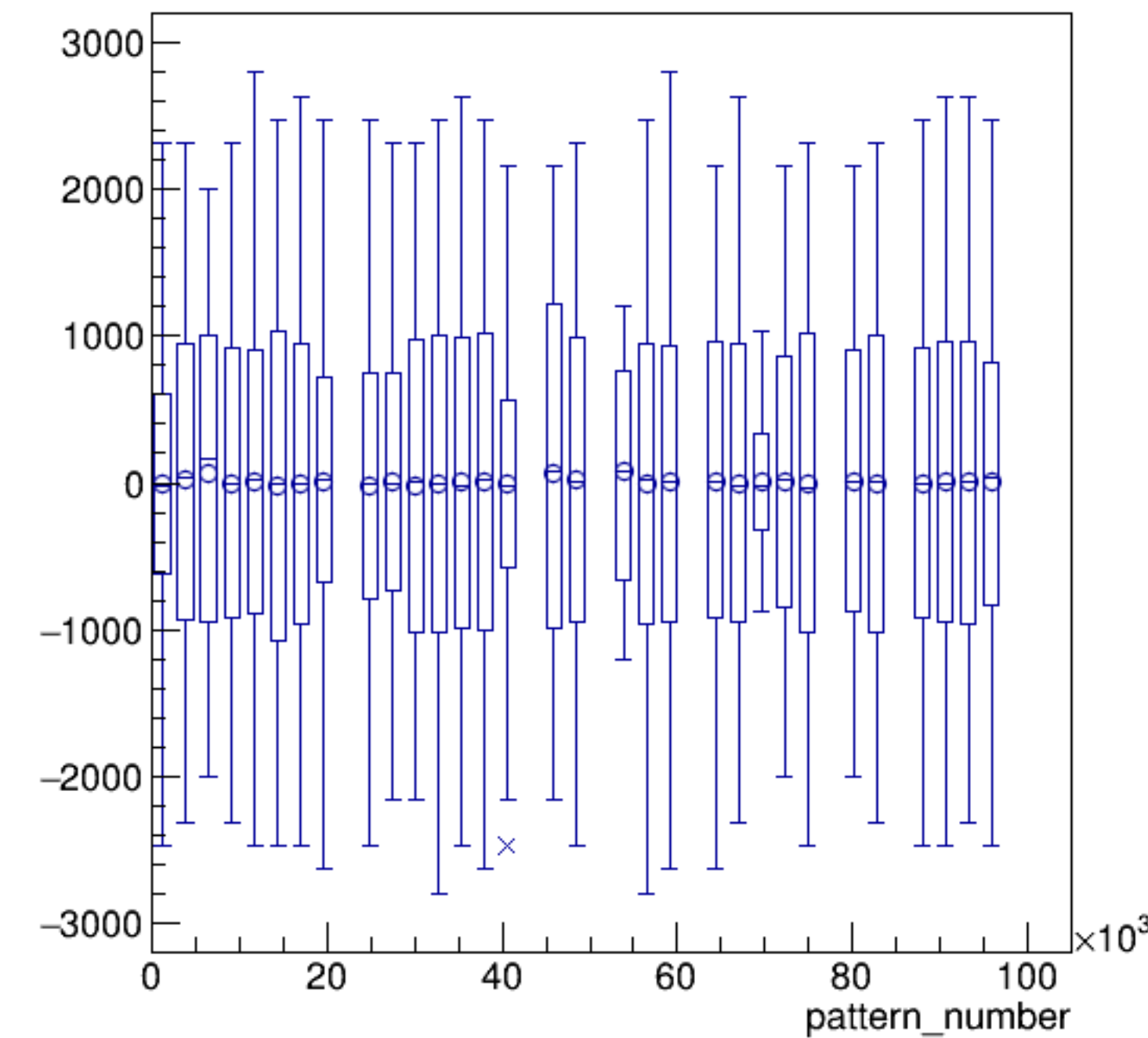


run5929_000_pairTree_dsl.png

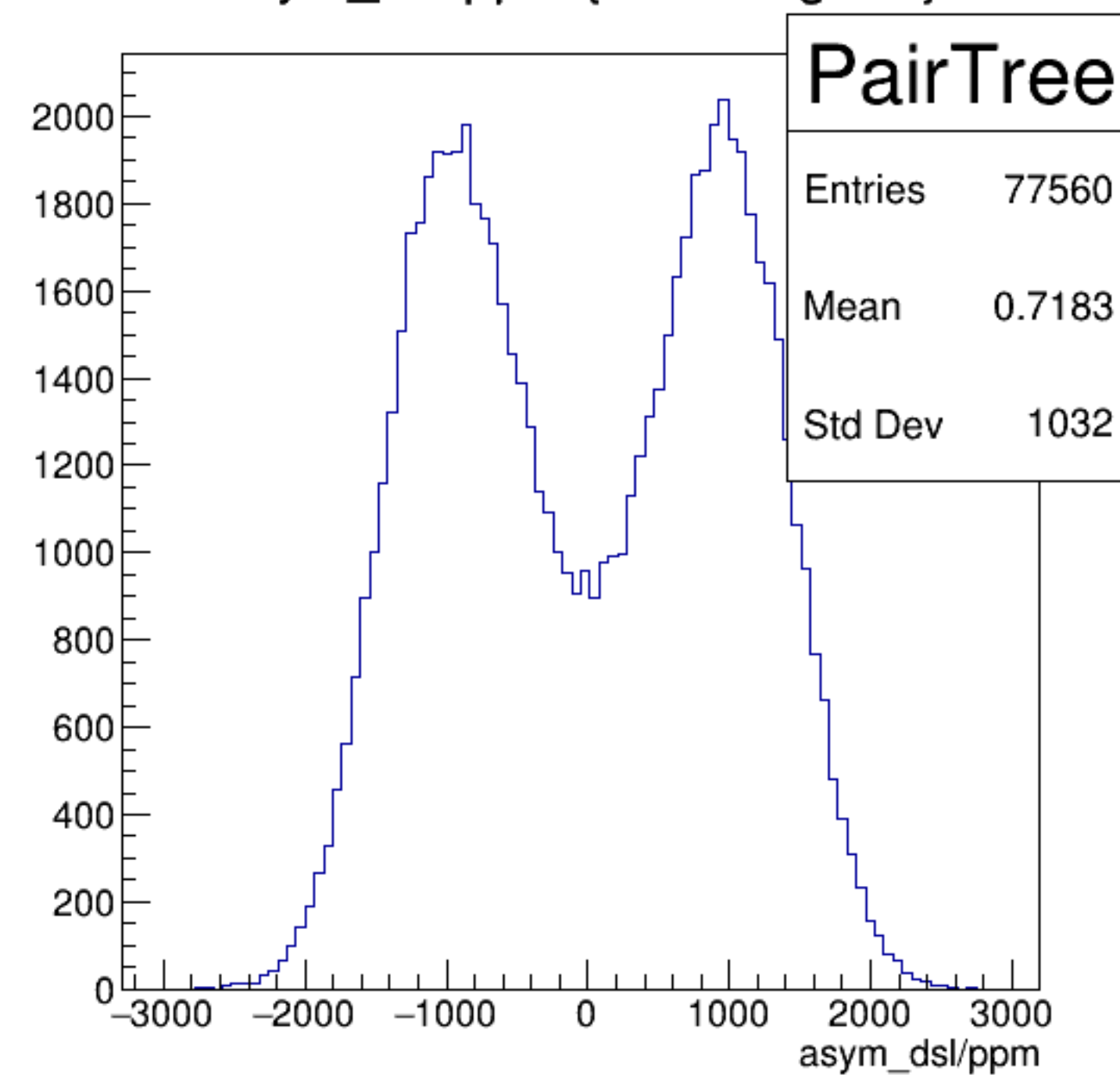
yield_dsl {ErrorFlag==0}

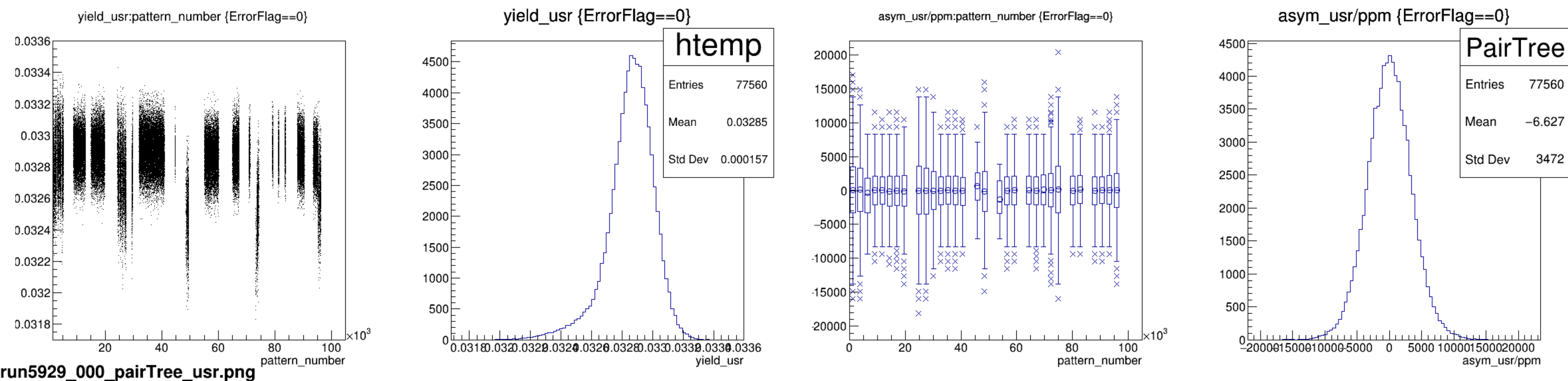


asym_dsl/ppm:pattern_number {ErrorFlag==0}

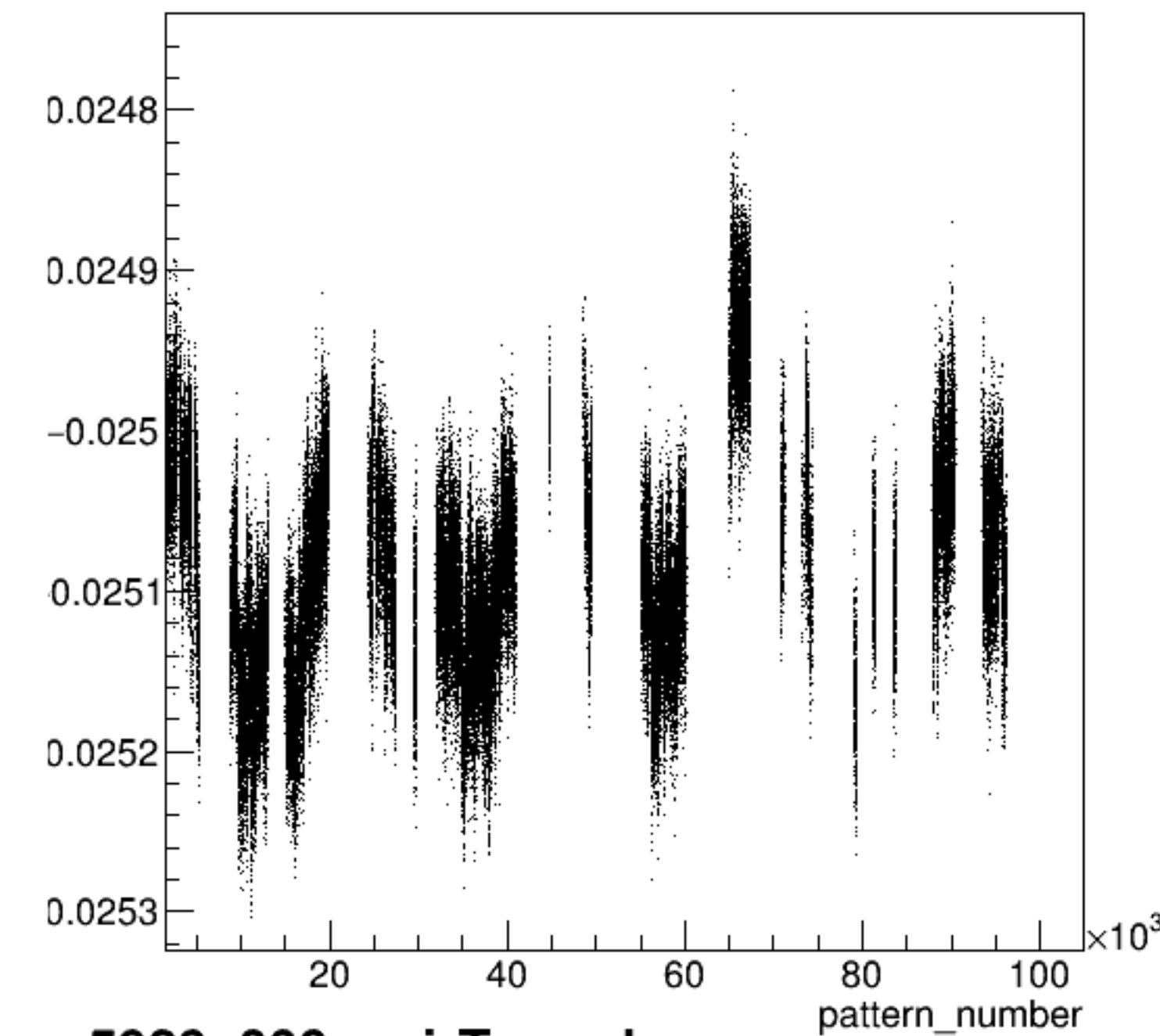


asym_dsl/ppm {ErrorFlag==0}



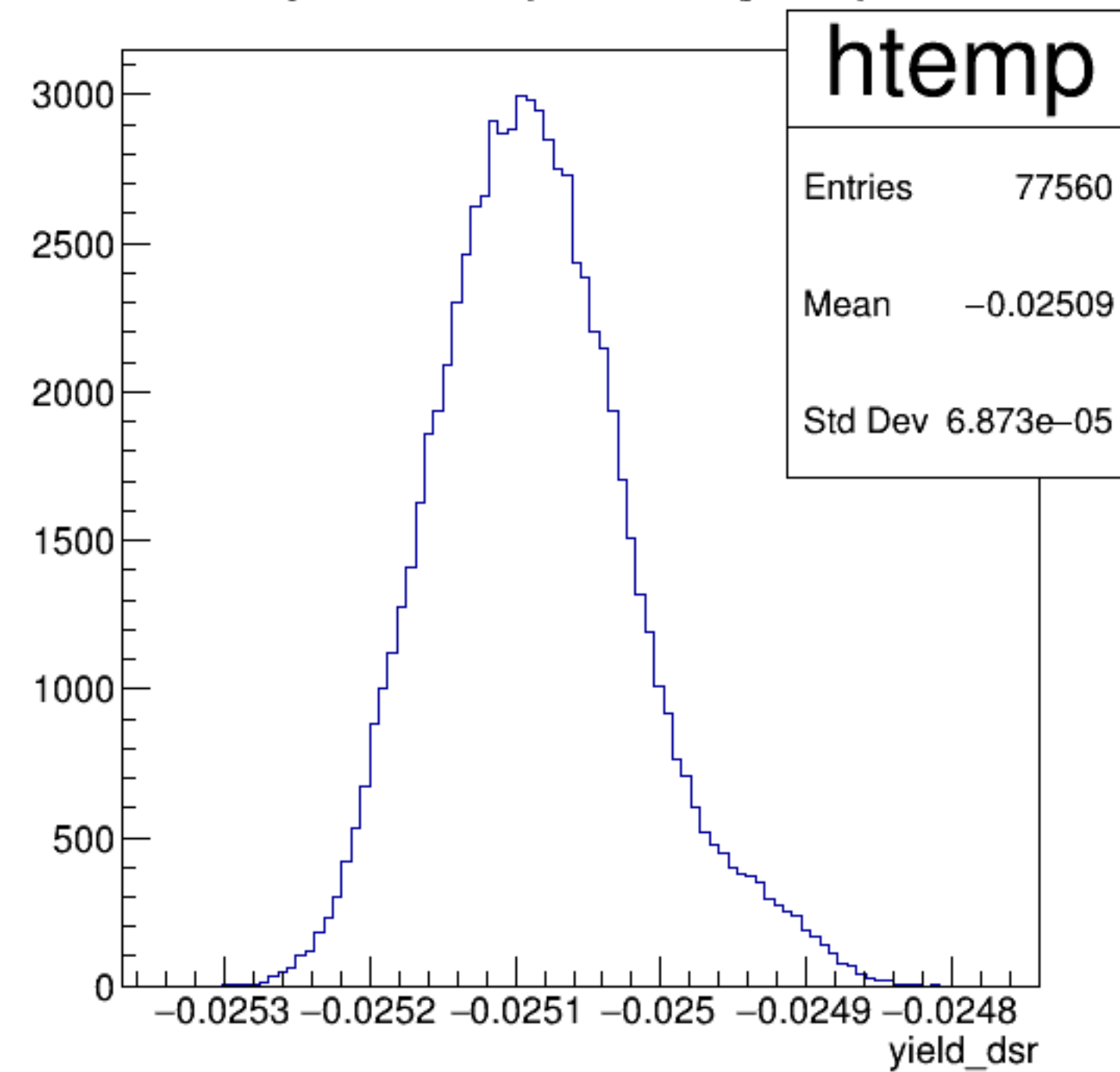


yield_dsr:pattern_number {ErrorFlag==0}

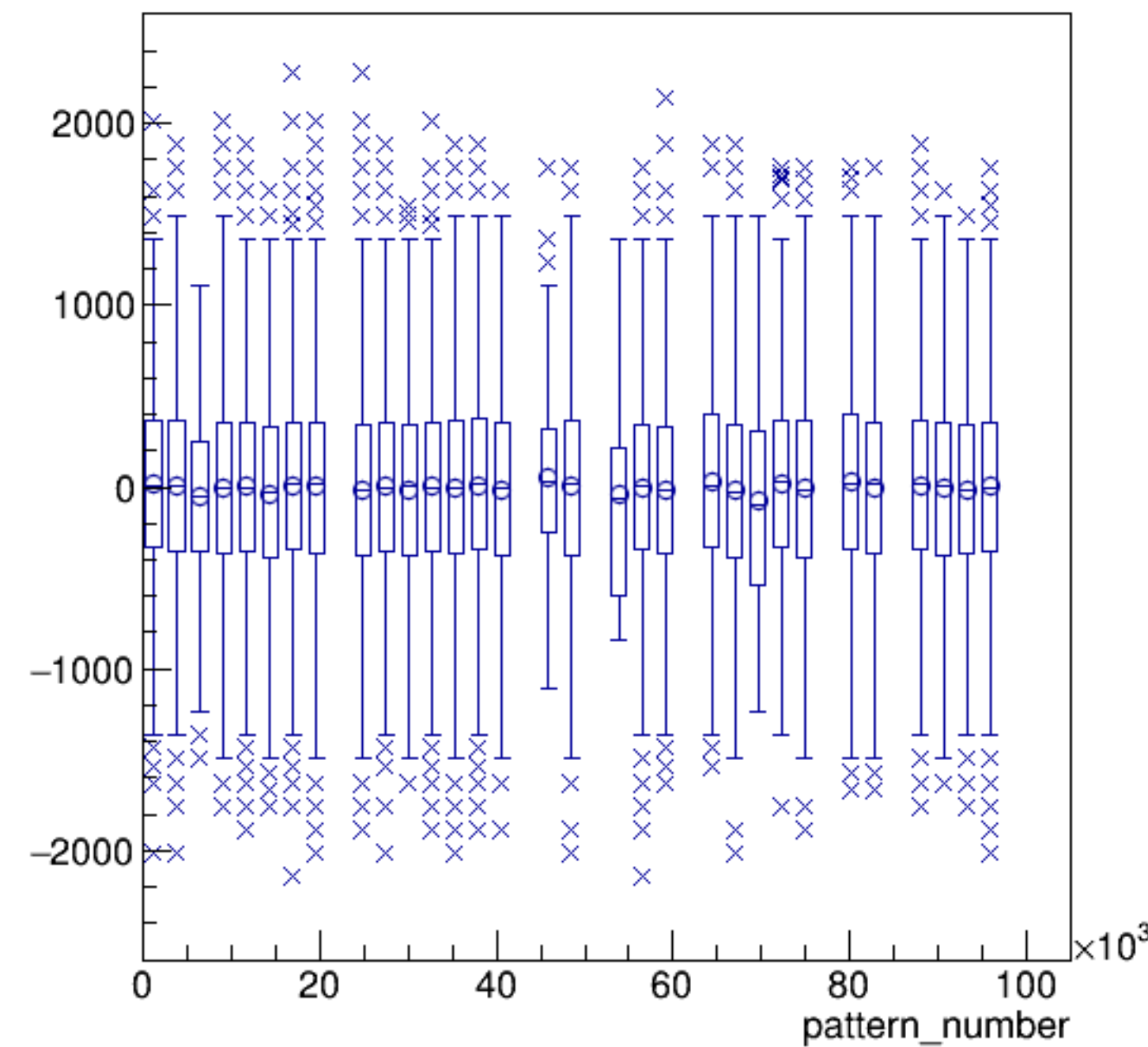


run5929_000_pairTree_dsr.png

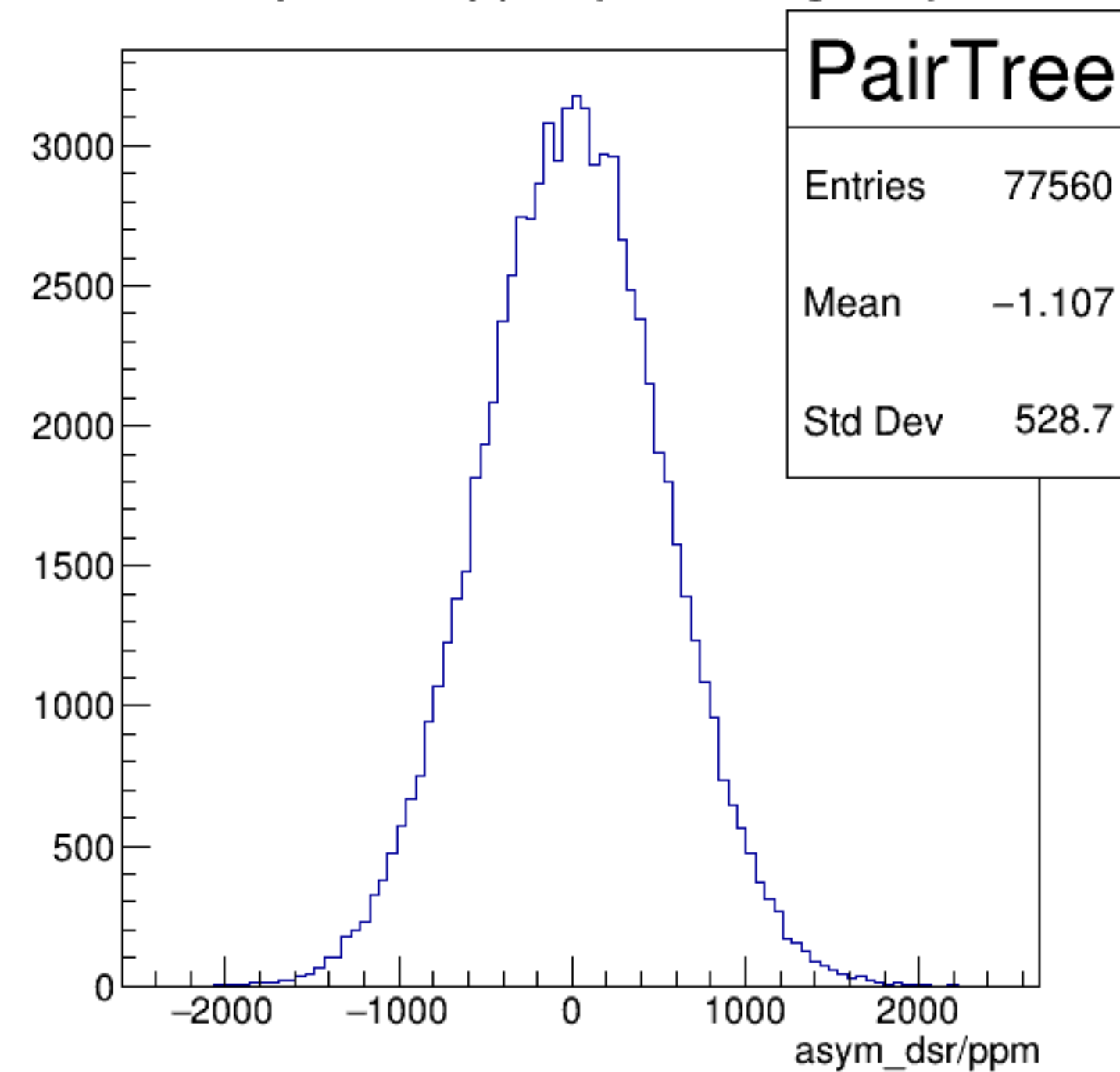
yield_dsr {ErrorFlag==0}



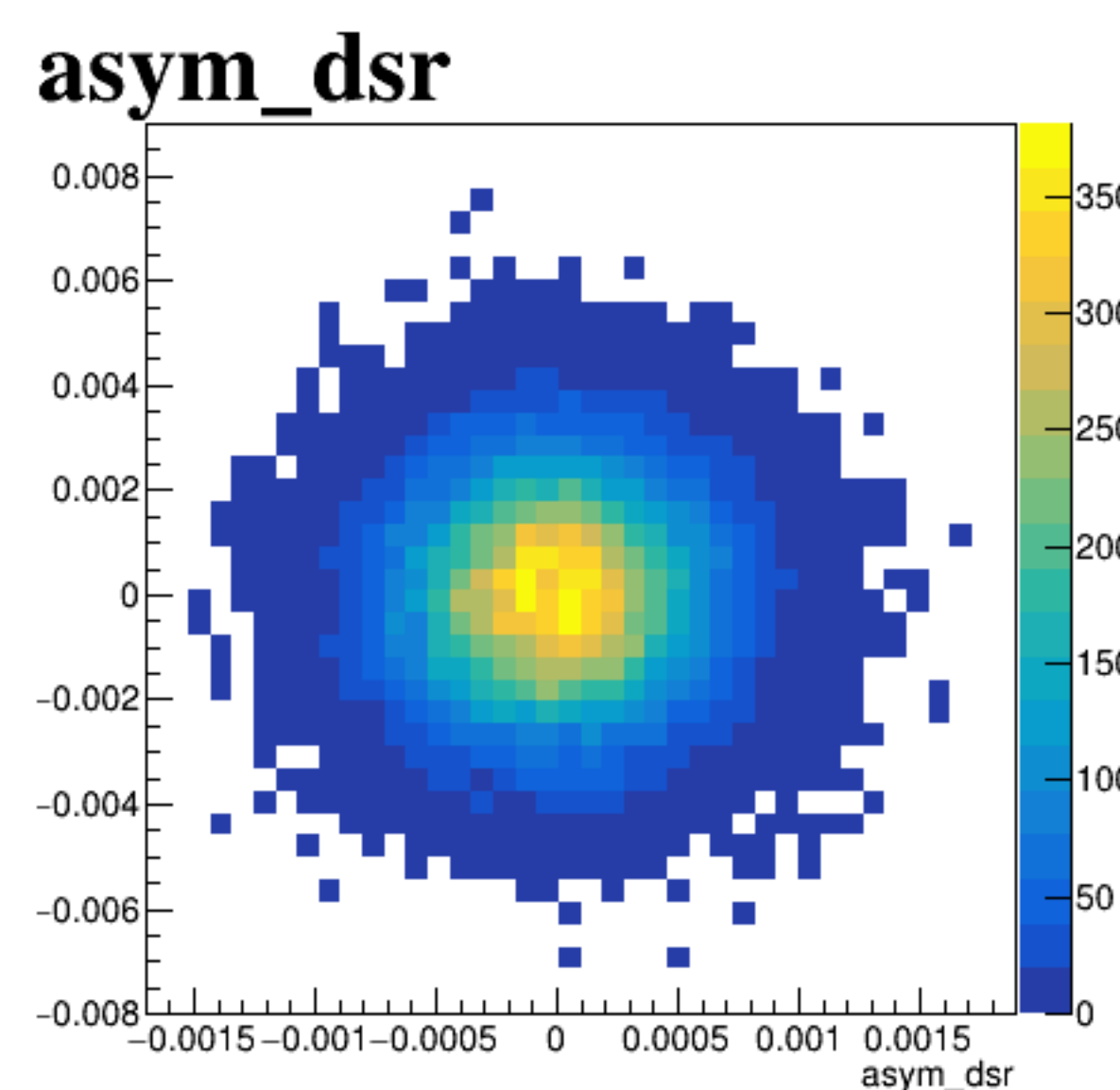
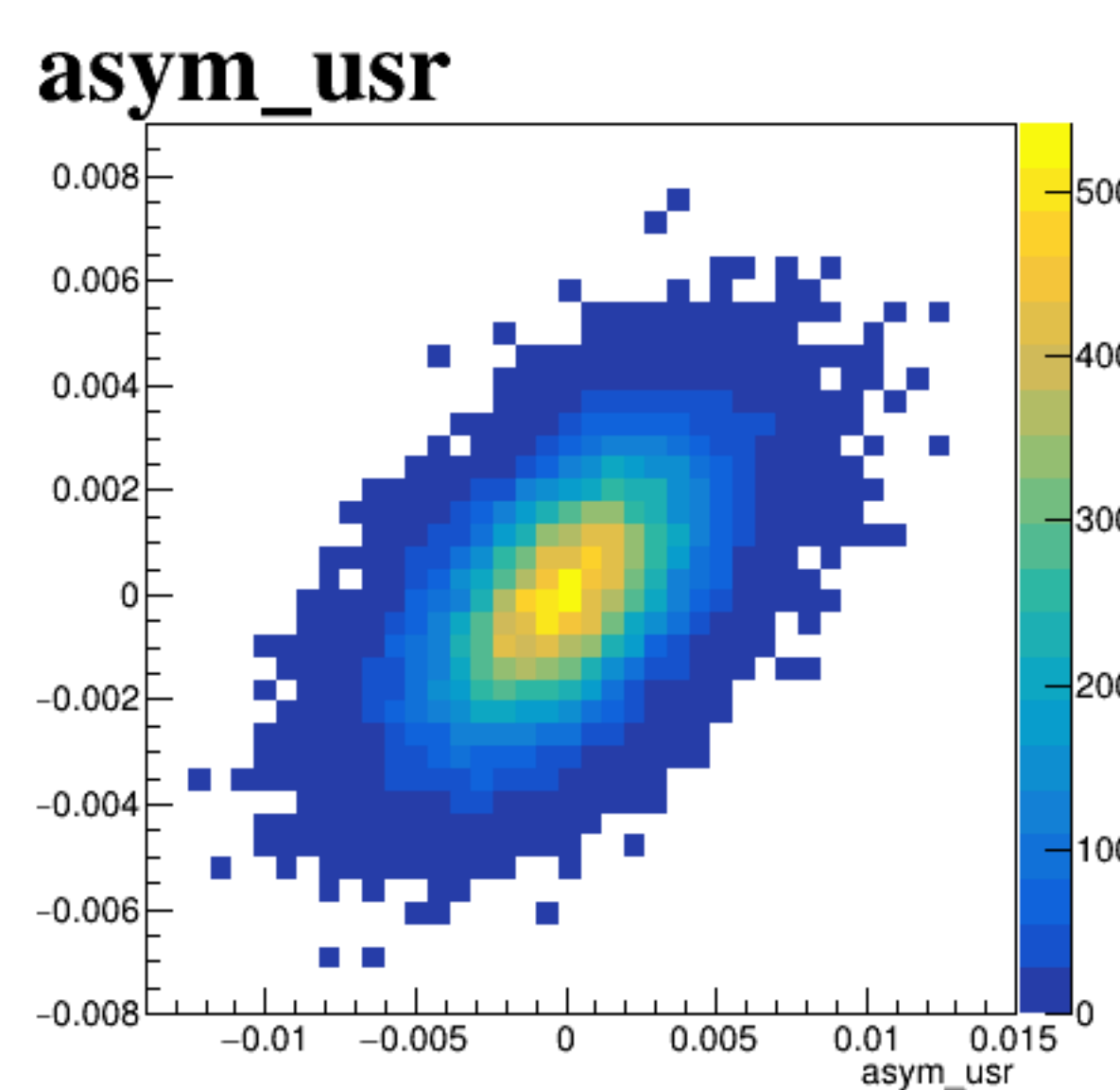
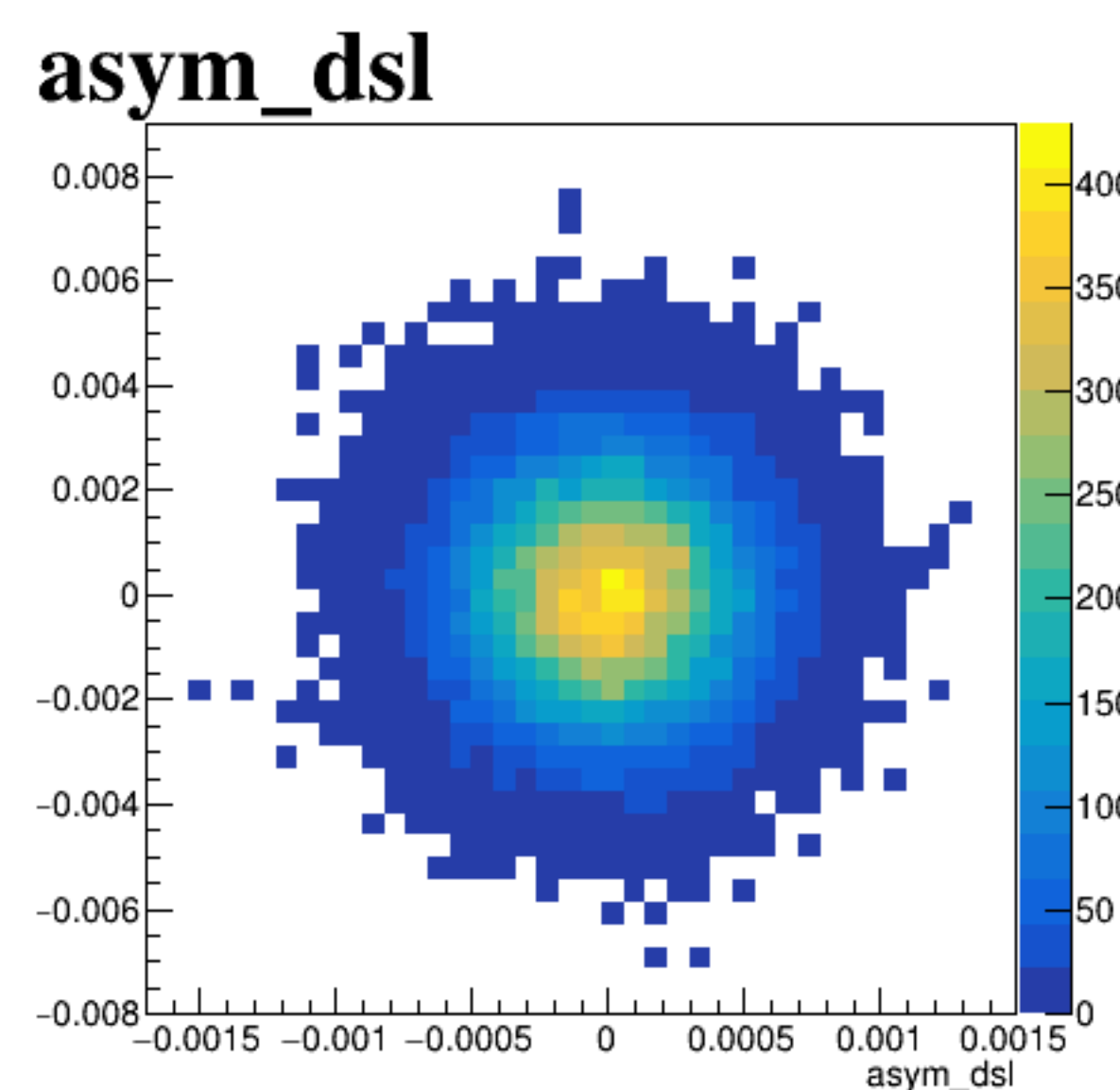
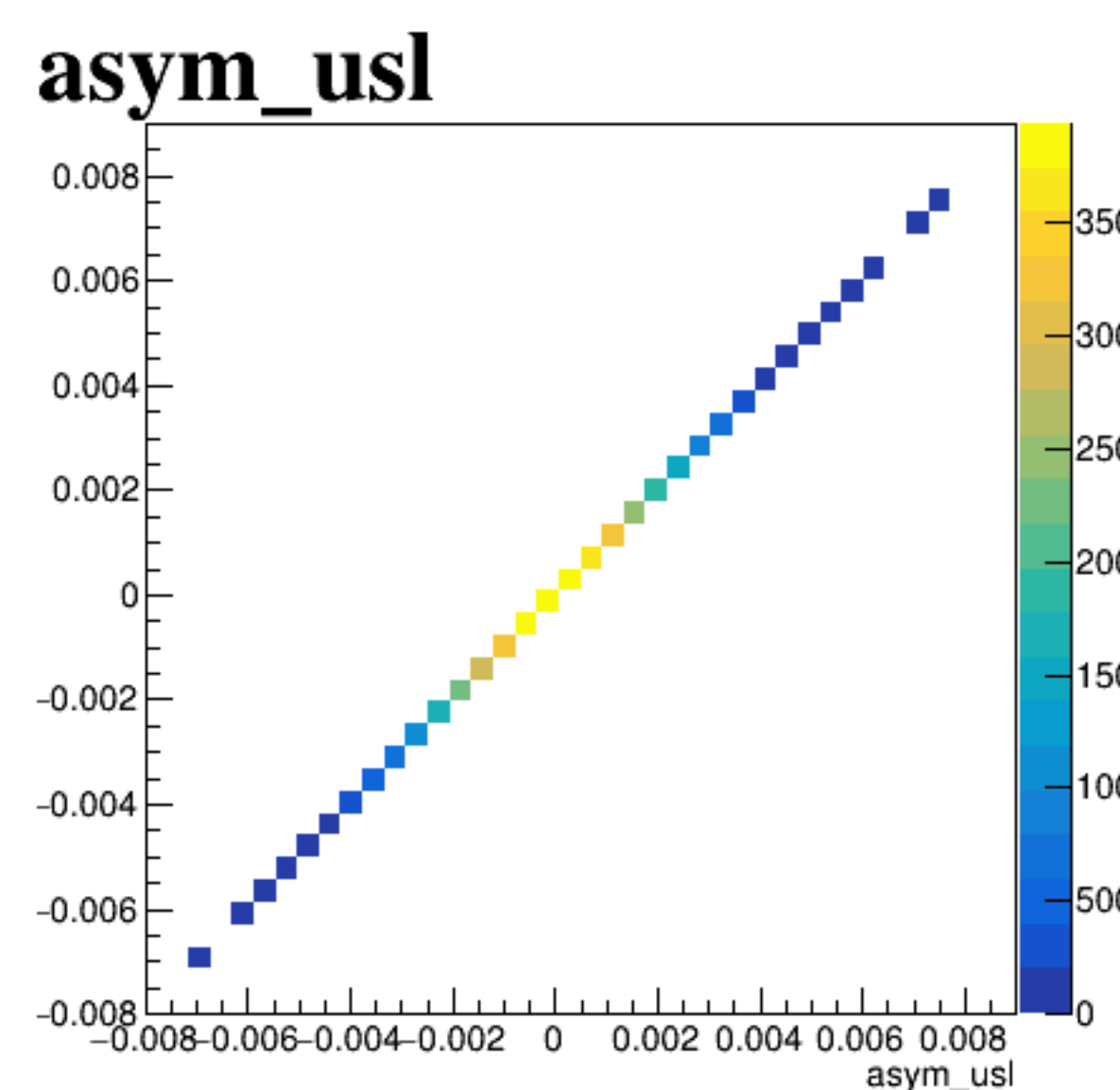
asym_dsr/ppm:pattern_number {ErrorFlag==0}



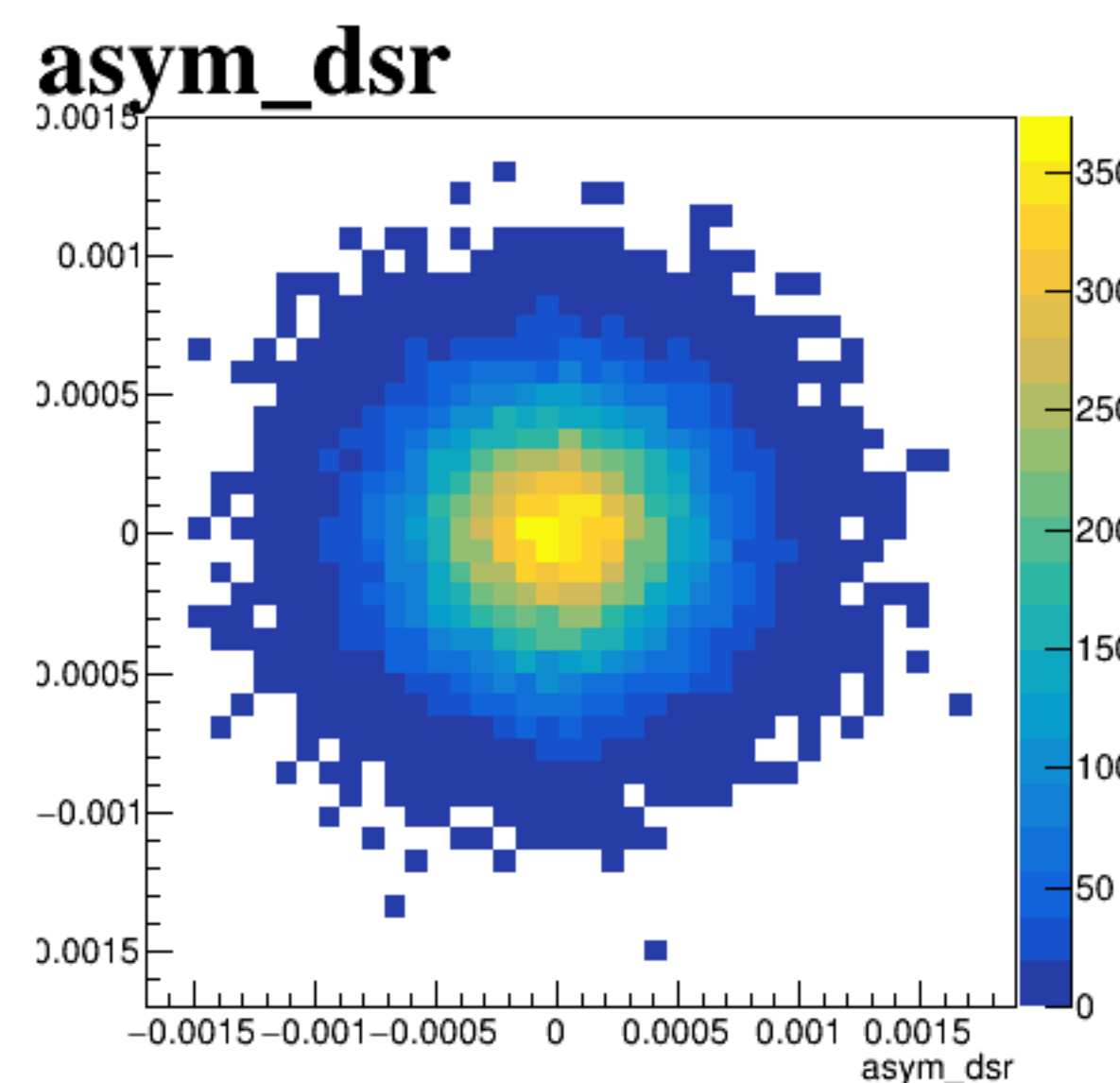
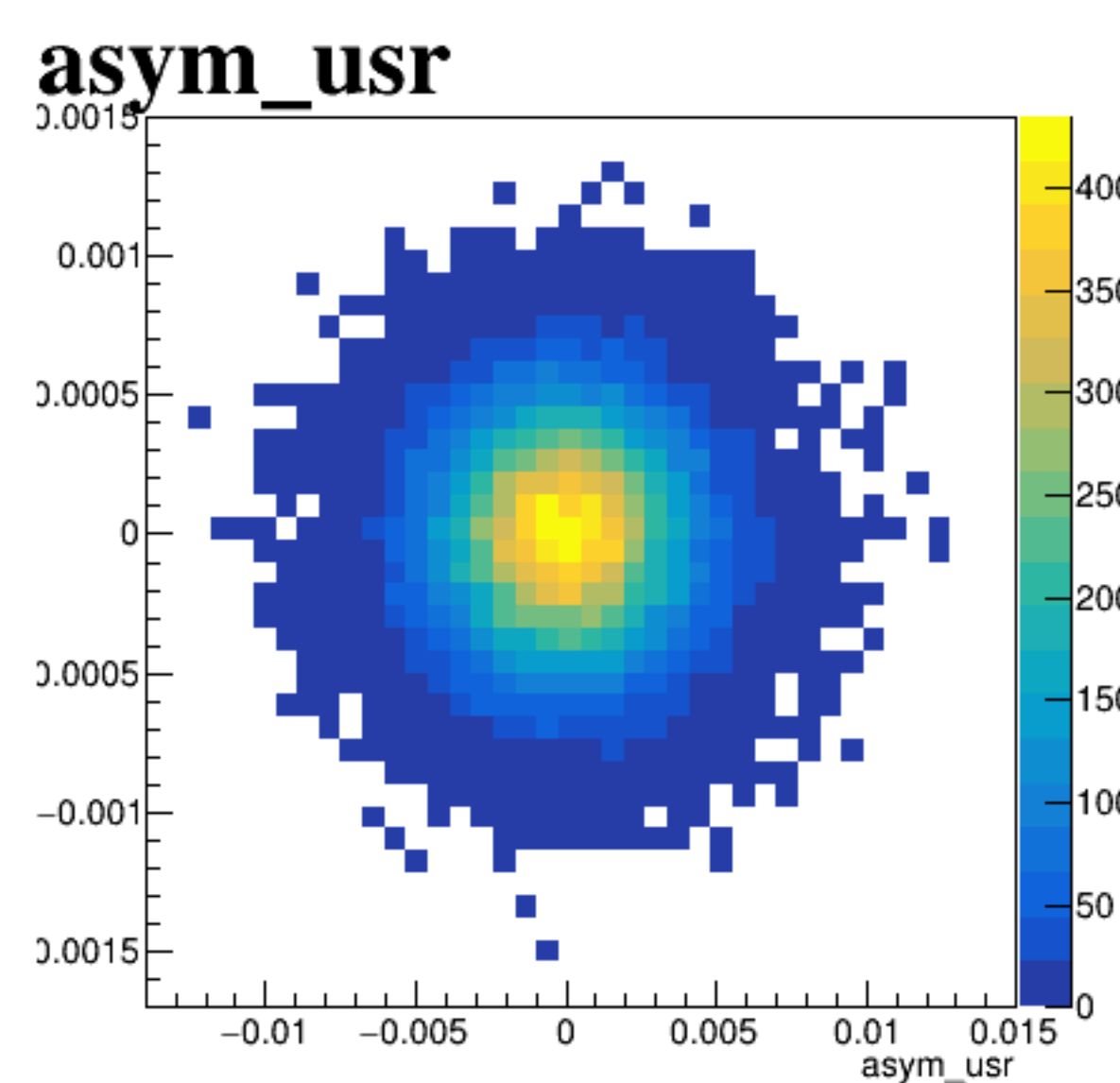
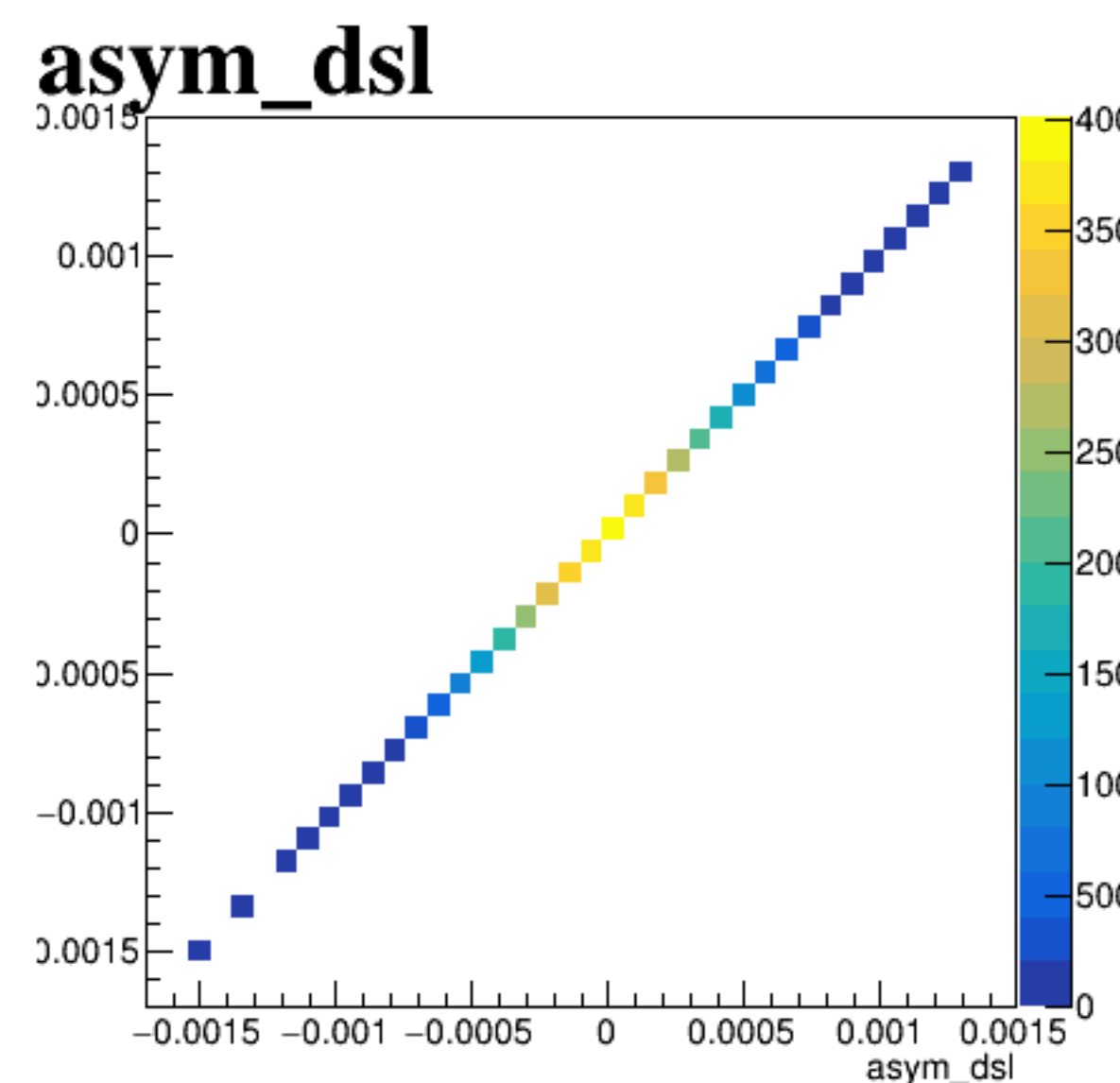
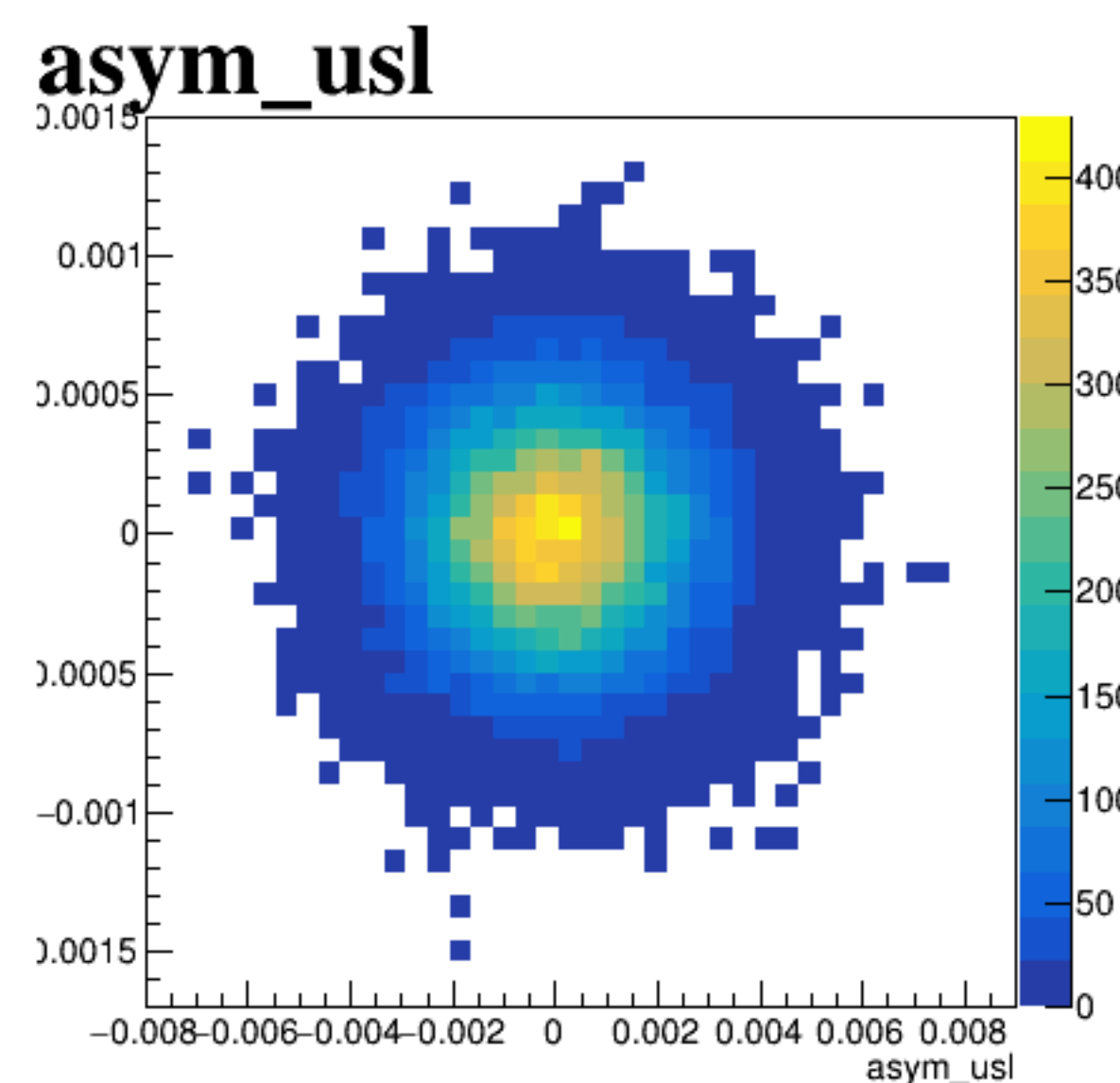
asym_dsr/ppm {ErrorFlag==0}



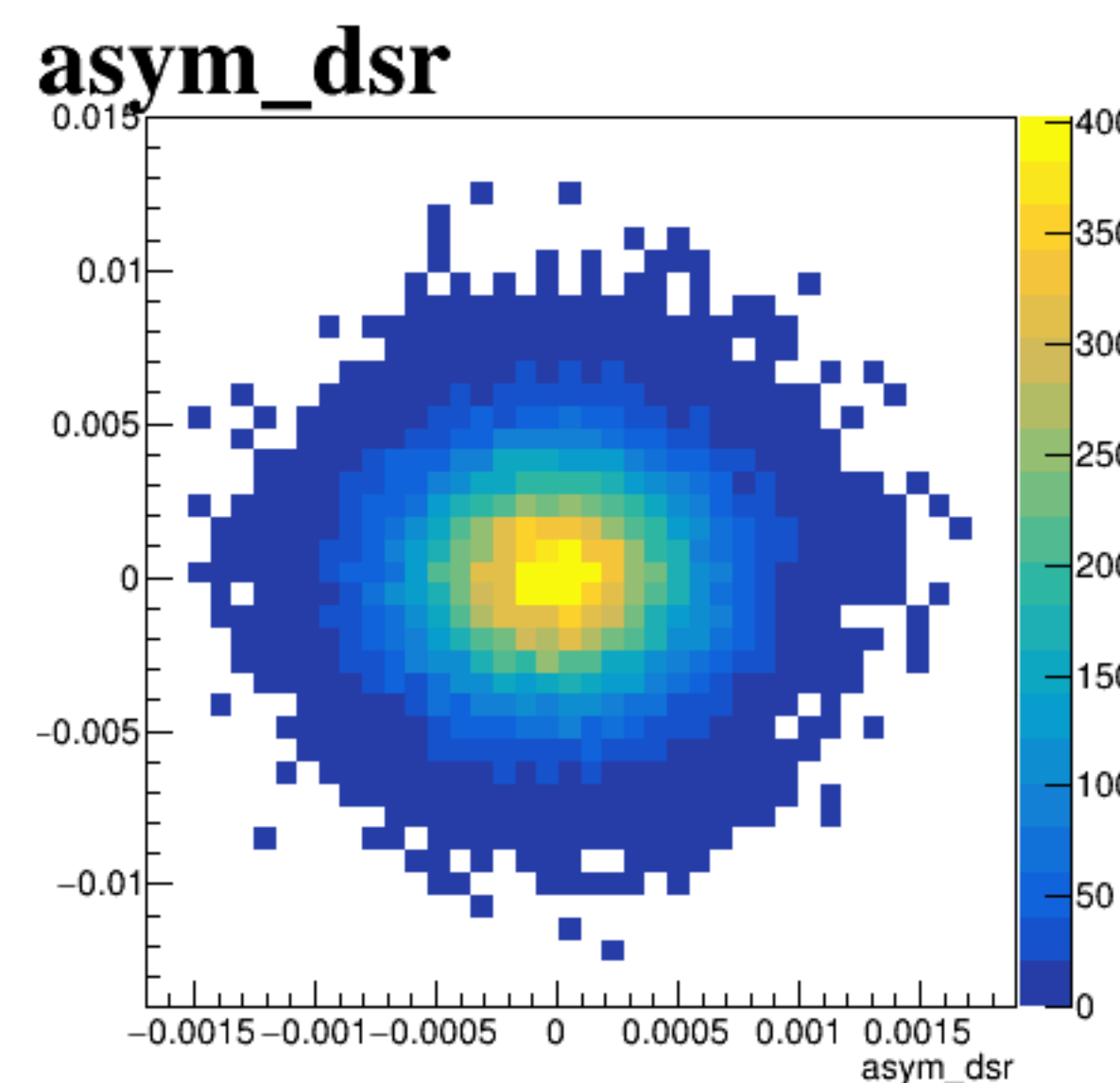
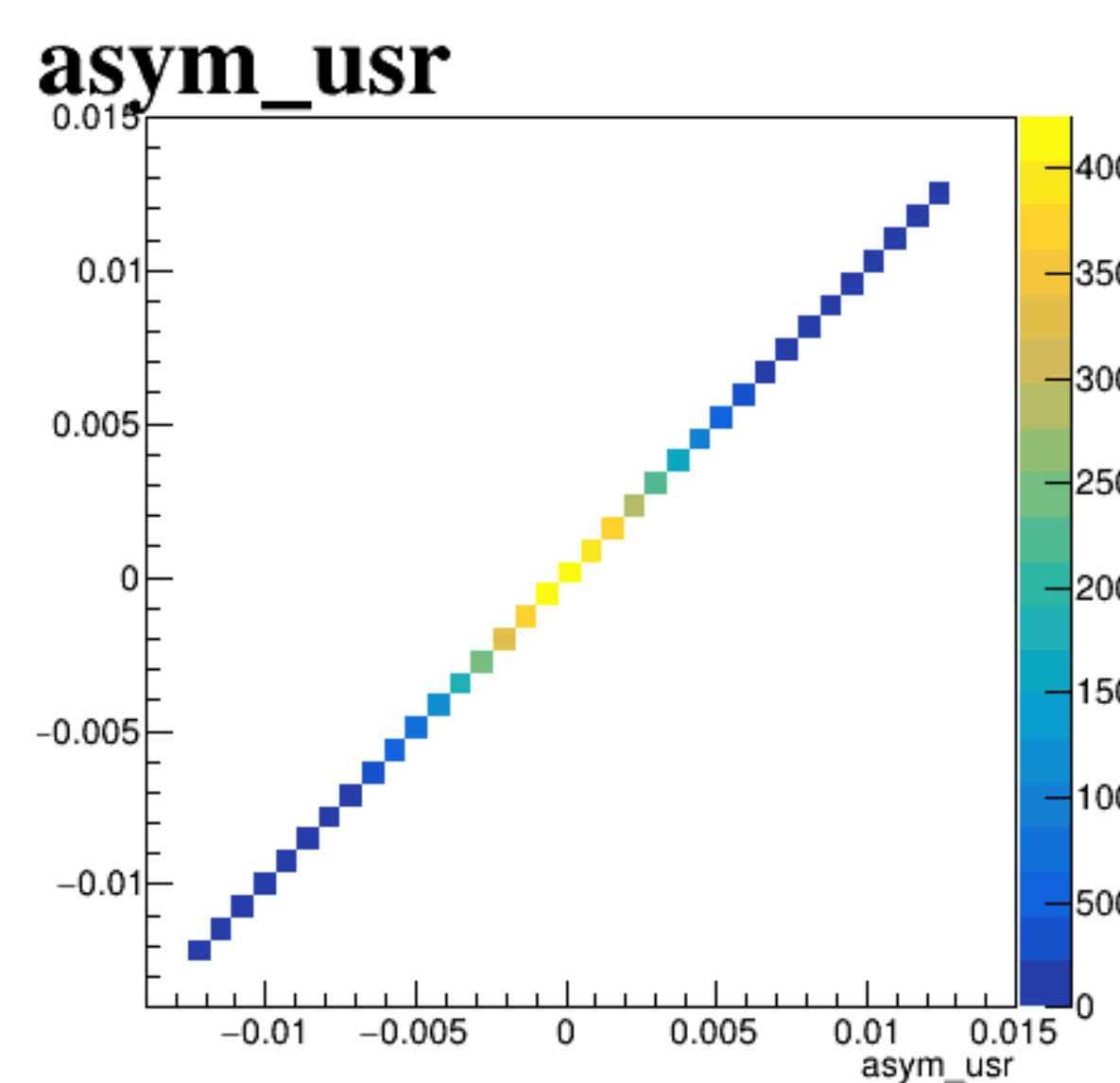
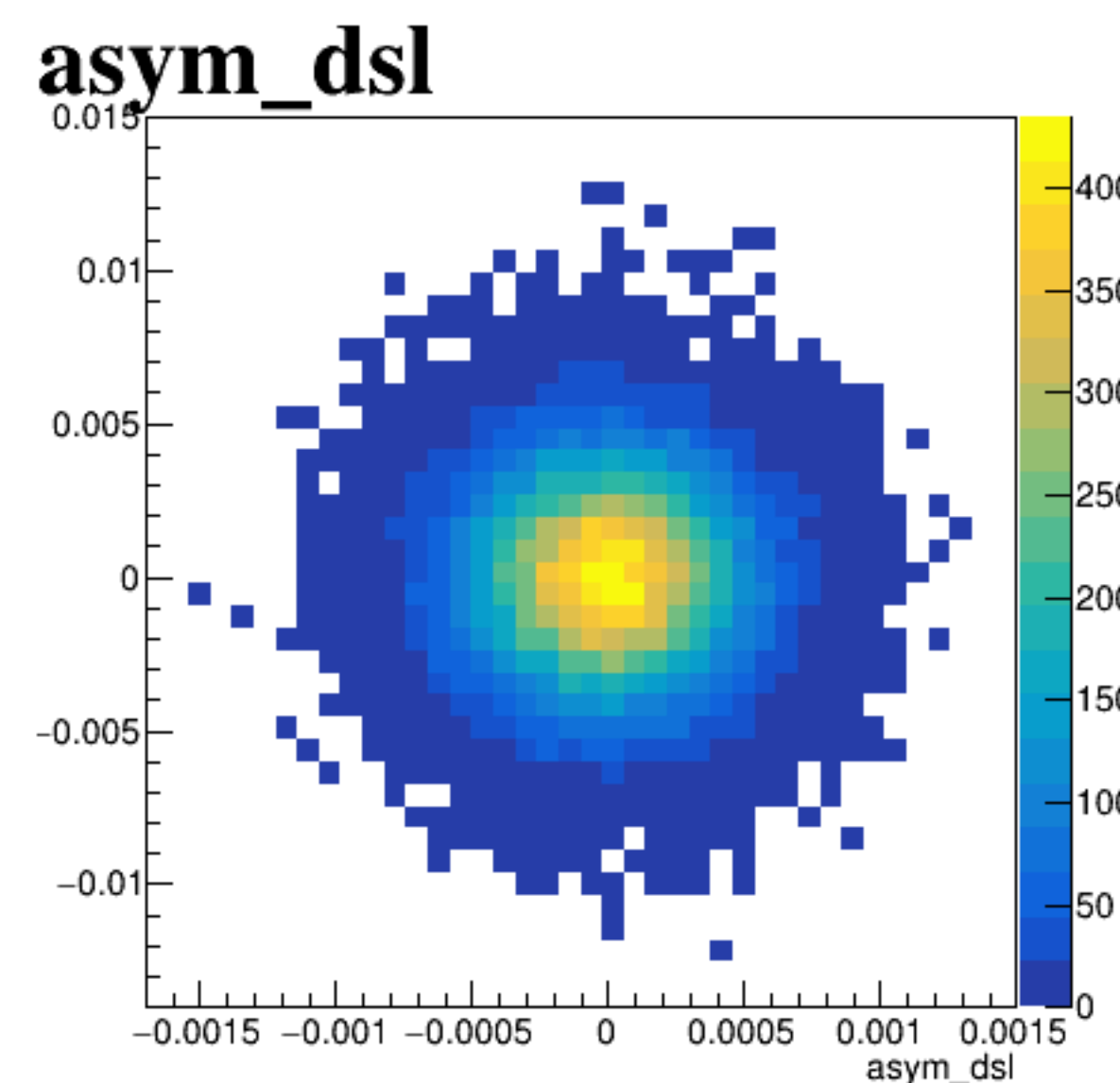
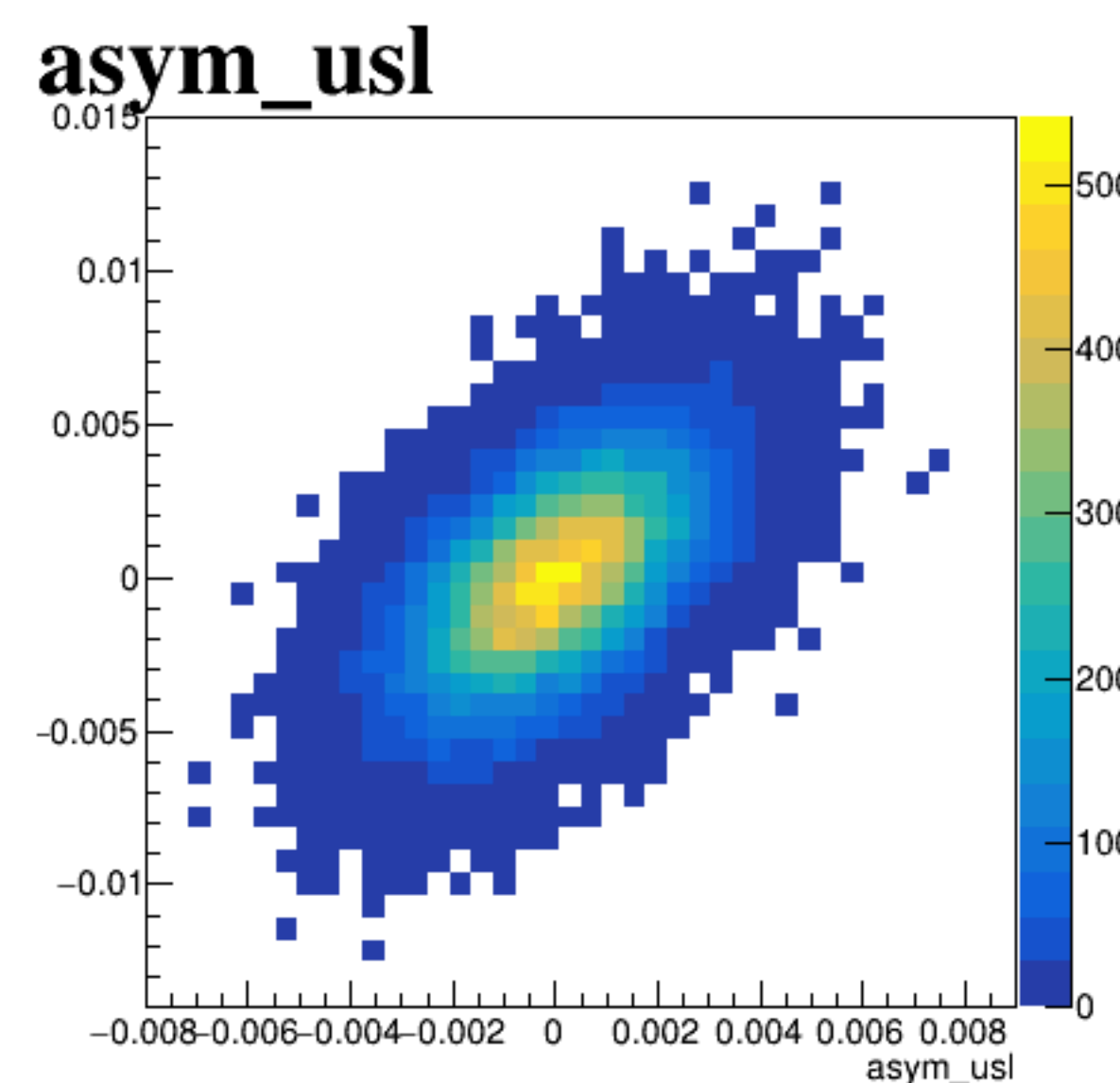
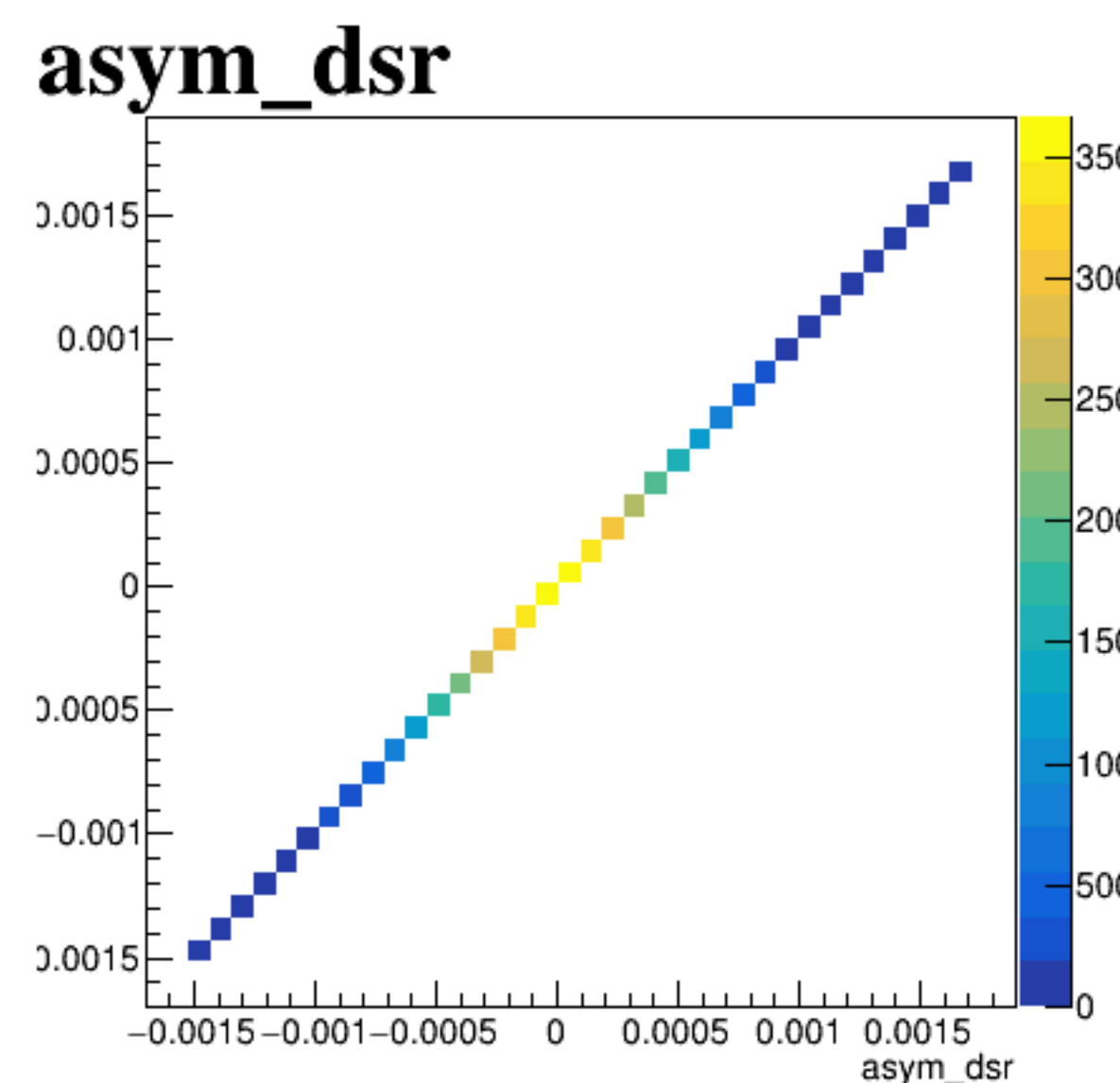
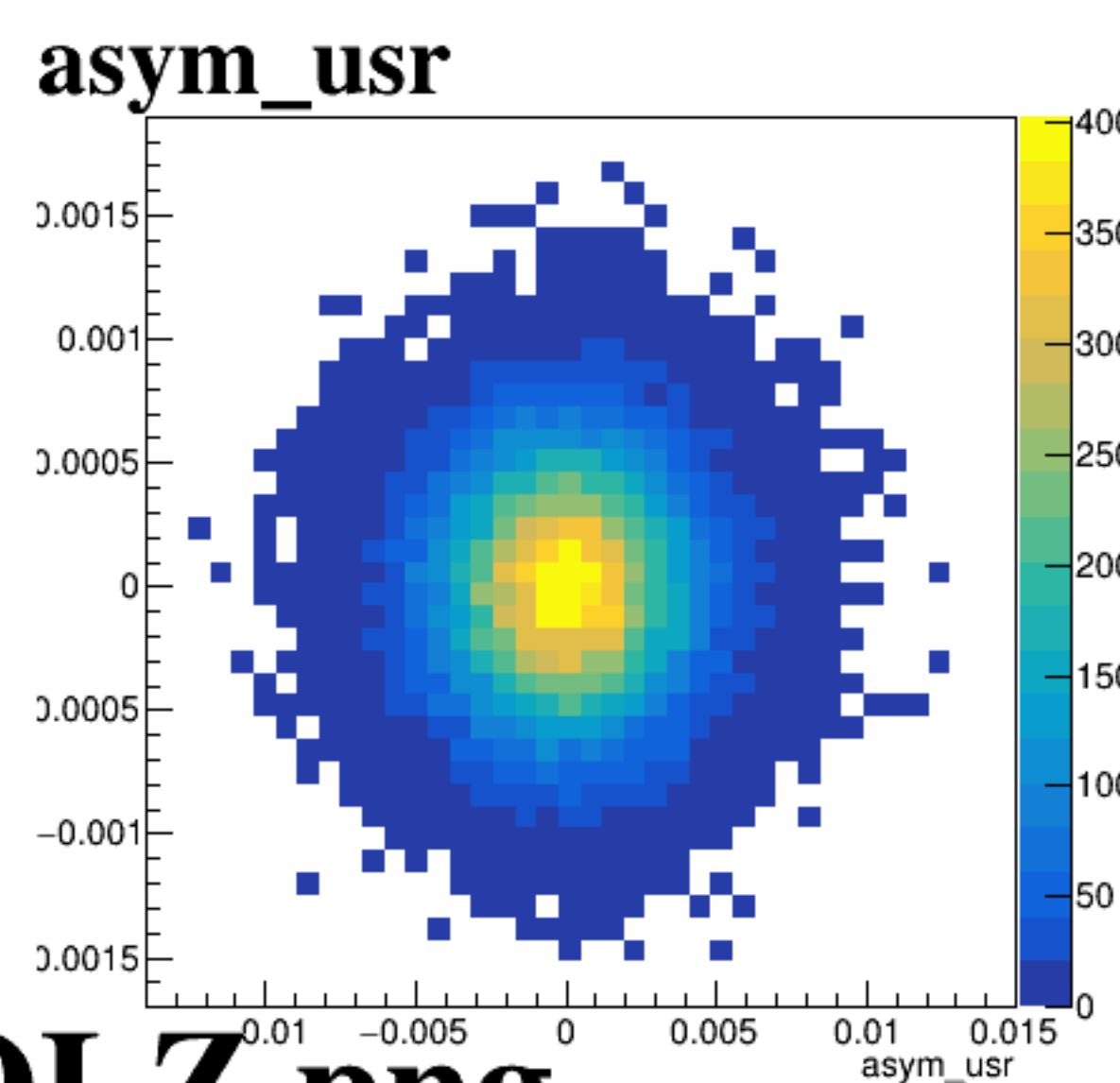
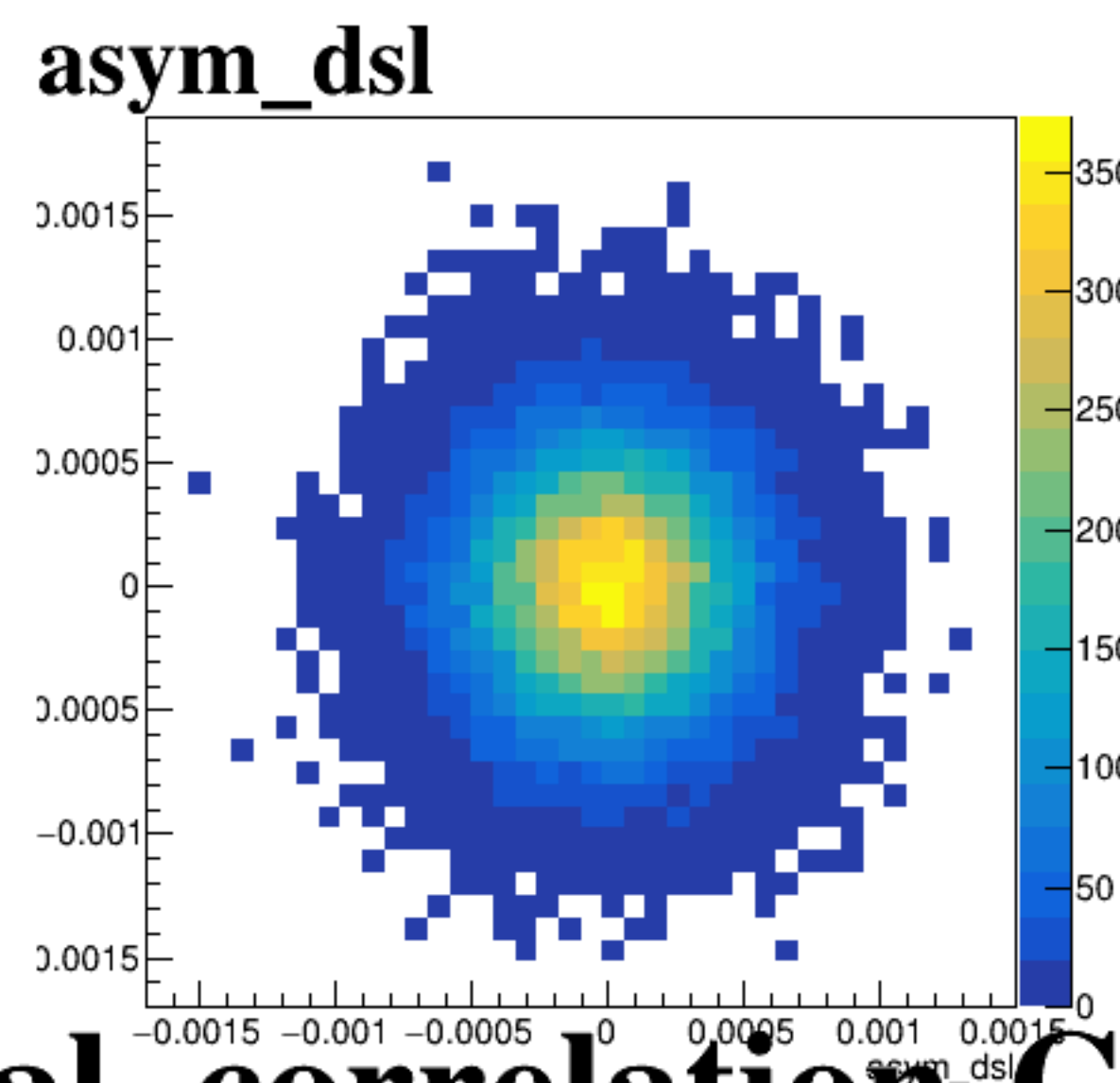
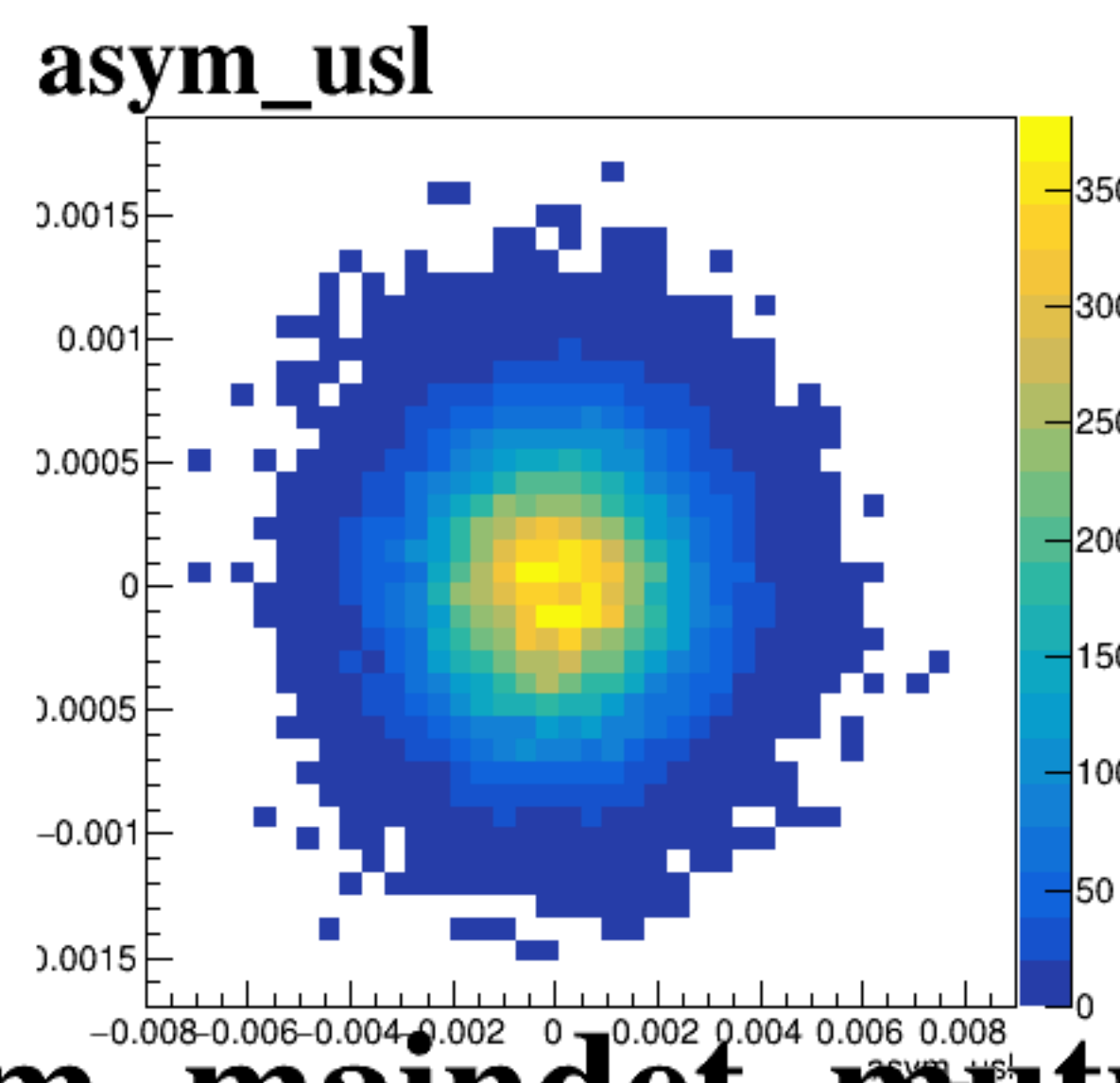
asym_usl



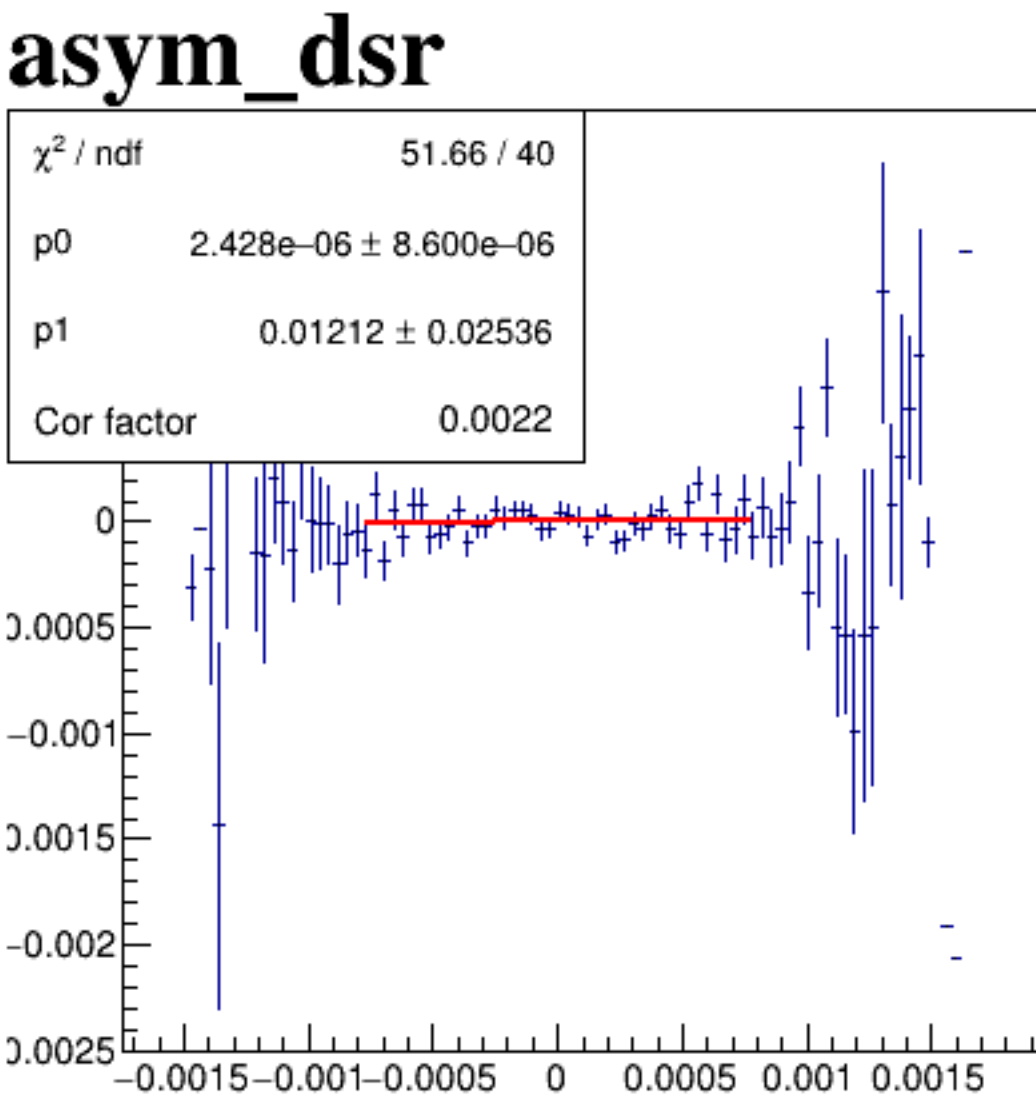
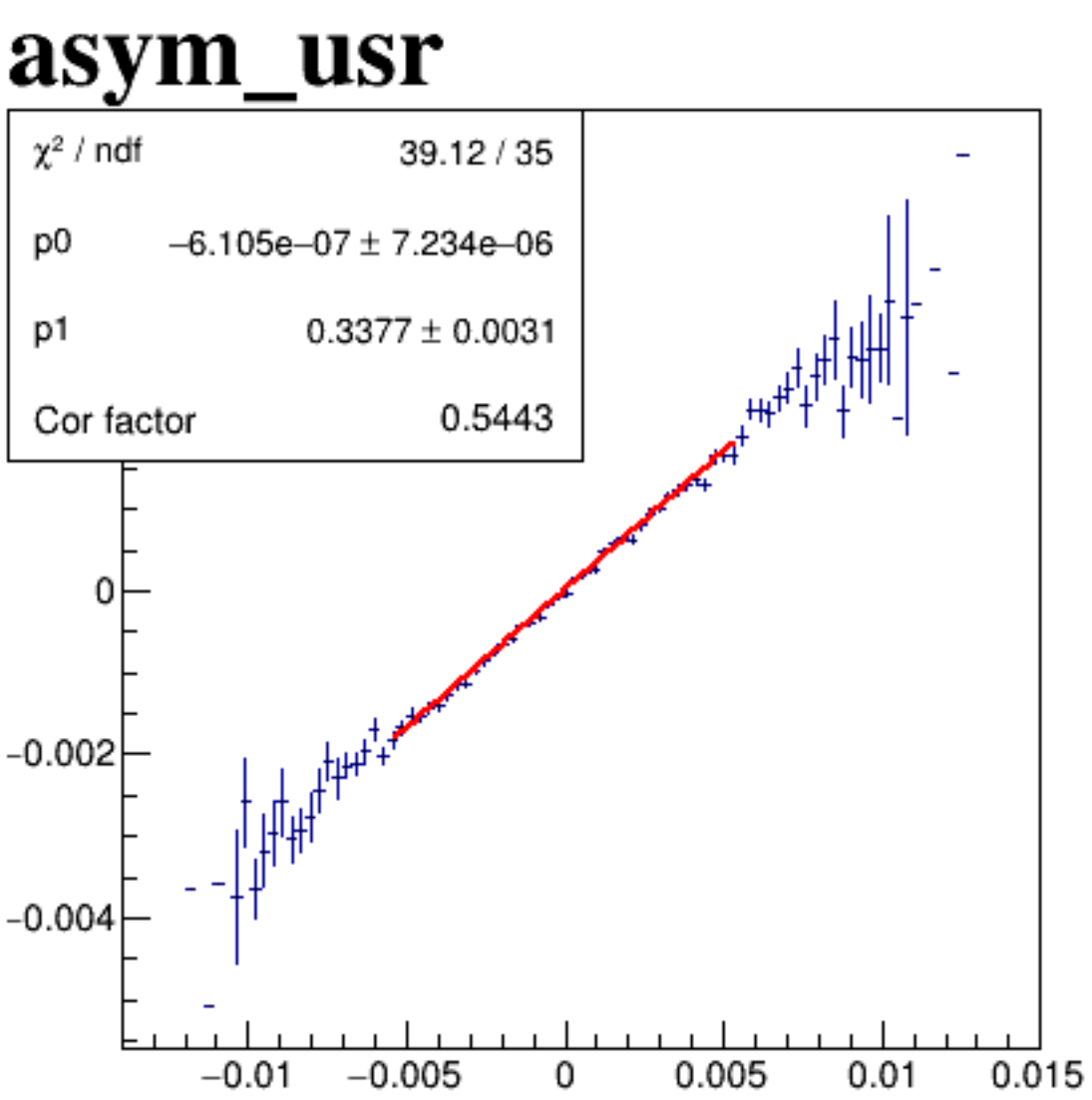
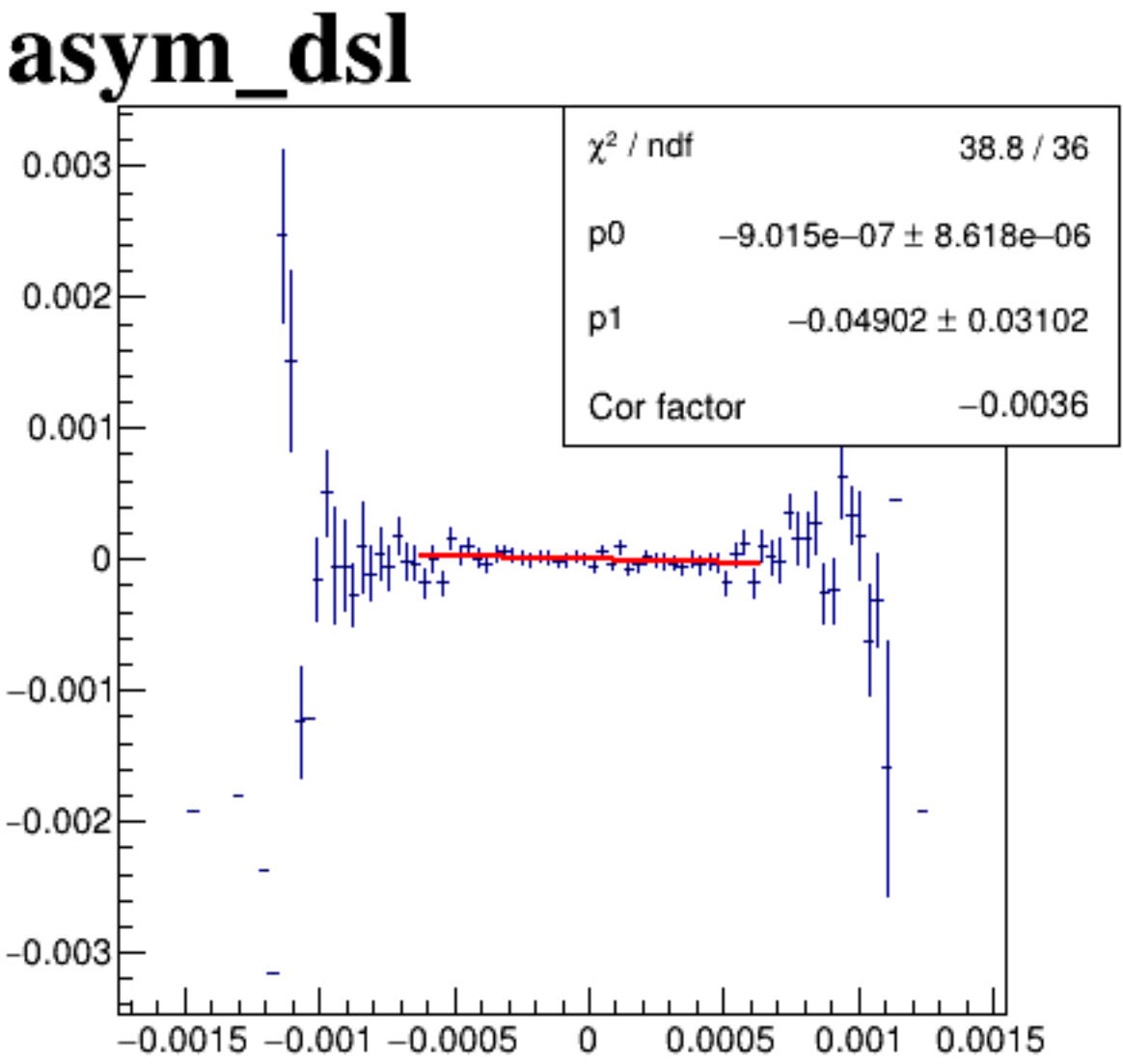
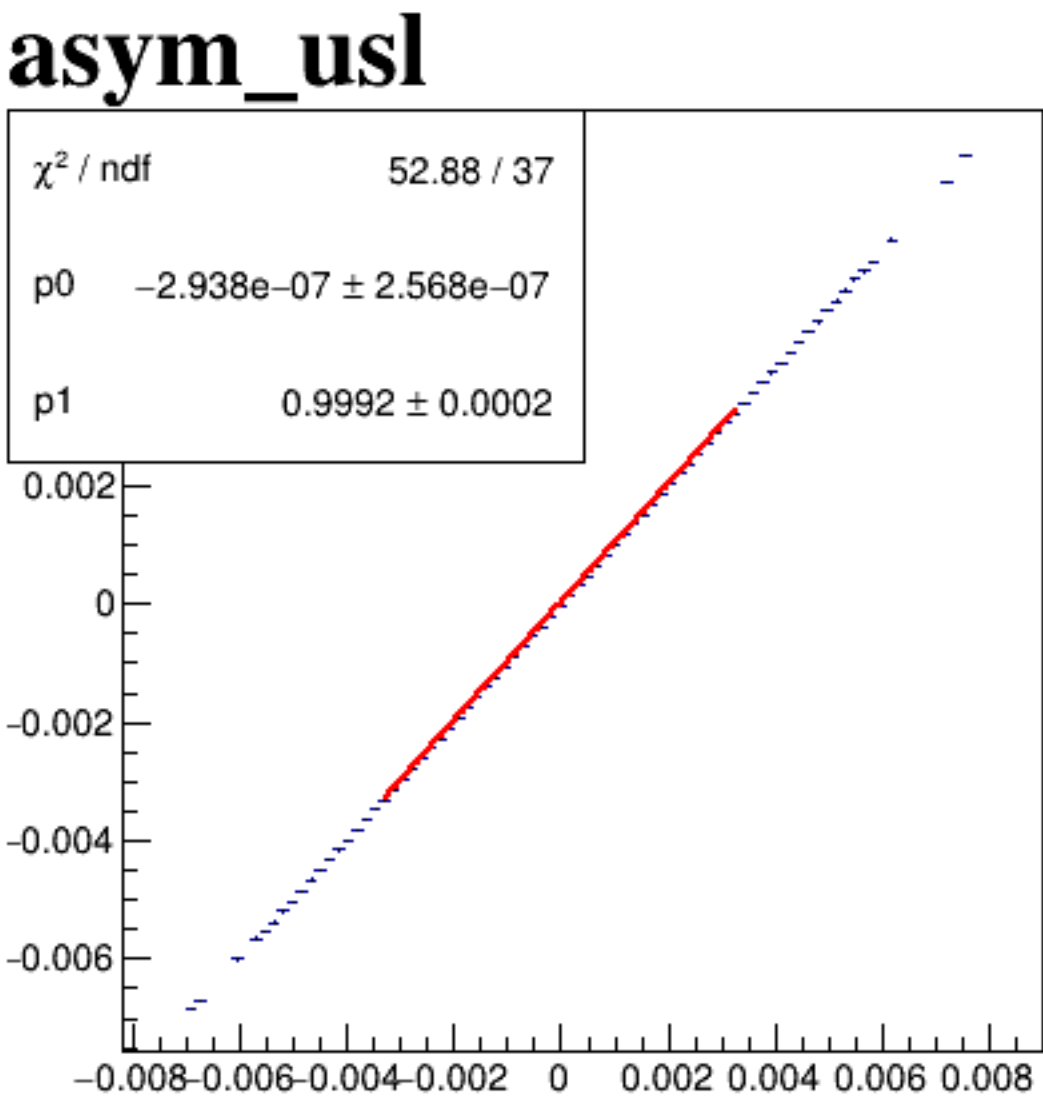
asym_dsl



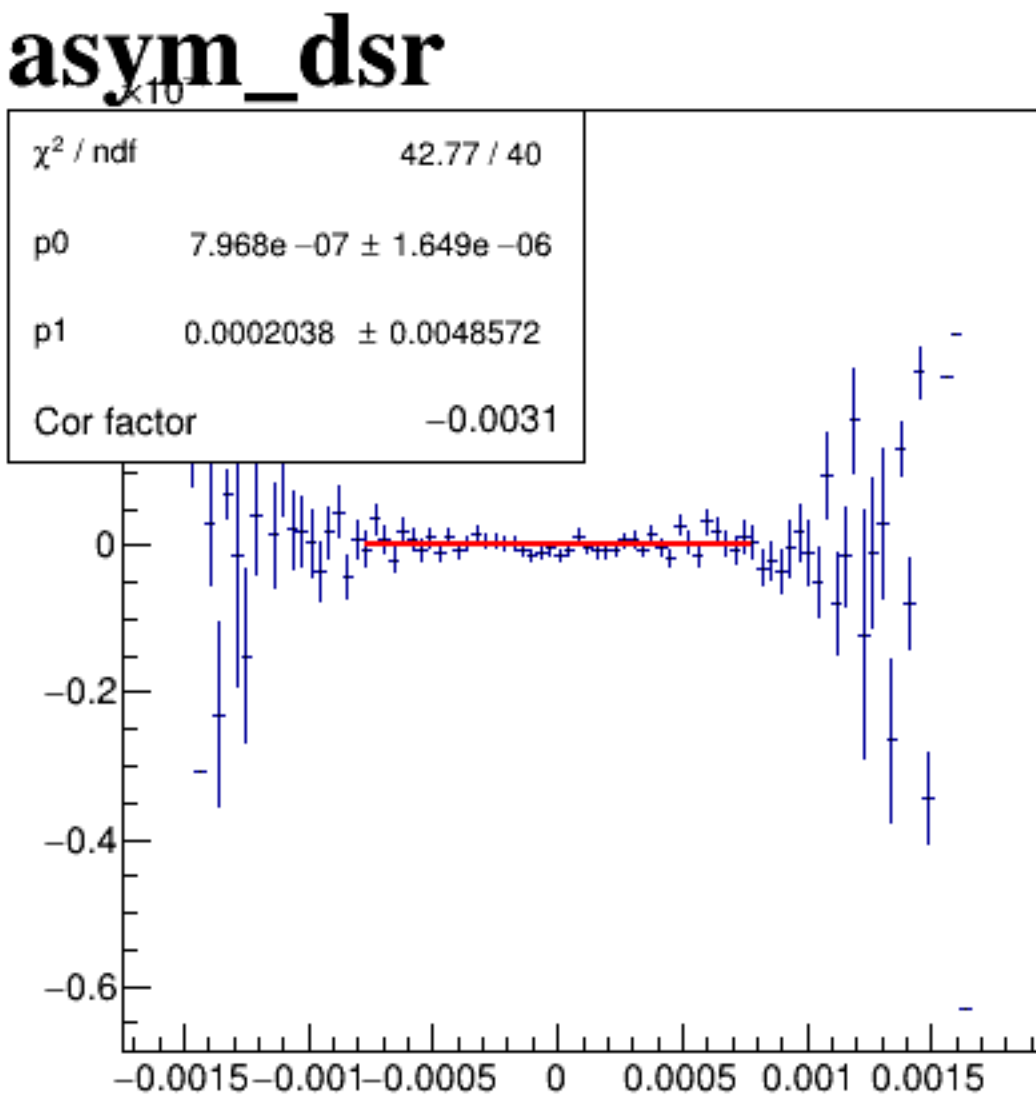
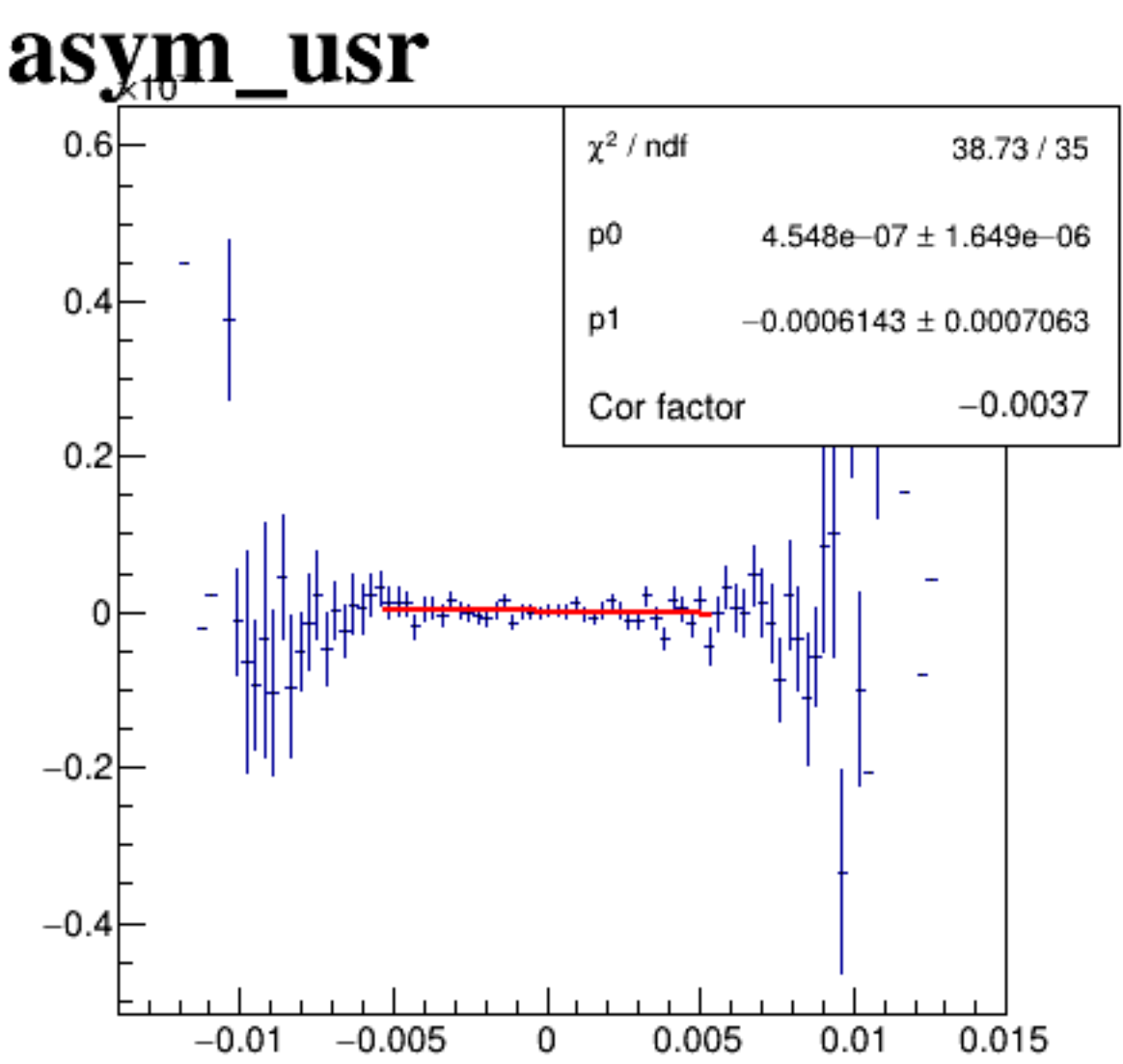
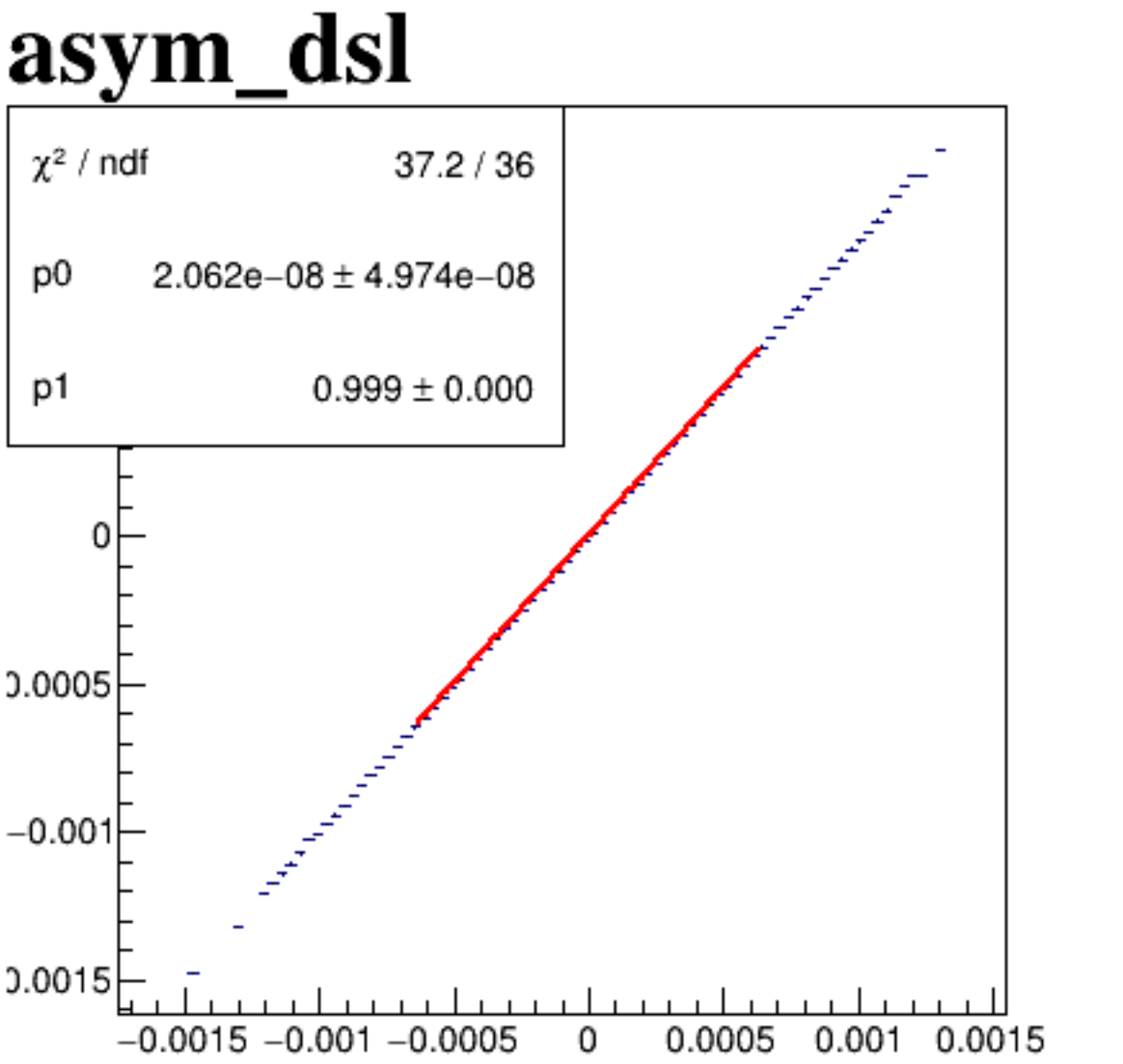
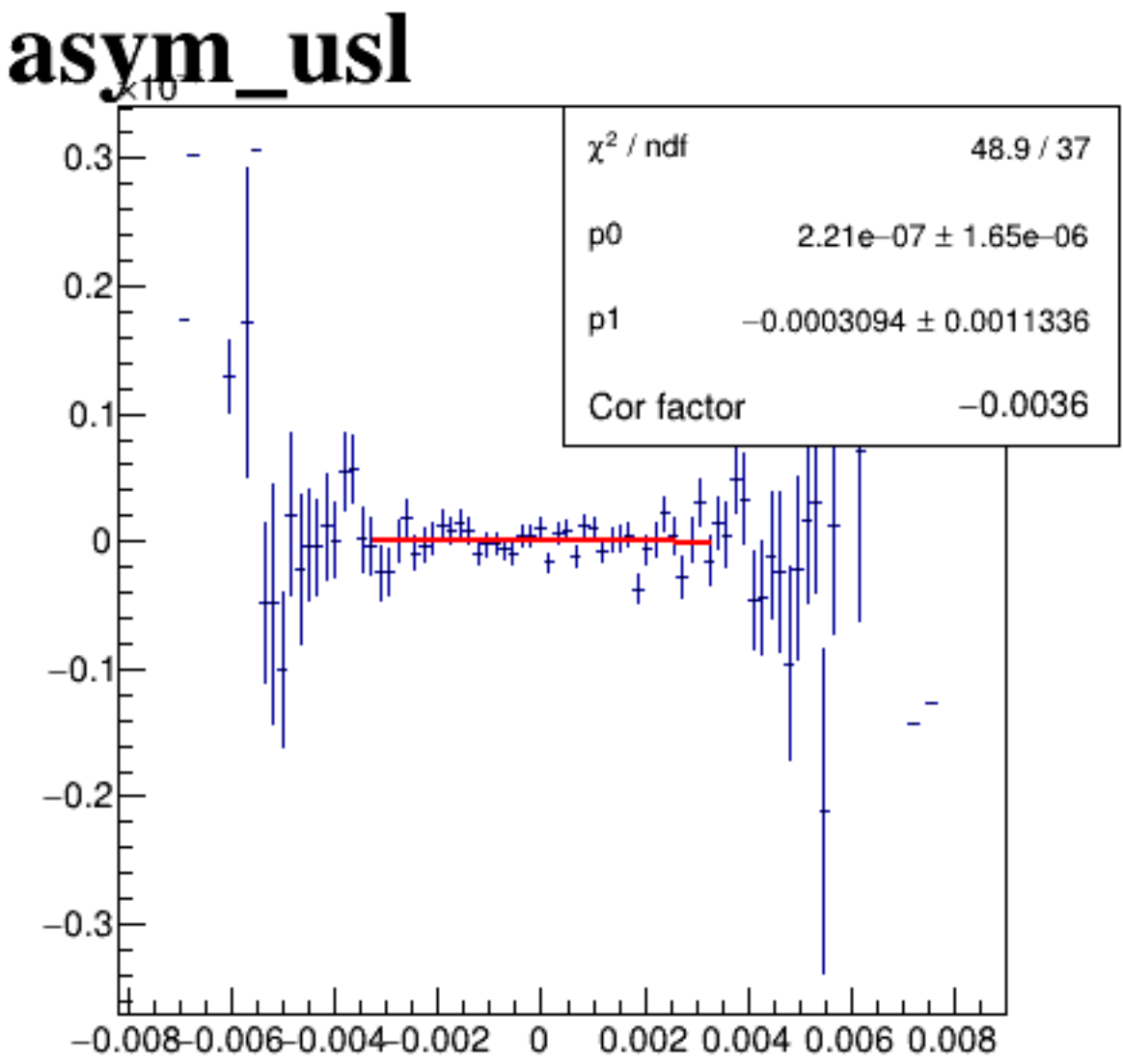
asym_usr

**asym_dsr**

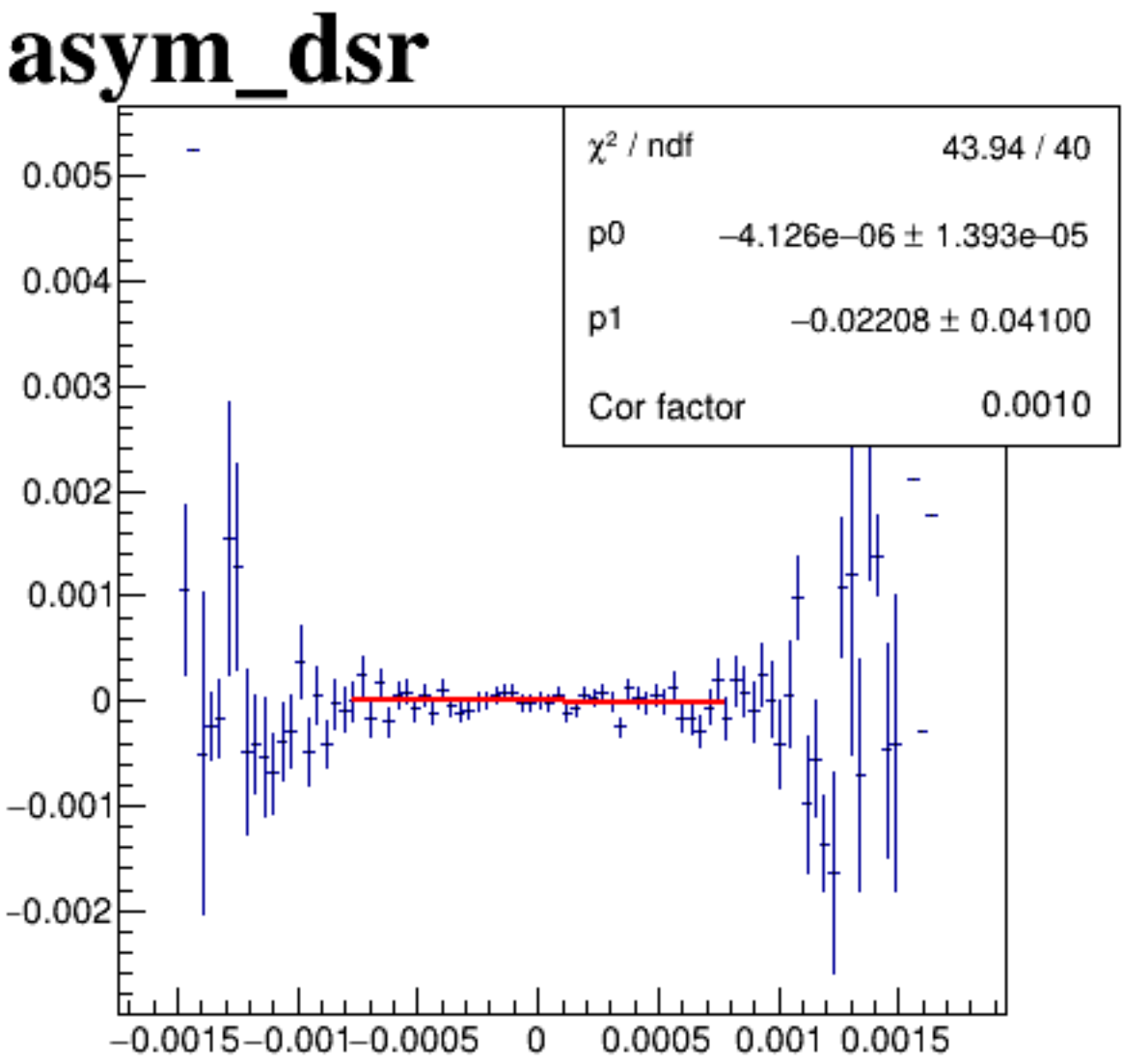
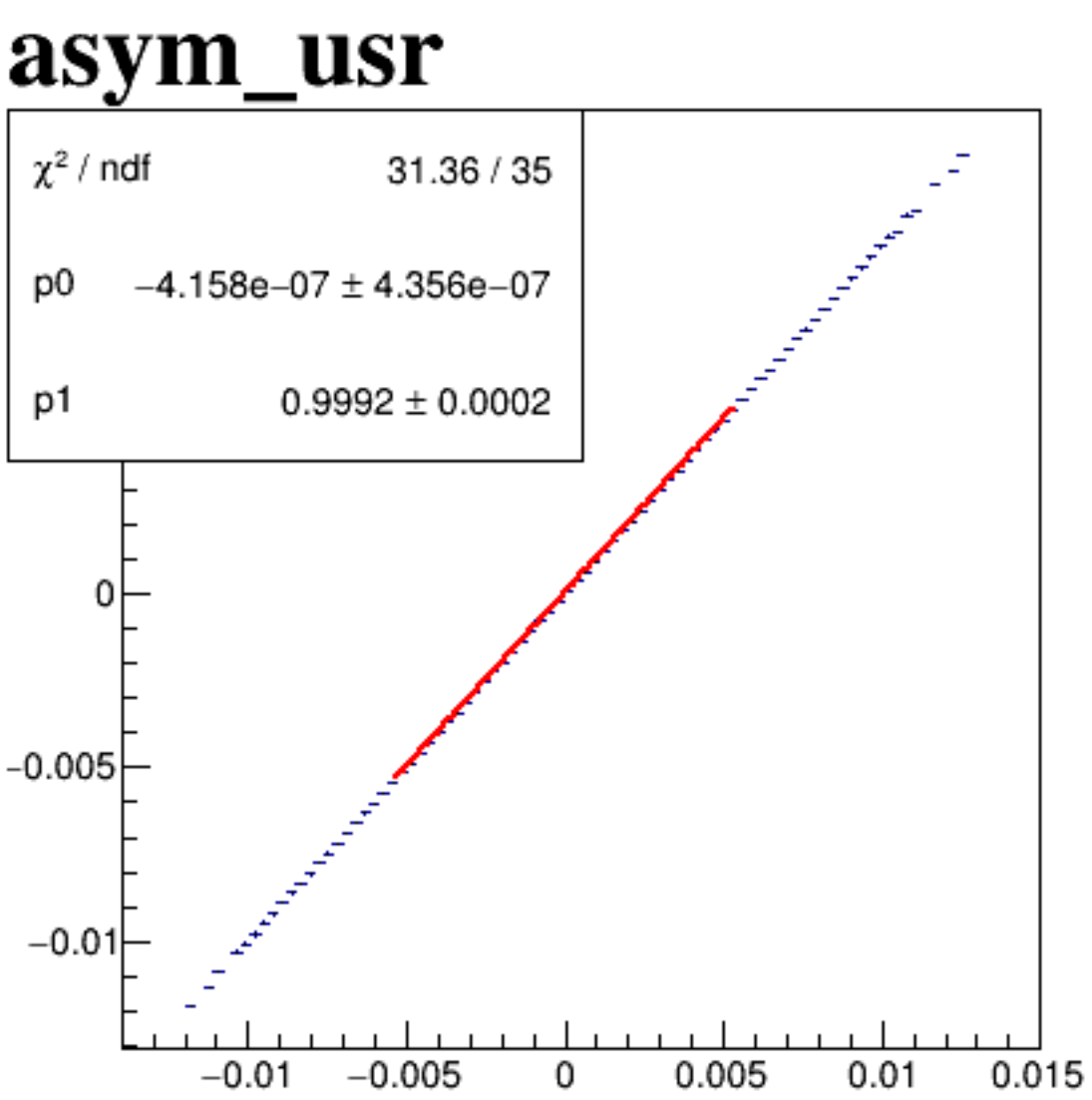
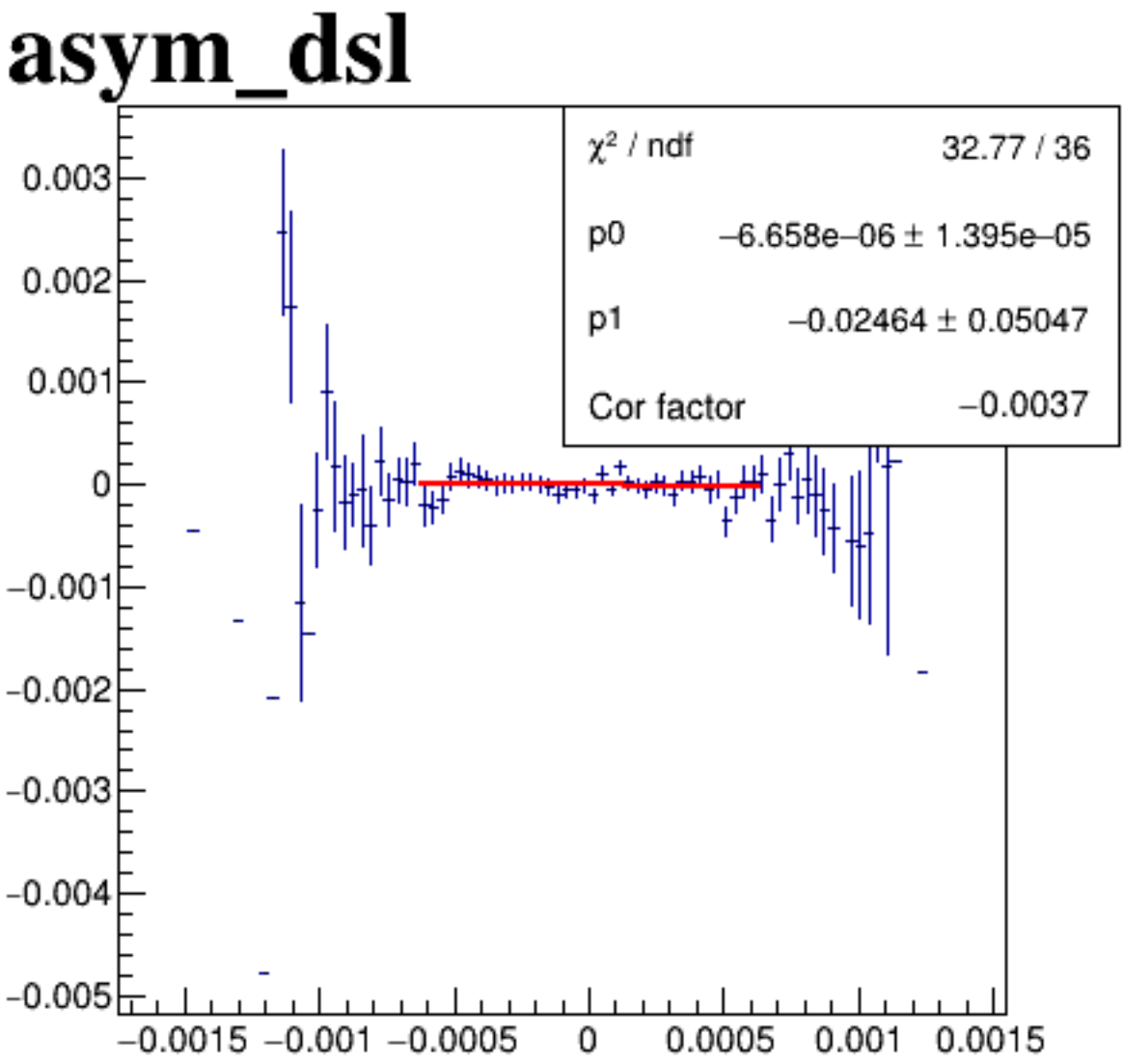
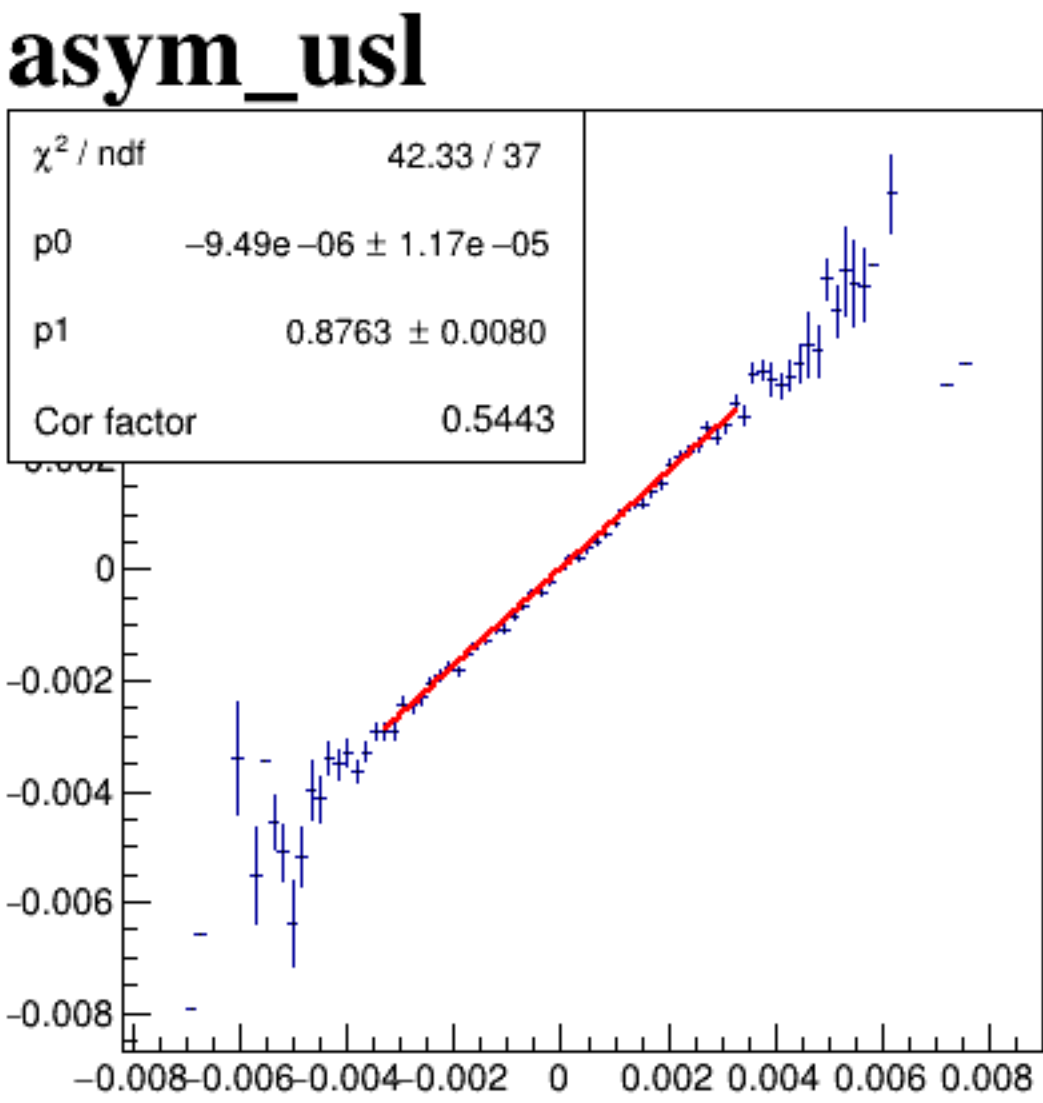
asym_usl



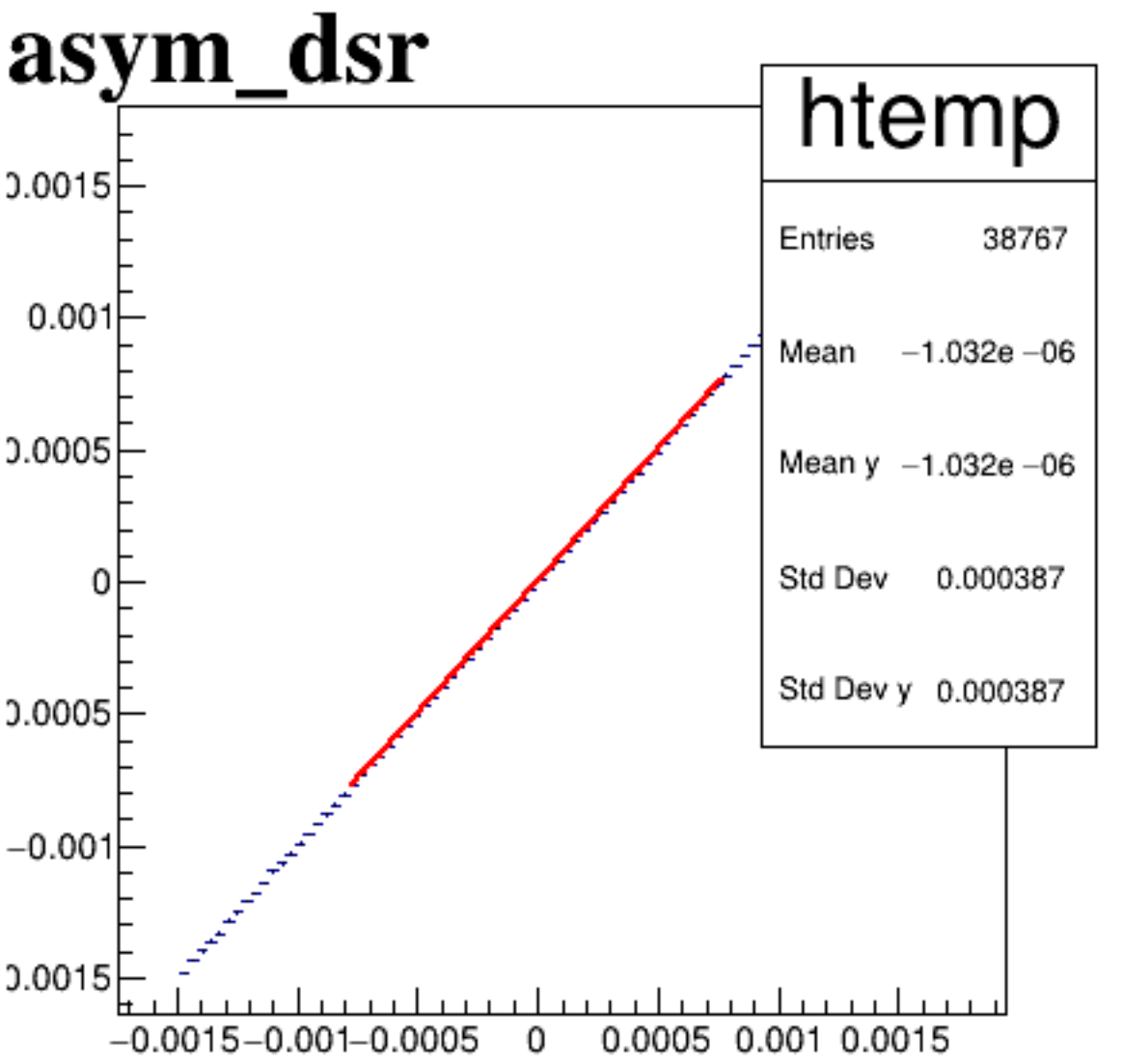
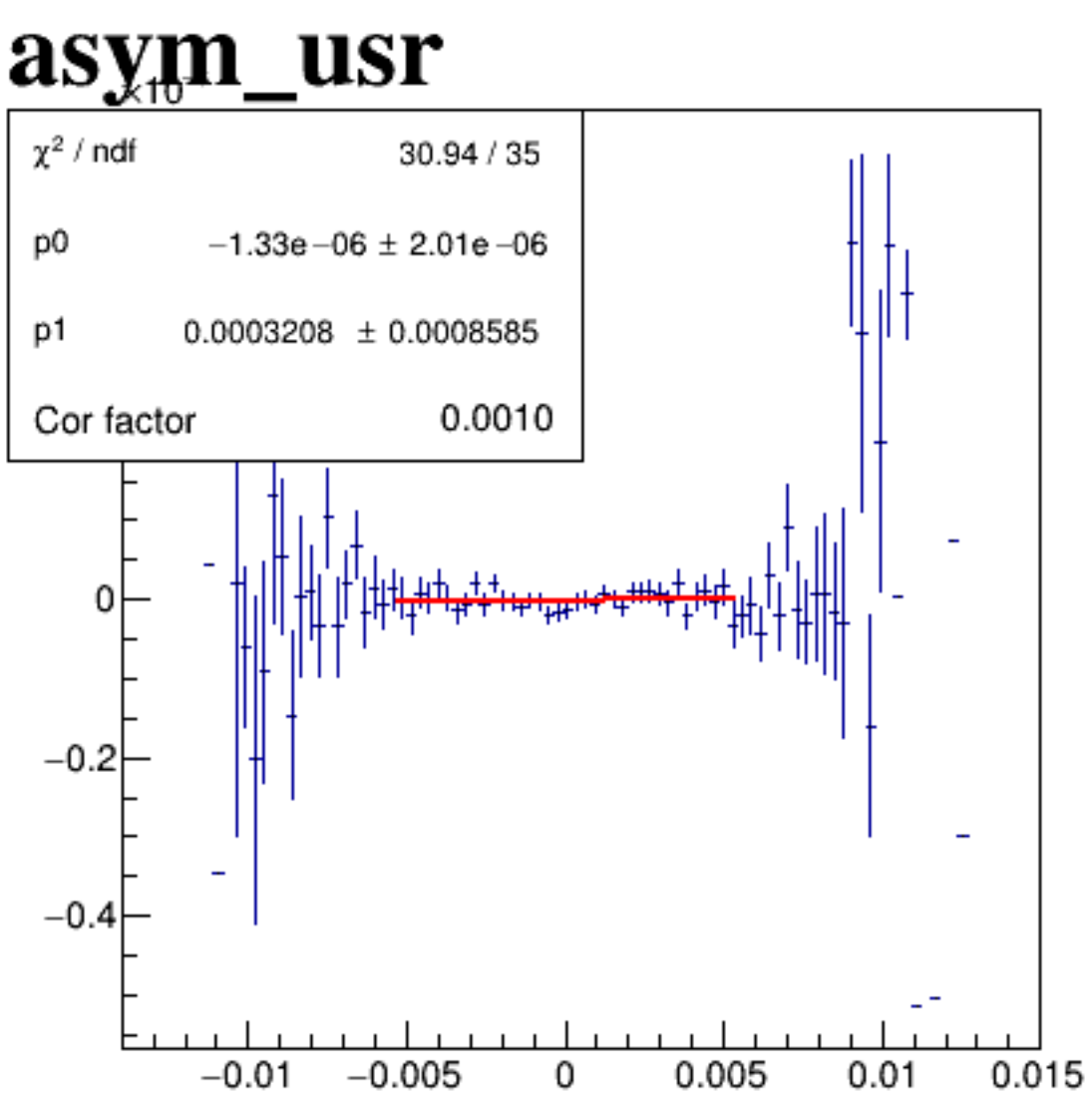
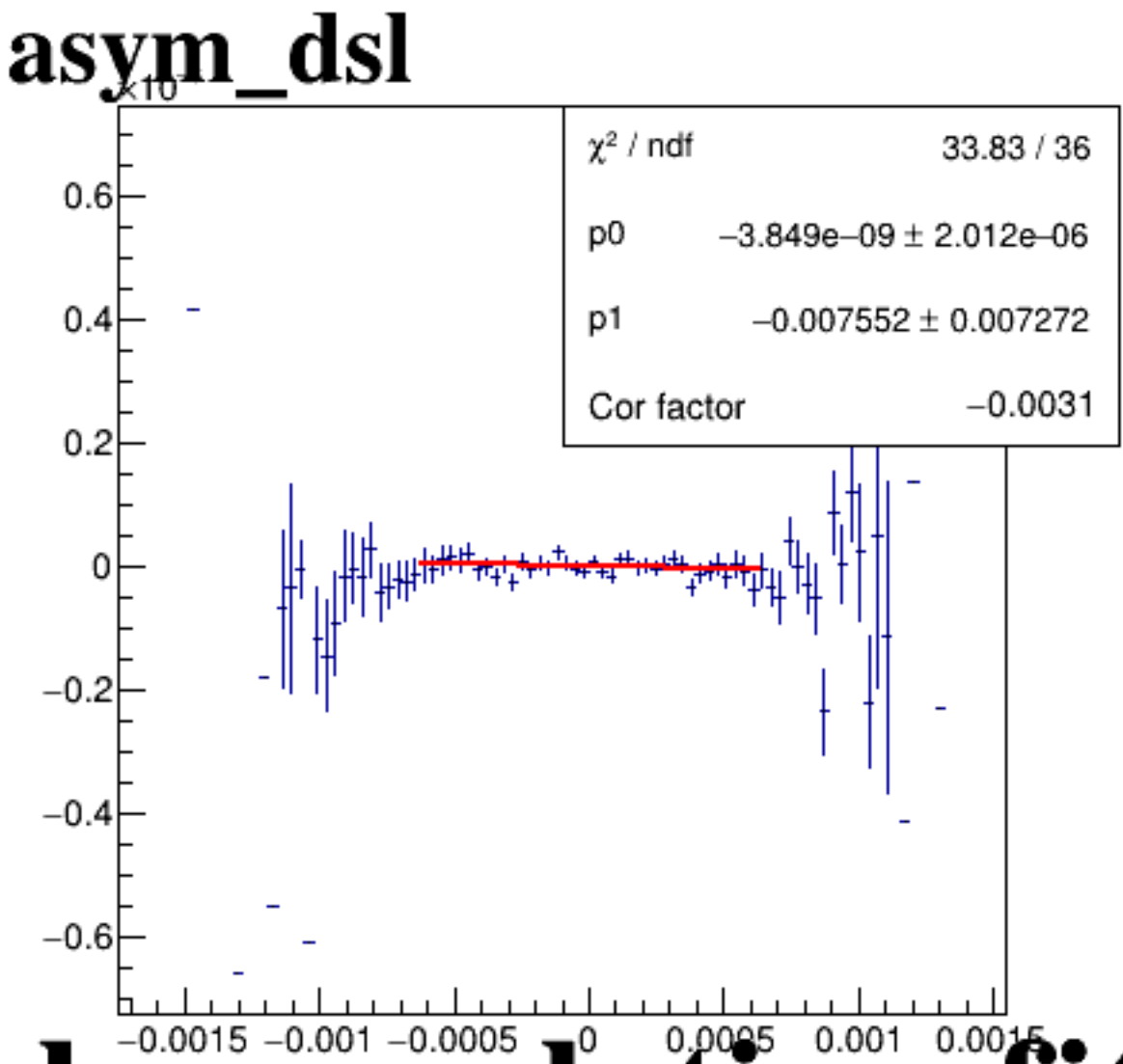
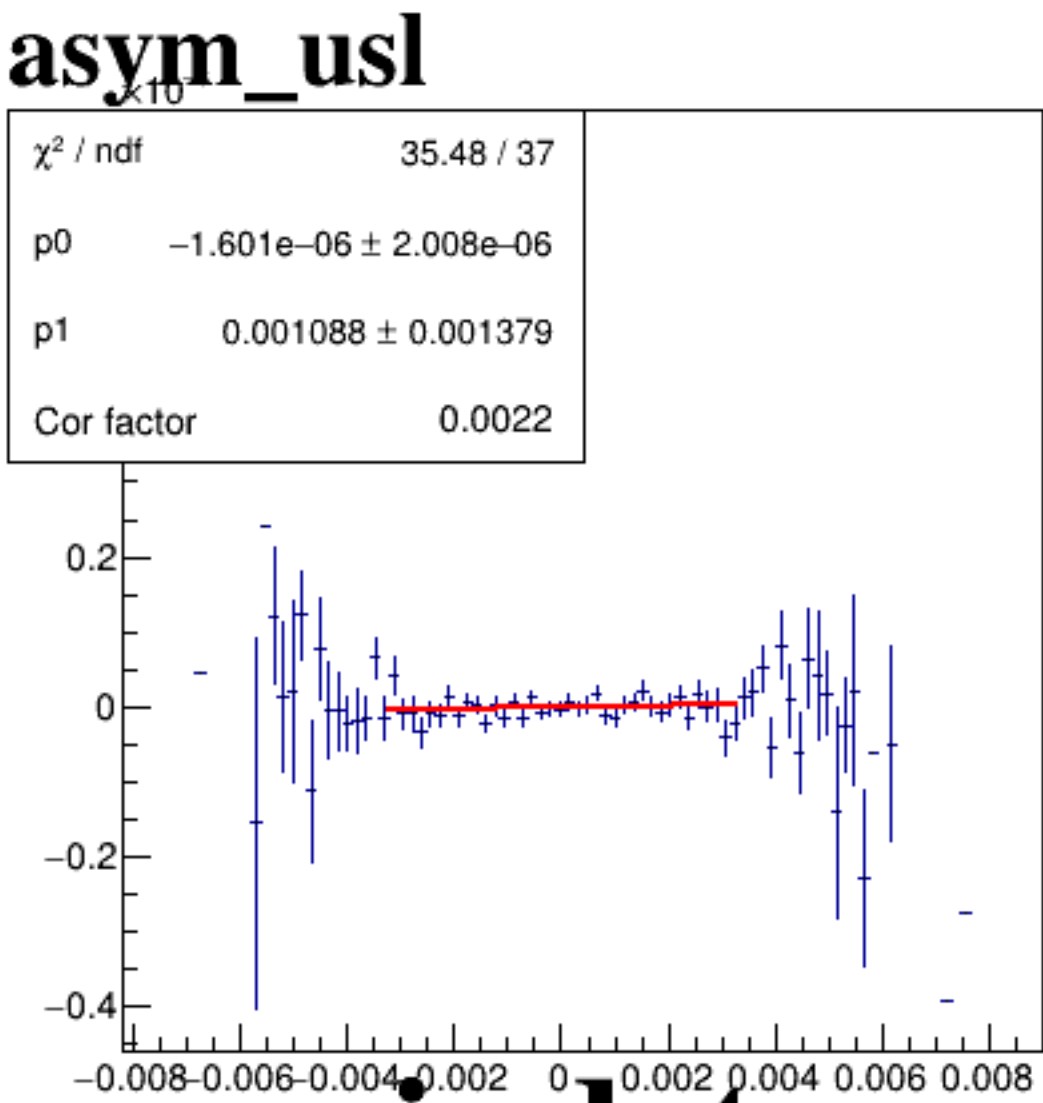
asym_dsl



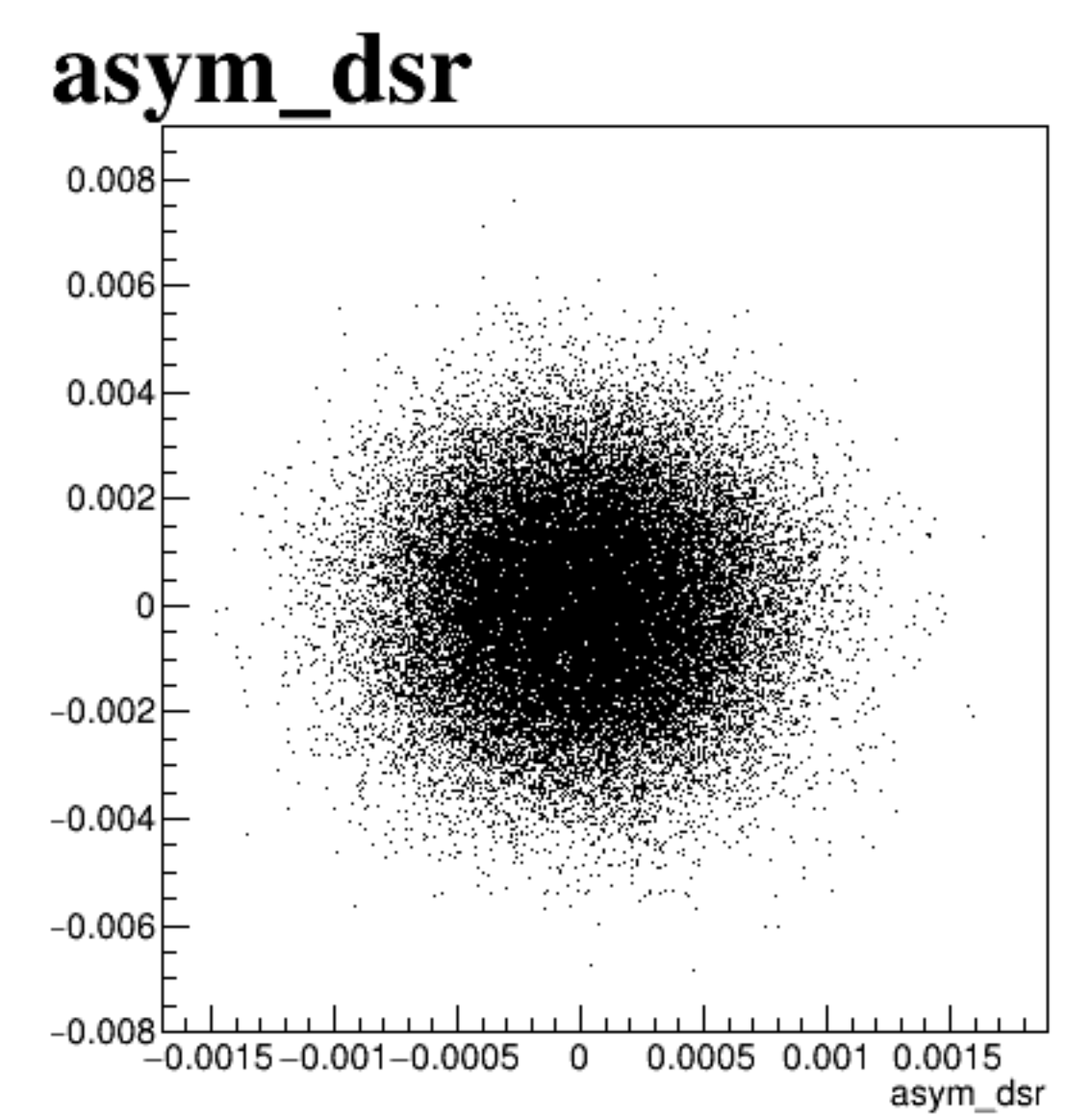
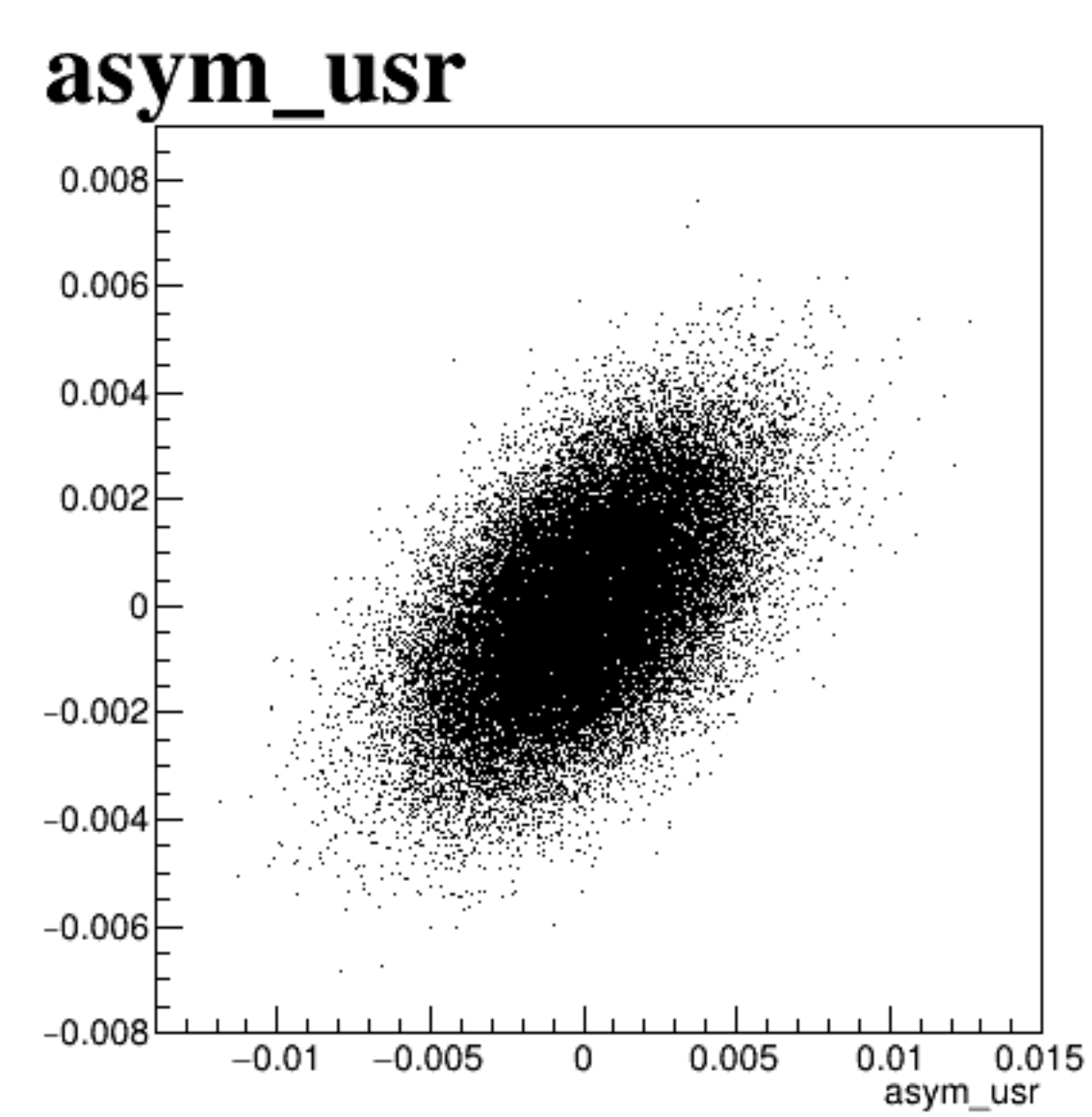
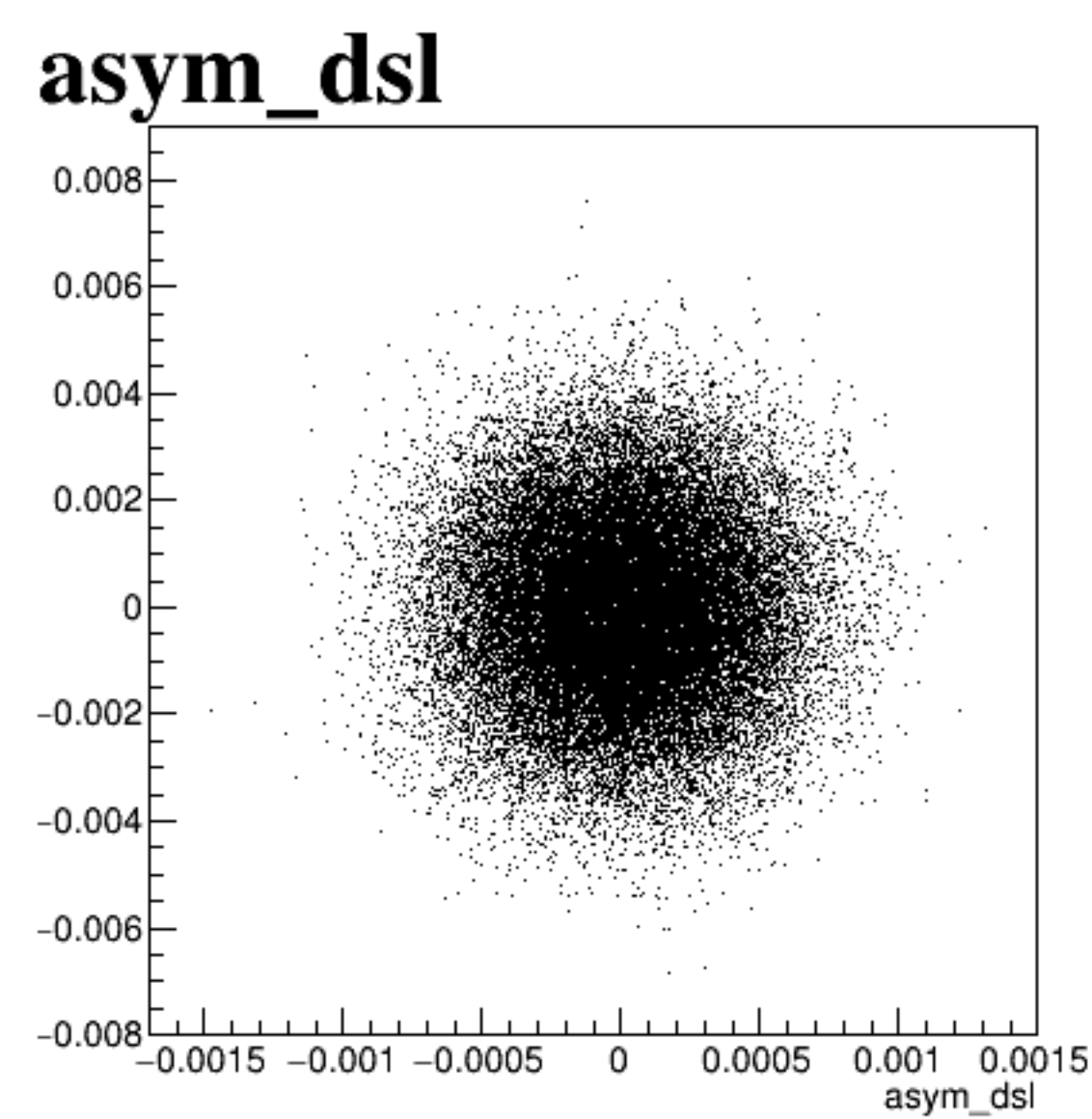
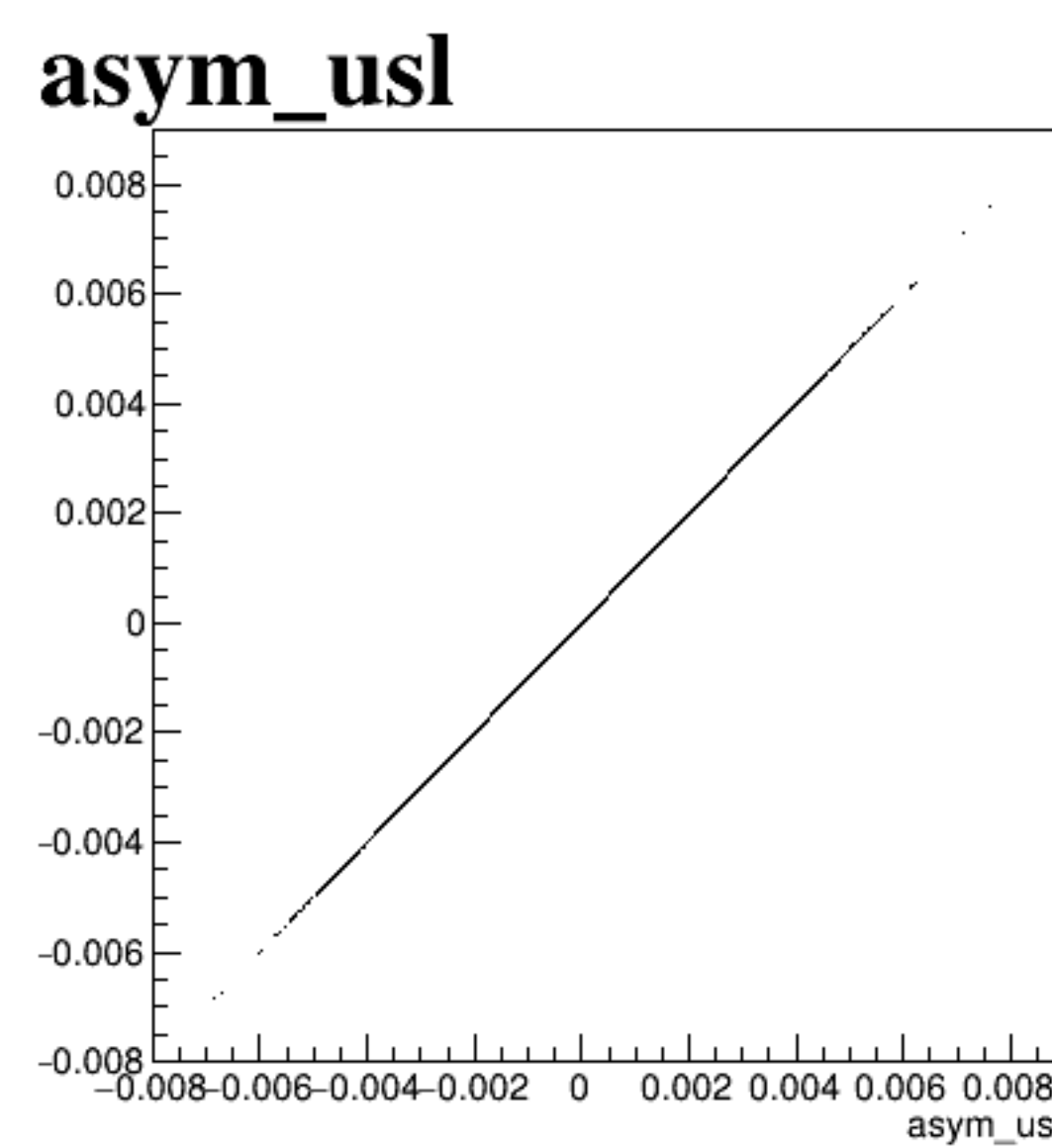
asym_usr



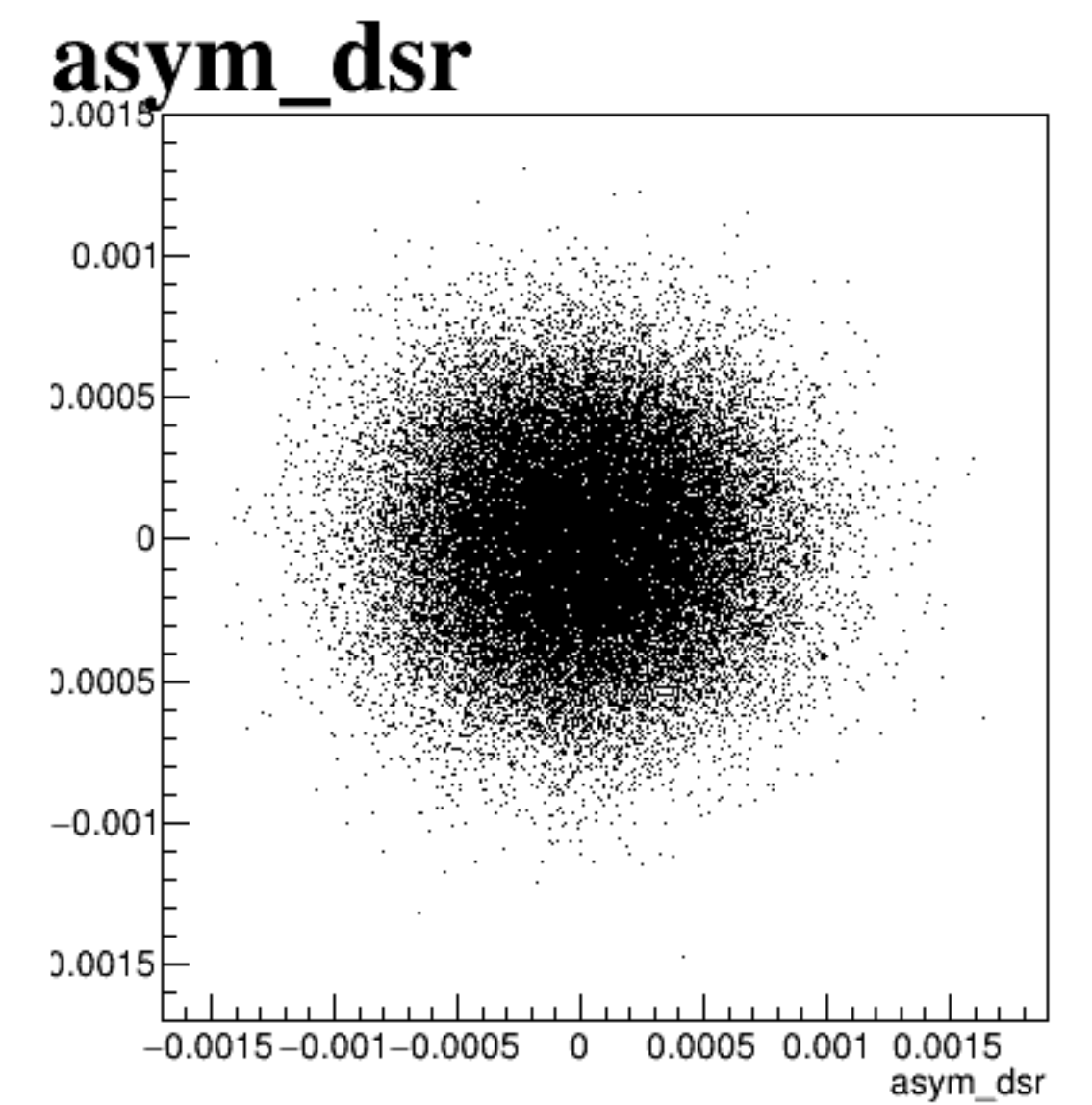
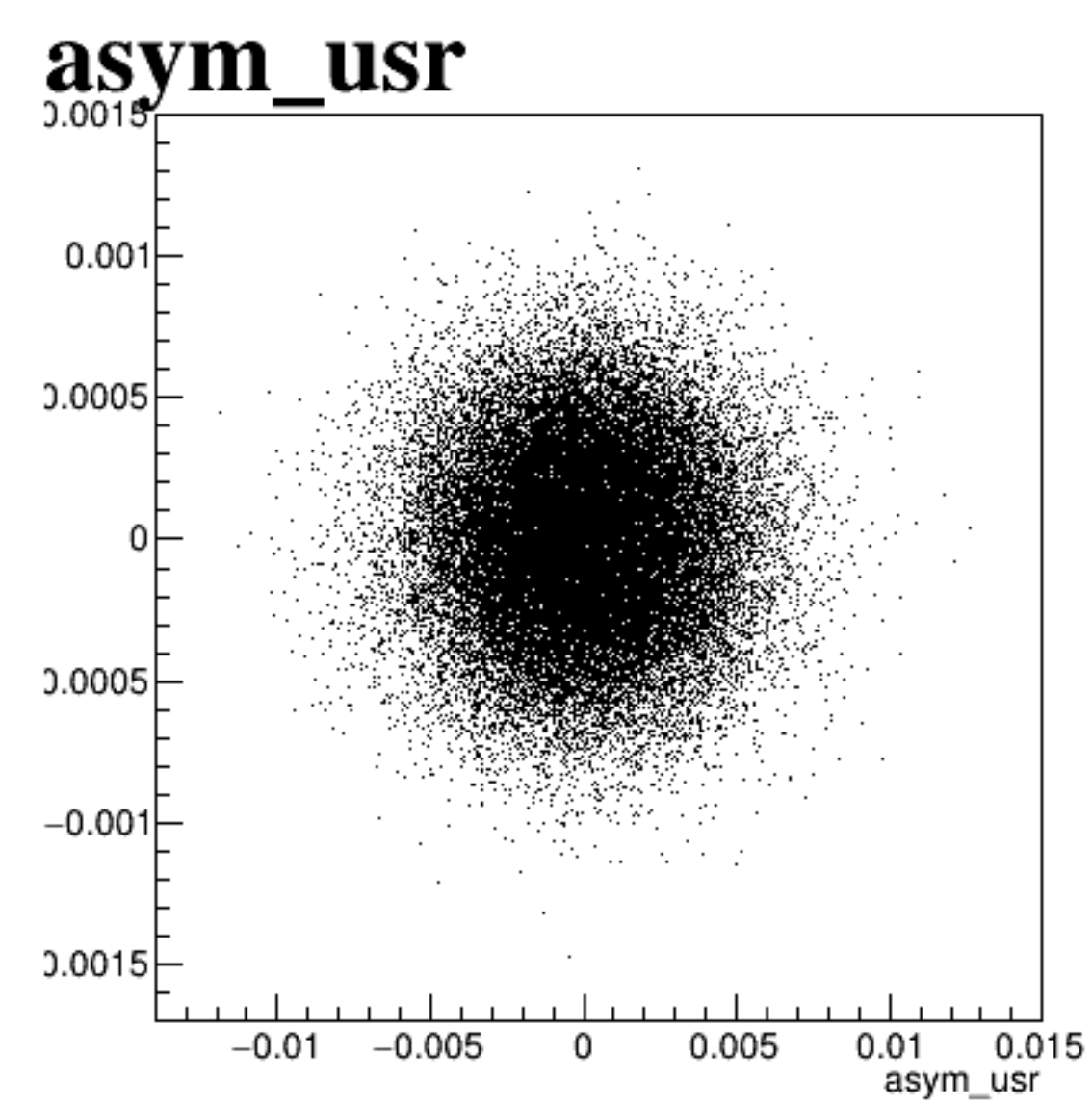
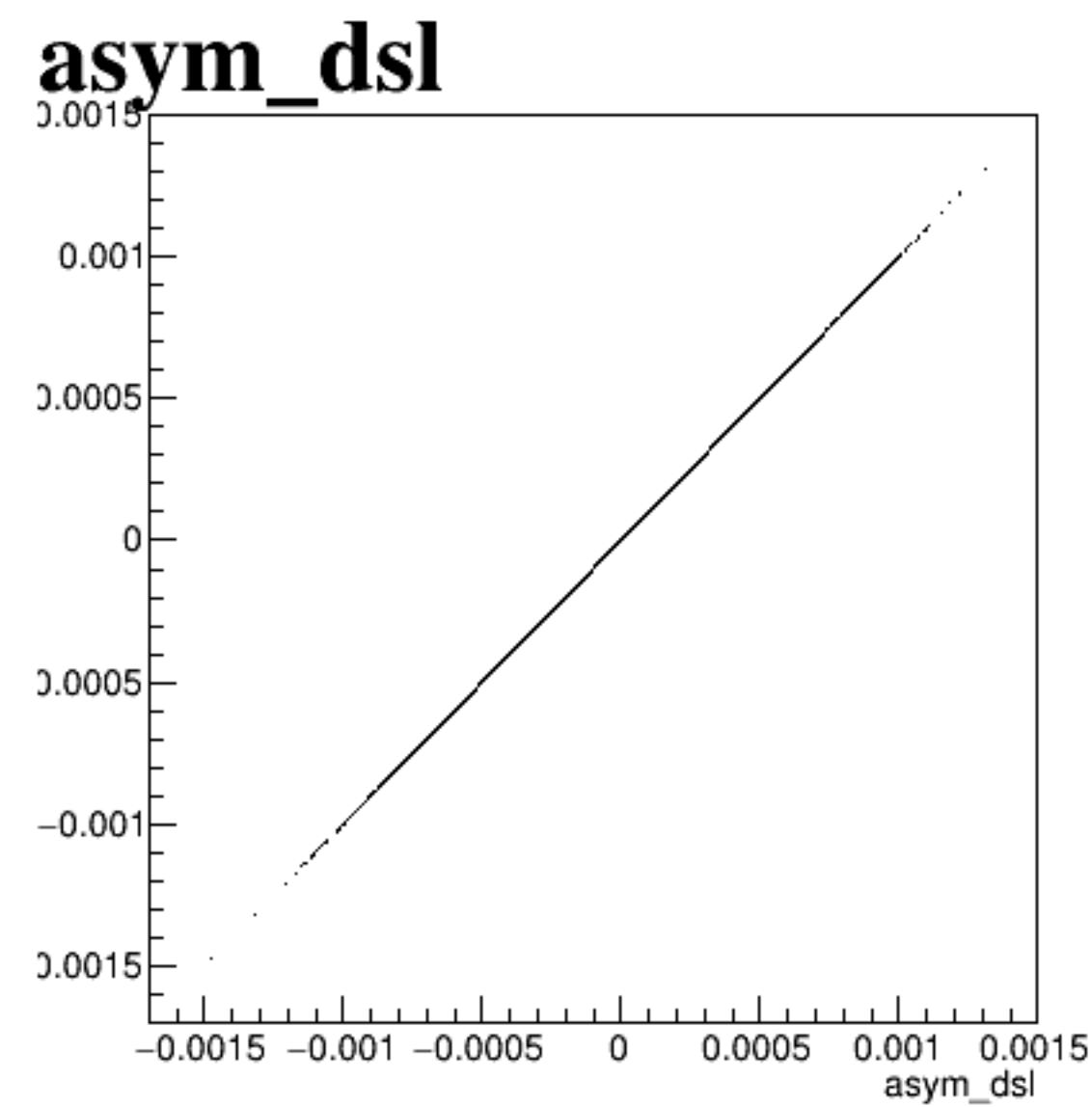
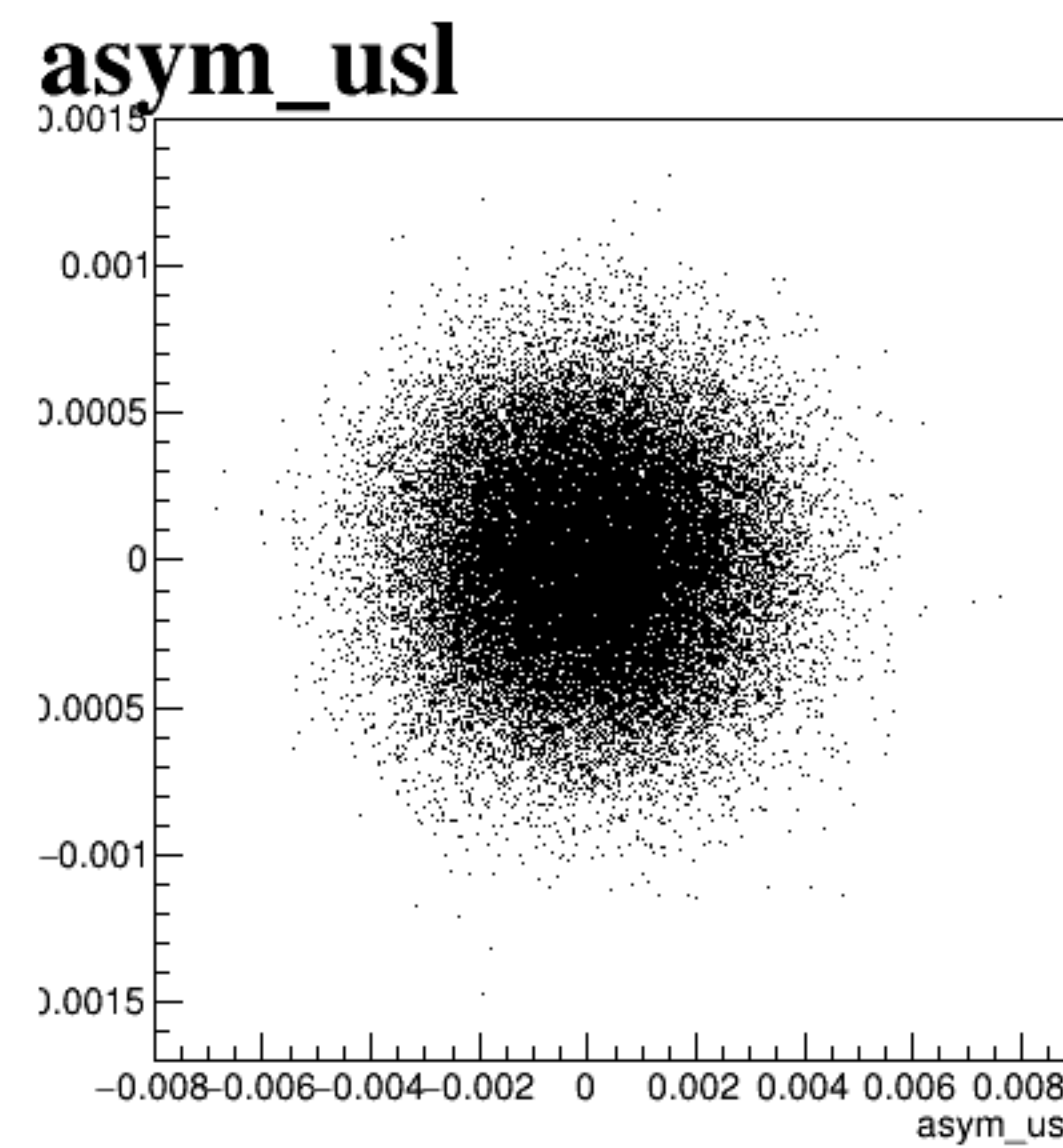
asym_dsr



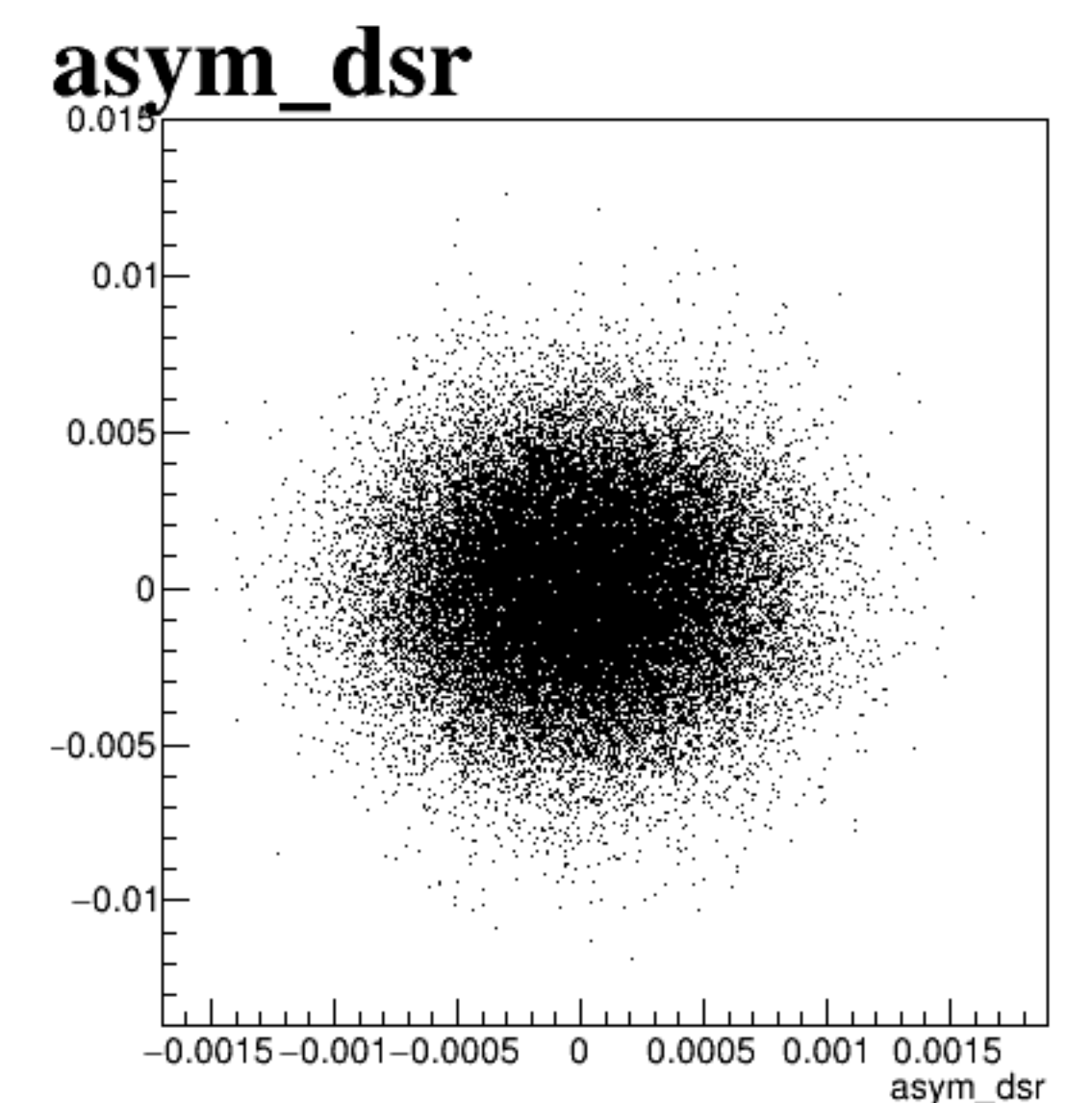
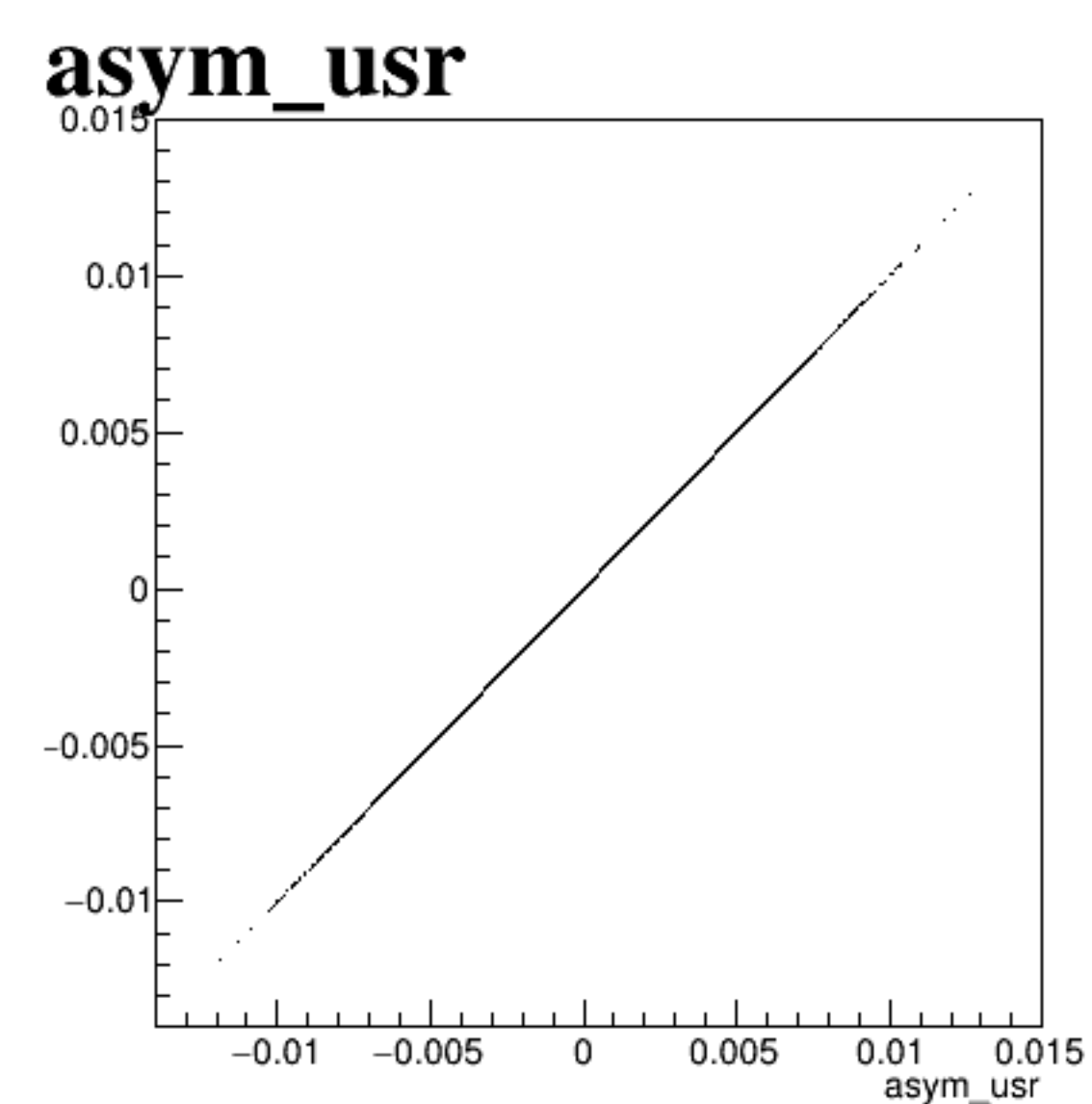
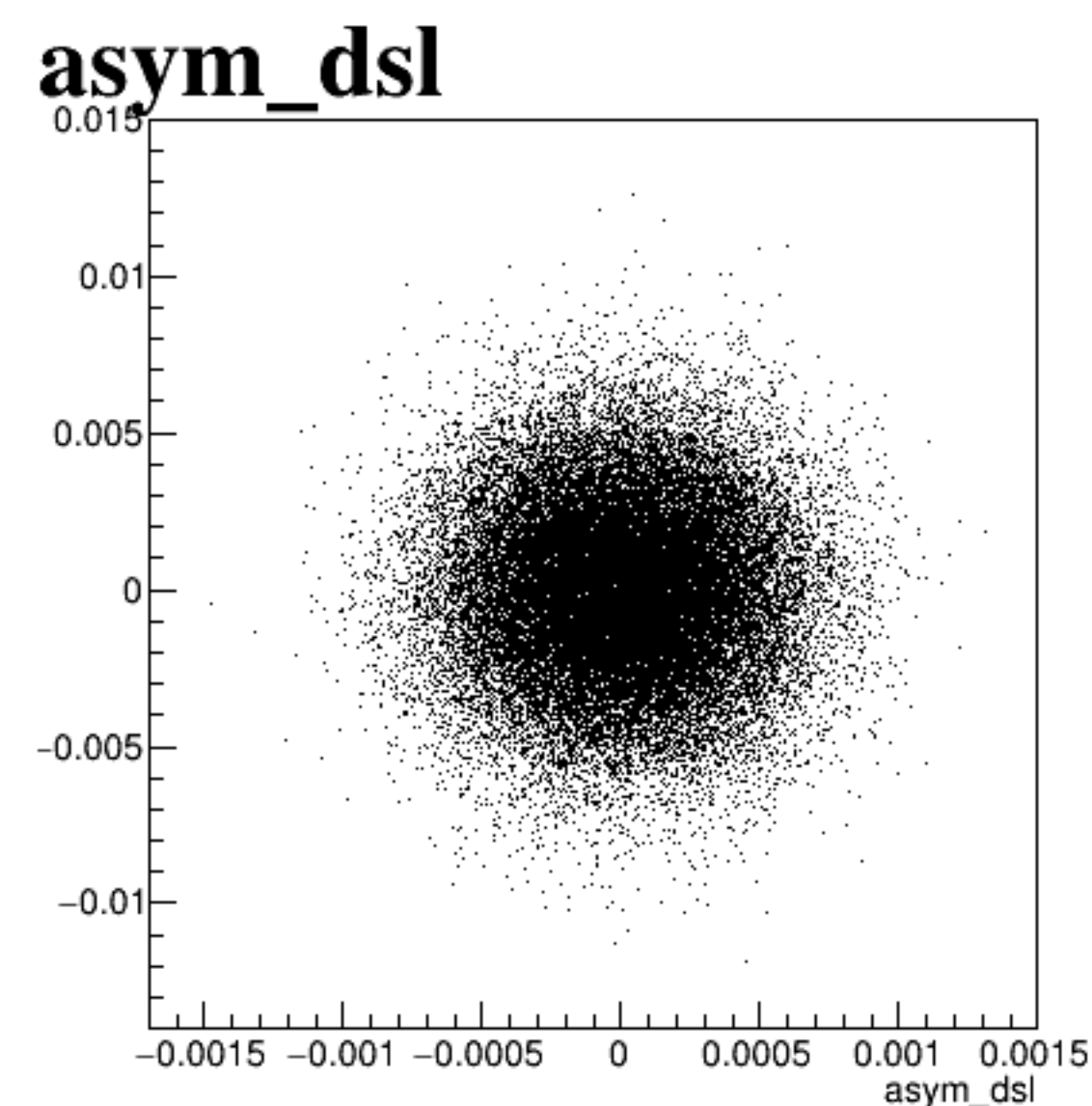
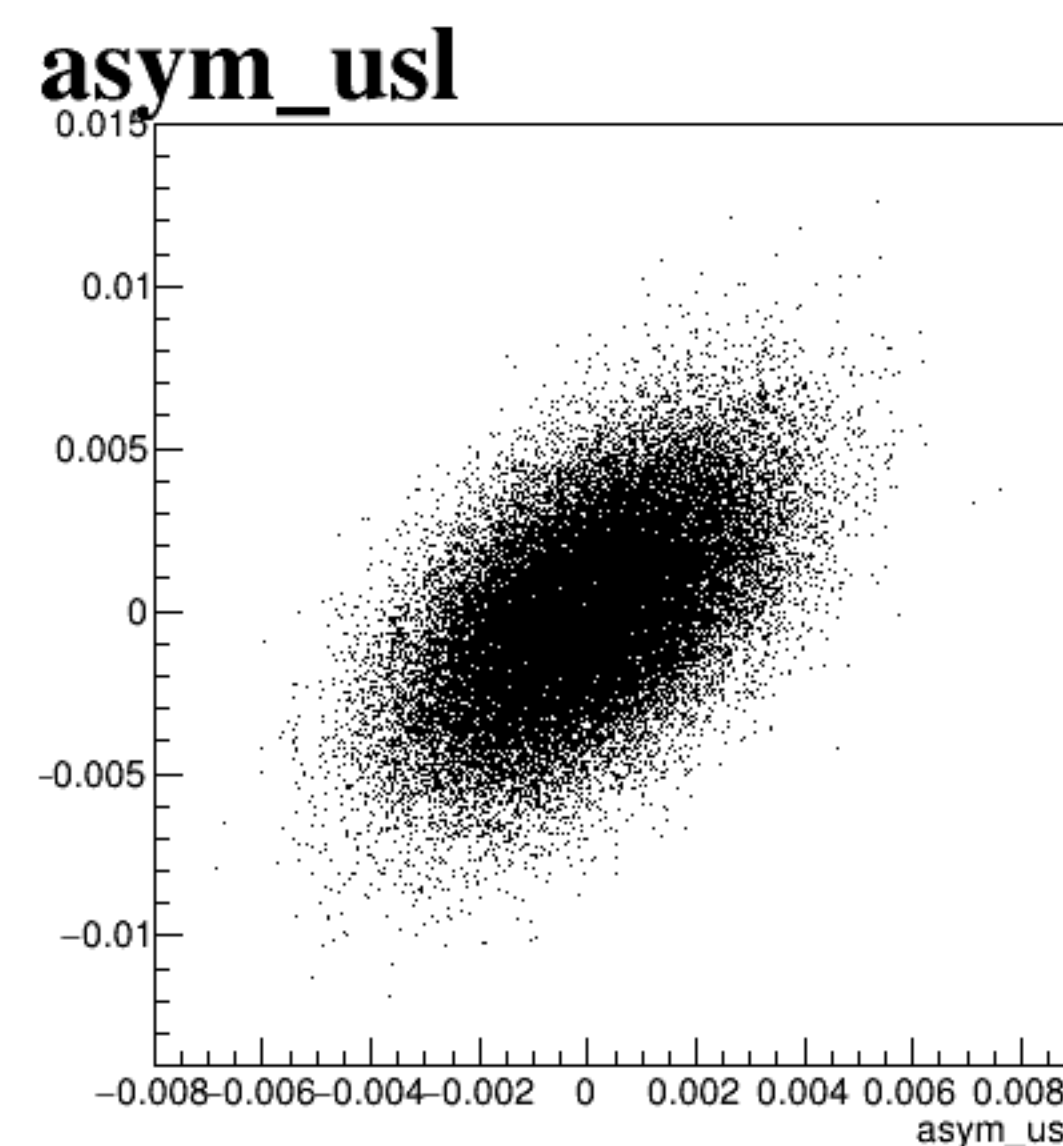
asym_usl



asym_dsl



asym_usr

**asym_dsr**