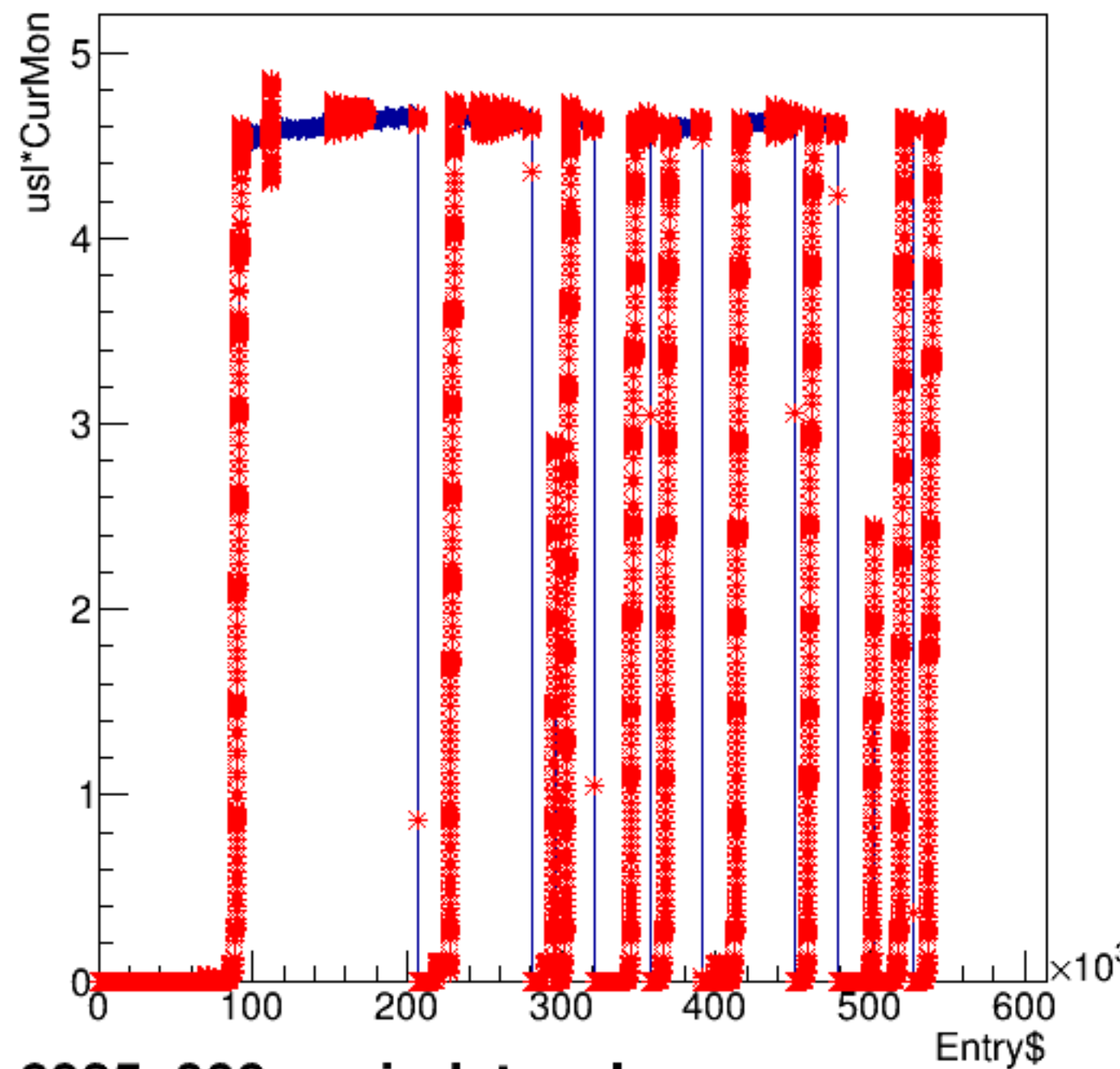
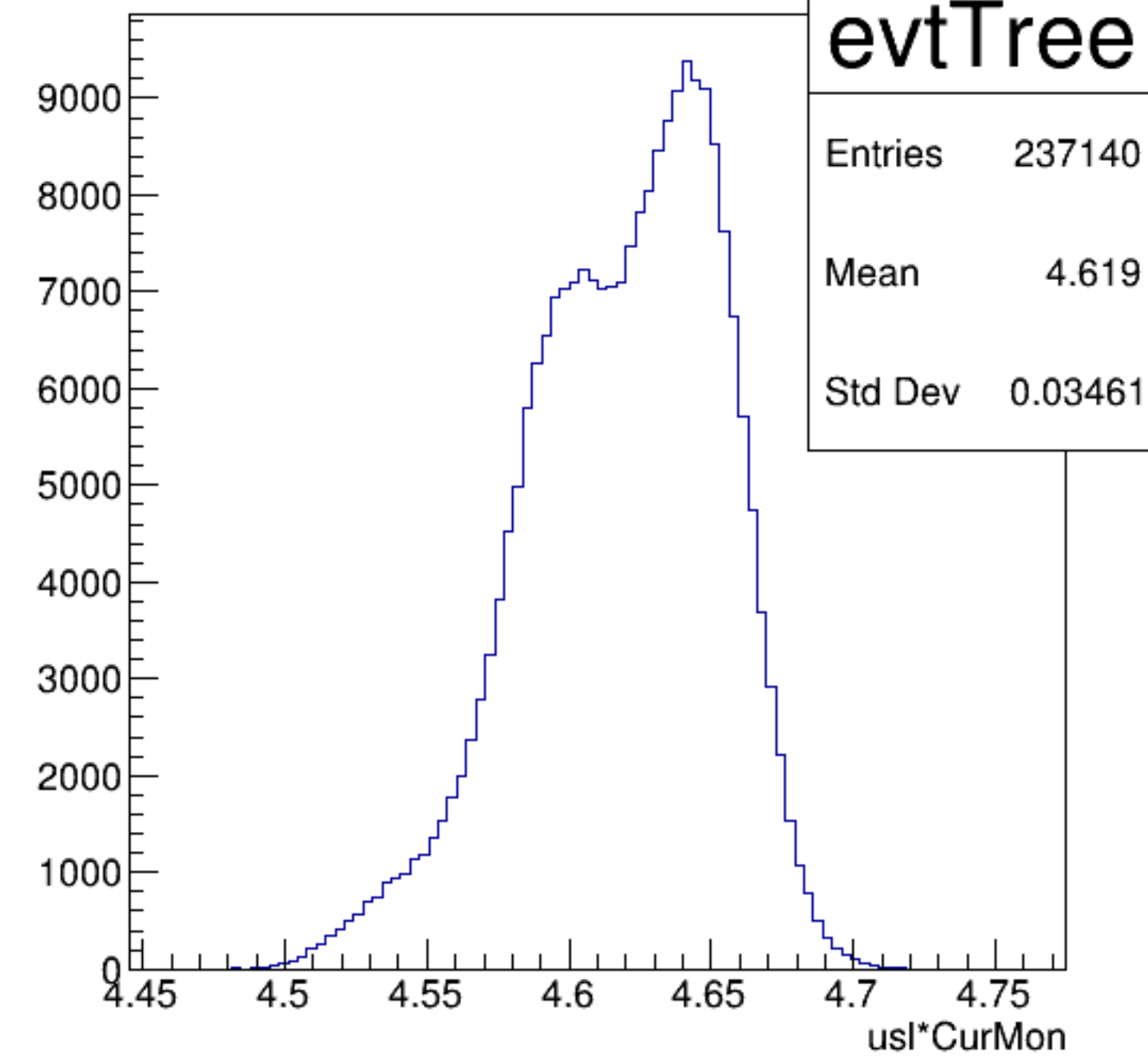


usl*CurMon:Entry\$



run6085_000_maindet_usl.png

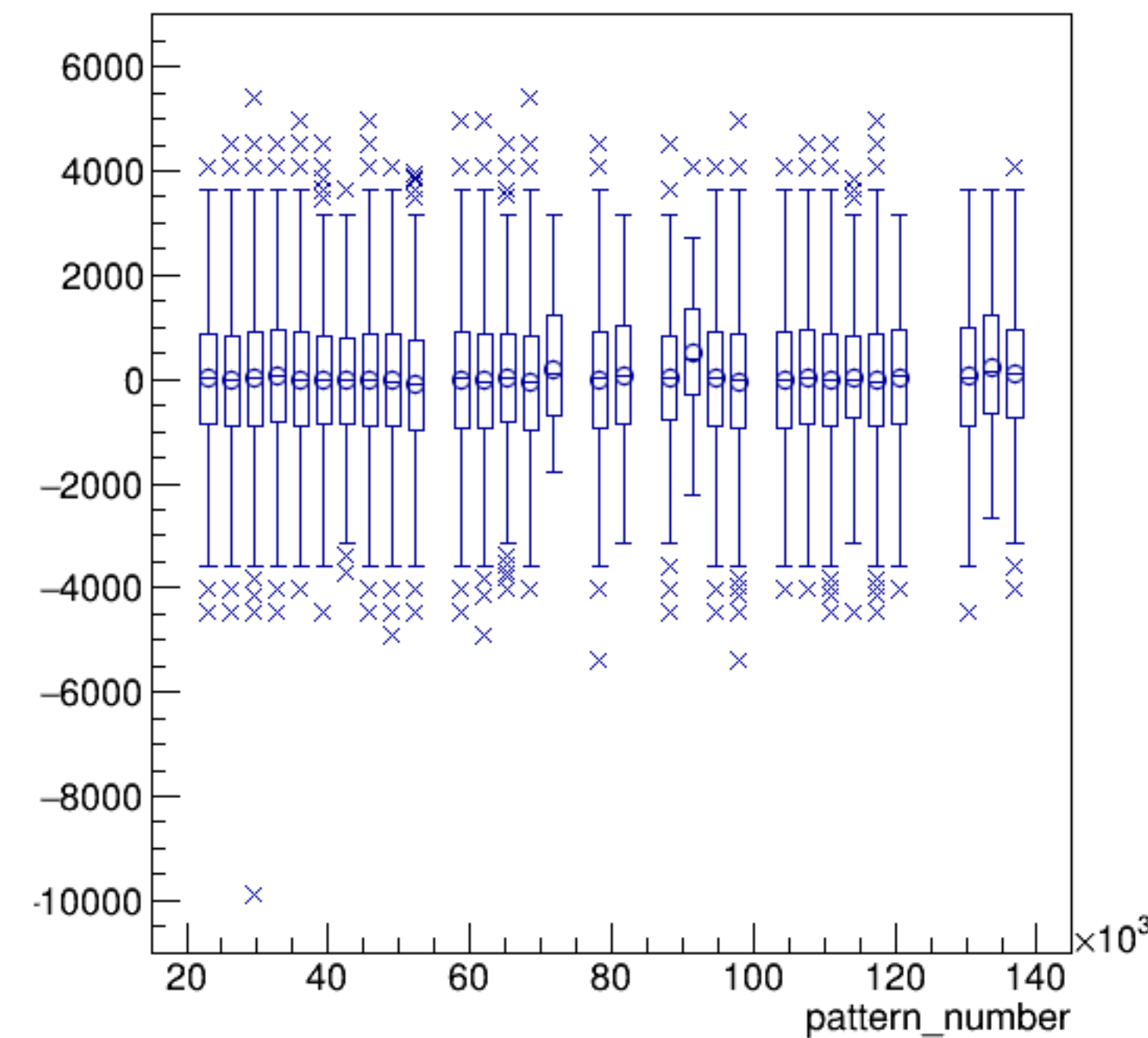
usl*CurMon {ErrorFlag==0}



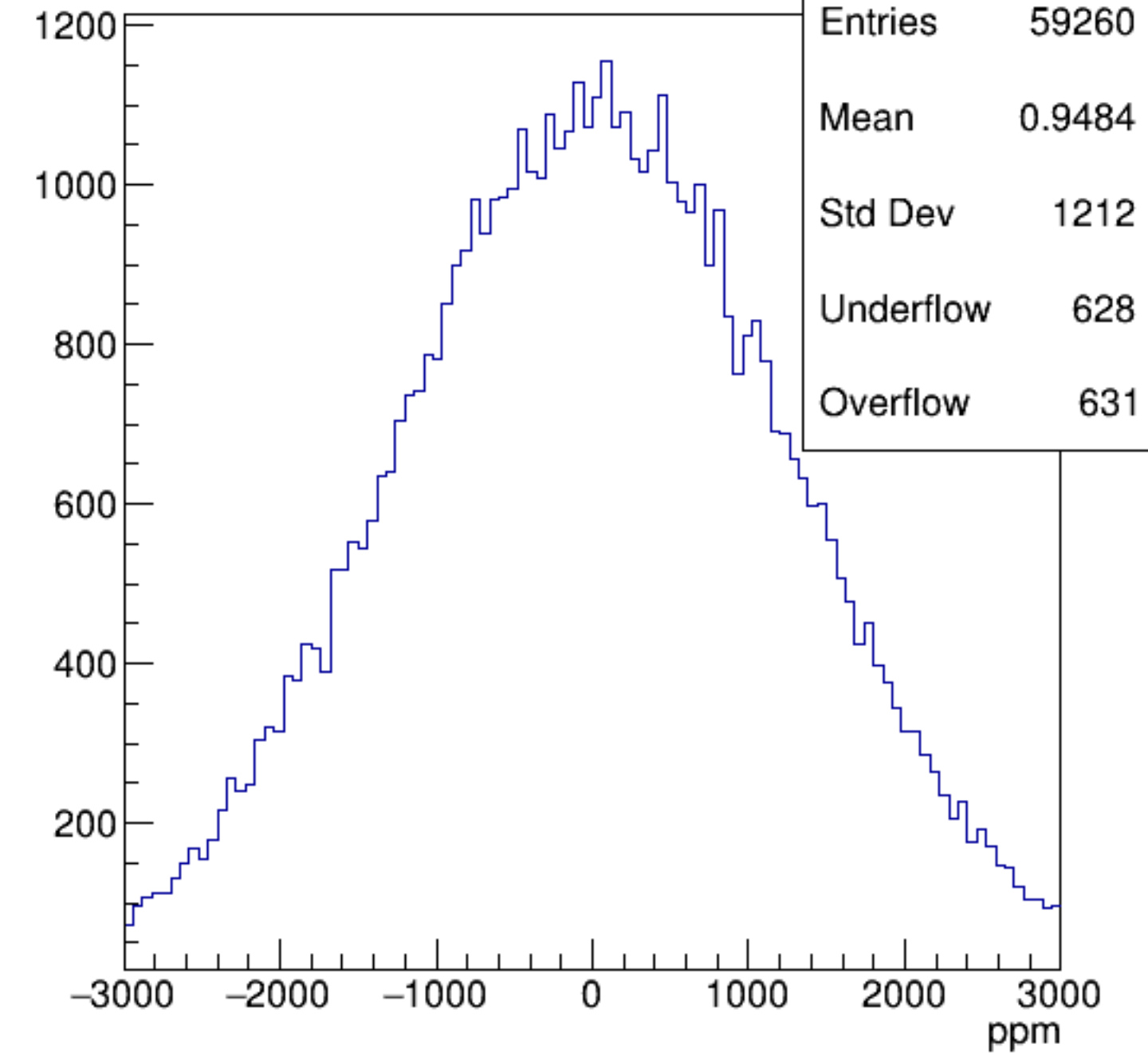
evtTree

Entries	237140
Mean	4.619
Std Dev	0.03461

asym_usl/ppm:pattern_number {ErrorFlag==0}

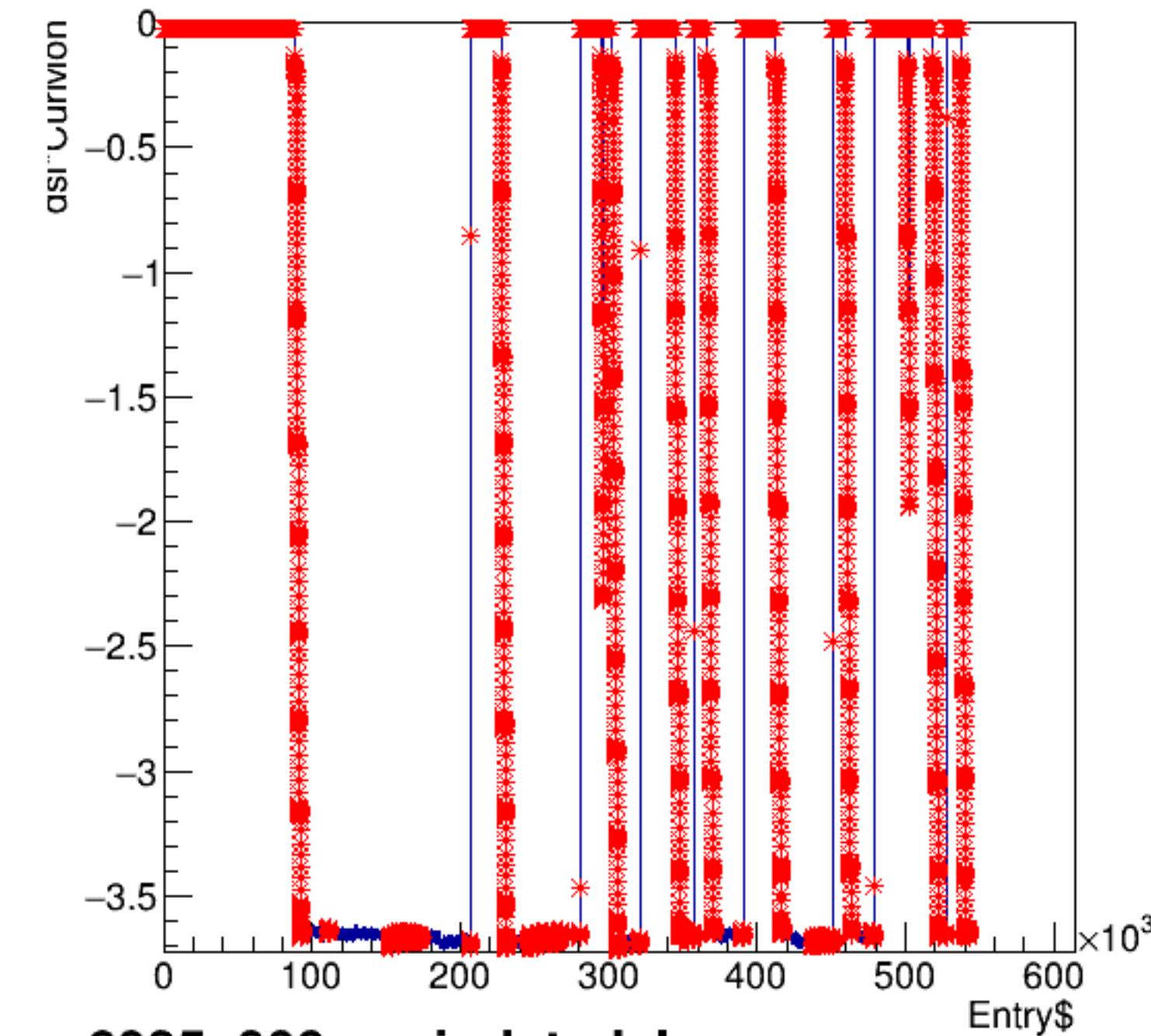


asym_usl



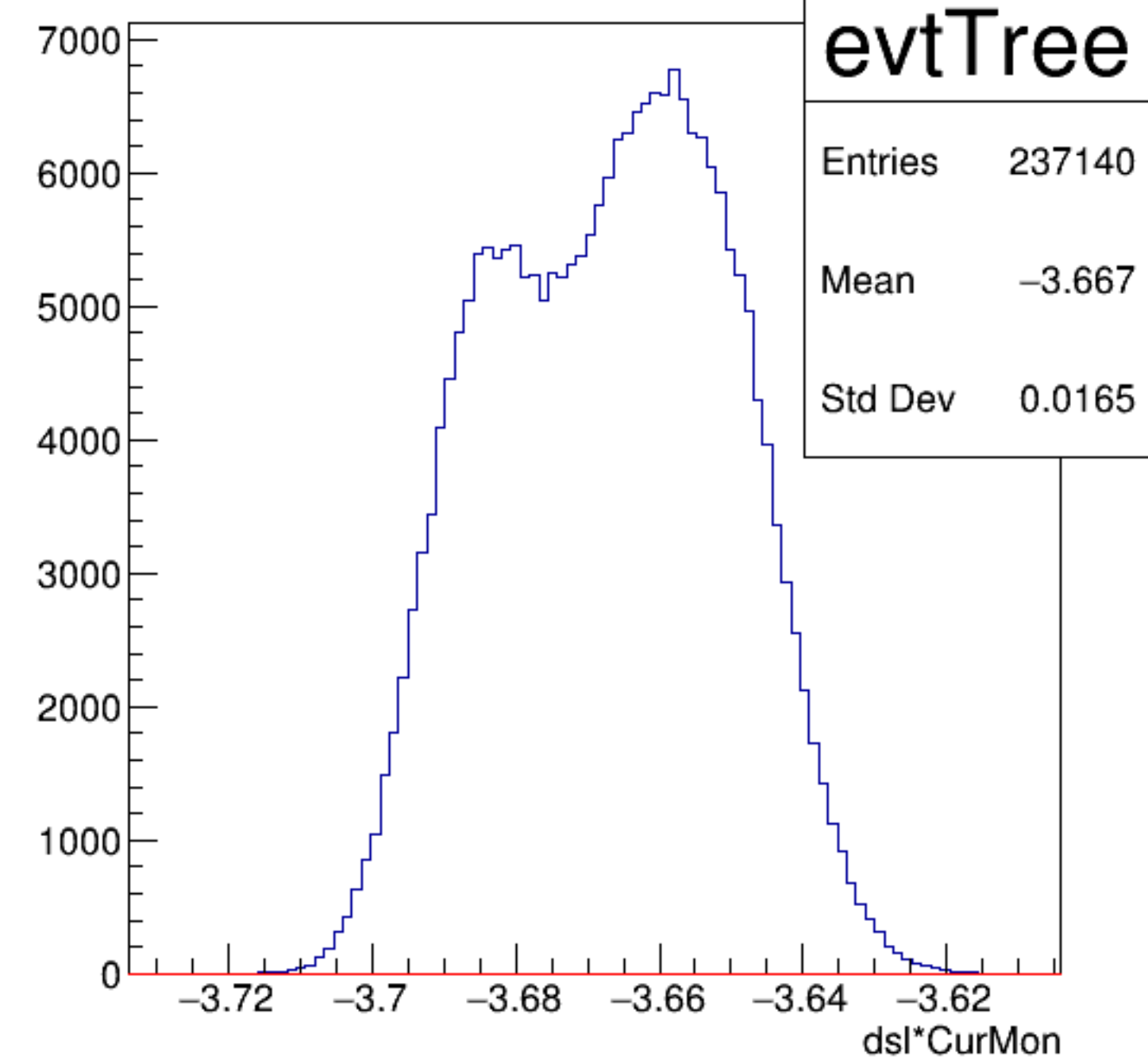
Entries	59260
Mean	0.9484
Std Dev	1212
Underflow	628
Overflow	631

dsl*CurMon:Entry\$

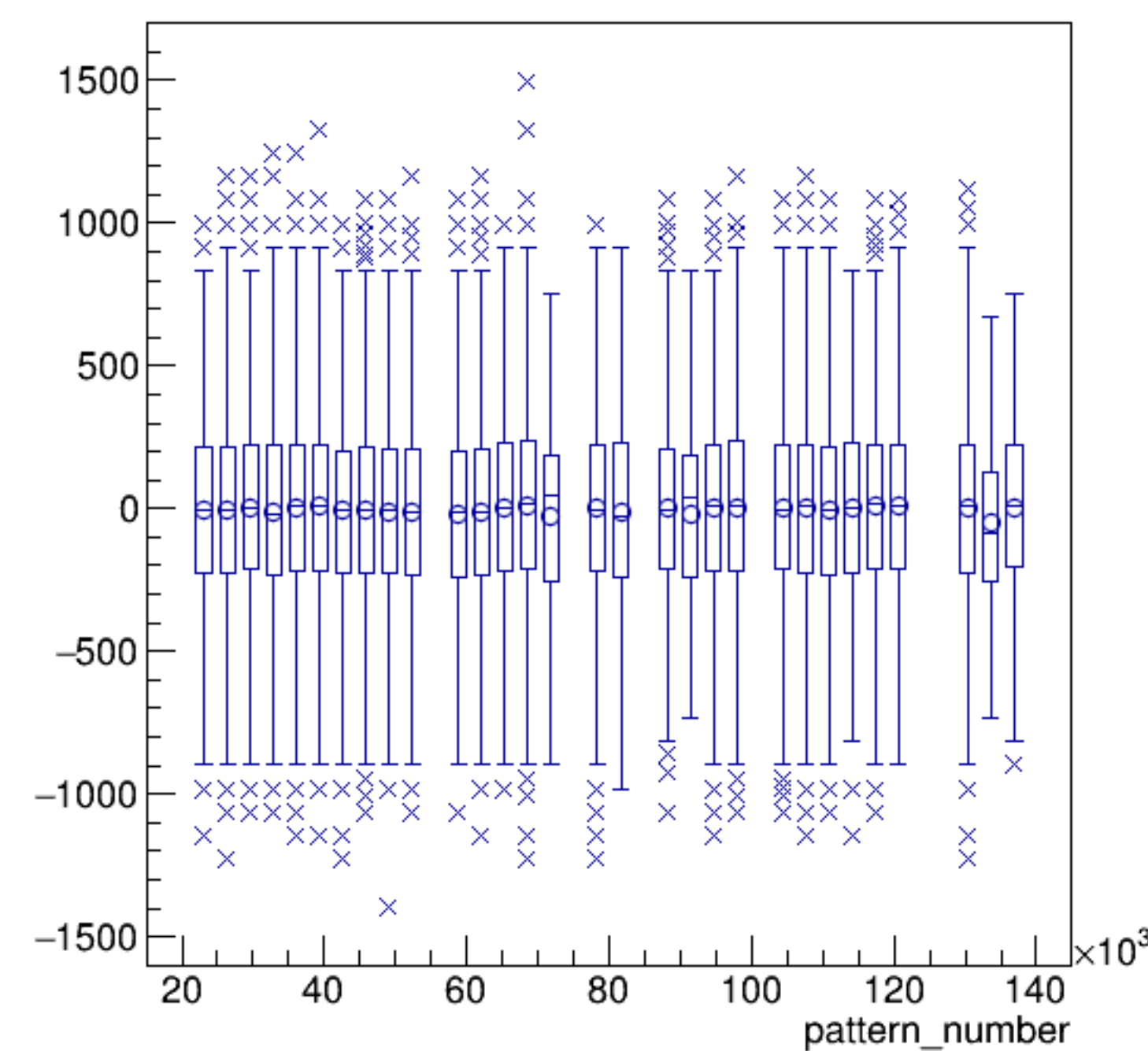


run6085_000_maindet_dsl.png

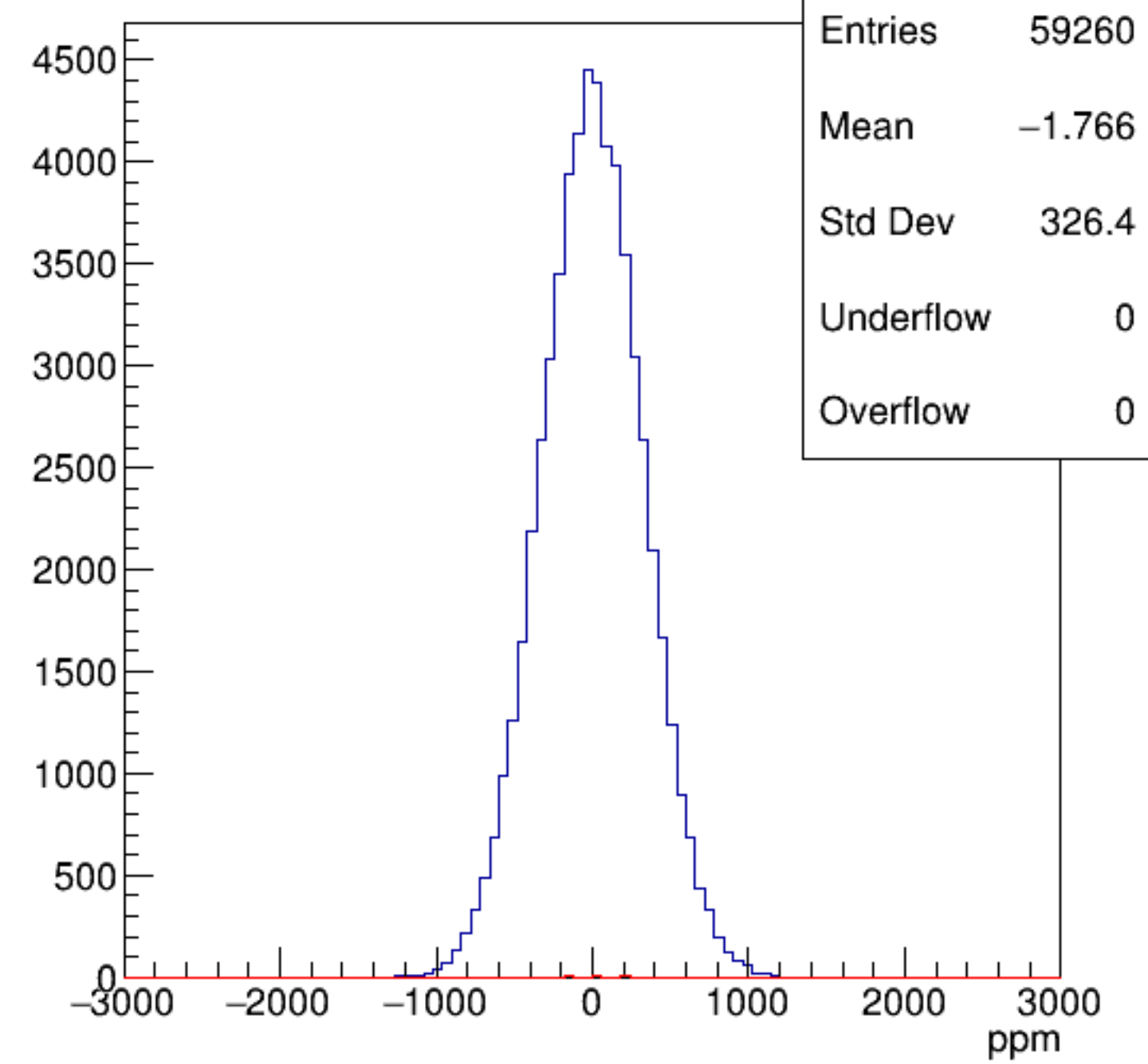
dsl*CurMon {ErrorFlag==0}



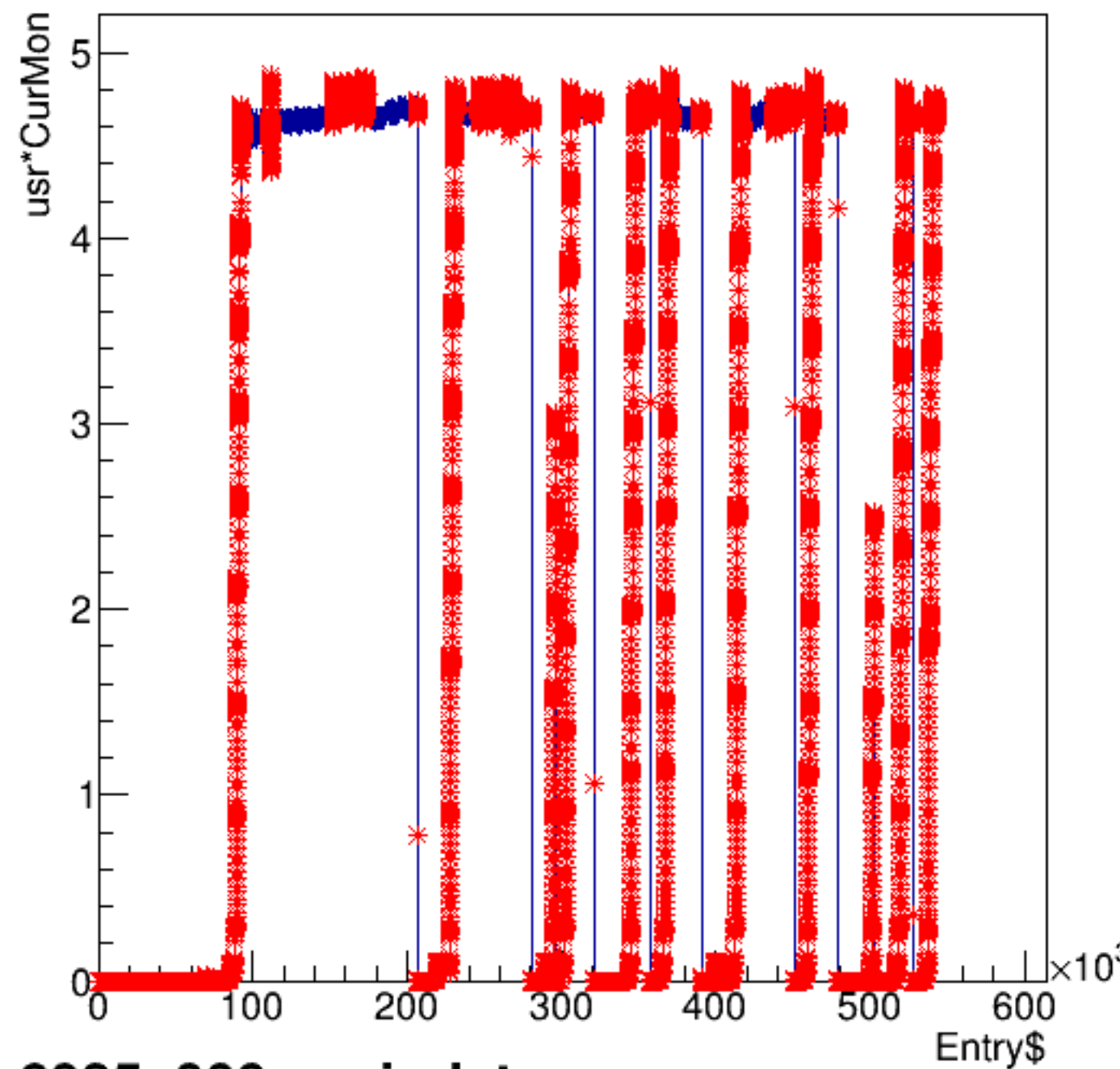
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl

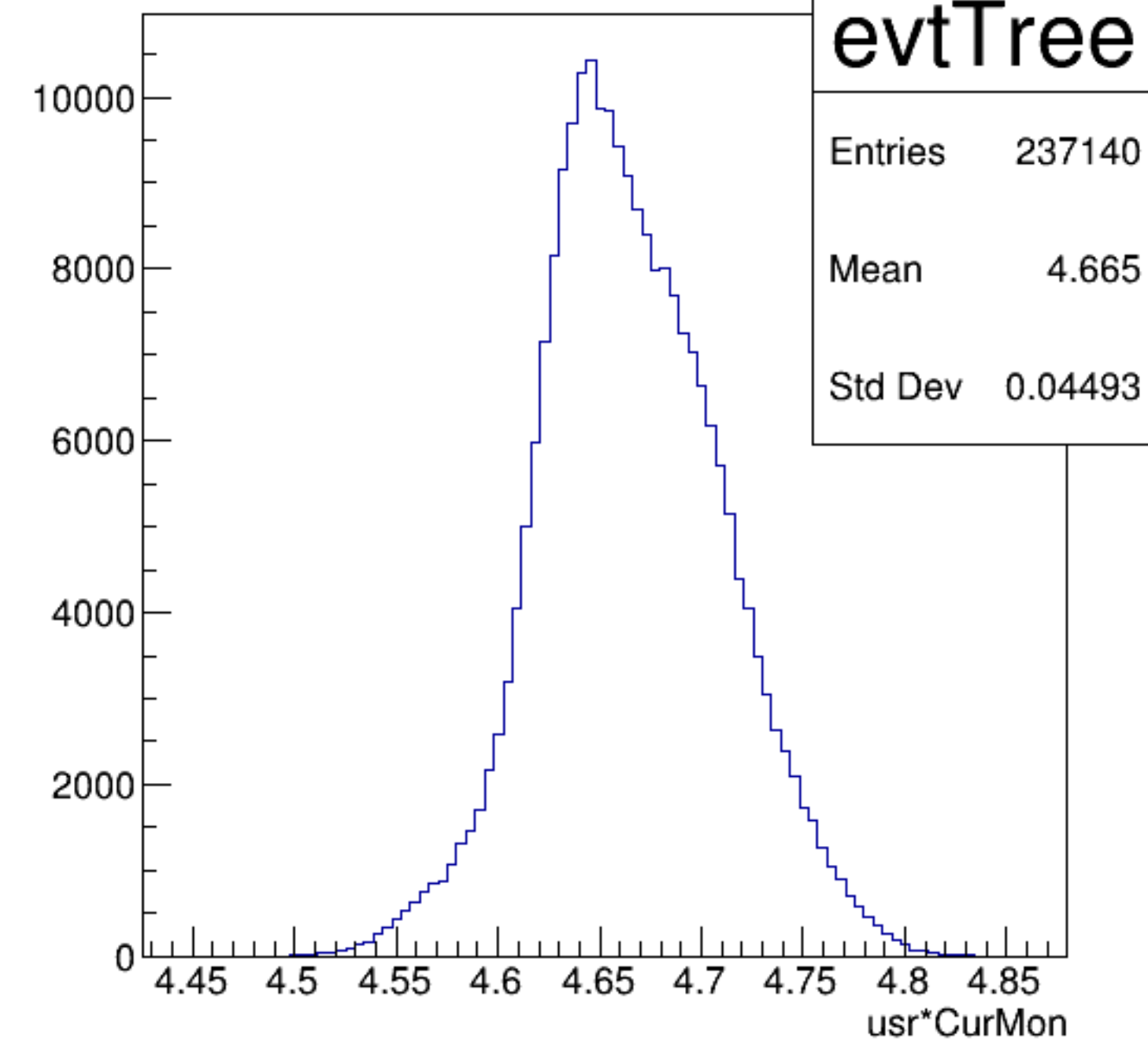


usr*CurMon:Entry\$



run6085_000_maindet_usr.png

usr*CurMon {ErrorFlag==0}



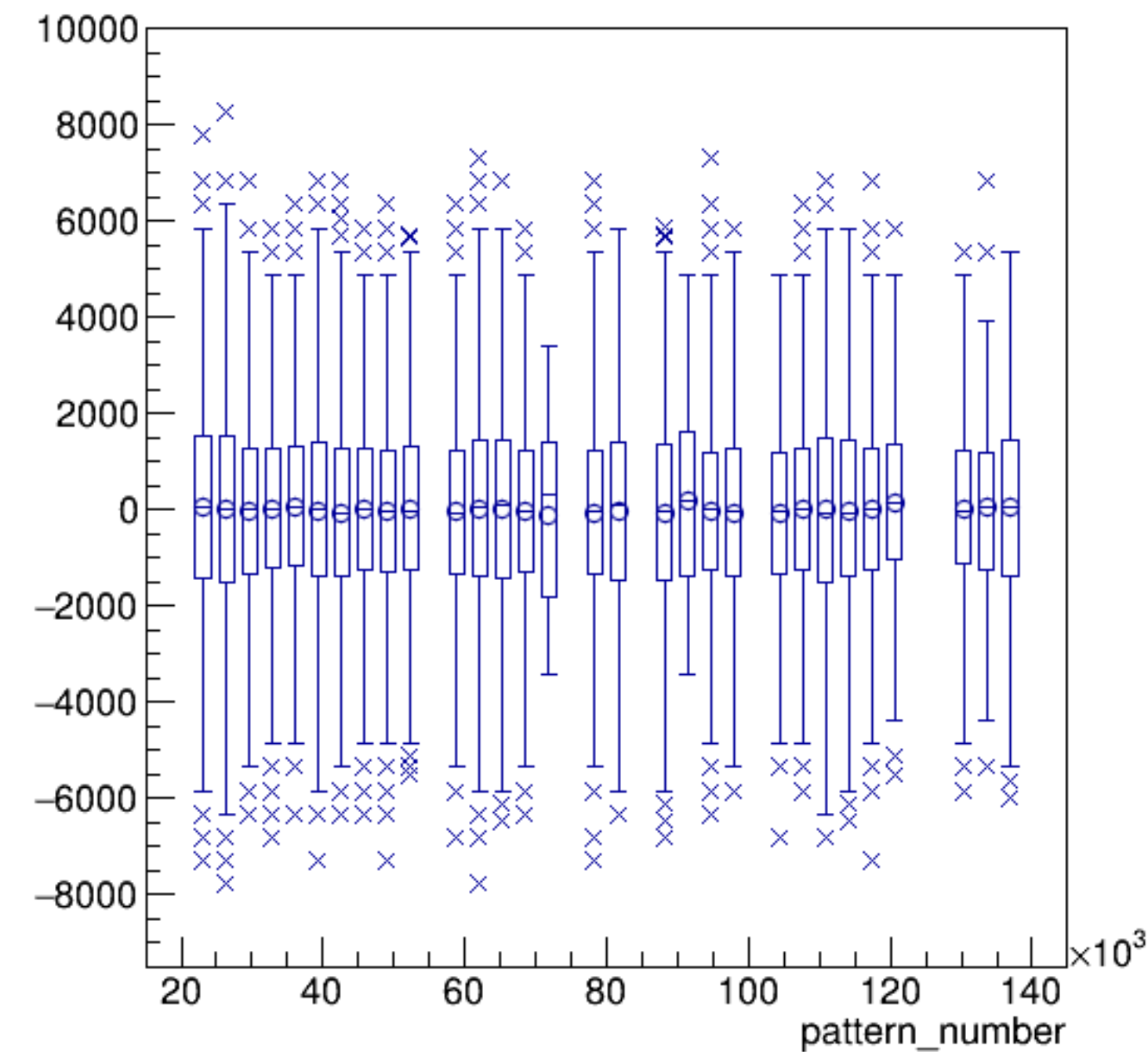
evtTree

Entries 237140

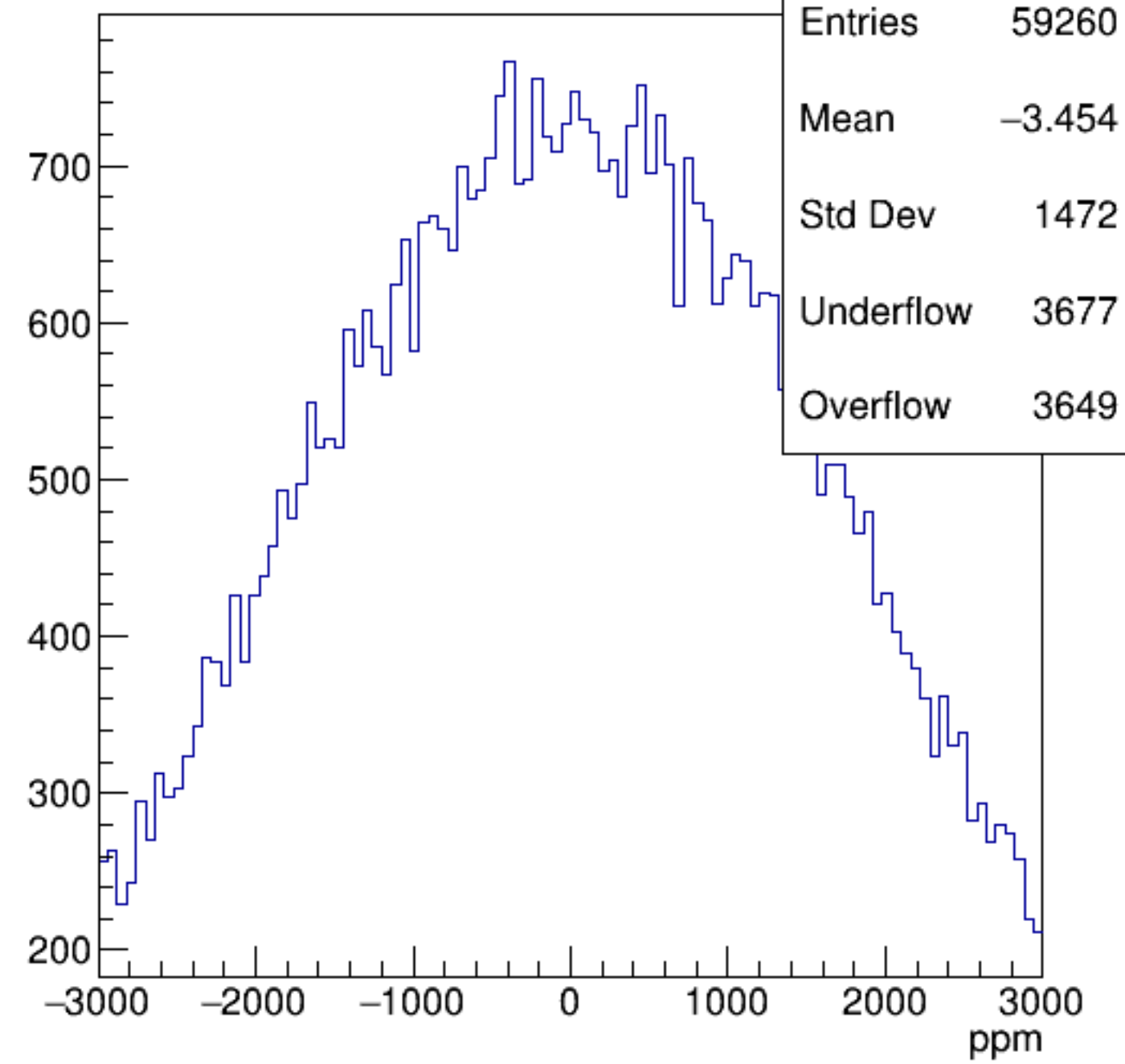
Mean 4.665

Std Dev 0.04493

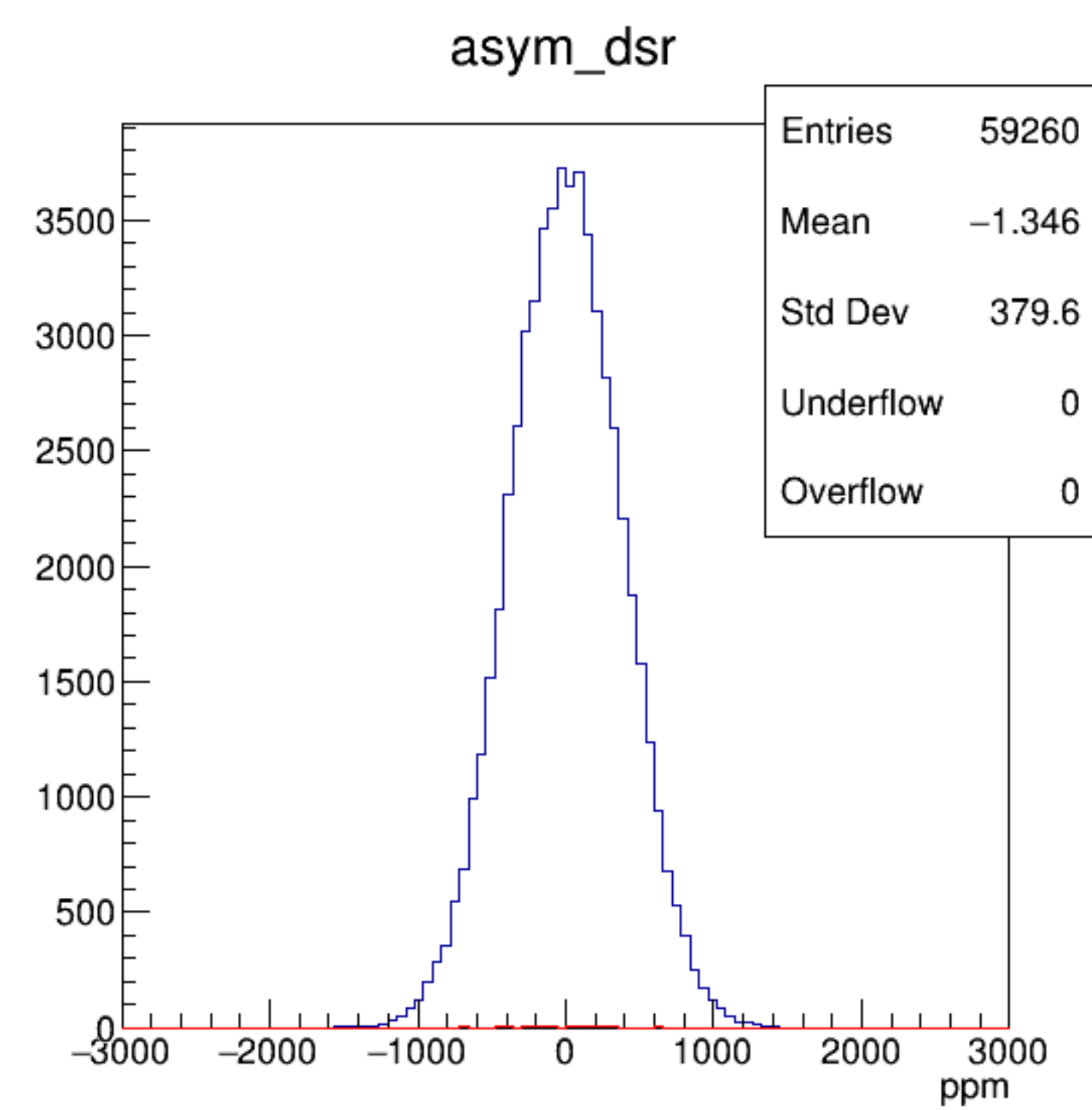
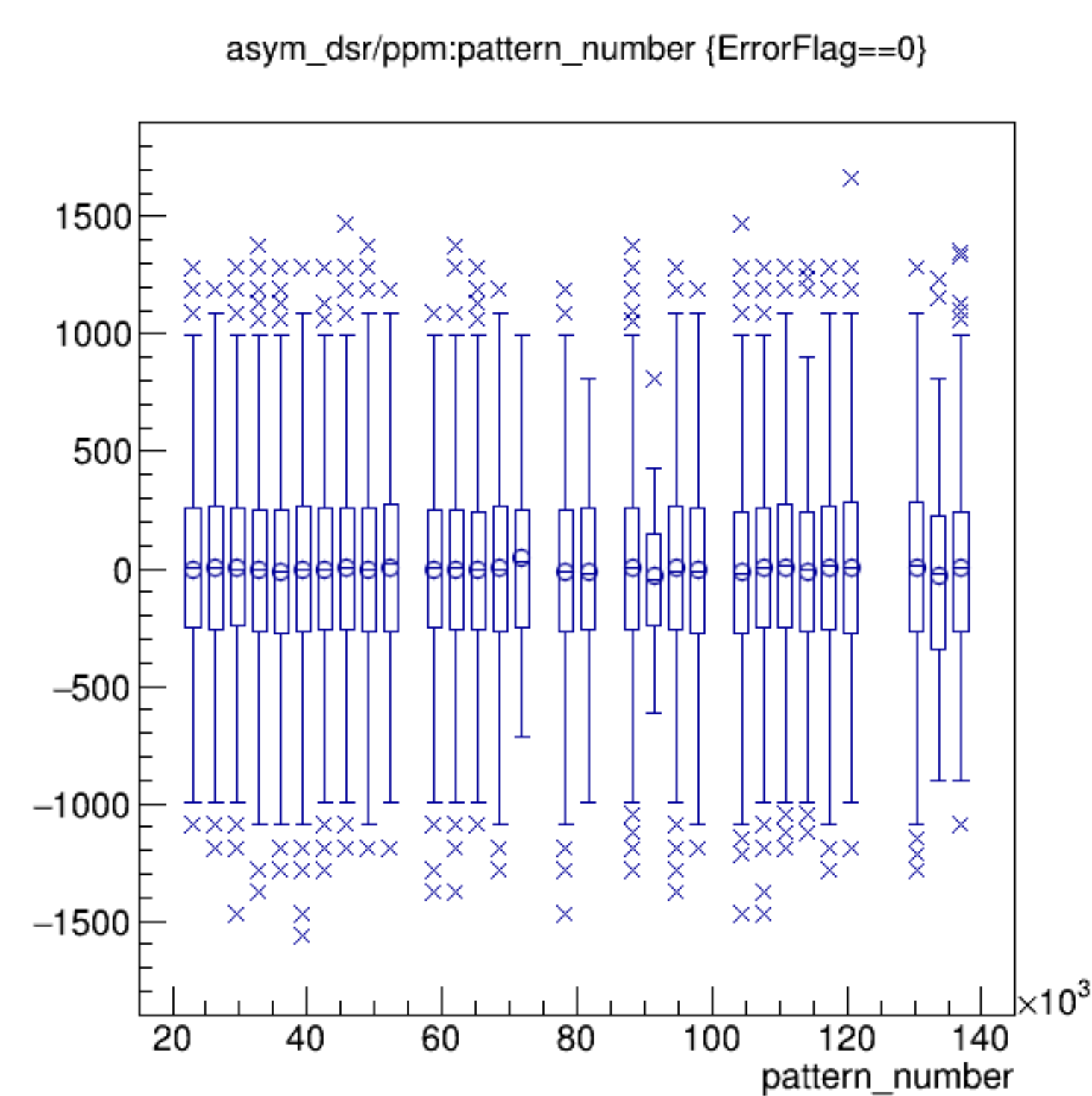
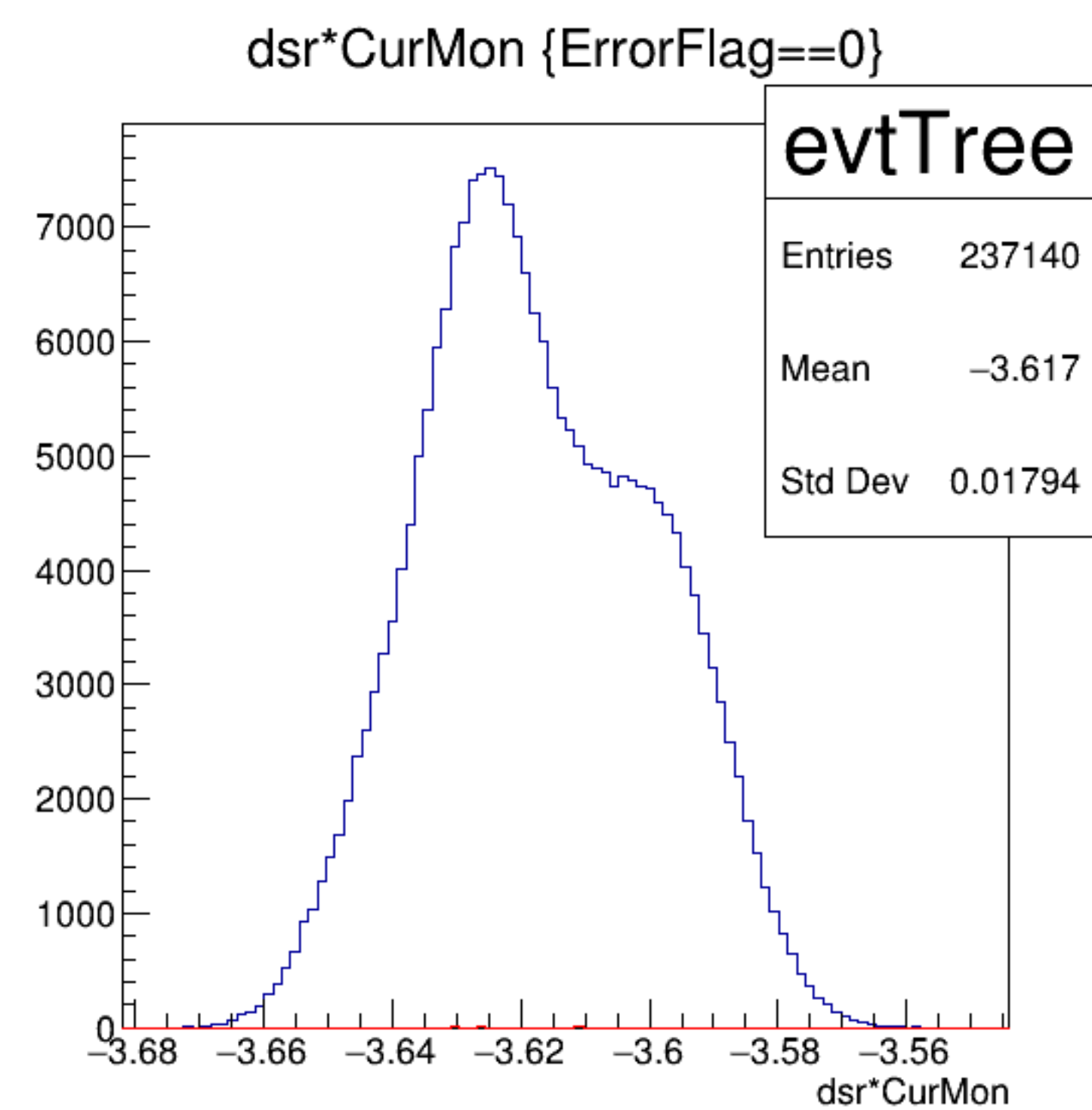
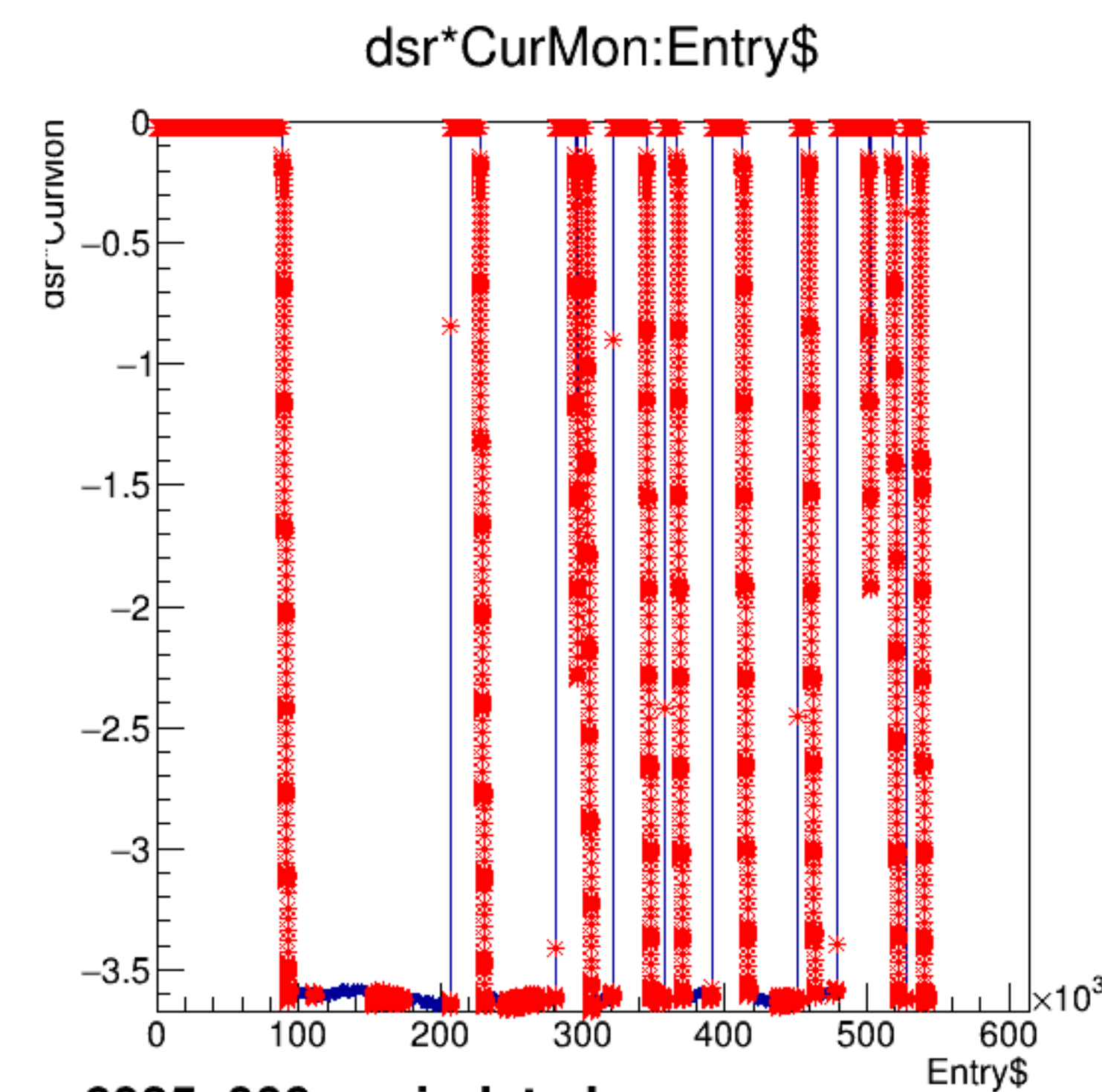
asym_usr/ppm:pattern_number {ErrorFlag==0}

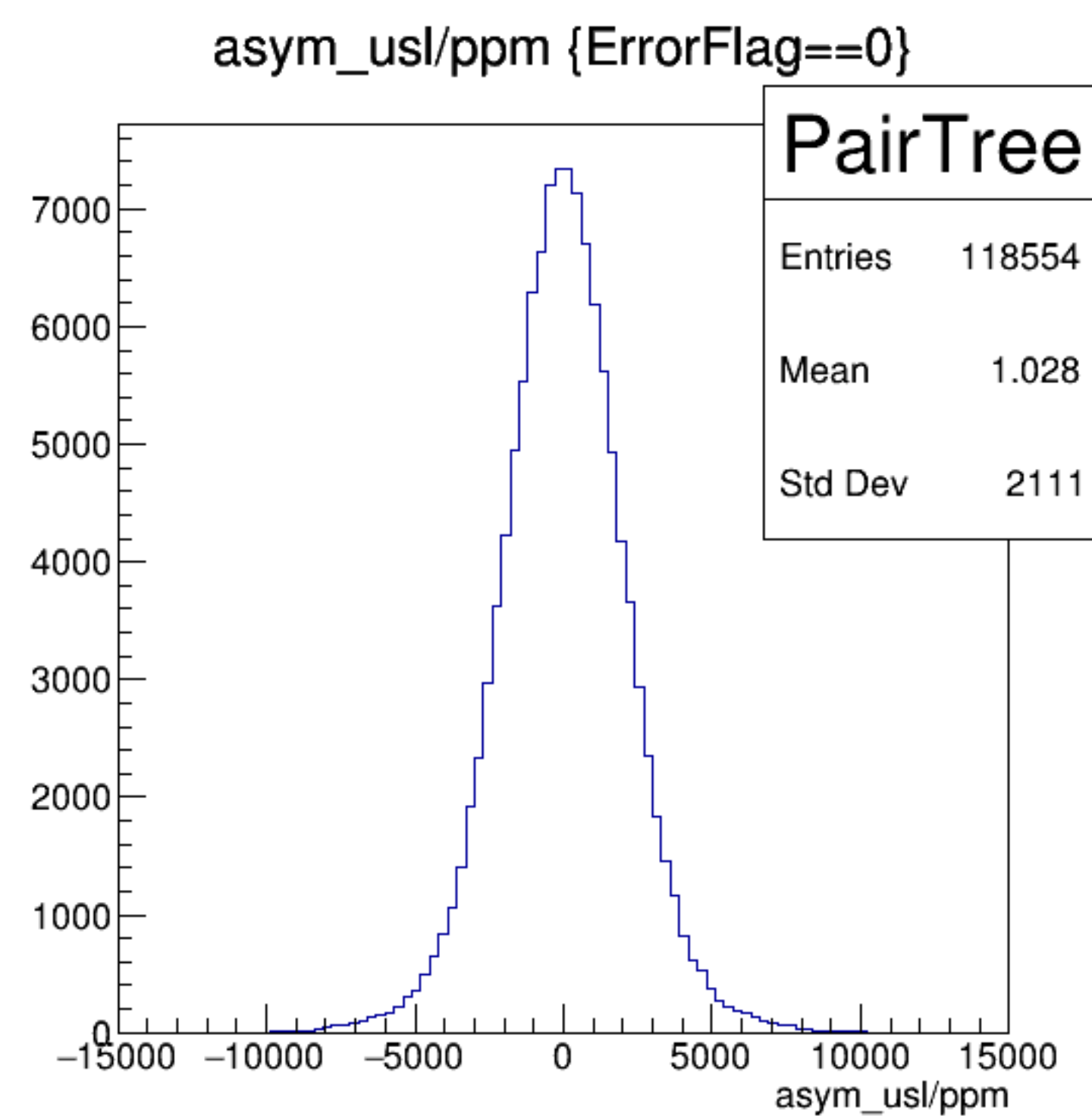
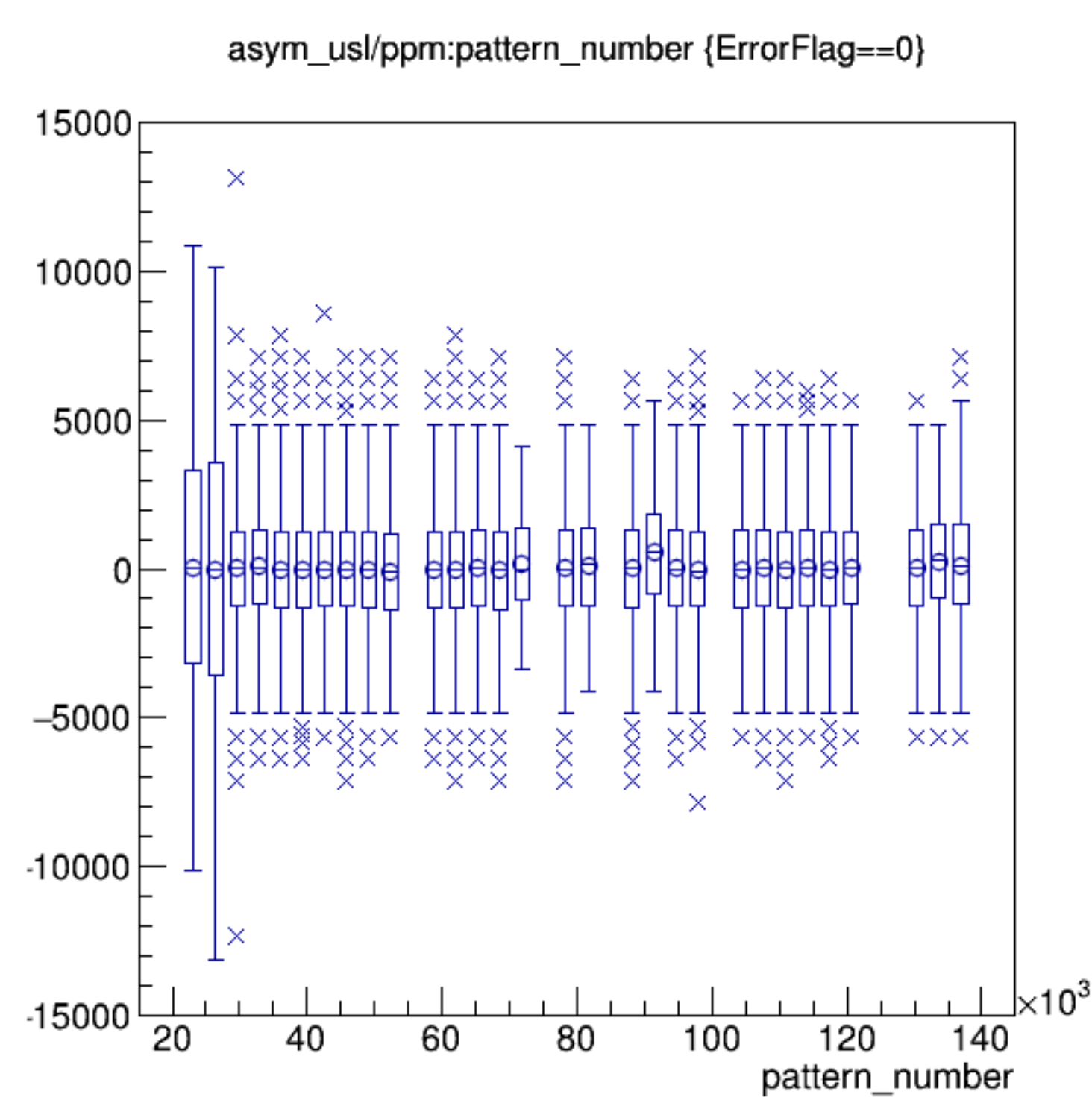
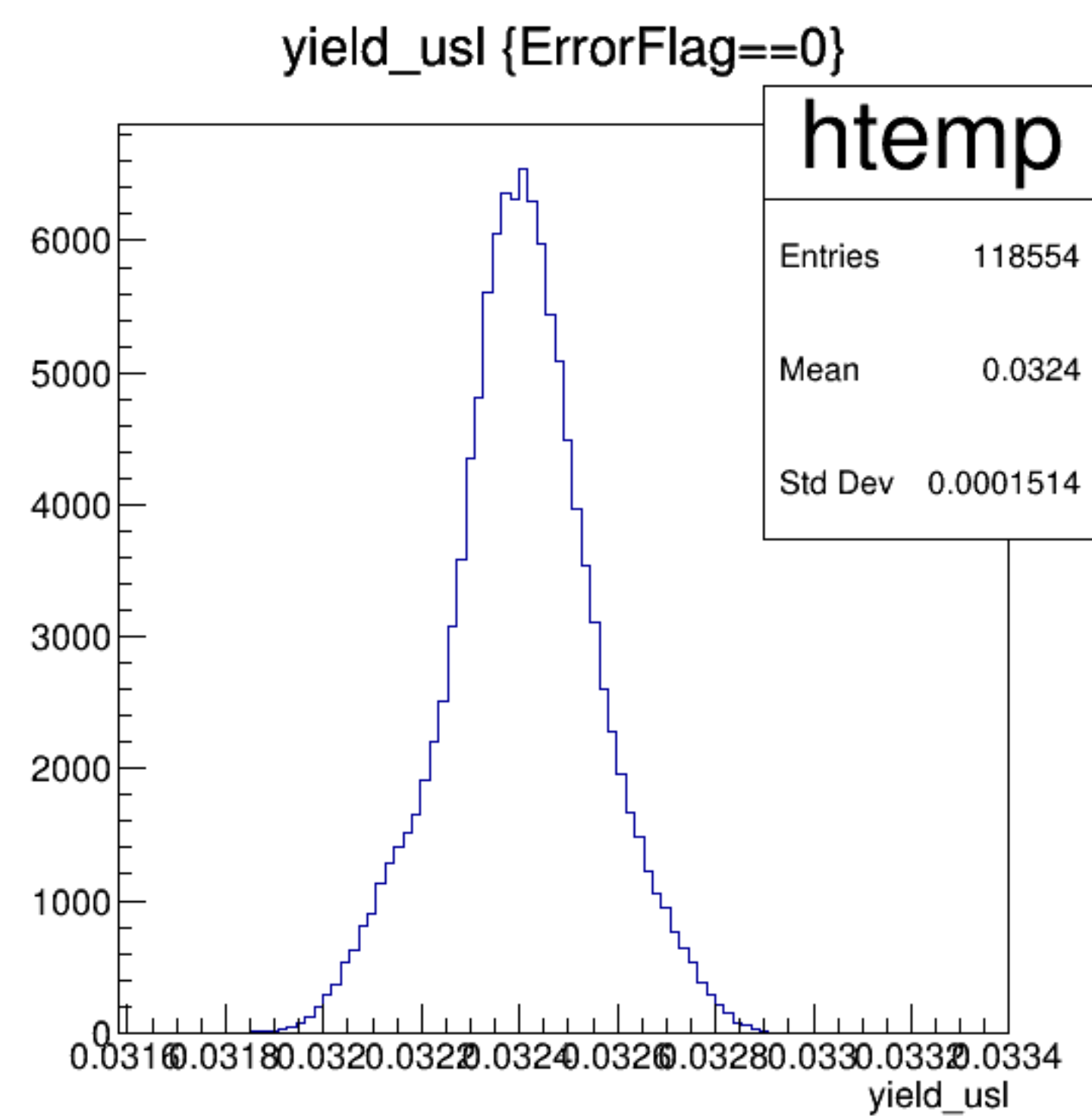
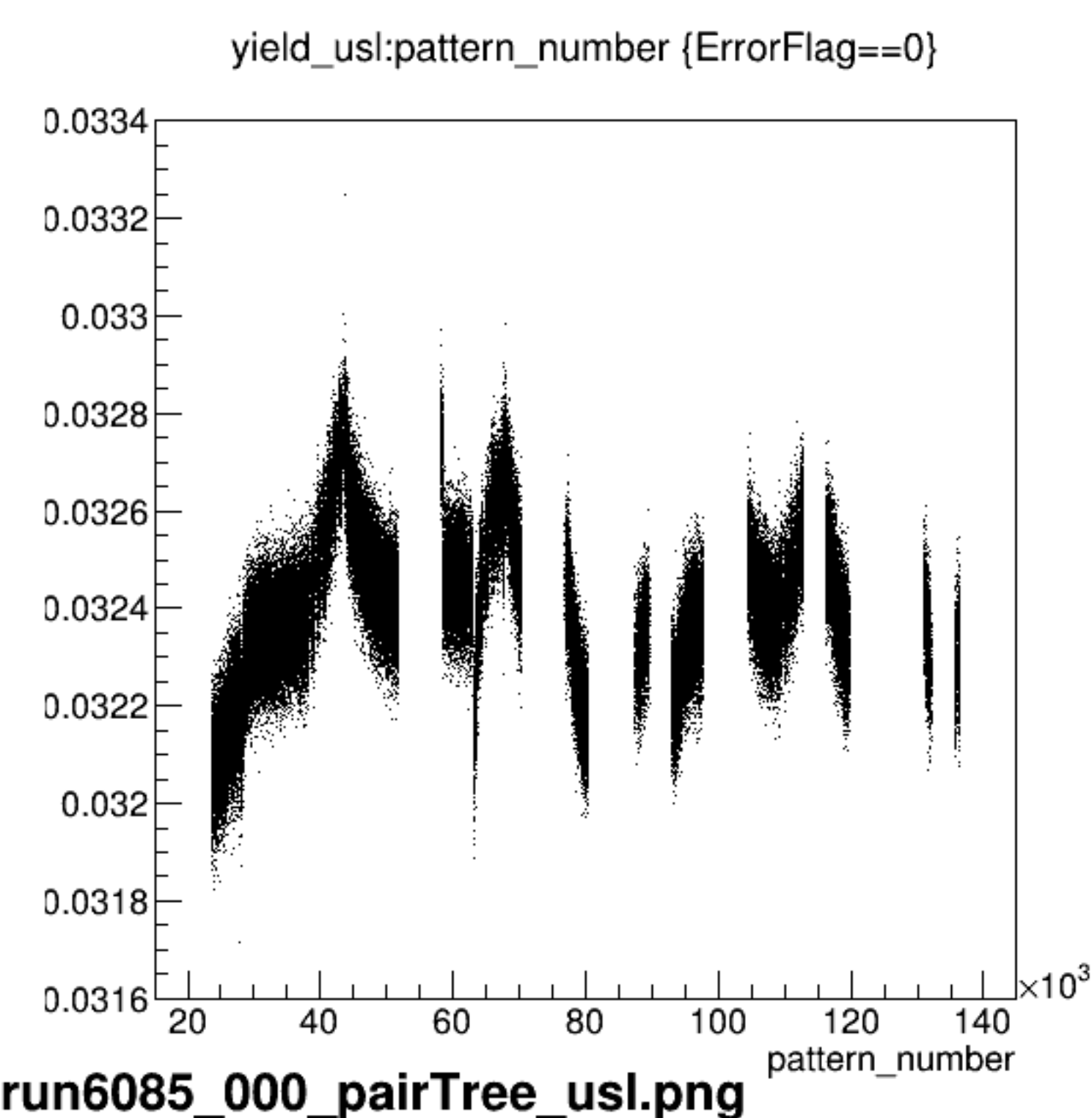


asym_usr

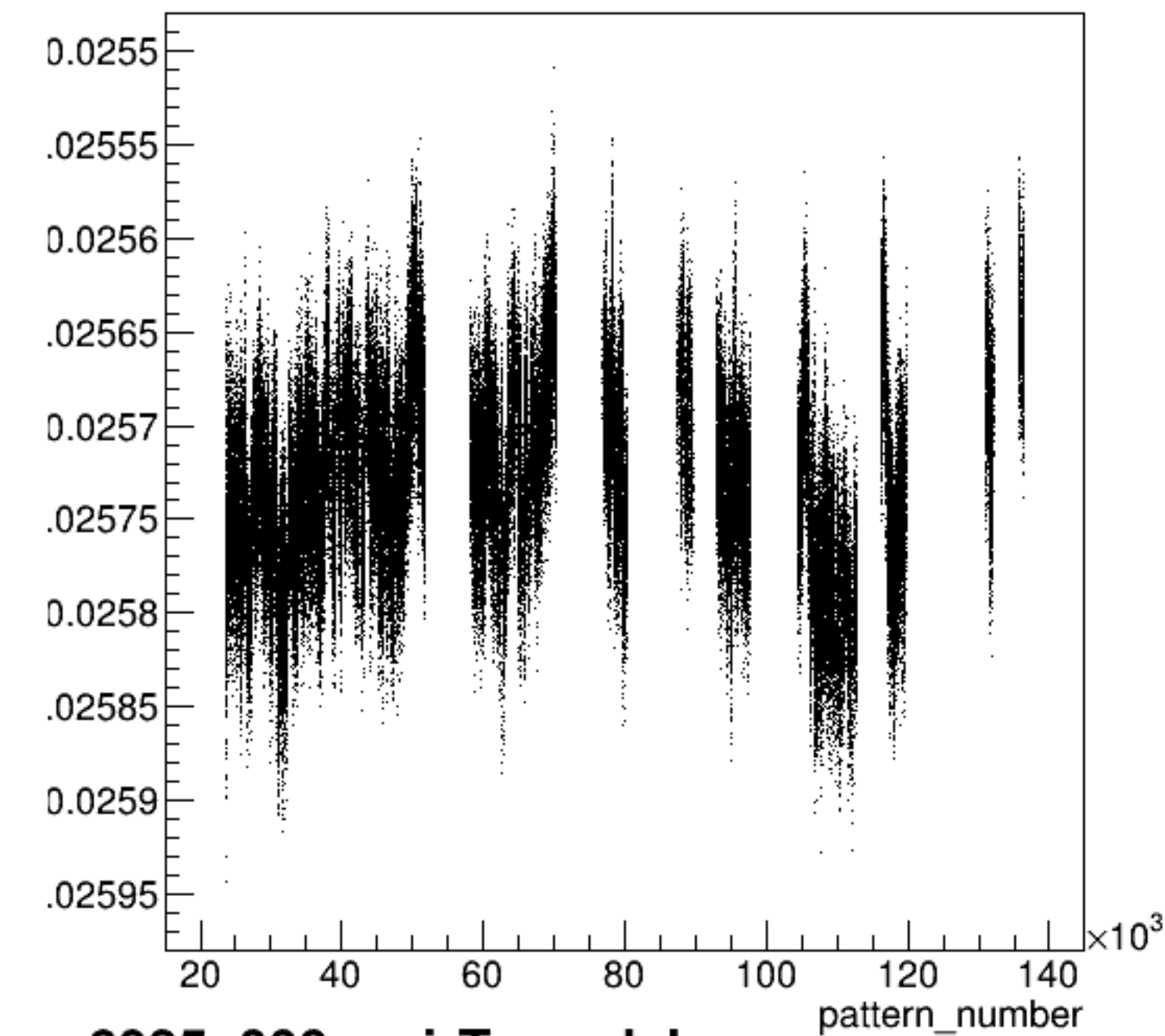


Entries	59260
Mean	-3.454
Std Dev	1472
Underflow	3677
Overflow	3649



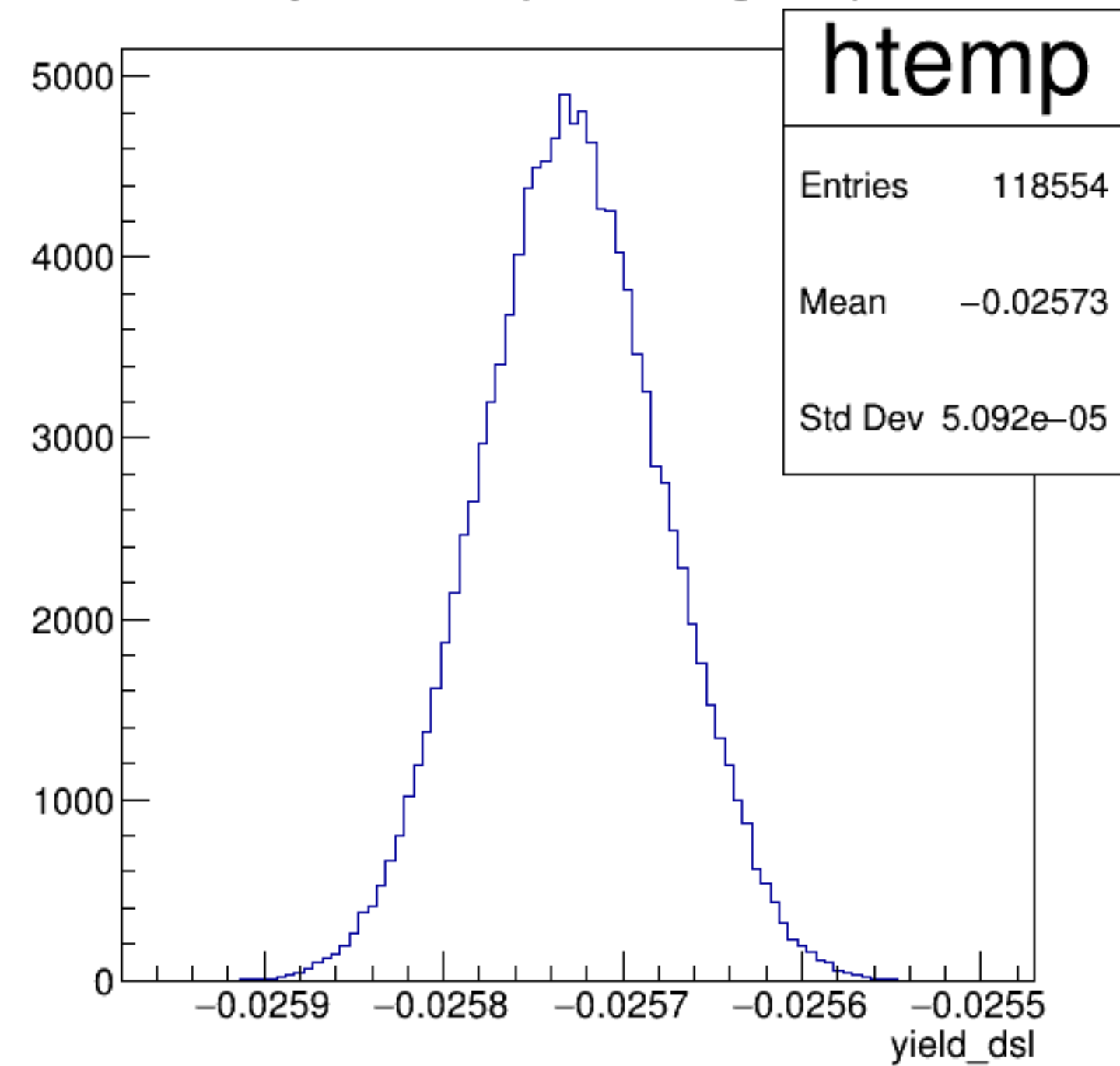


yield_dsl:pattern_number {ErrorFlag==0}

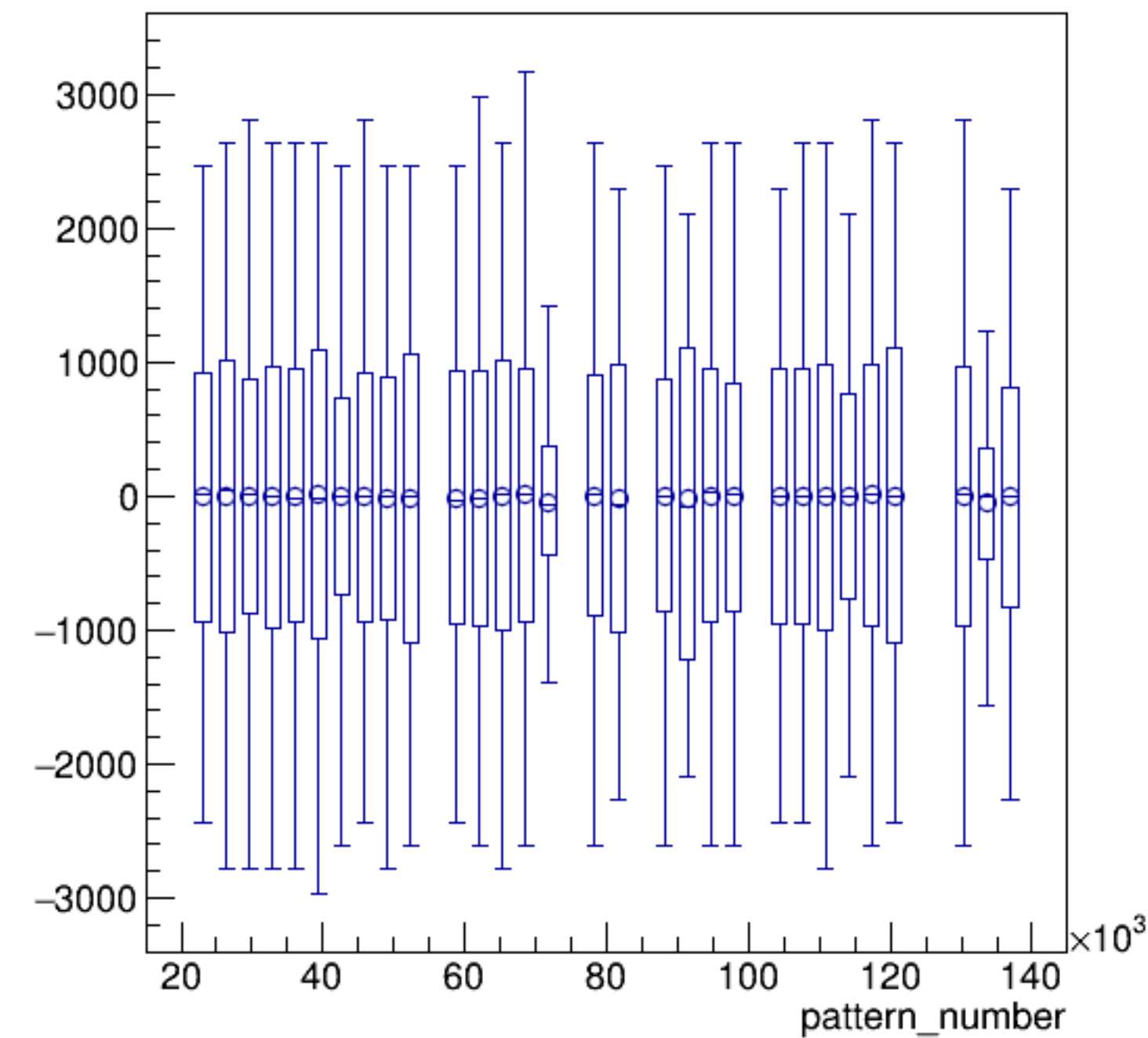


run6085_000_pairTree_dsl.png

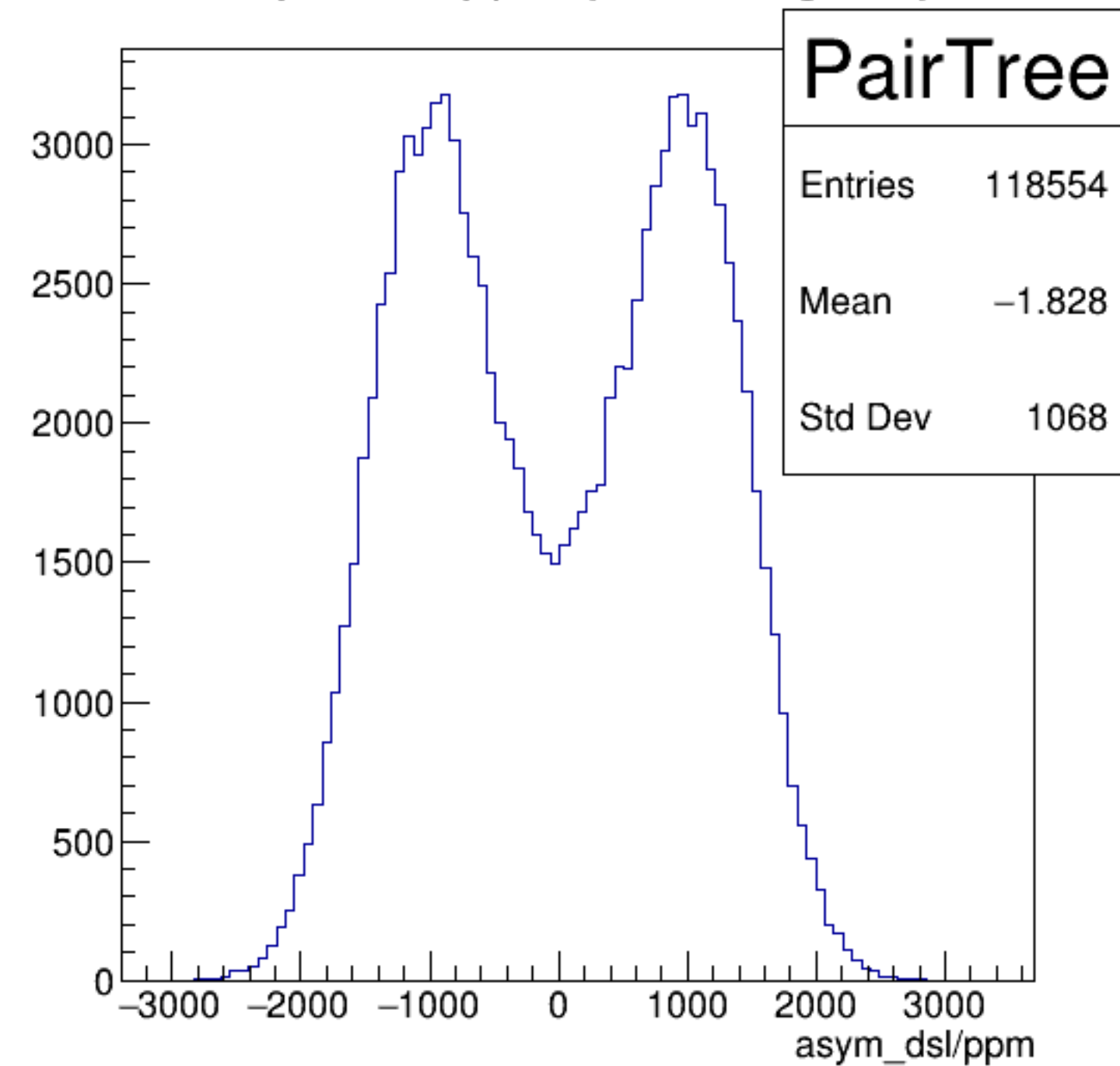
yield_dsl {ErrorFlag==0}

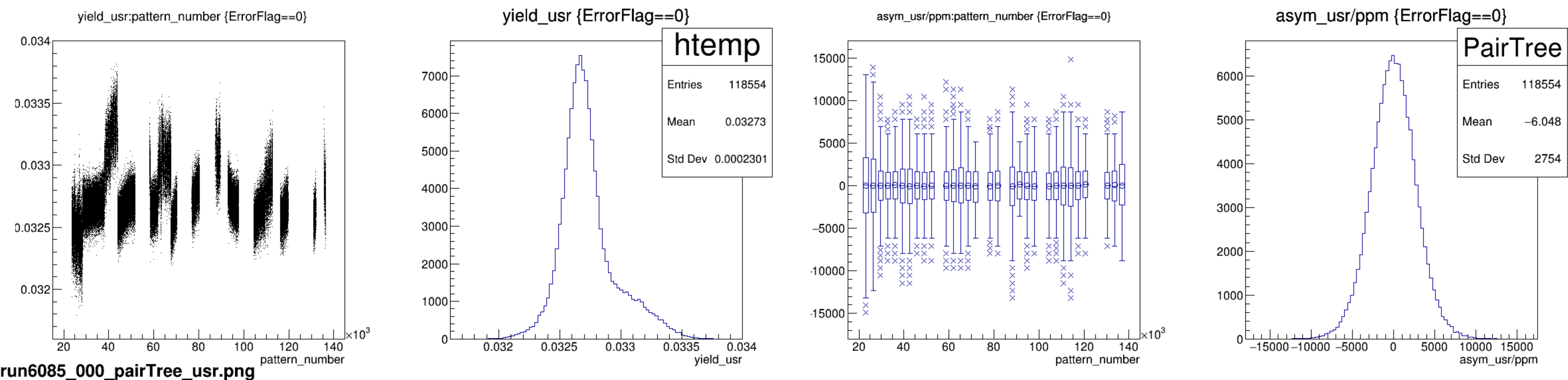


asym_dsl/ppm:pattern_number {ErrorFlag==0}

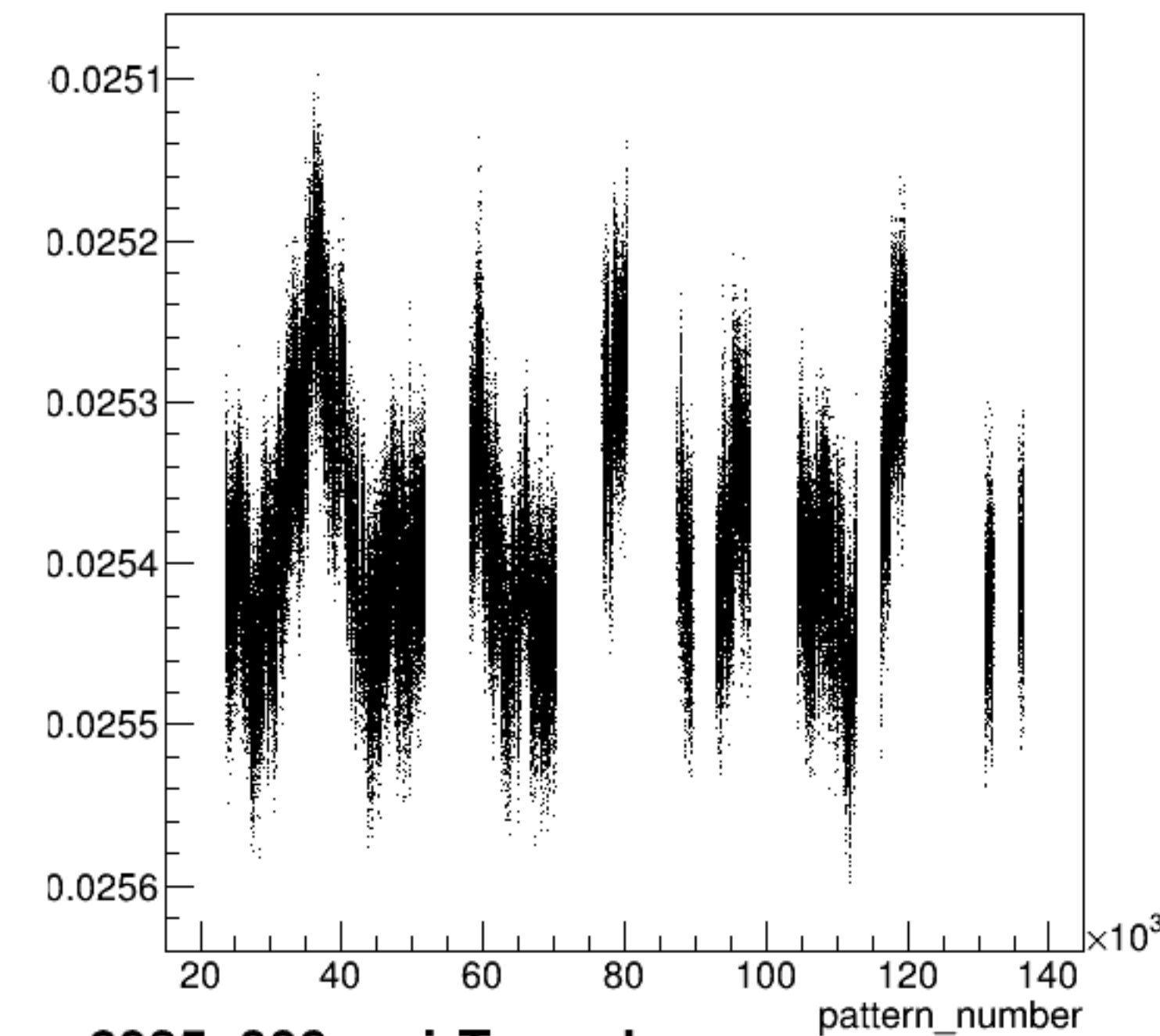


asym_dsl/ppm {ErrorFlag==0}



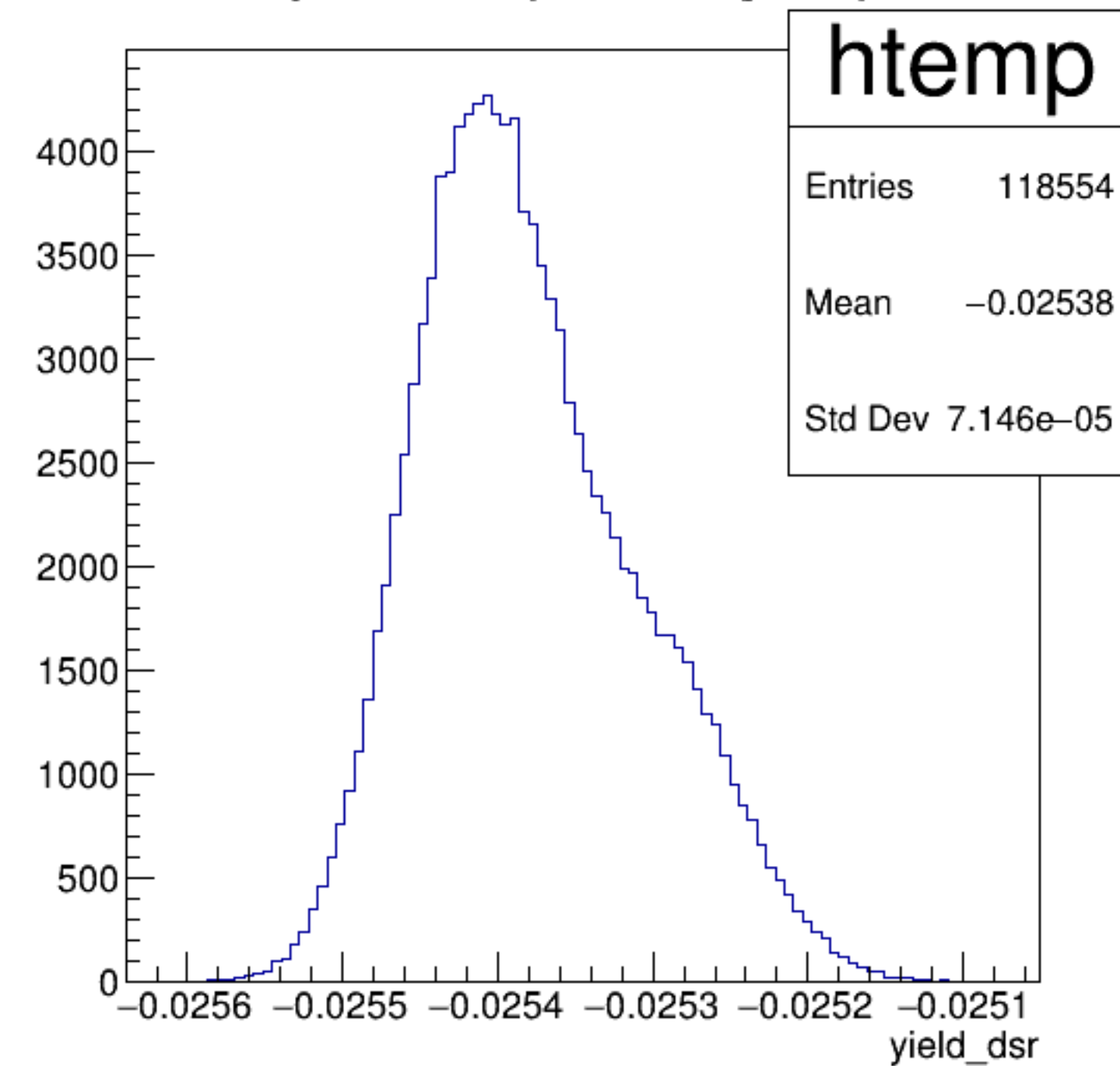


yield_dsr:pattern_number {ErrorFlag==0}

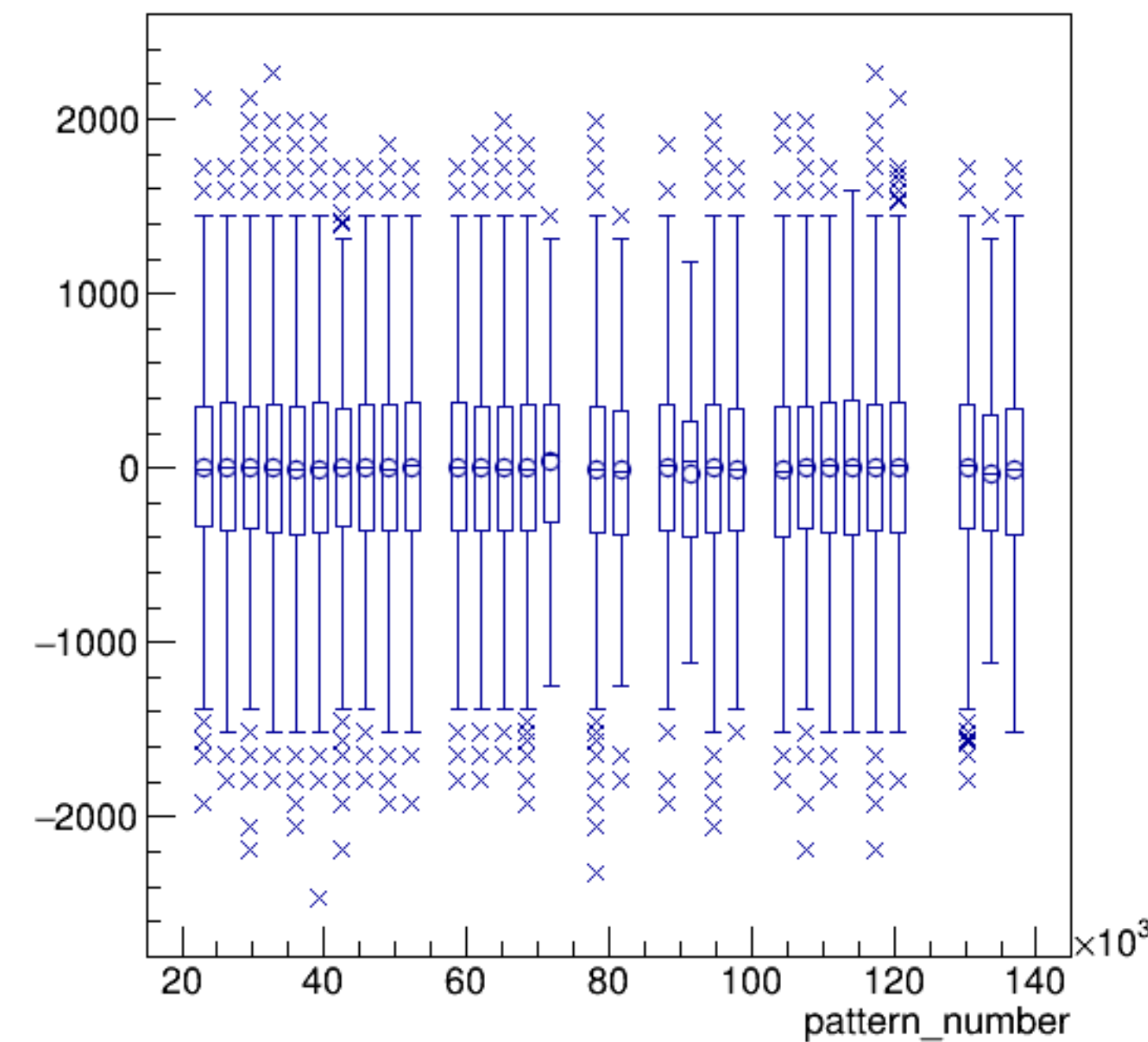


run6085_000_pairTree_dsr.png

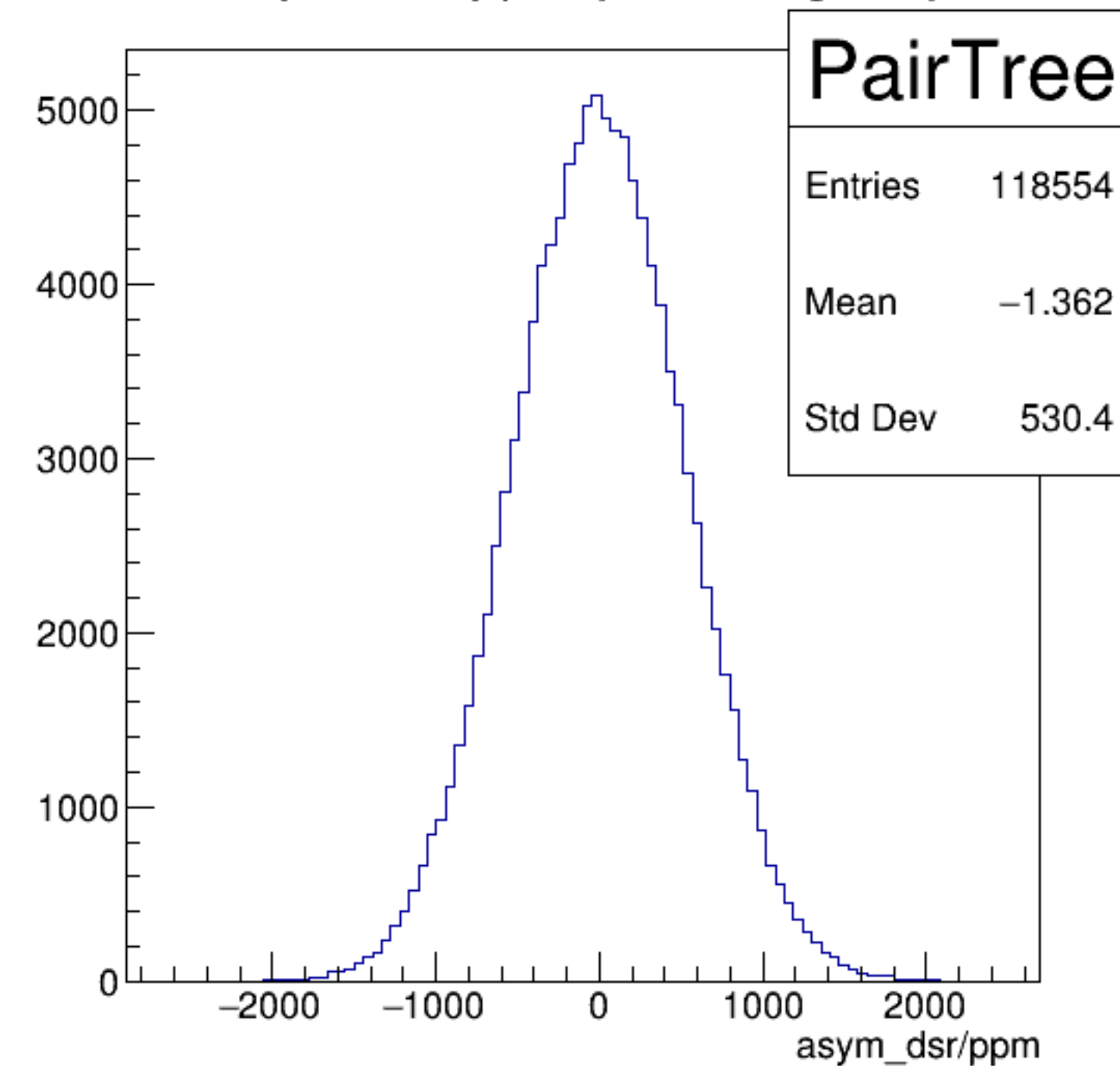
yield_dsr {ErrorFlag==0}



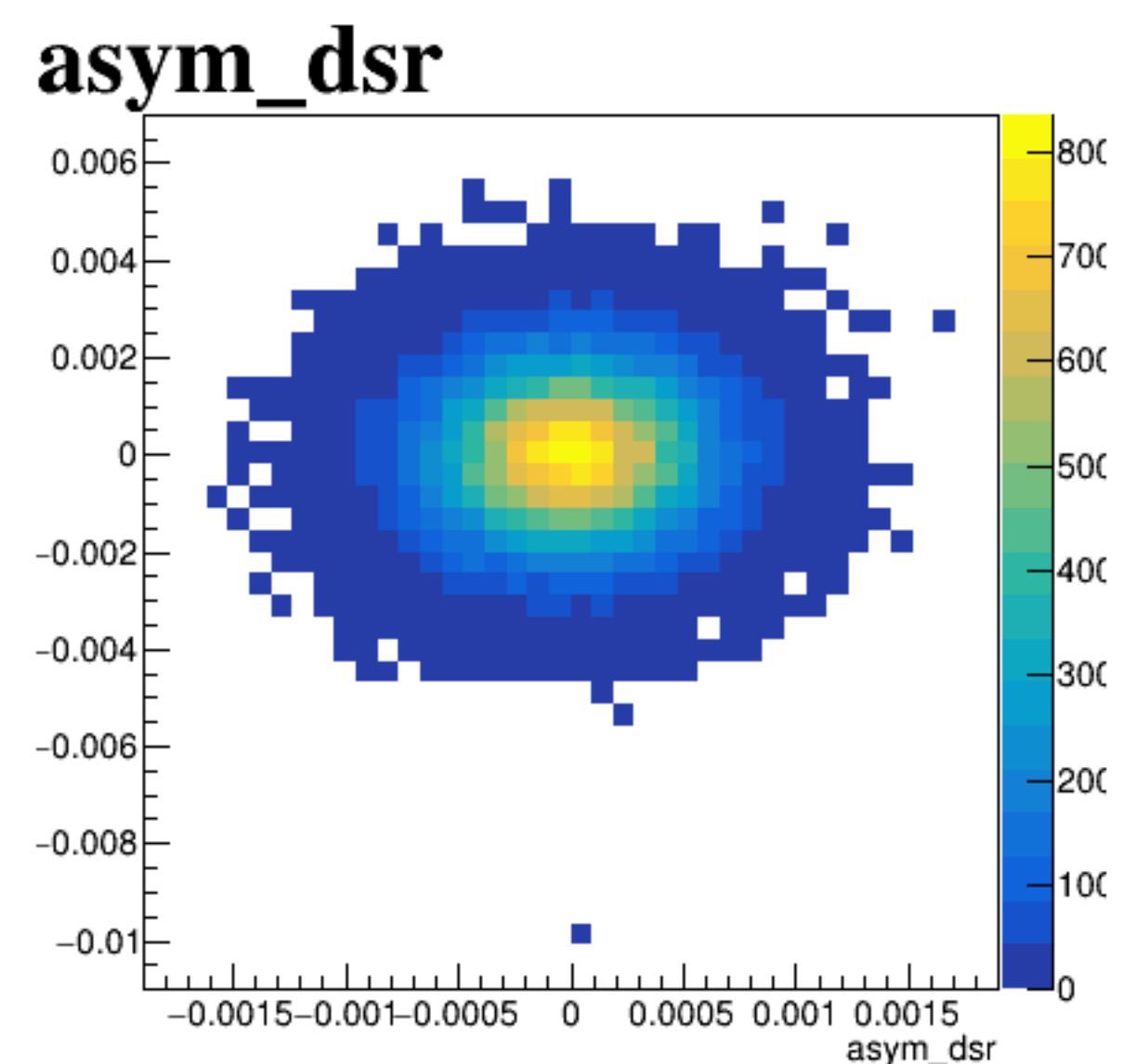
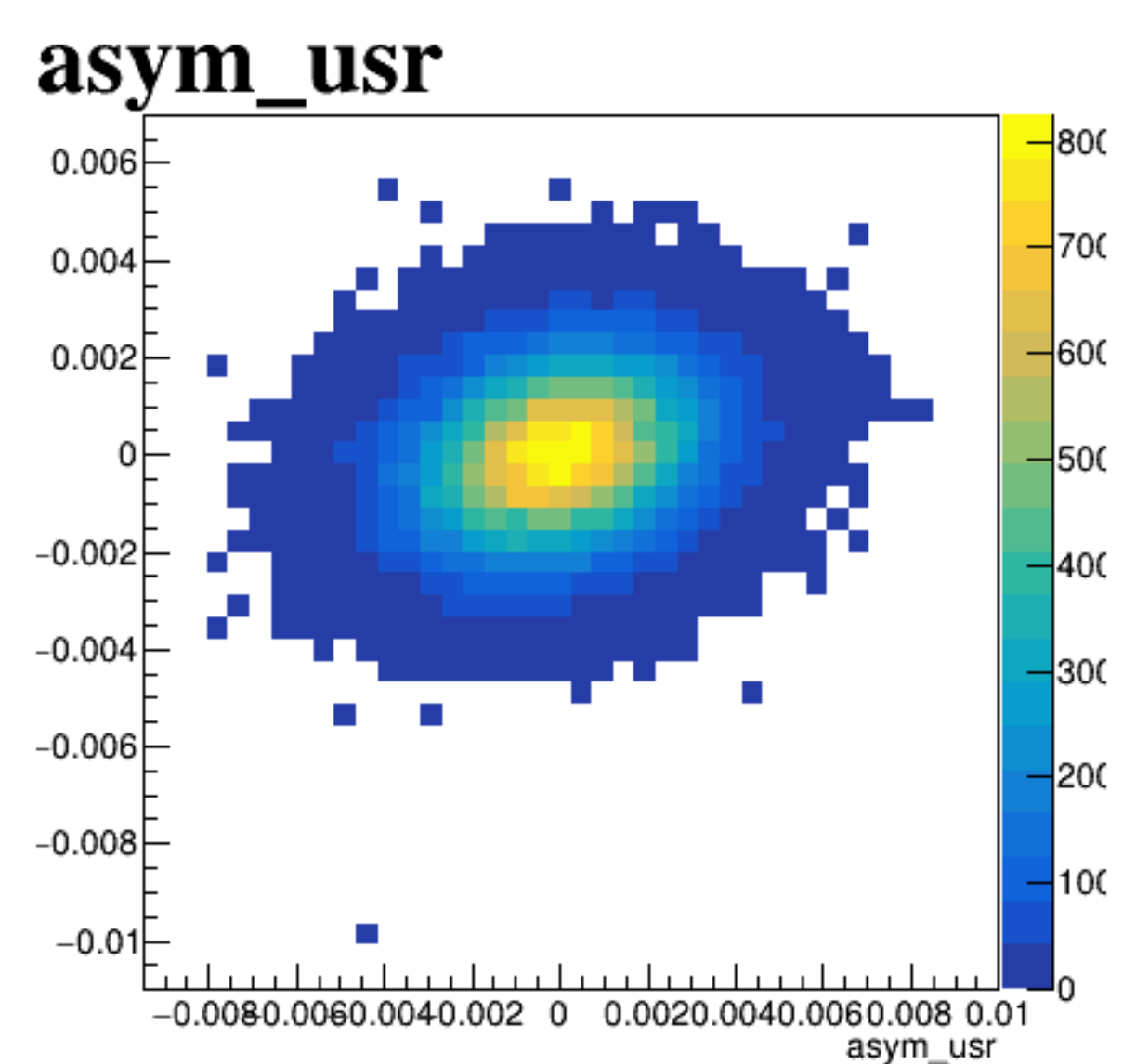
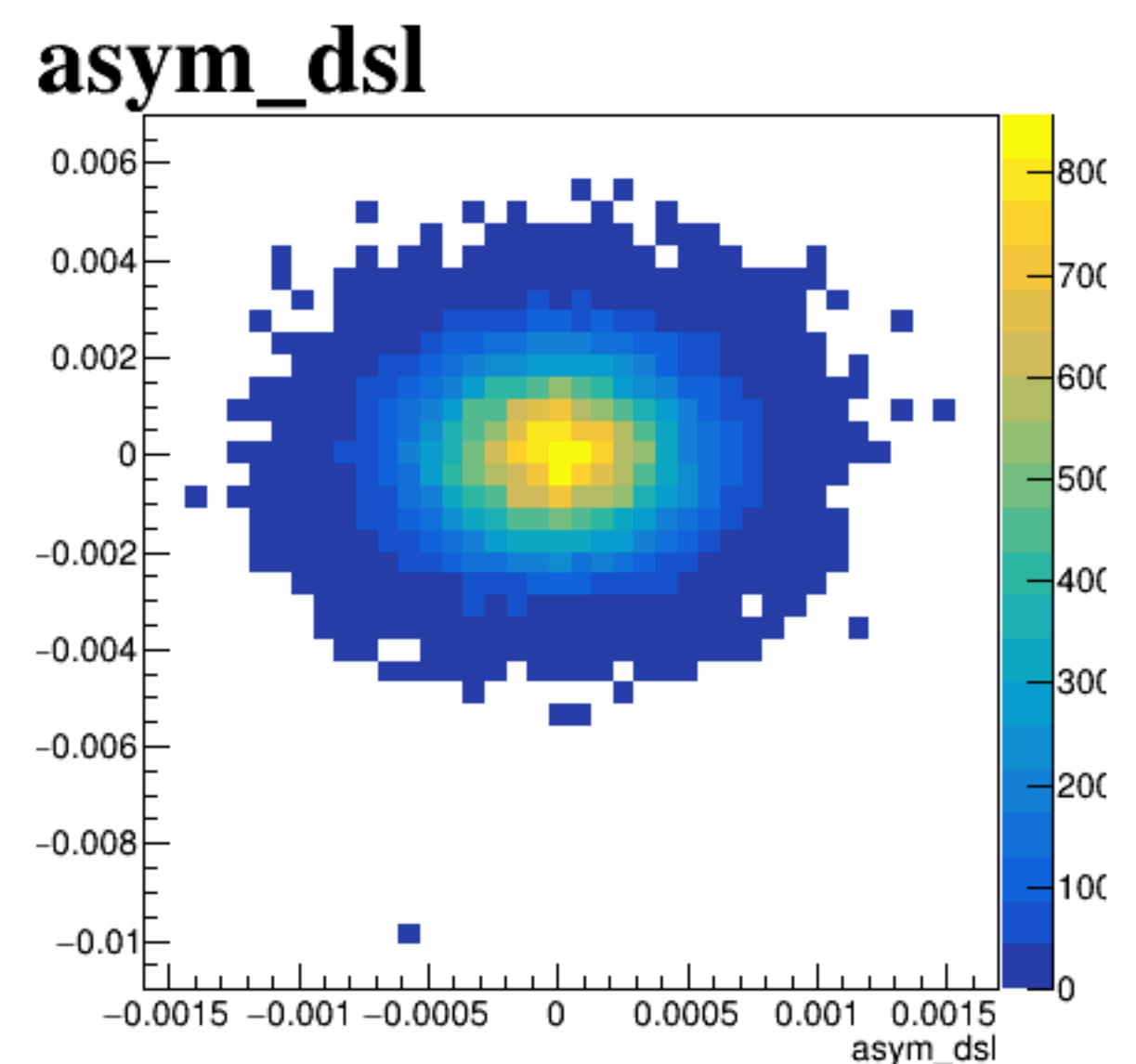
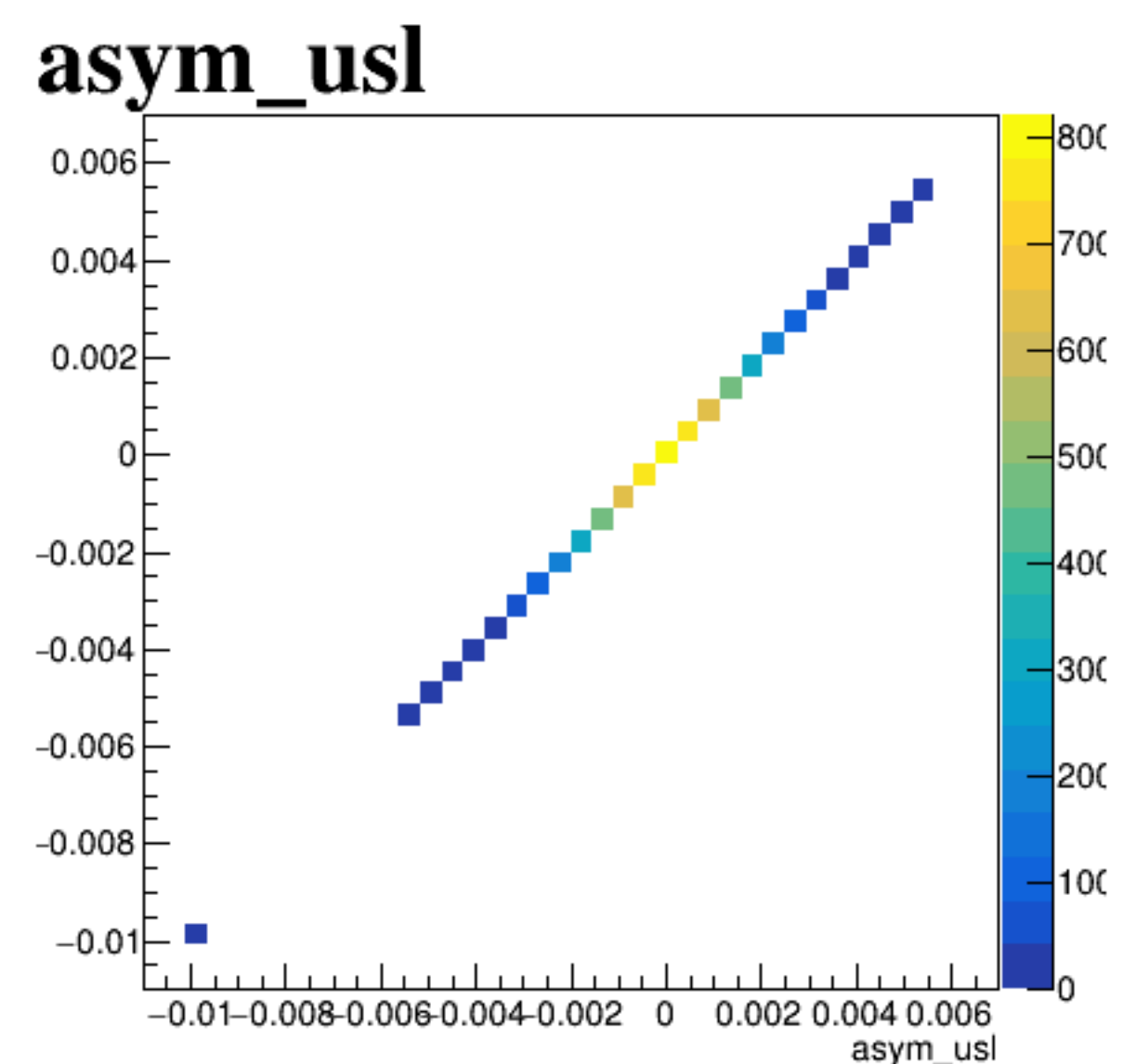
asym_dsr/ppm:pattern_number {ErrorFlag==0}



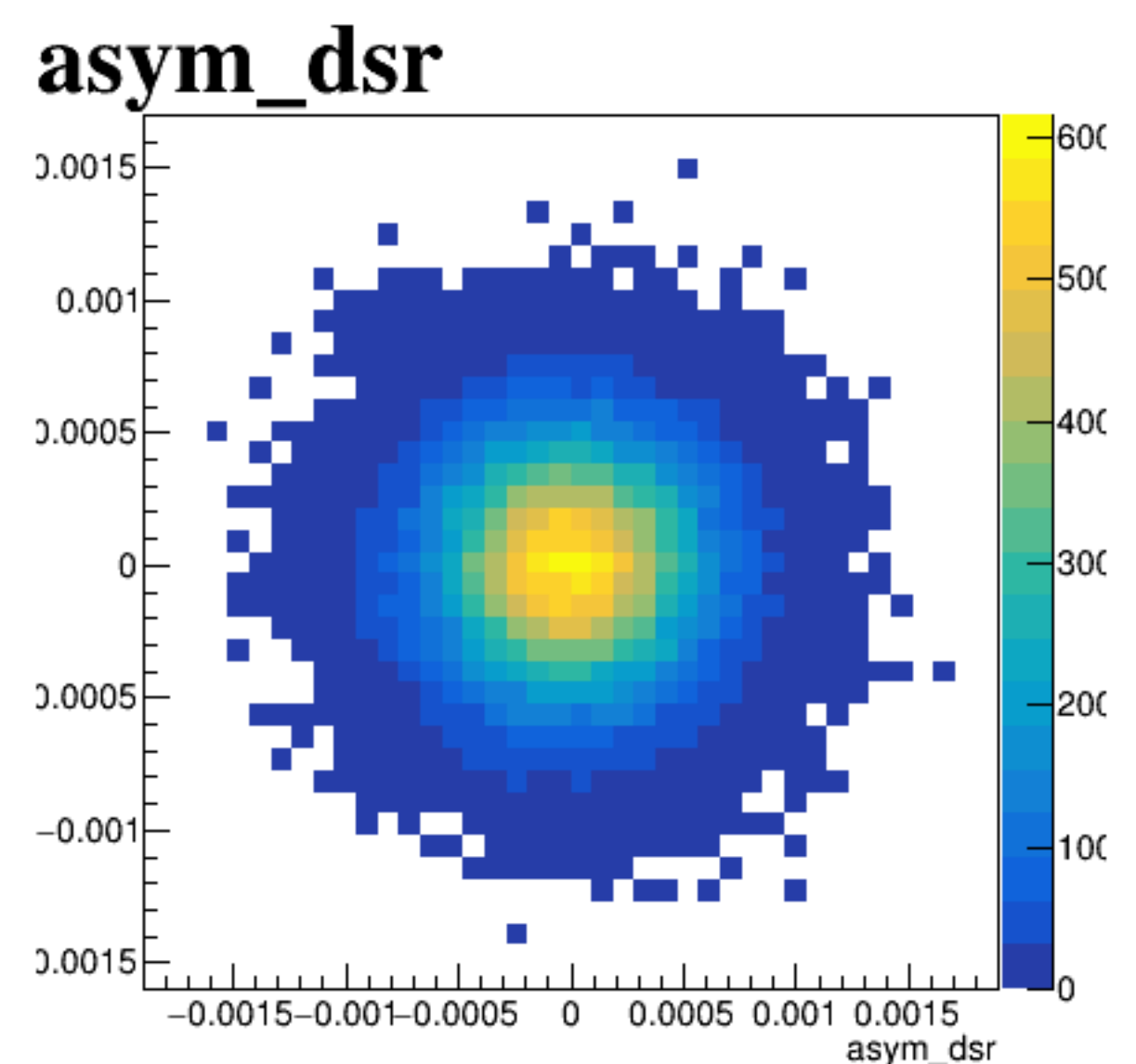
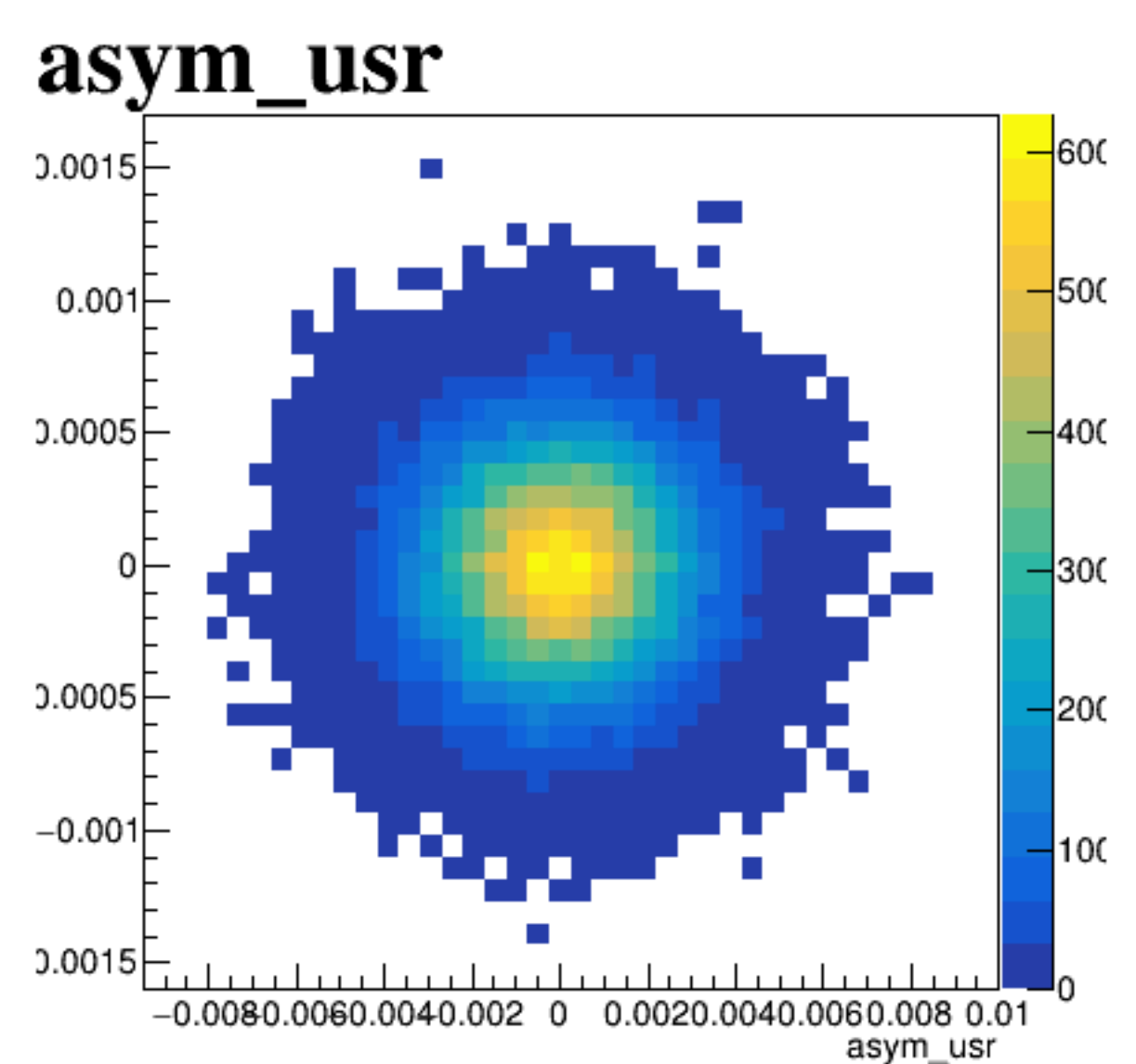
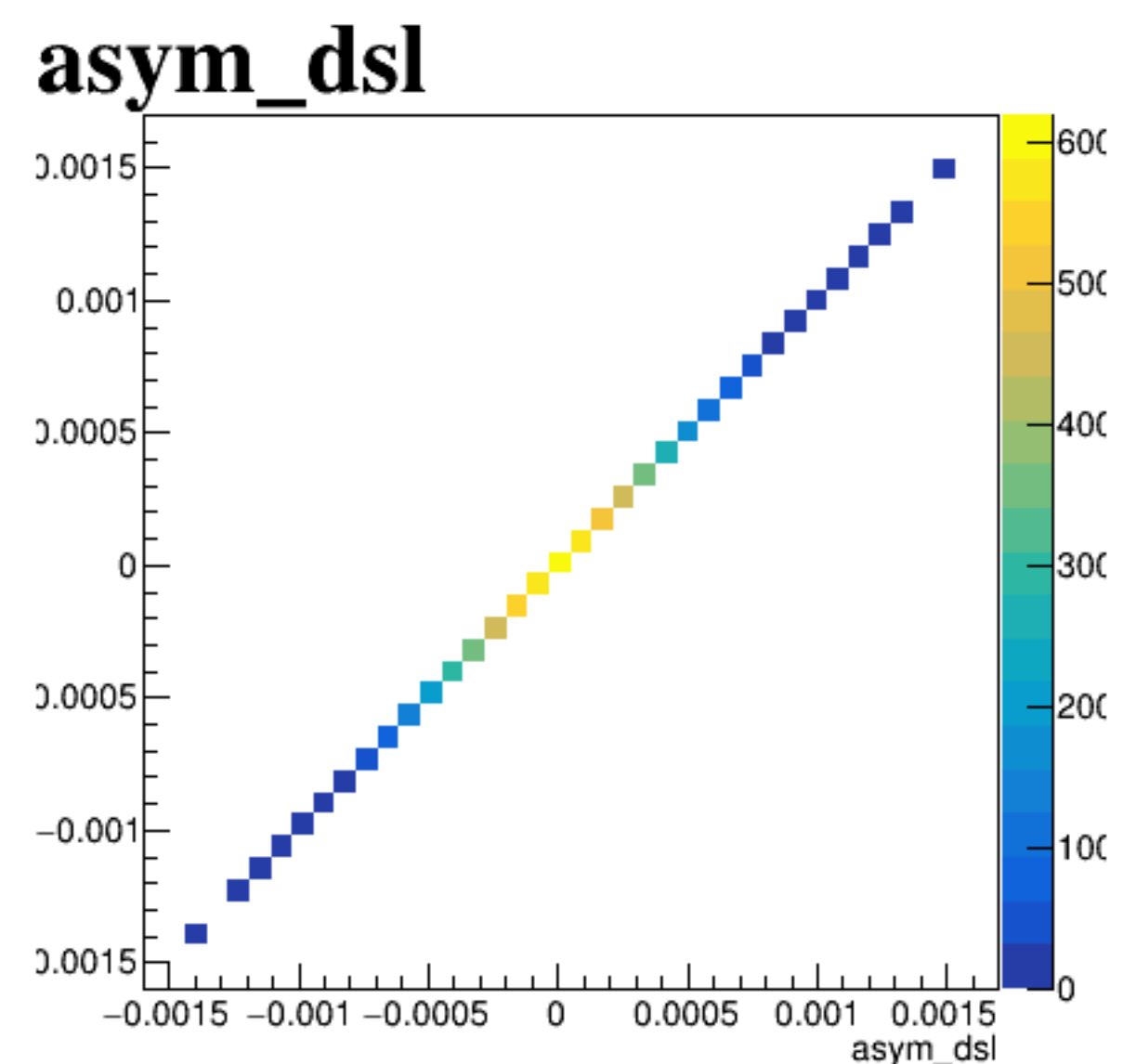
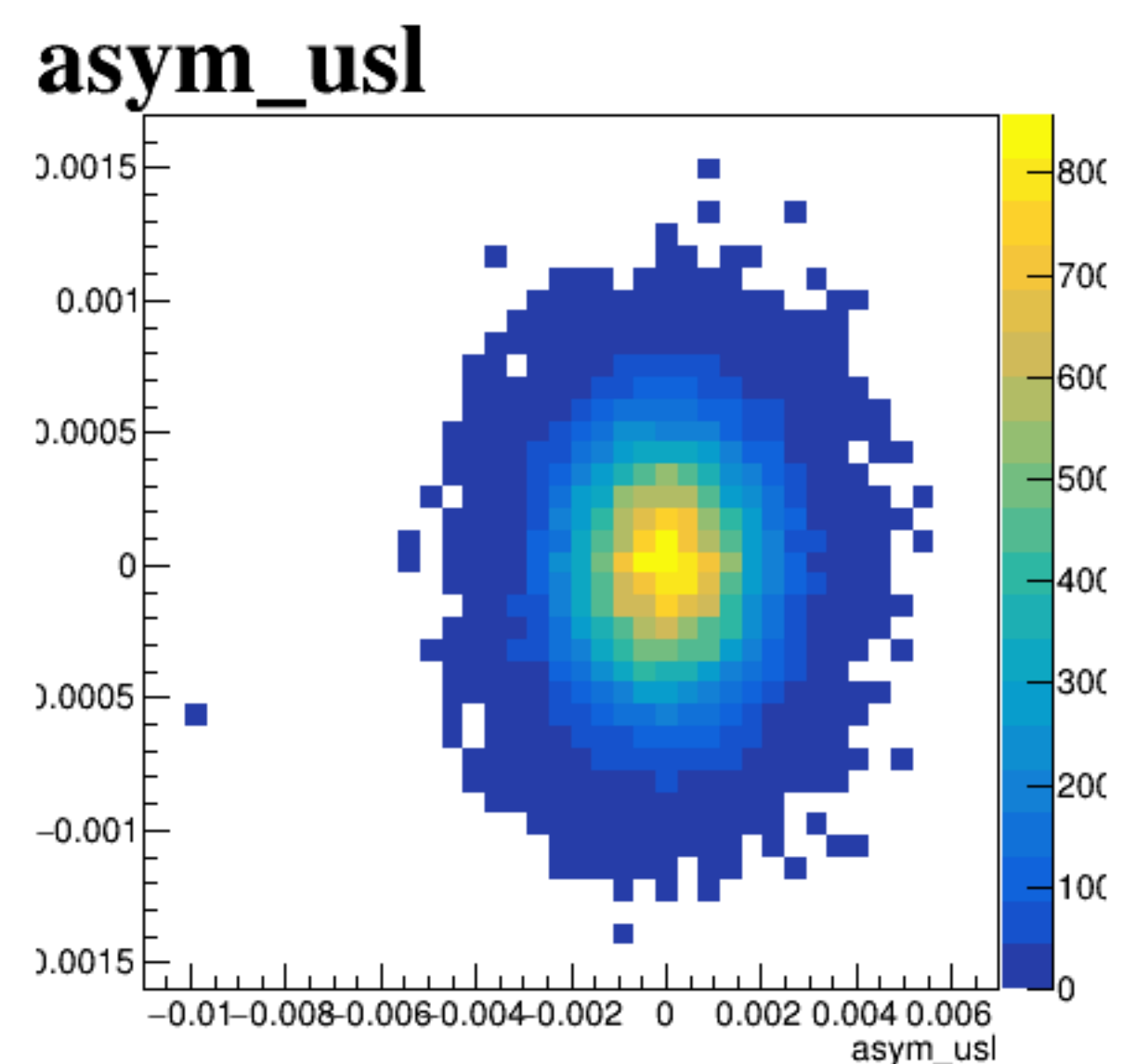
asym_dsr/ppm {ErrorFlag==0}



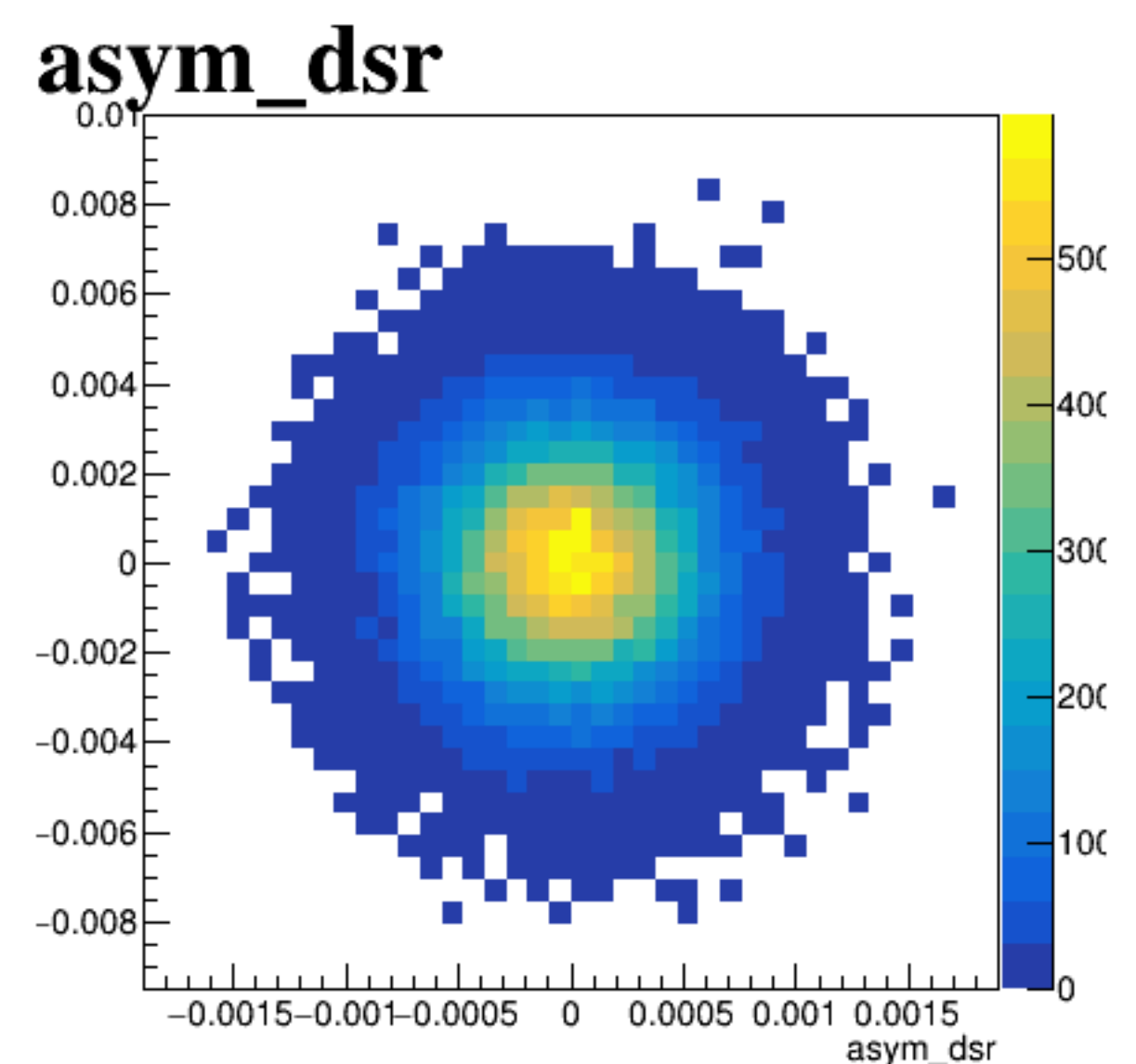
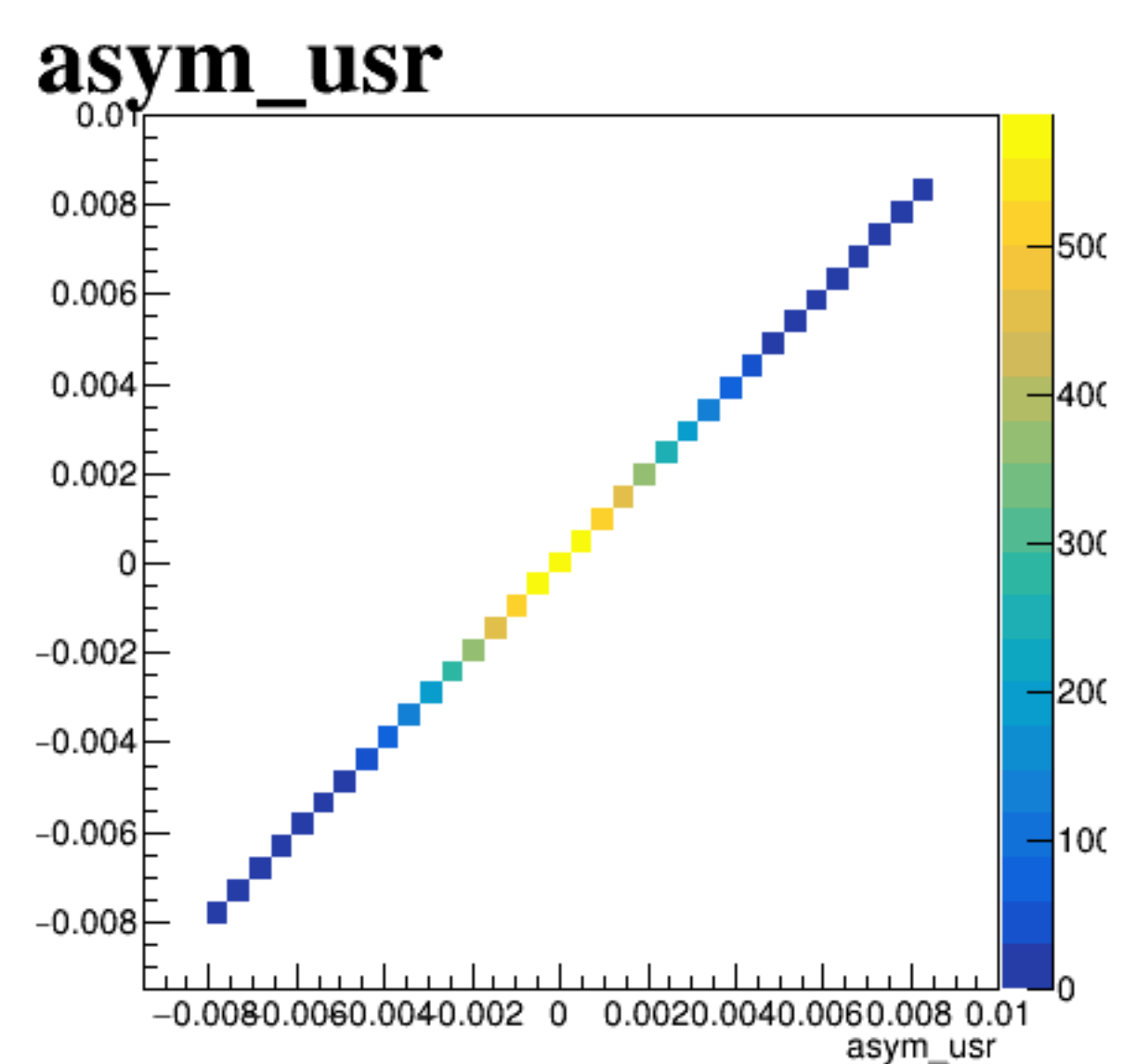
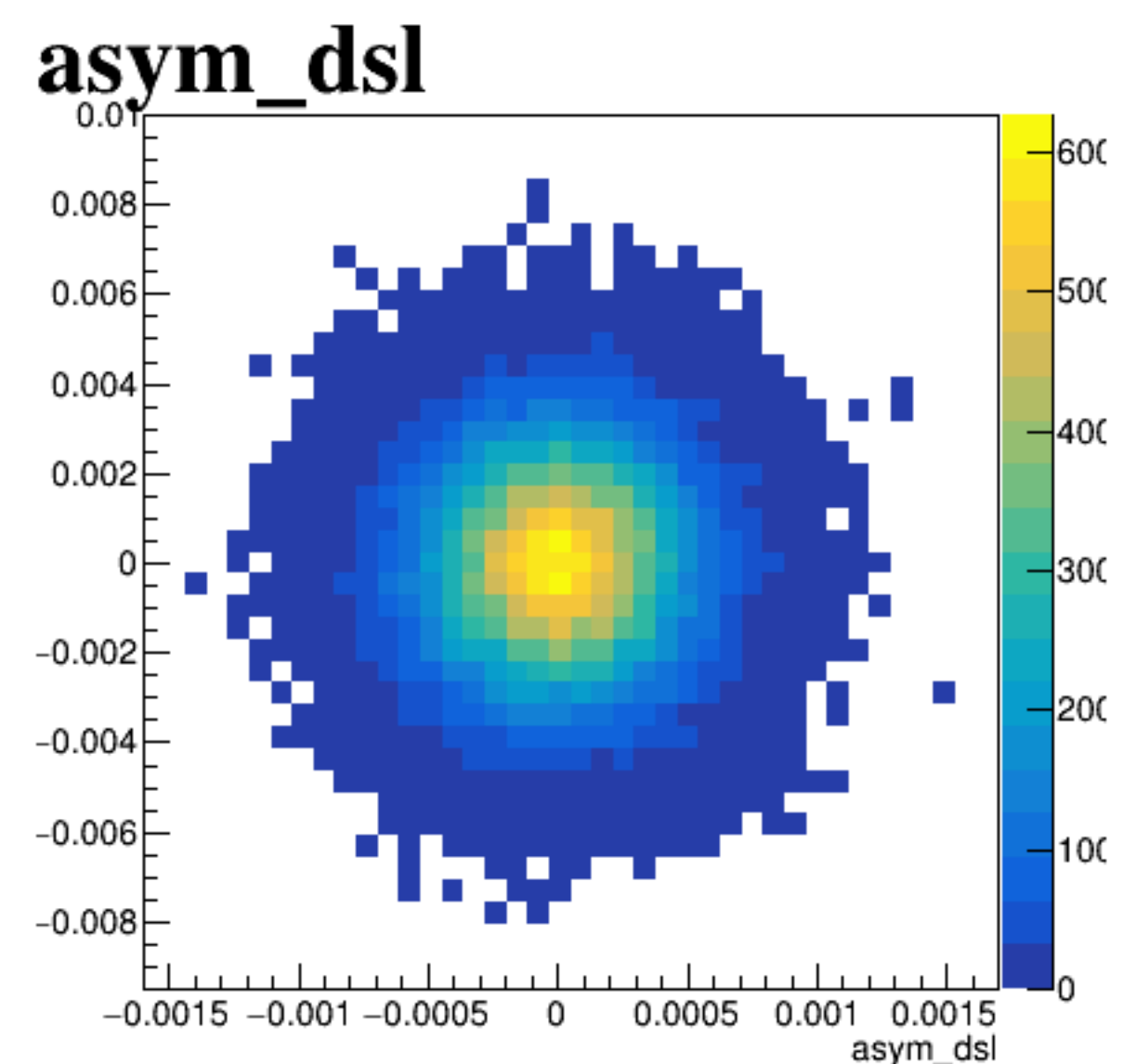
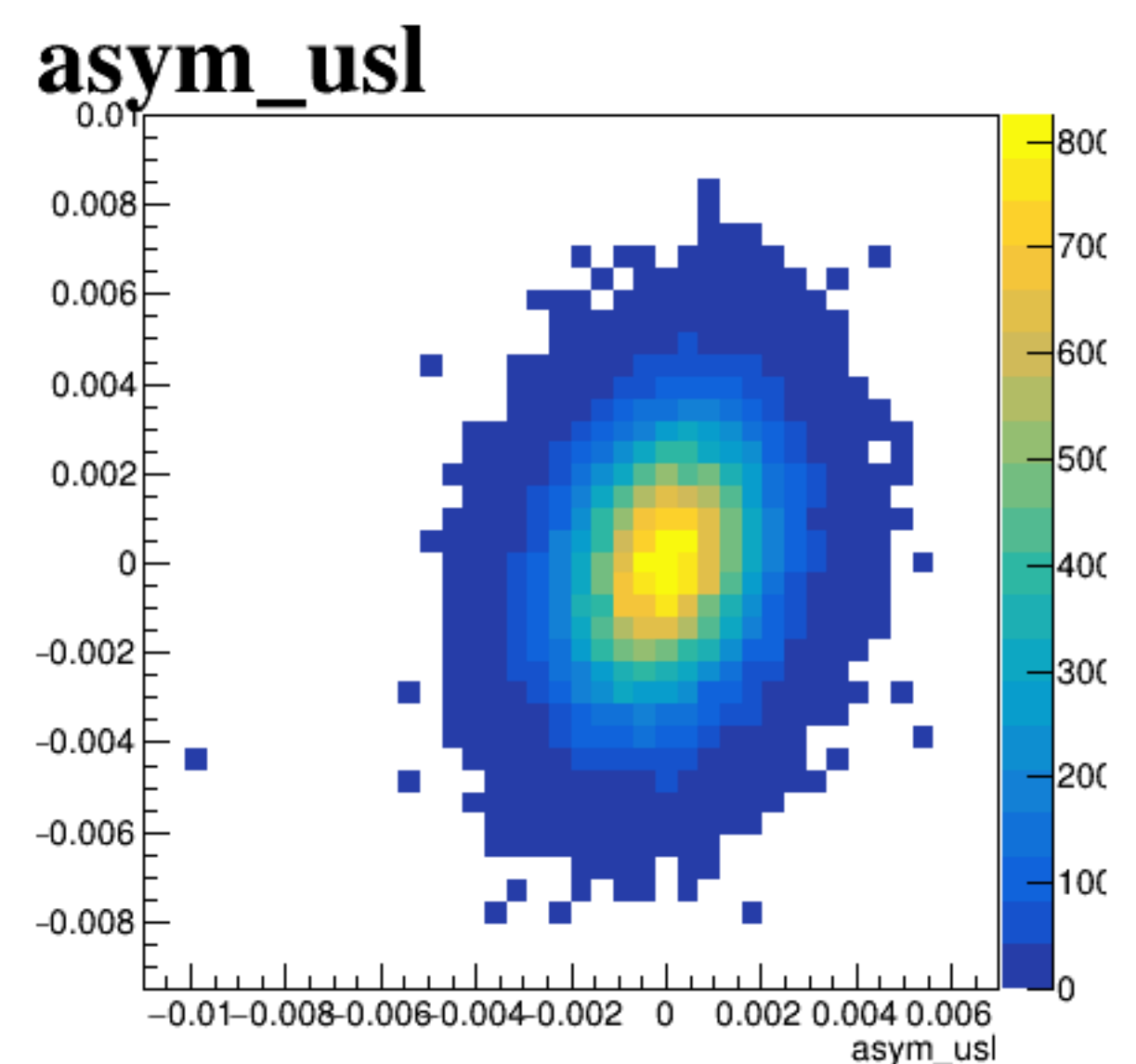
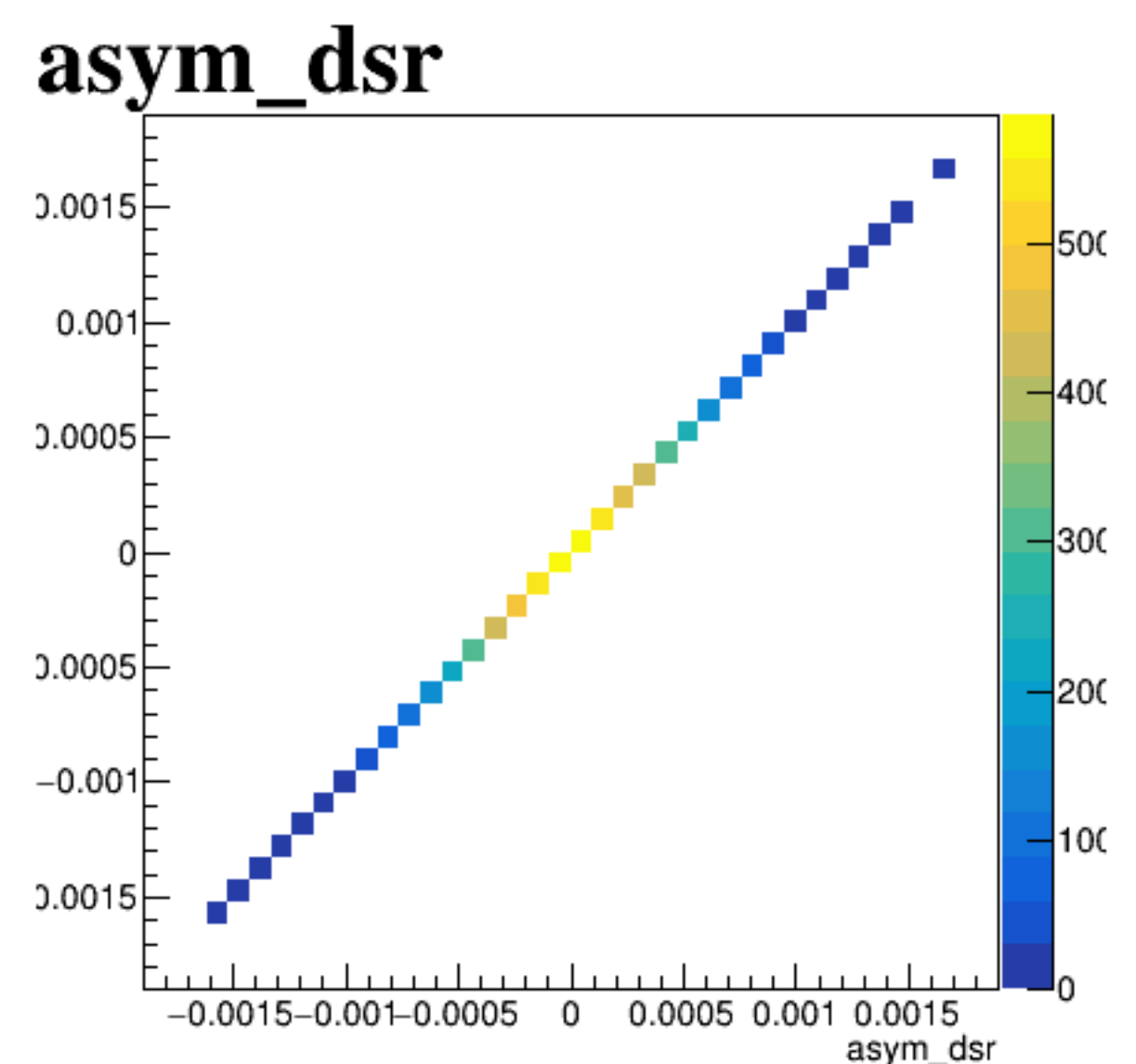
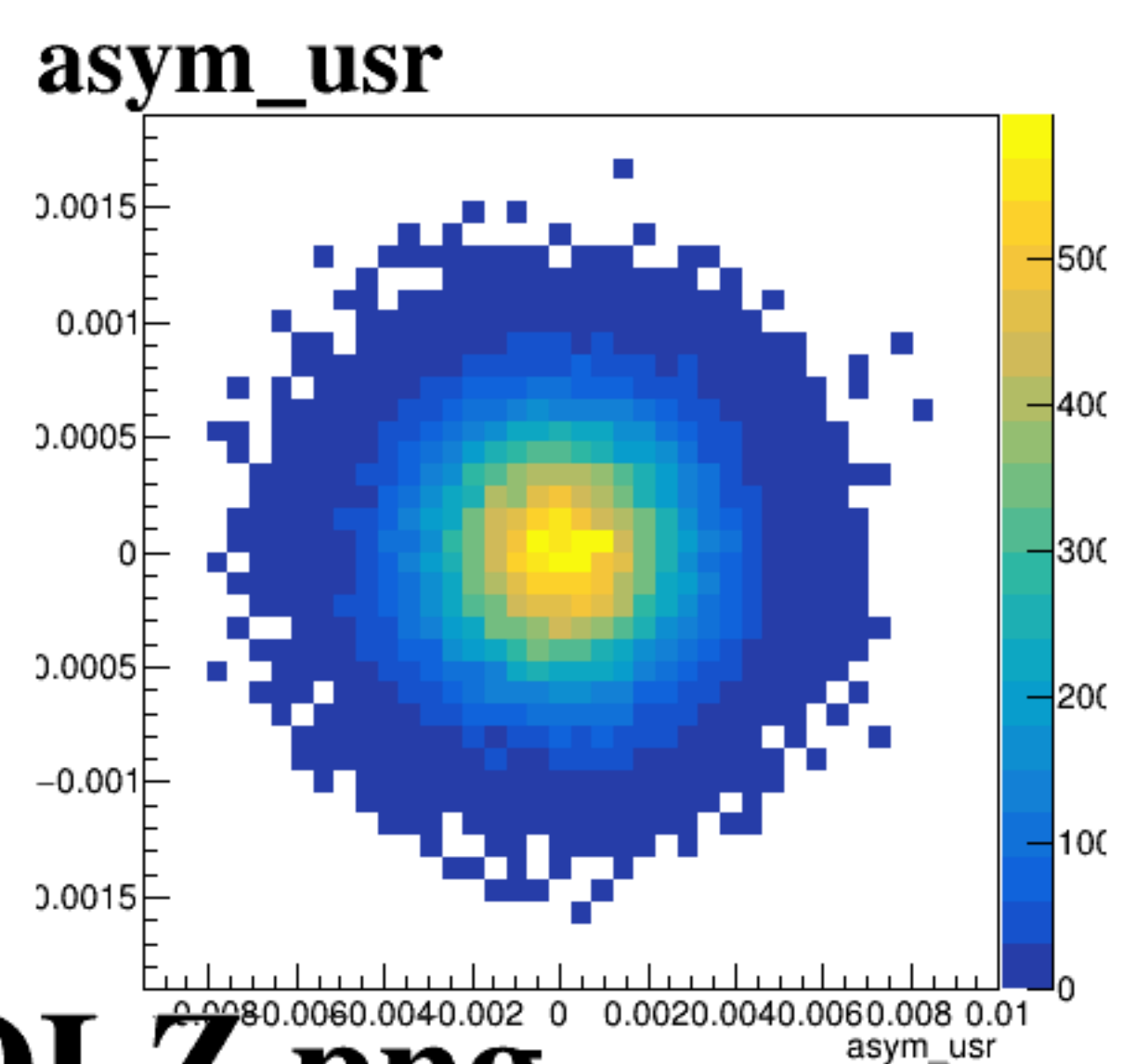
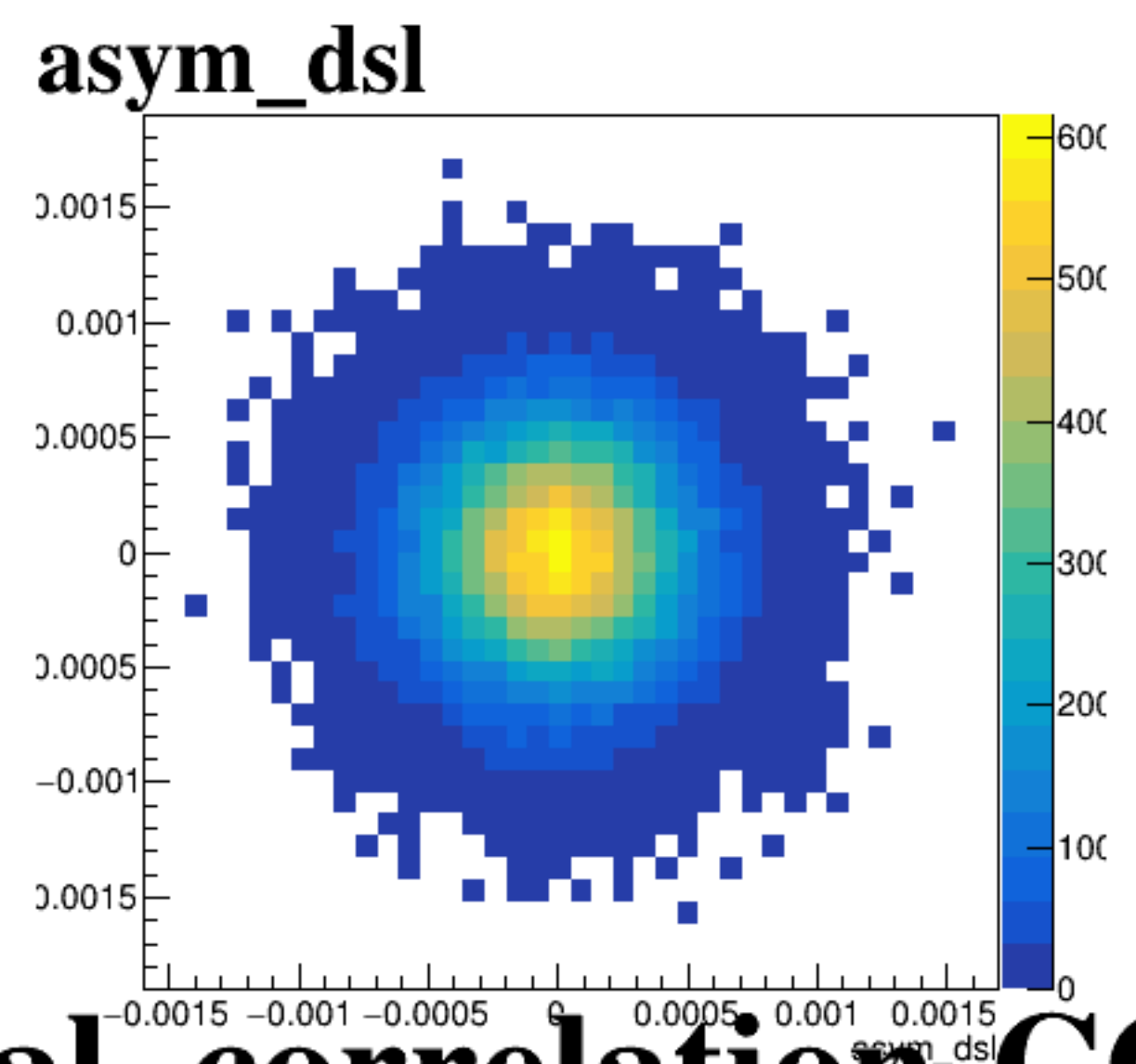
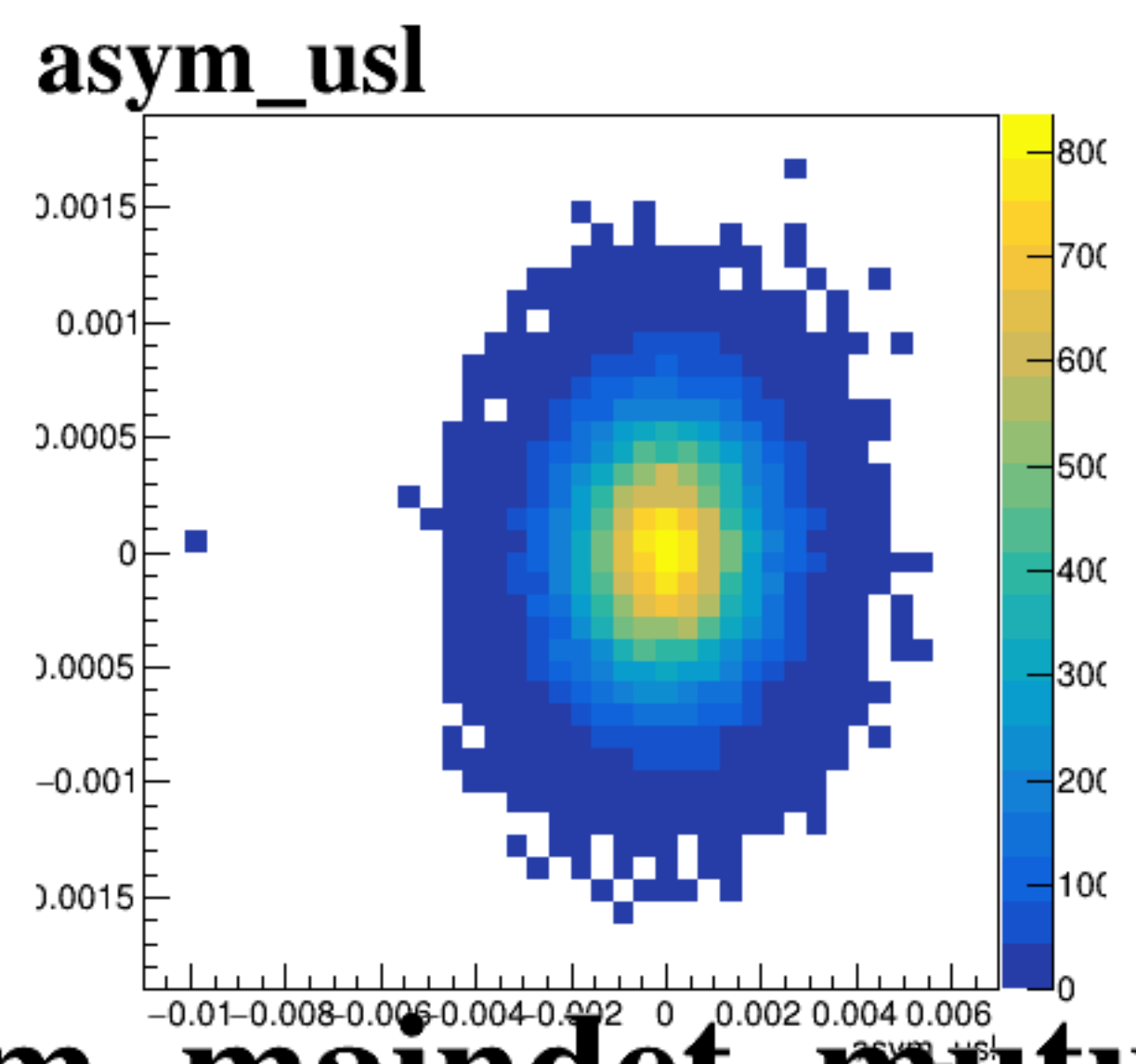
asym_usl



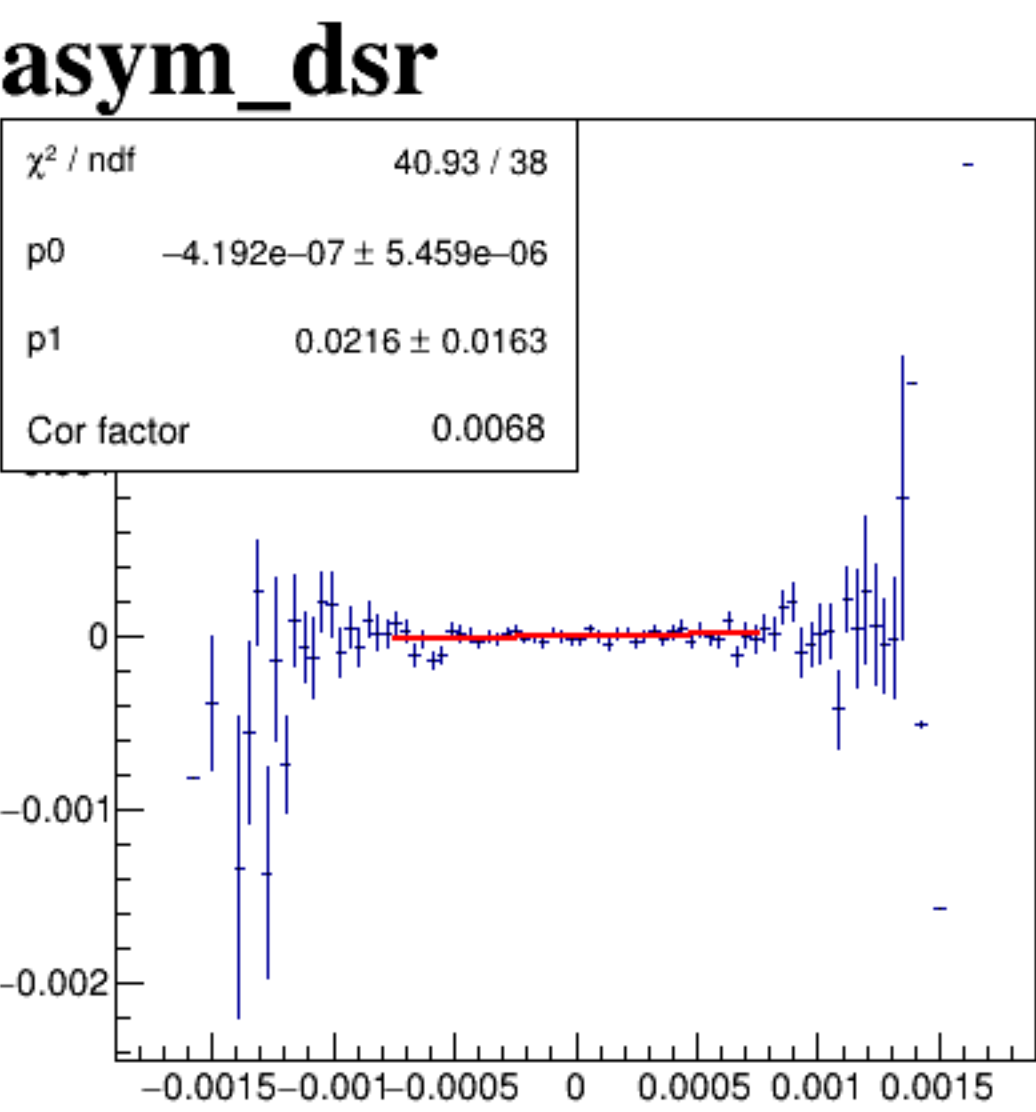
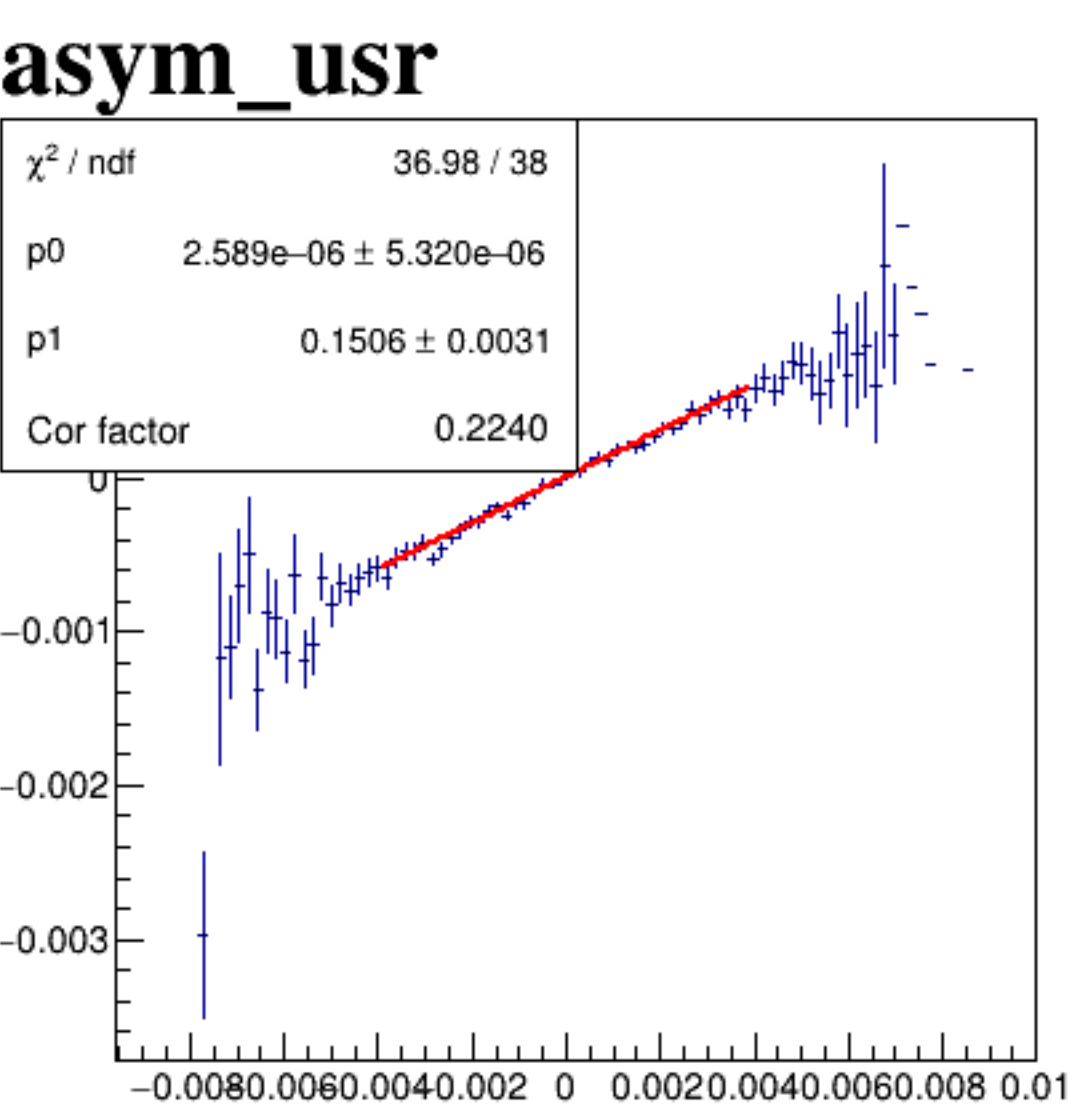
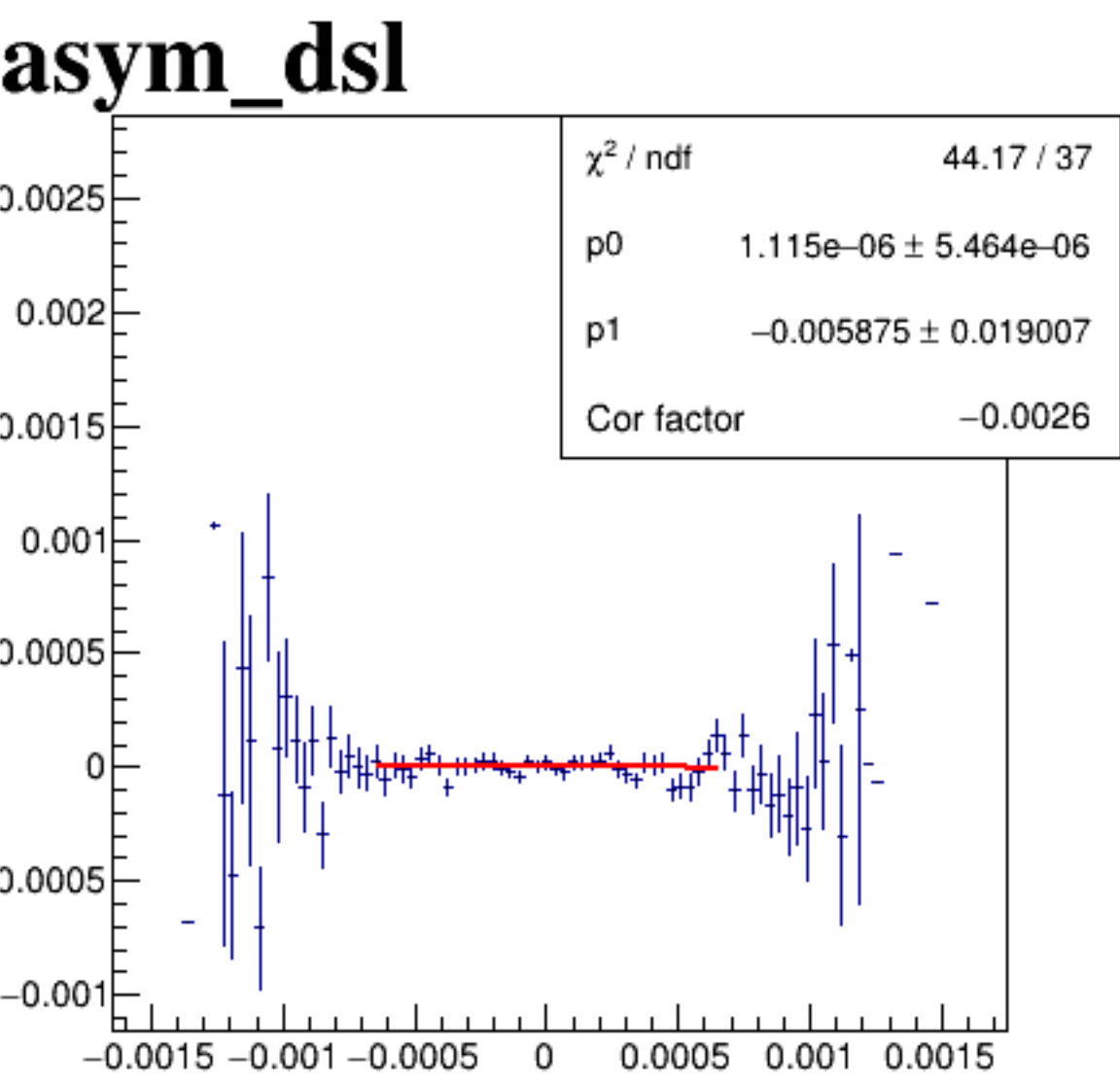
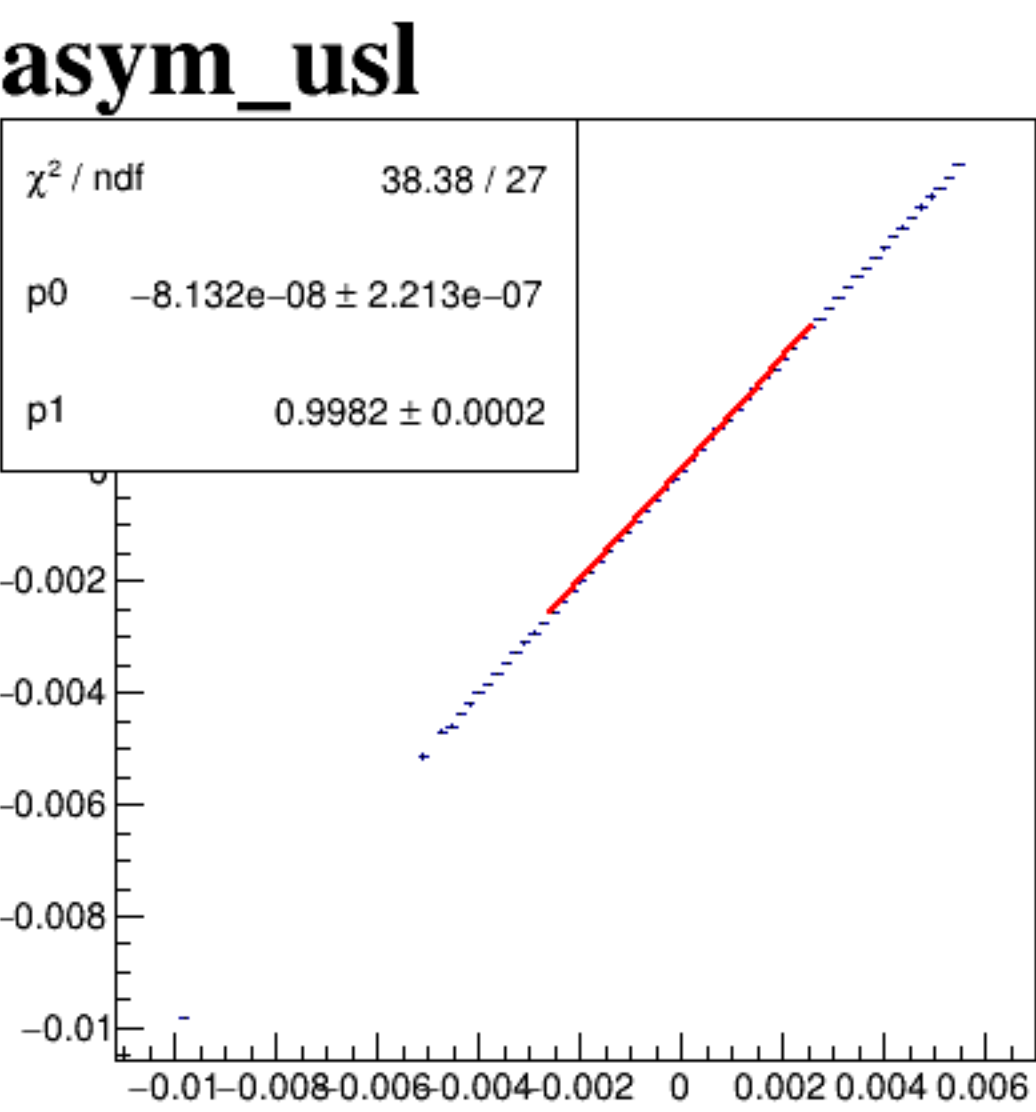
asym_dsl



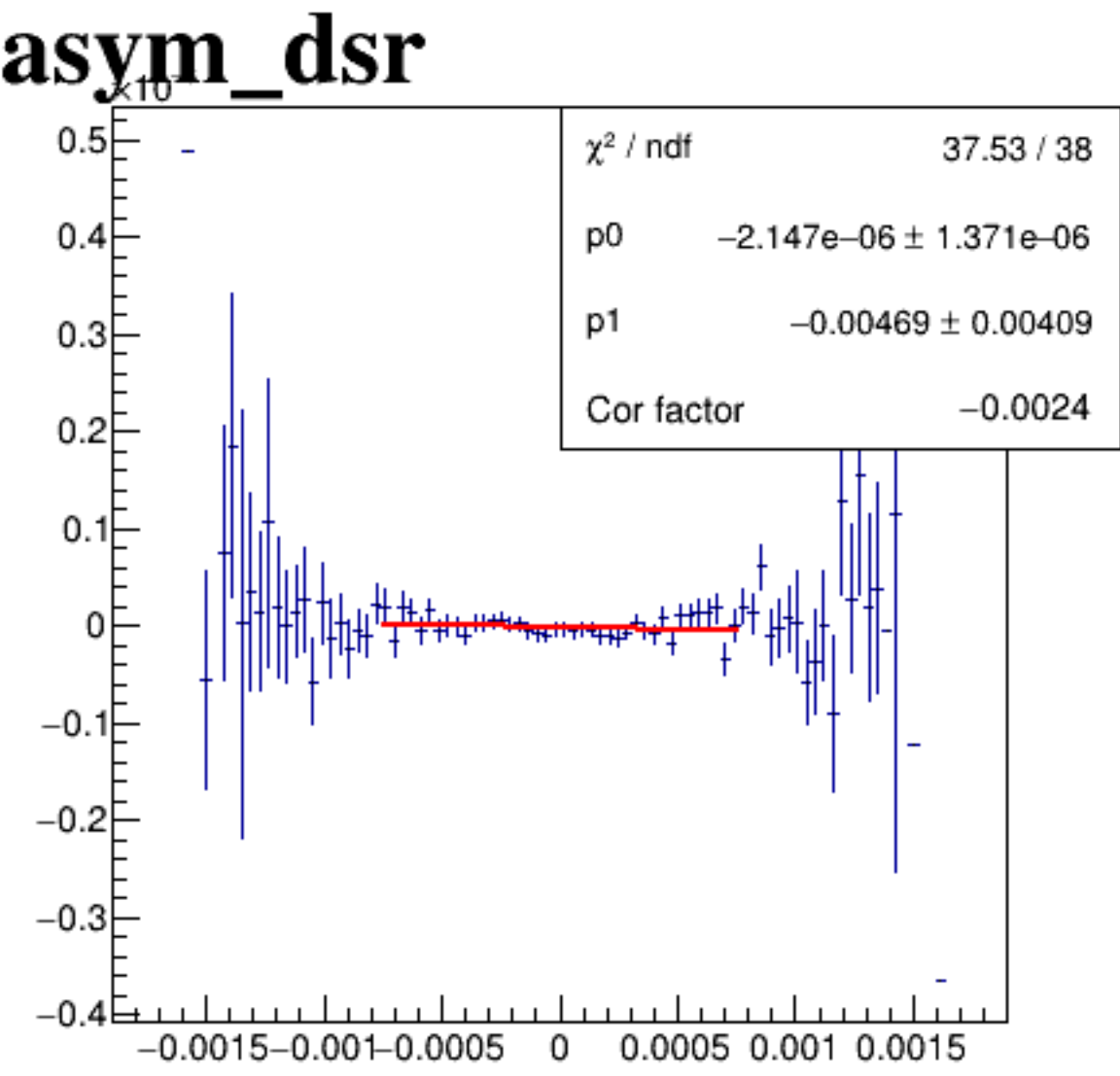
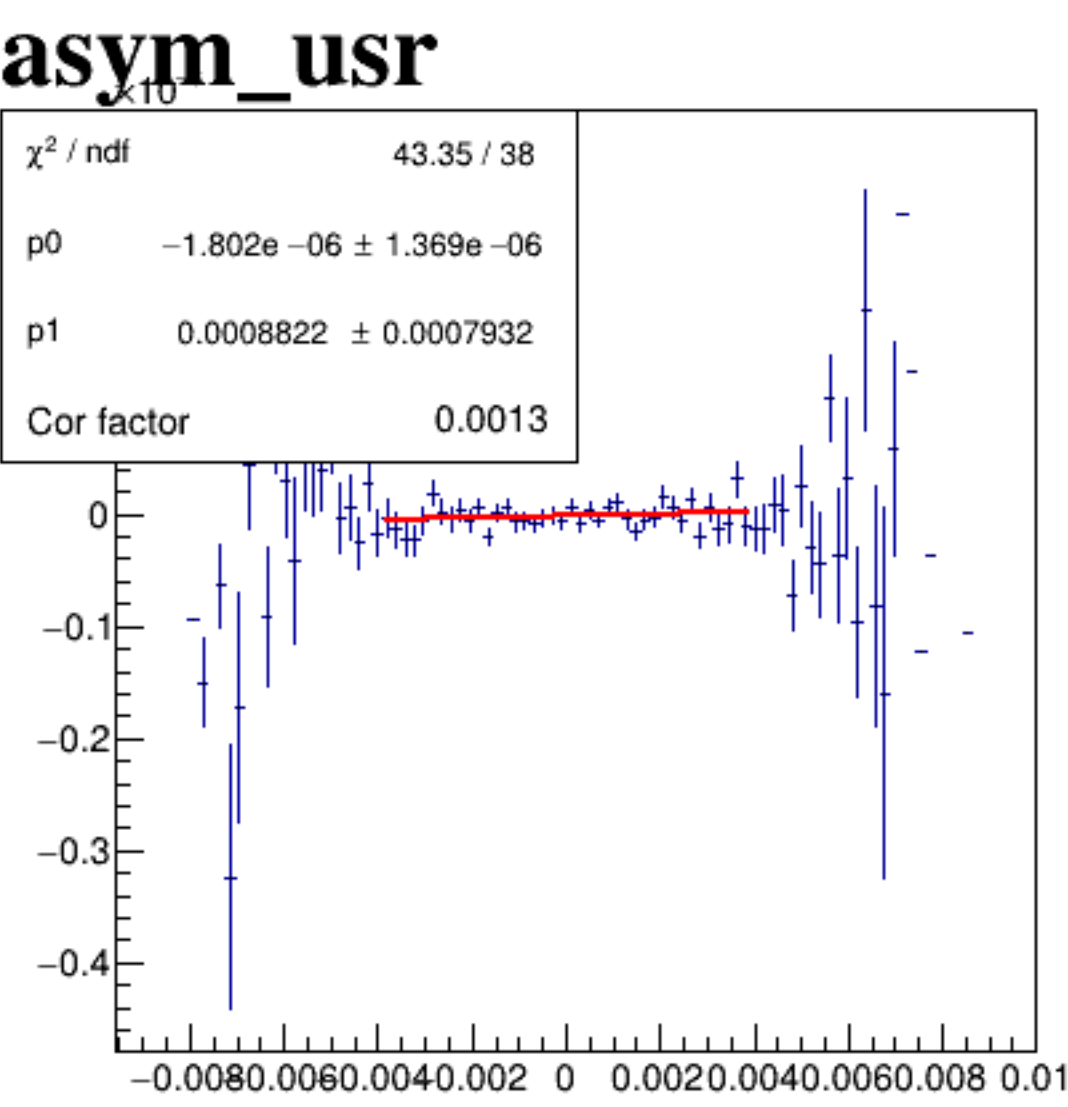
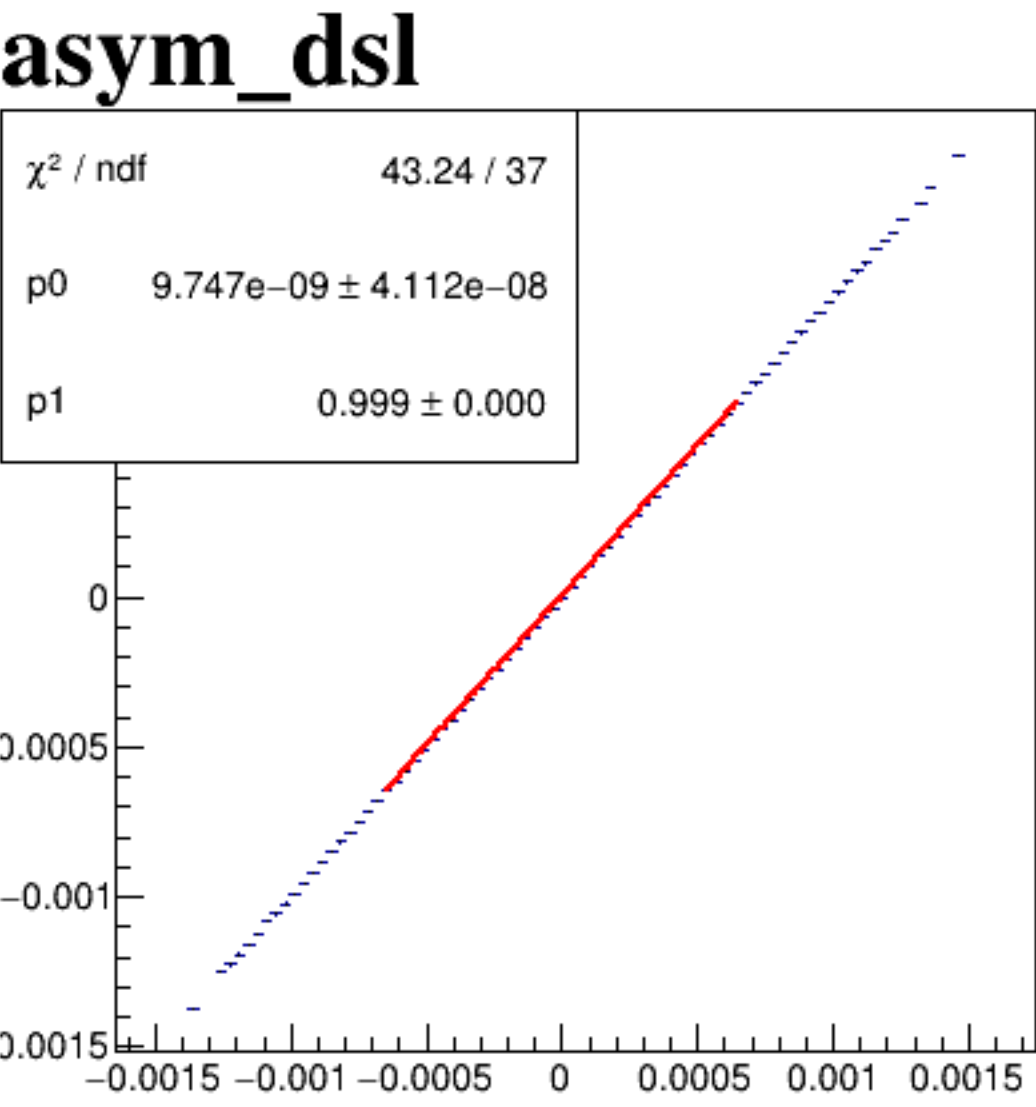
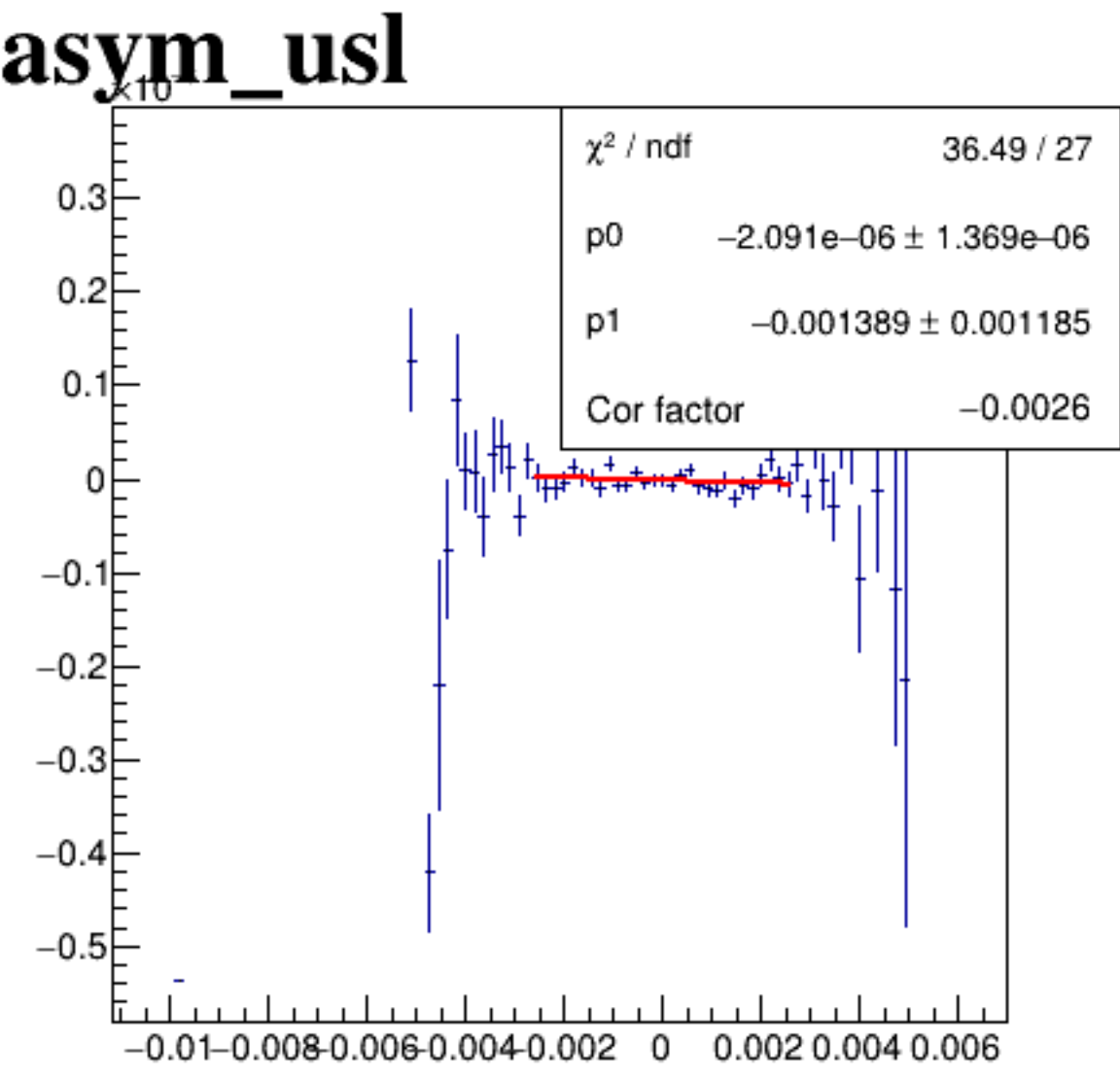
asym_usr

**asym_dsr**

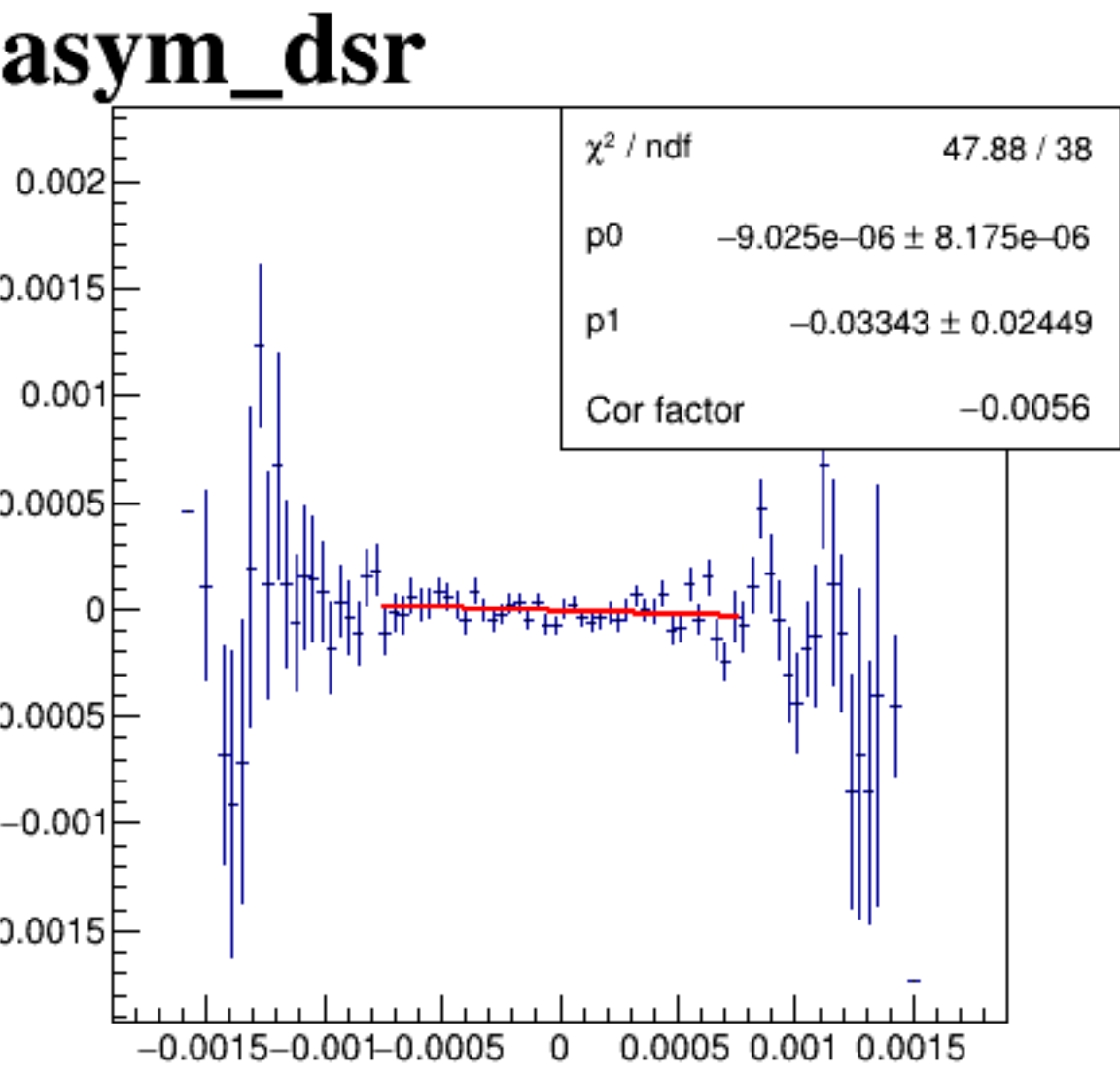
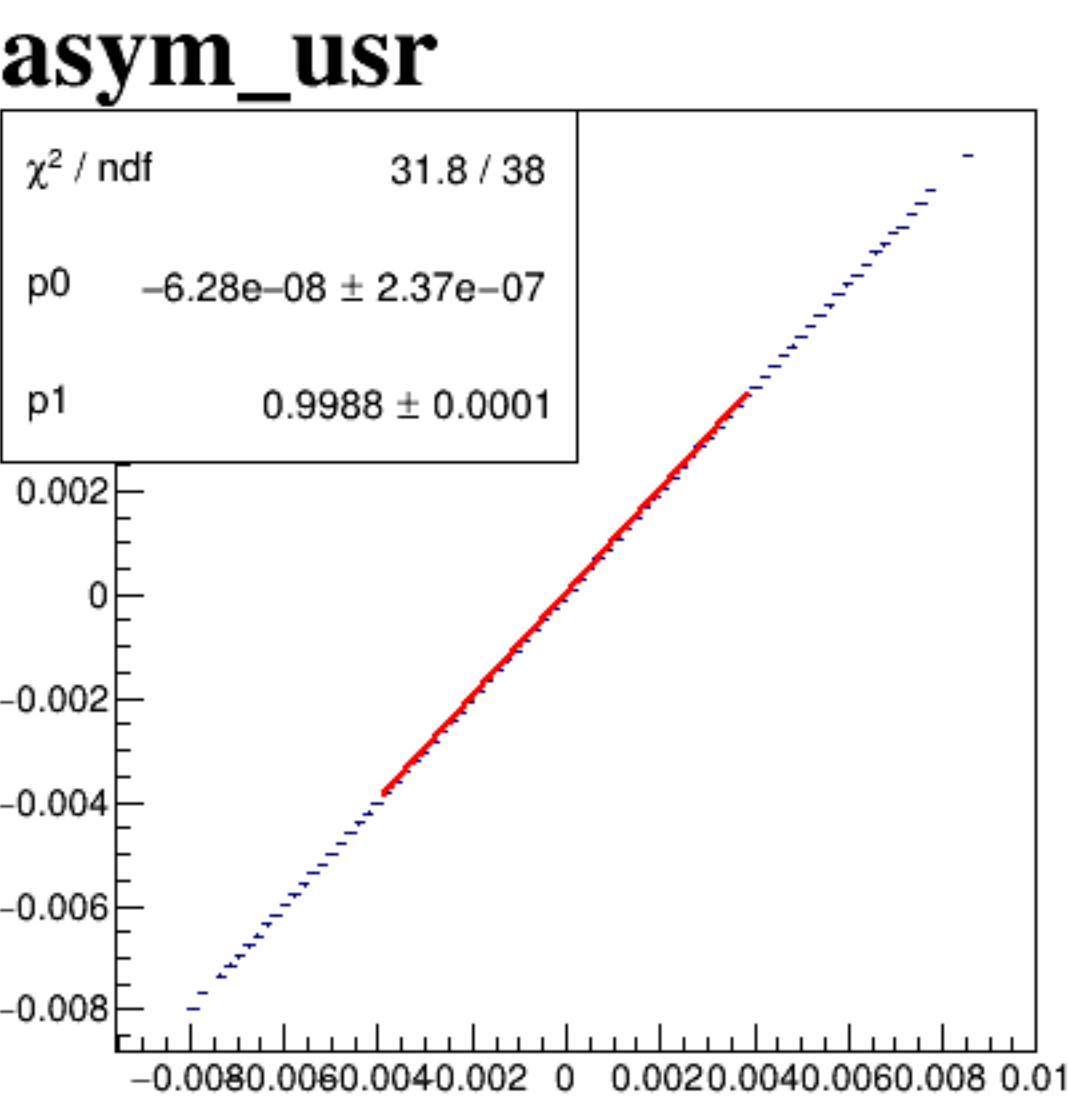
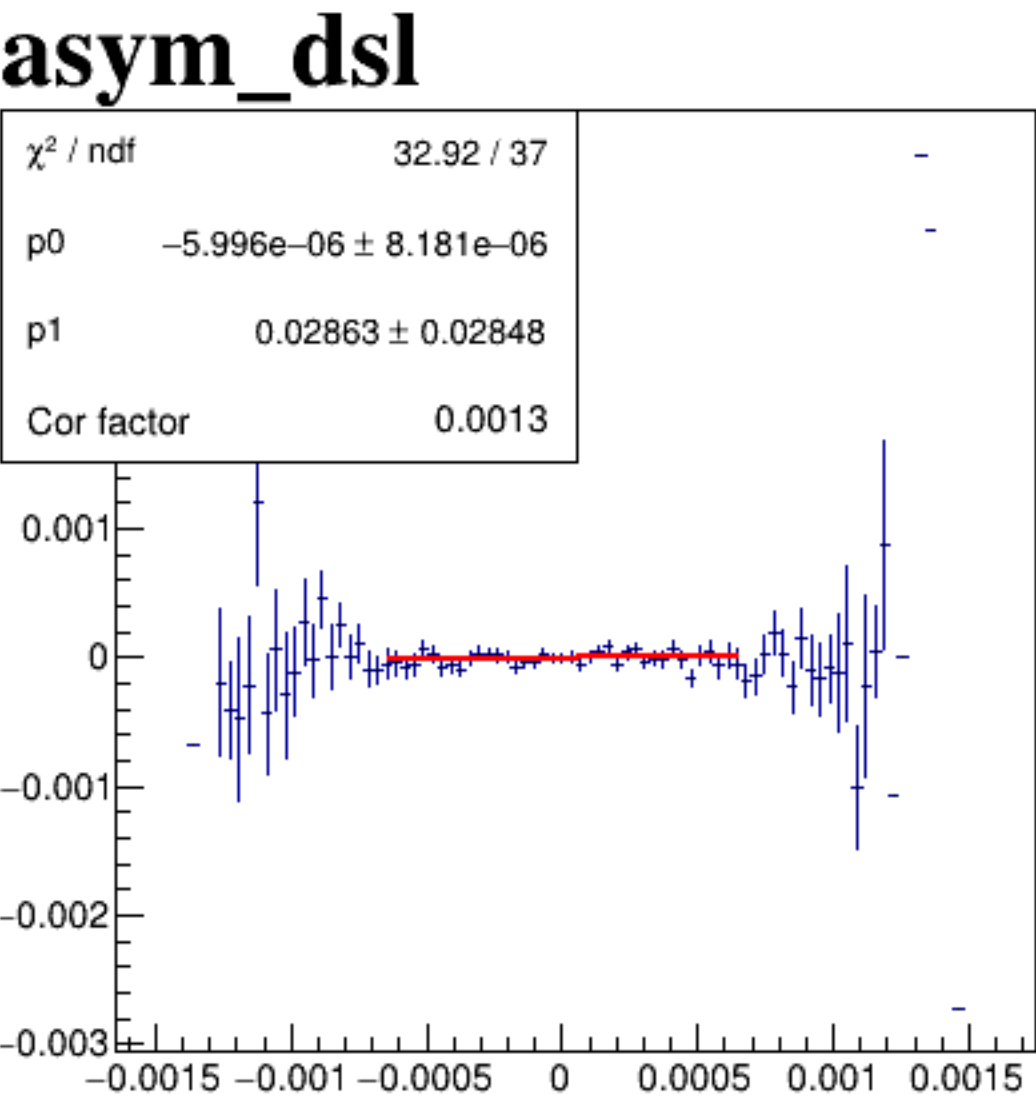
asym_usl



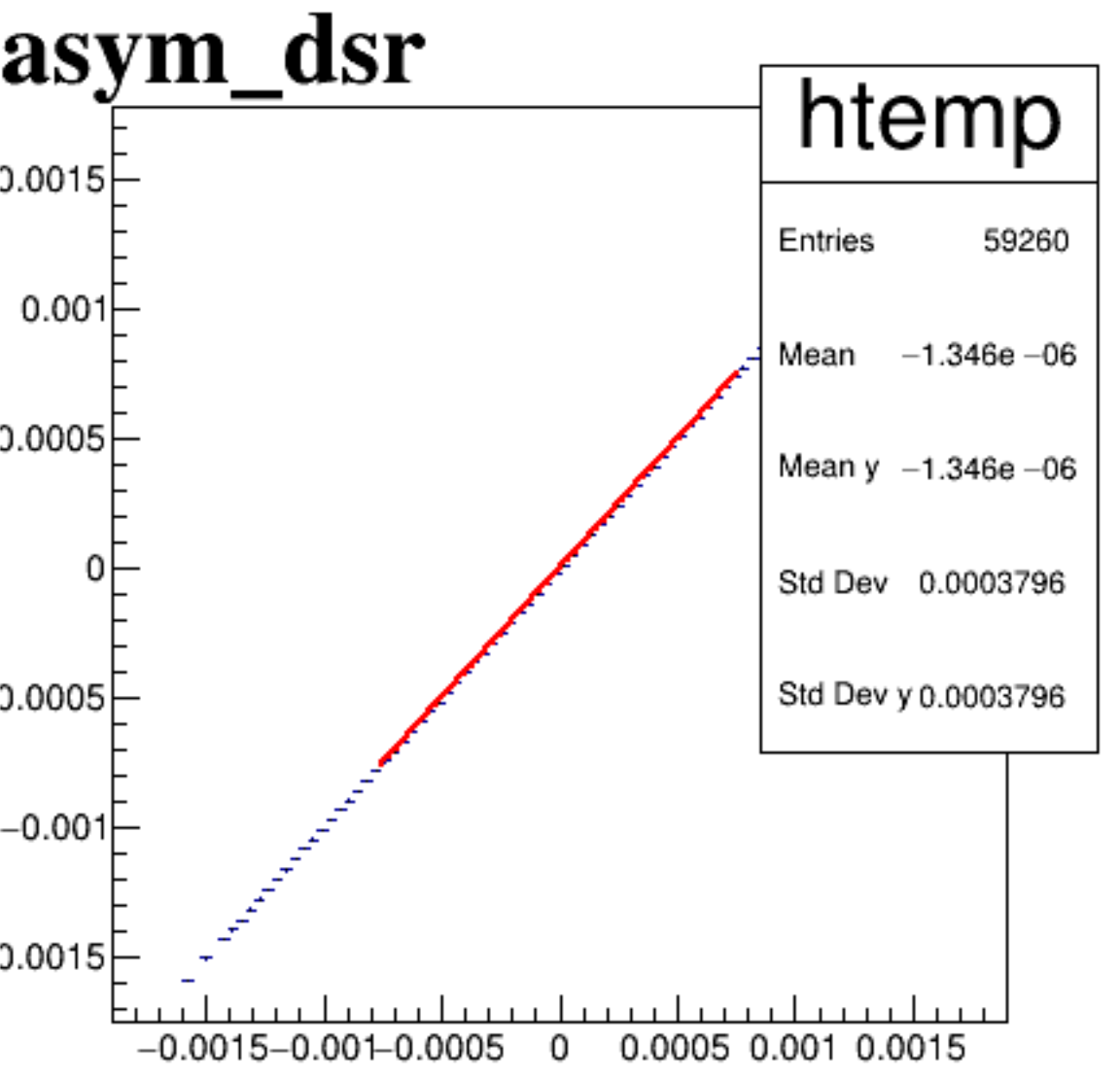
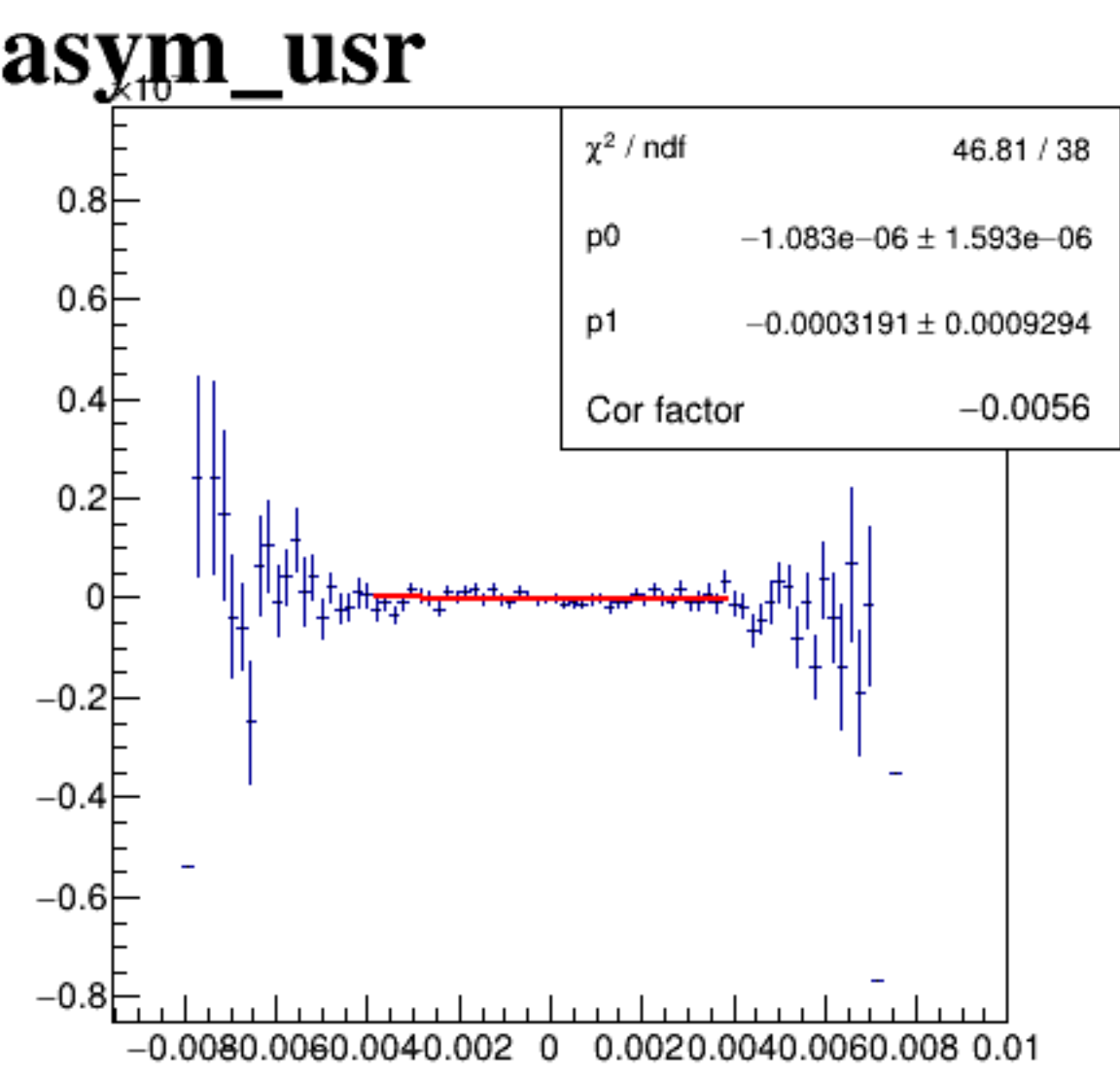
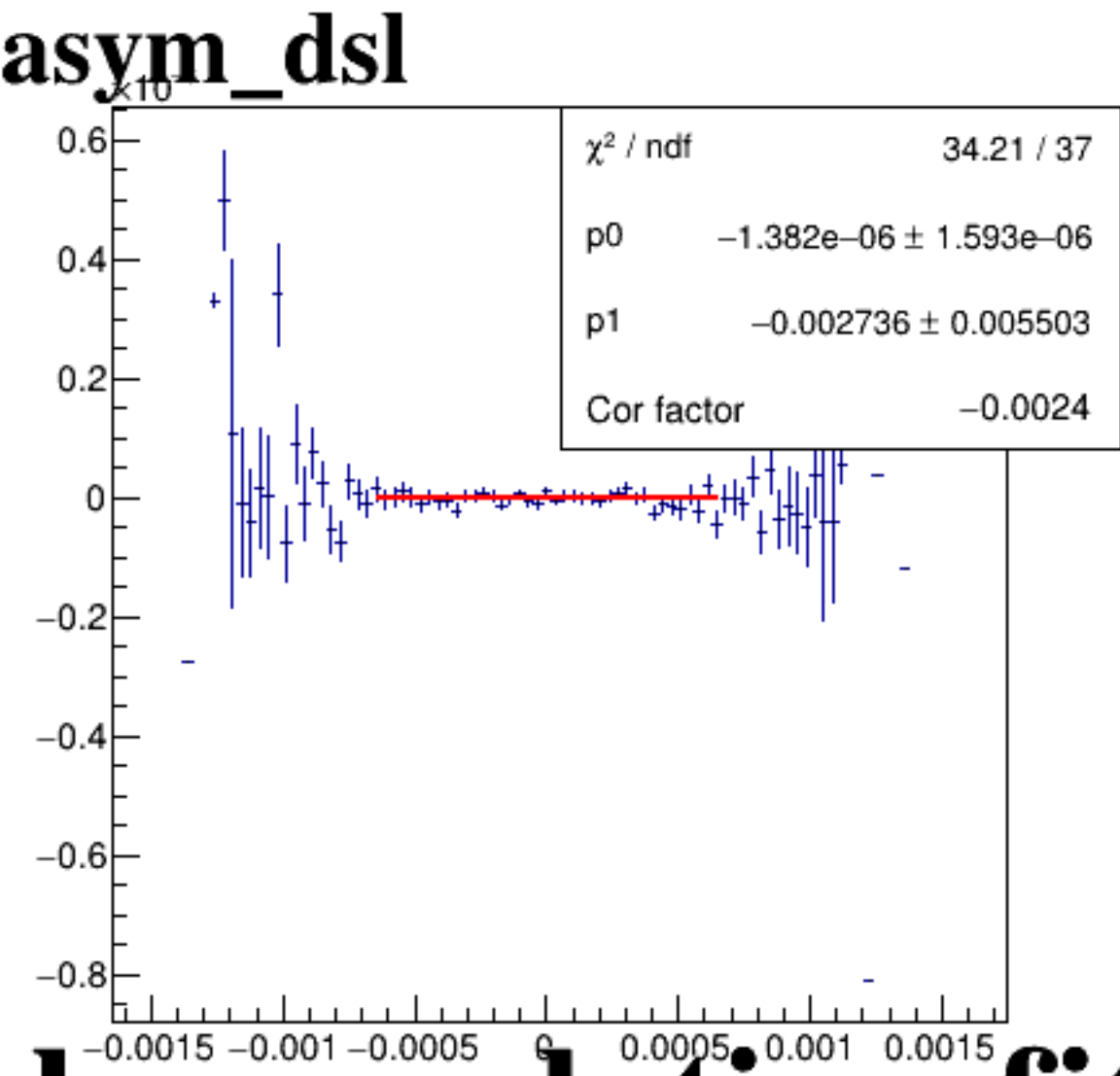
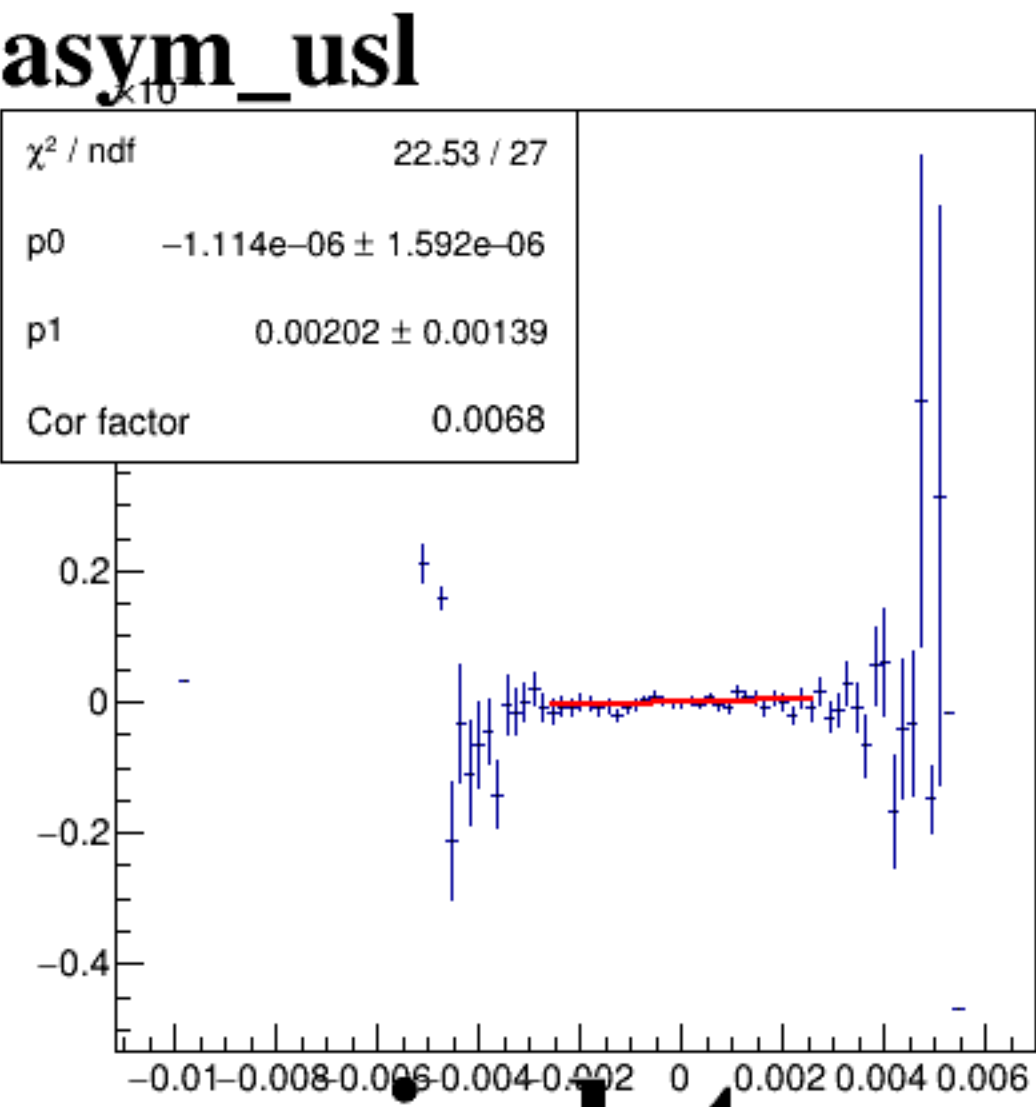
asym_dsl



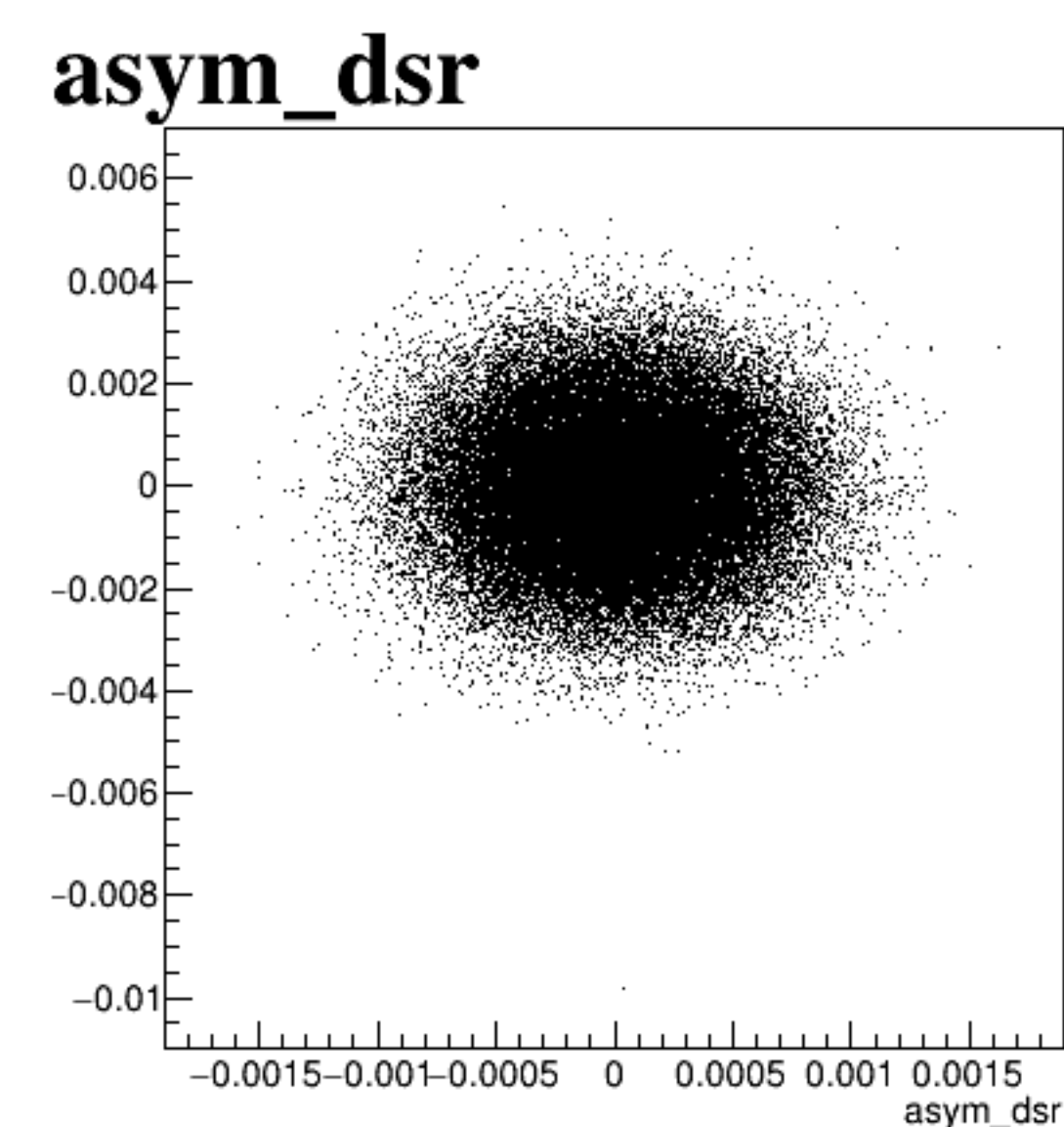
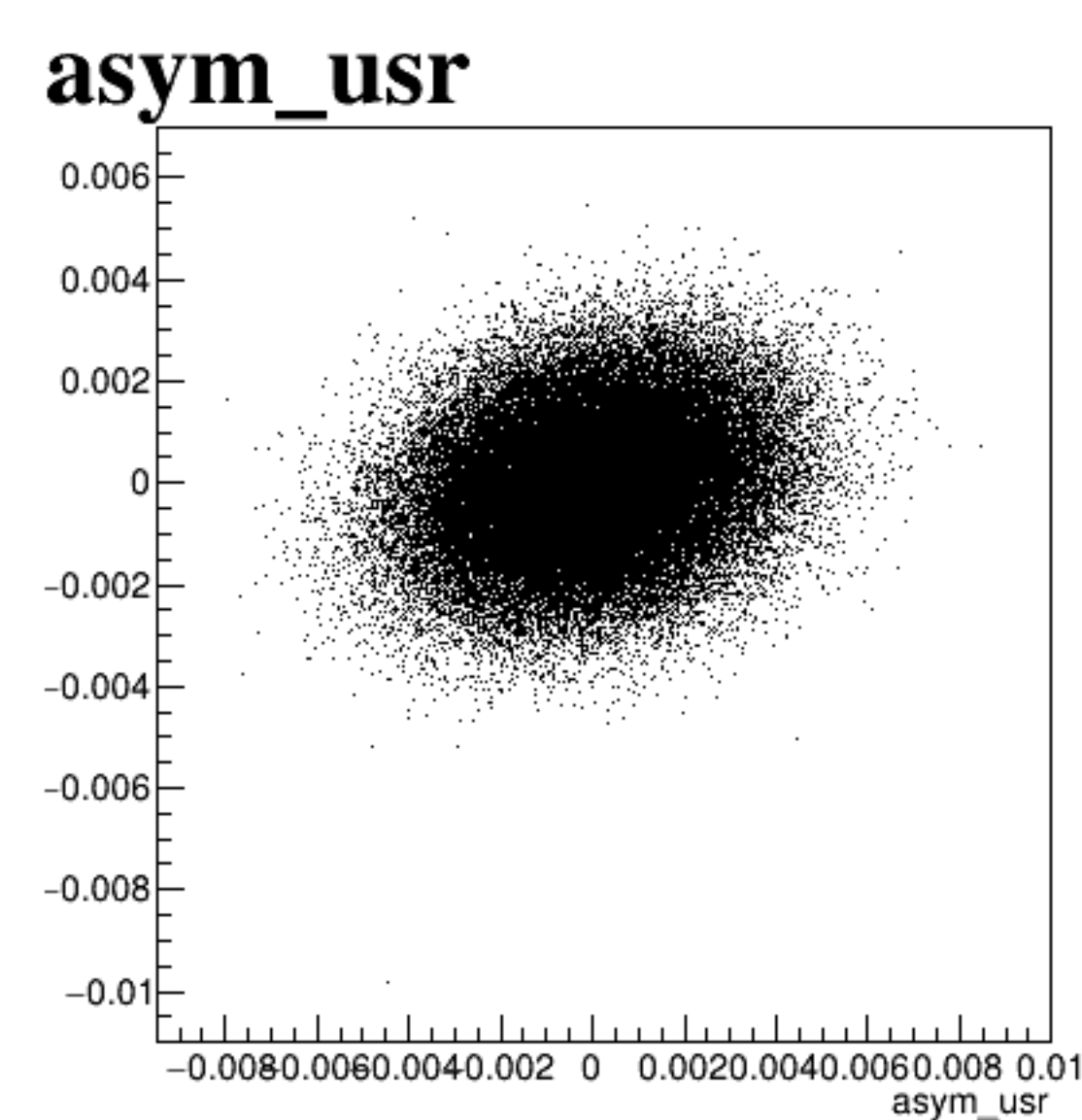
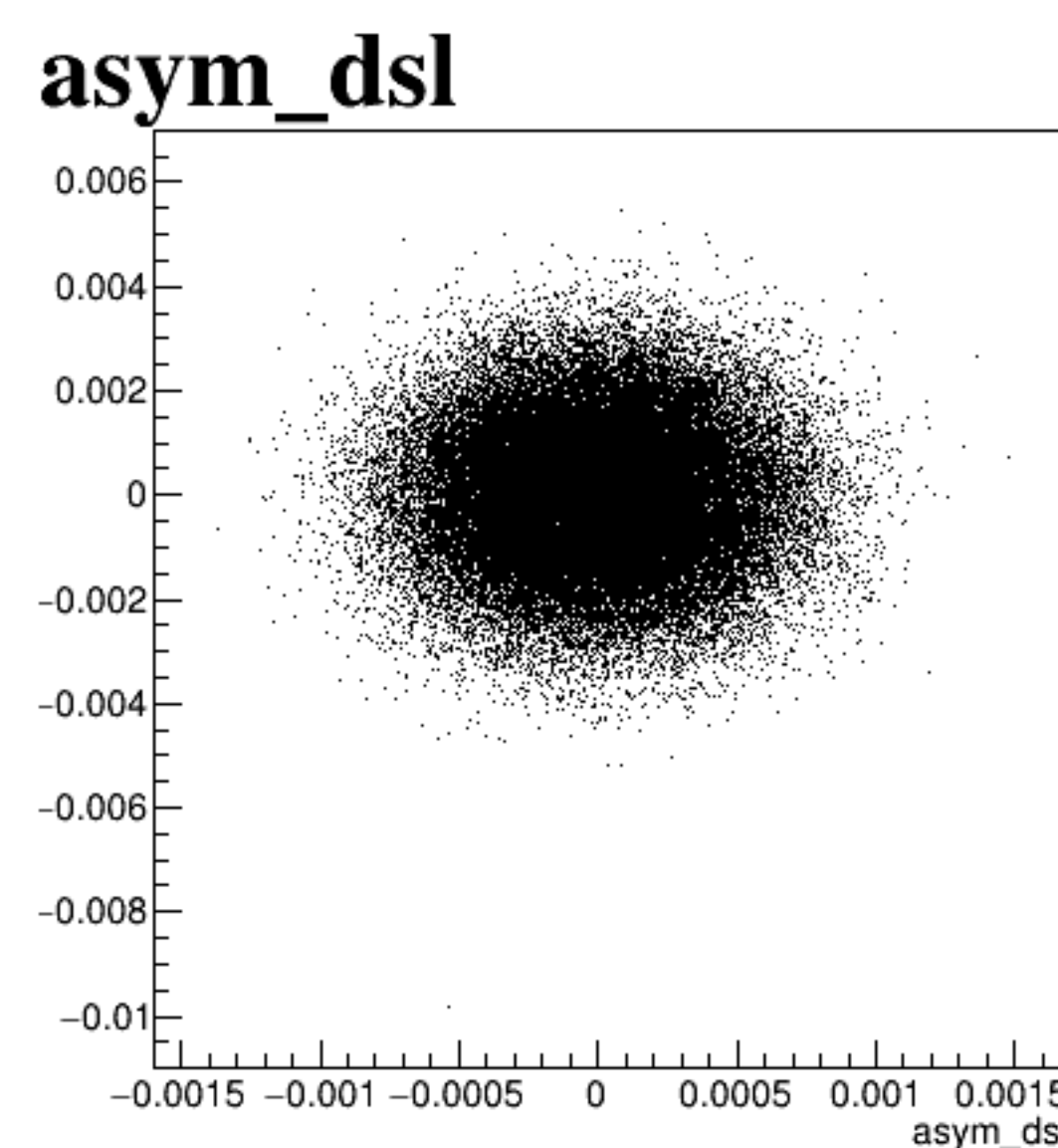
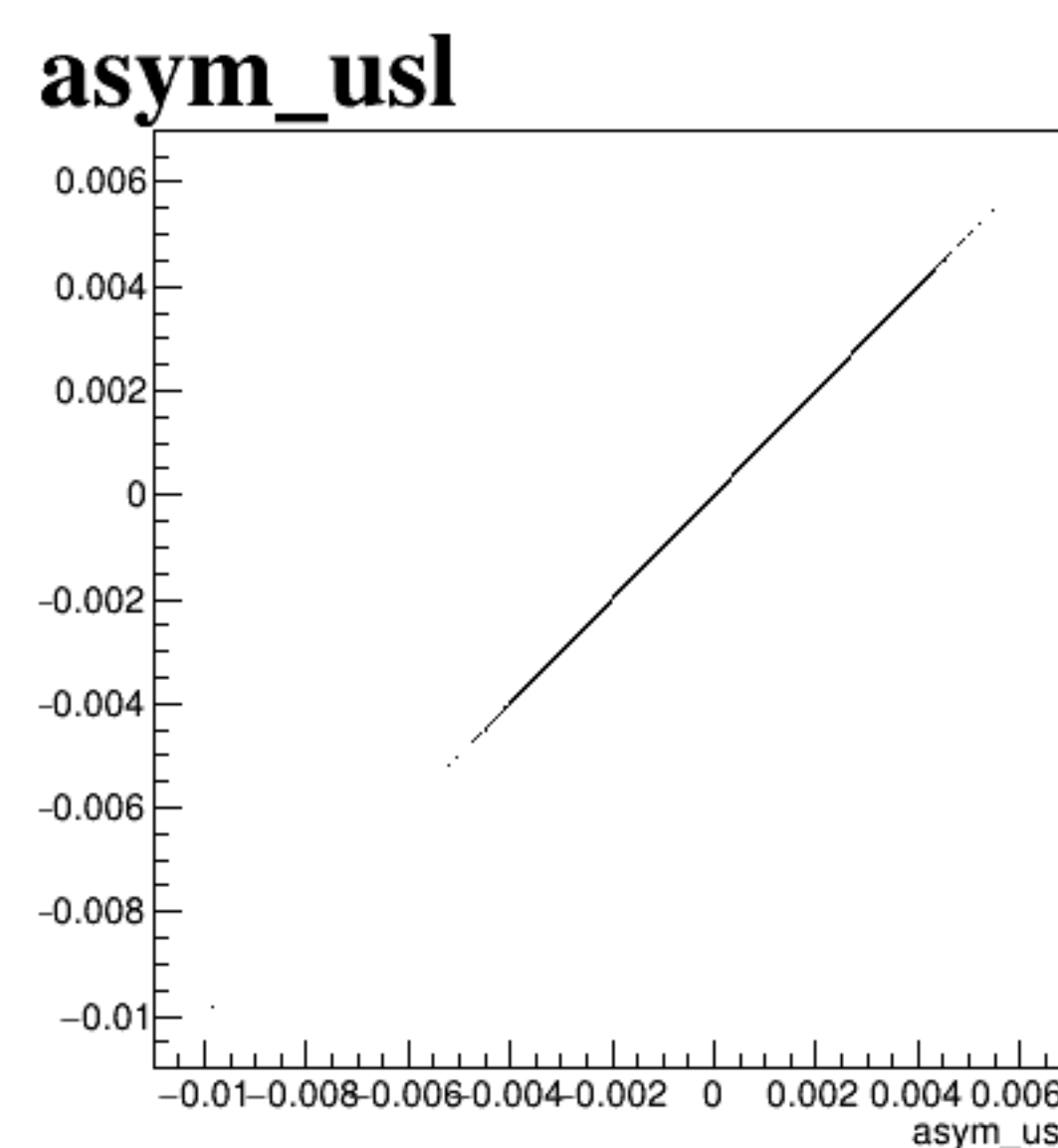
asym_usr



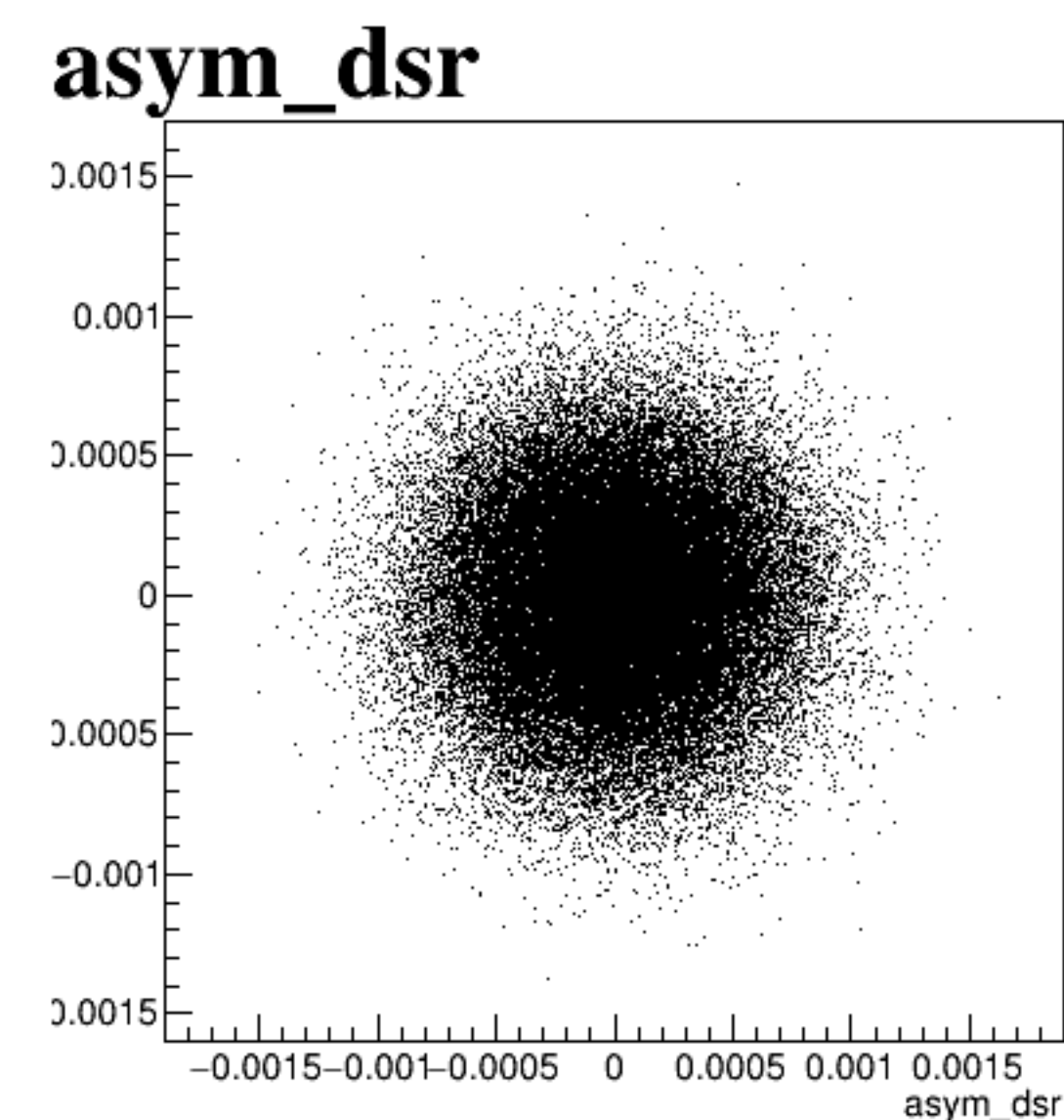
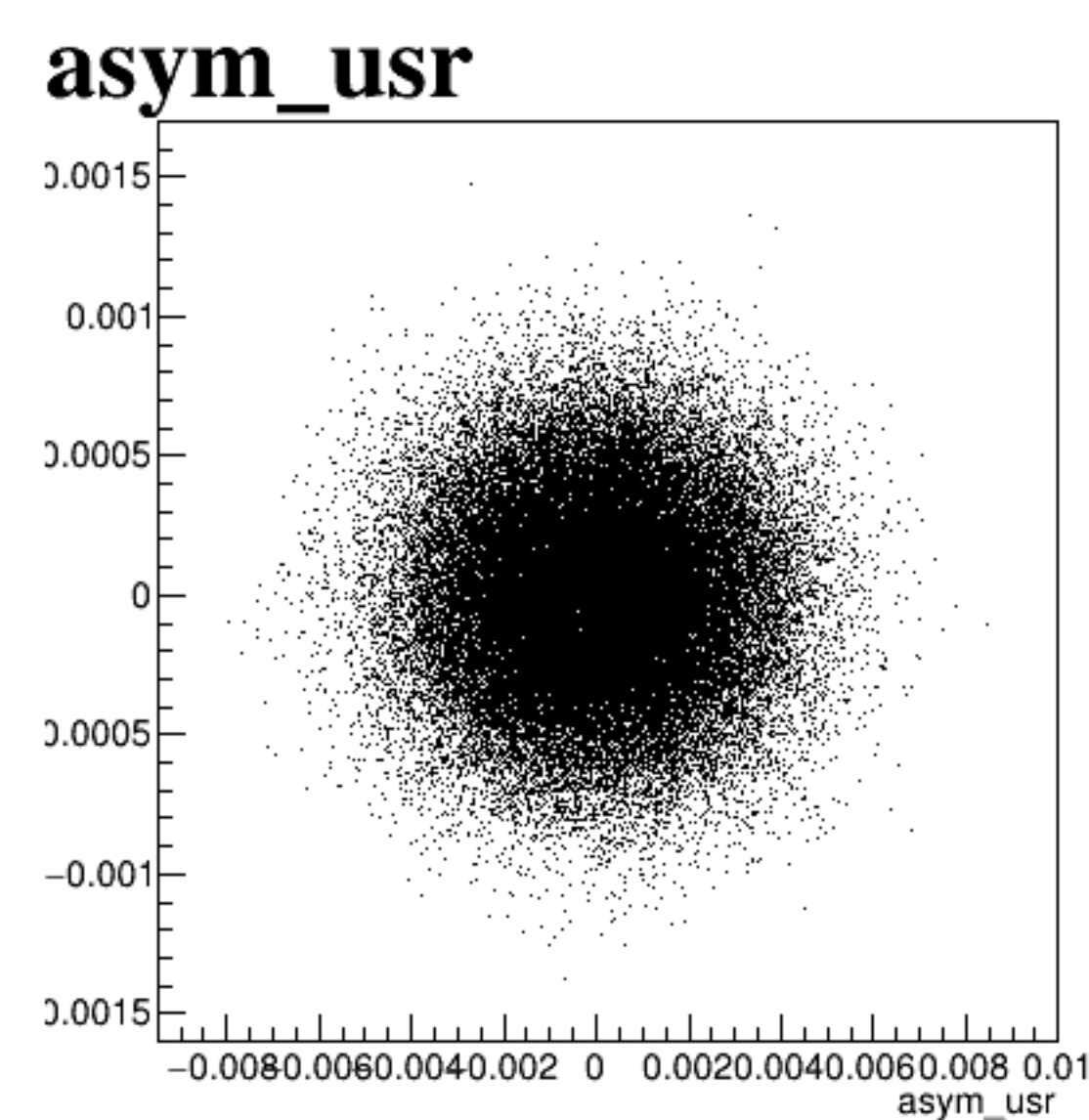
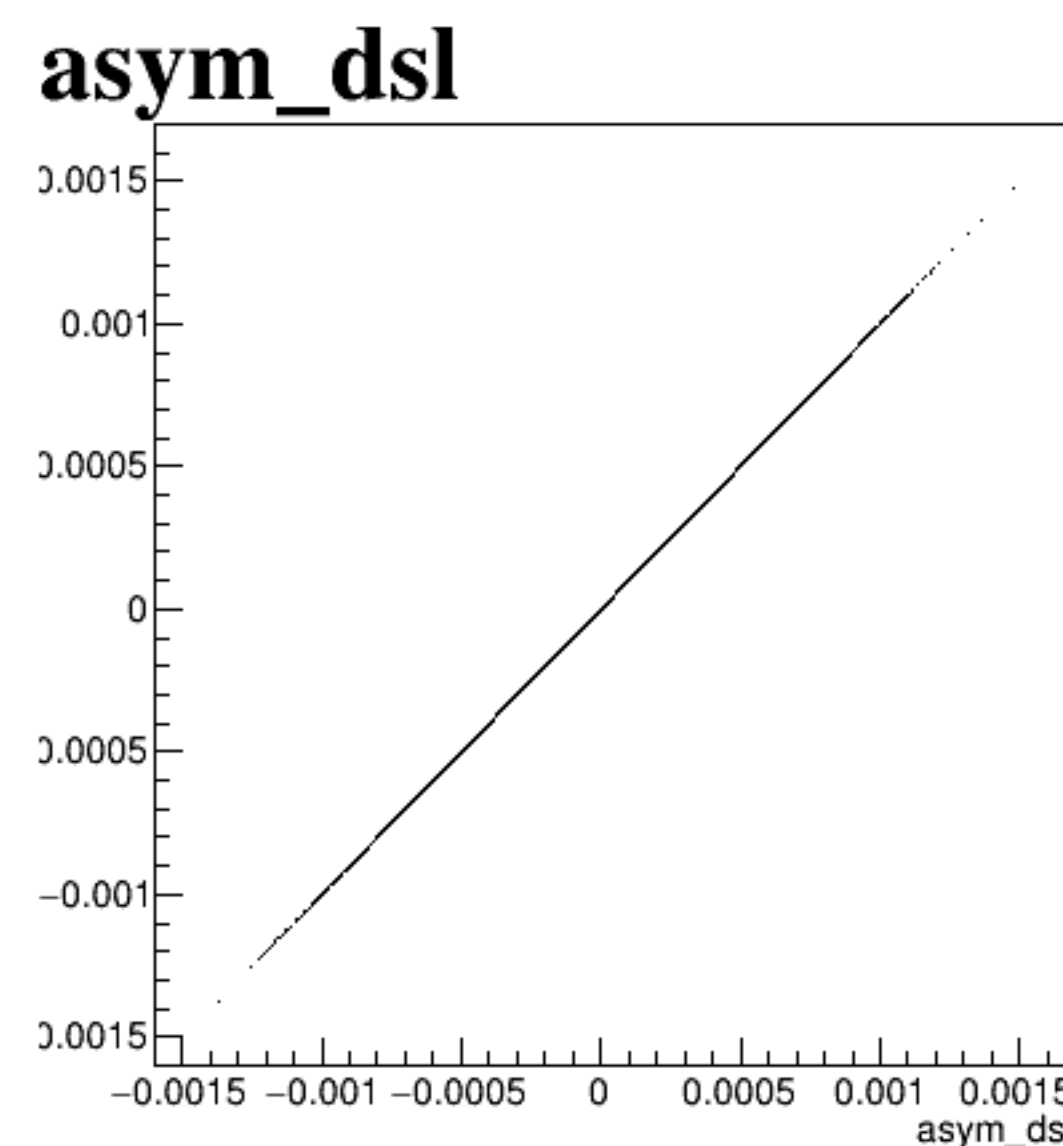
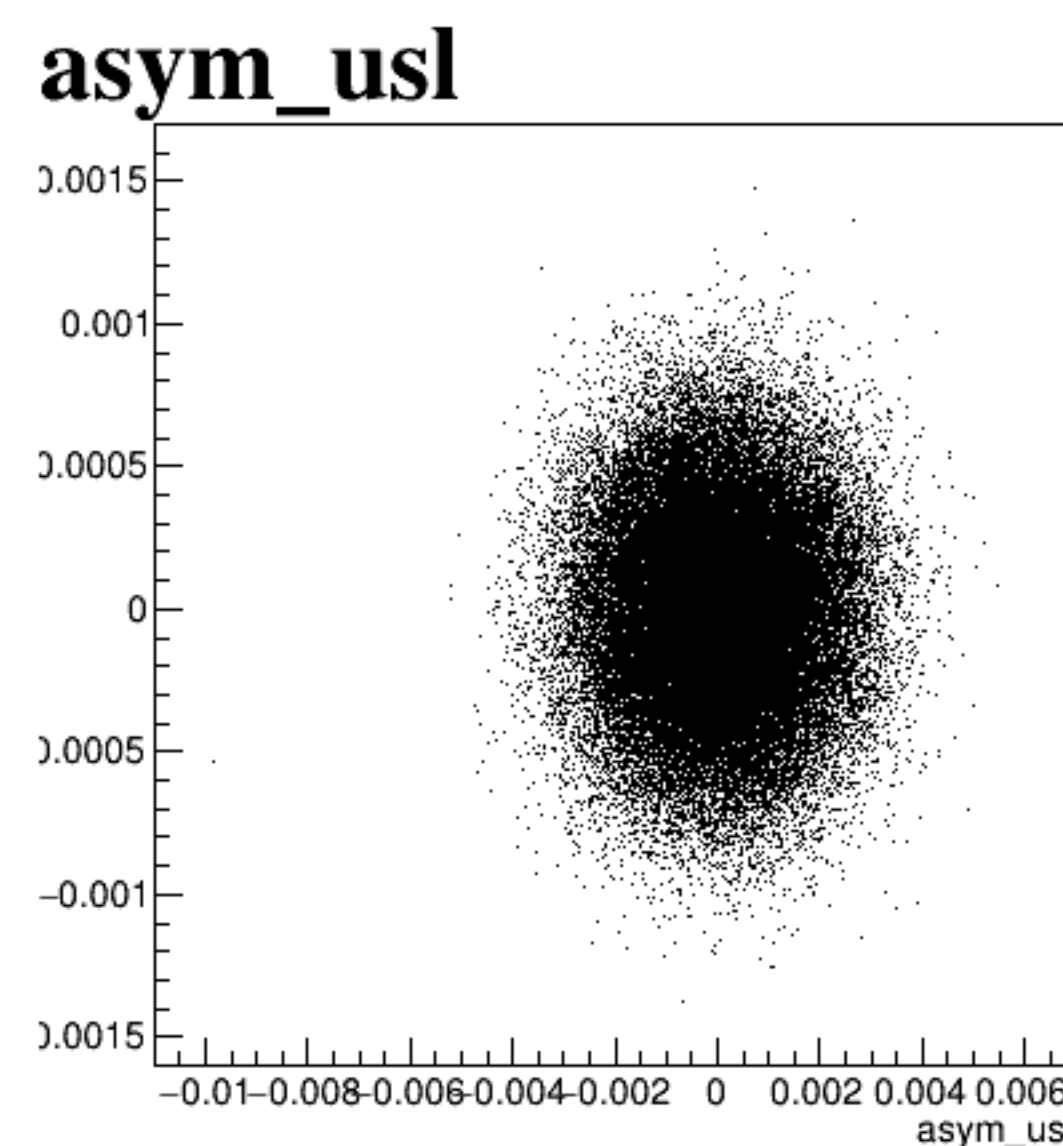
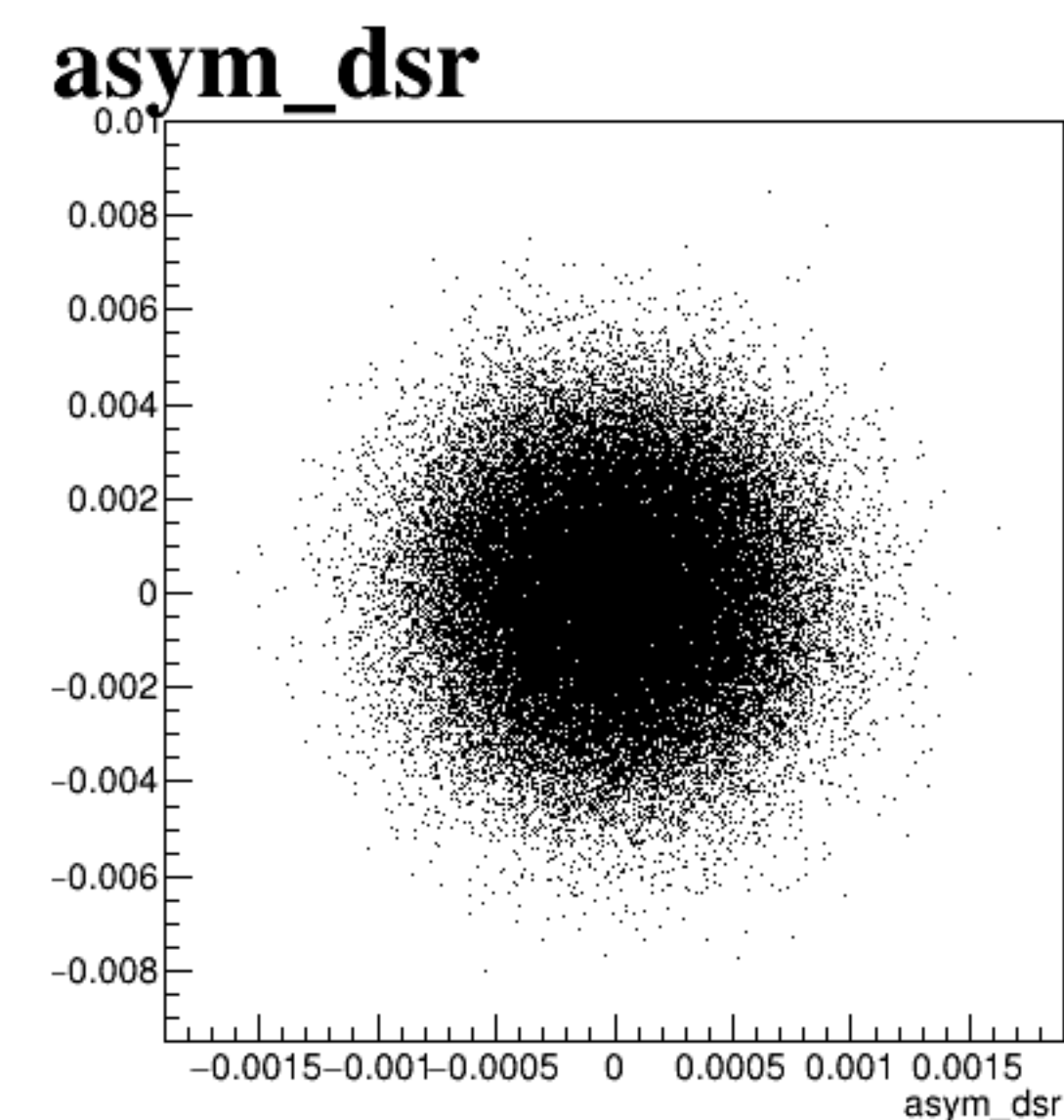
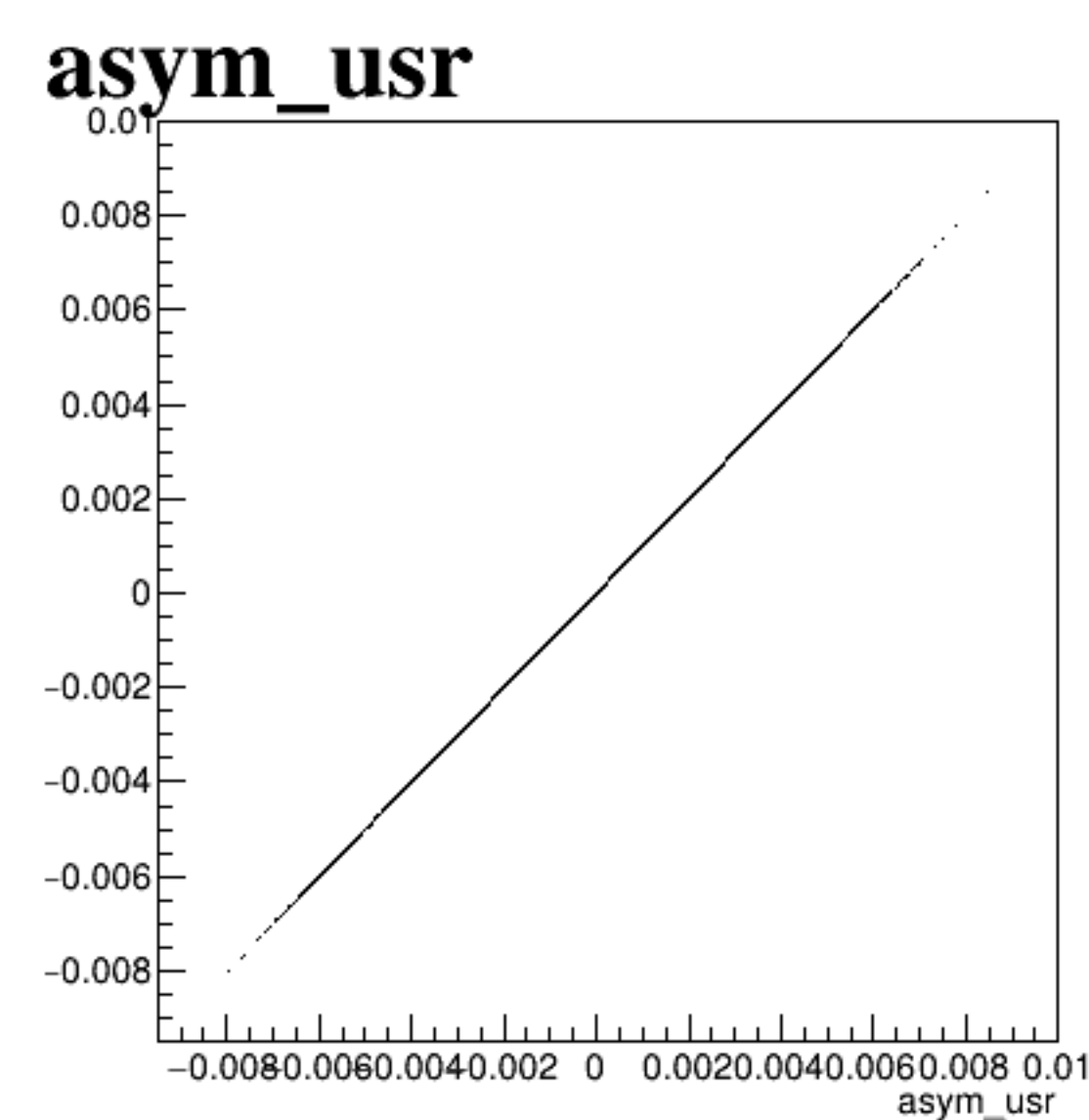
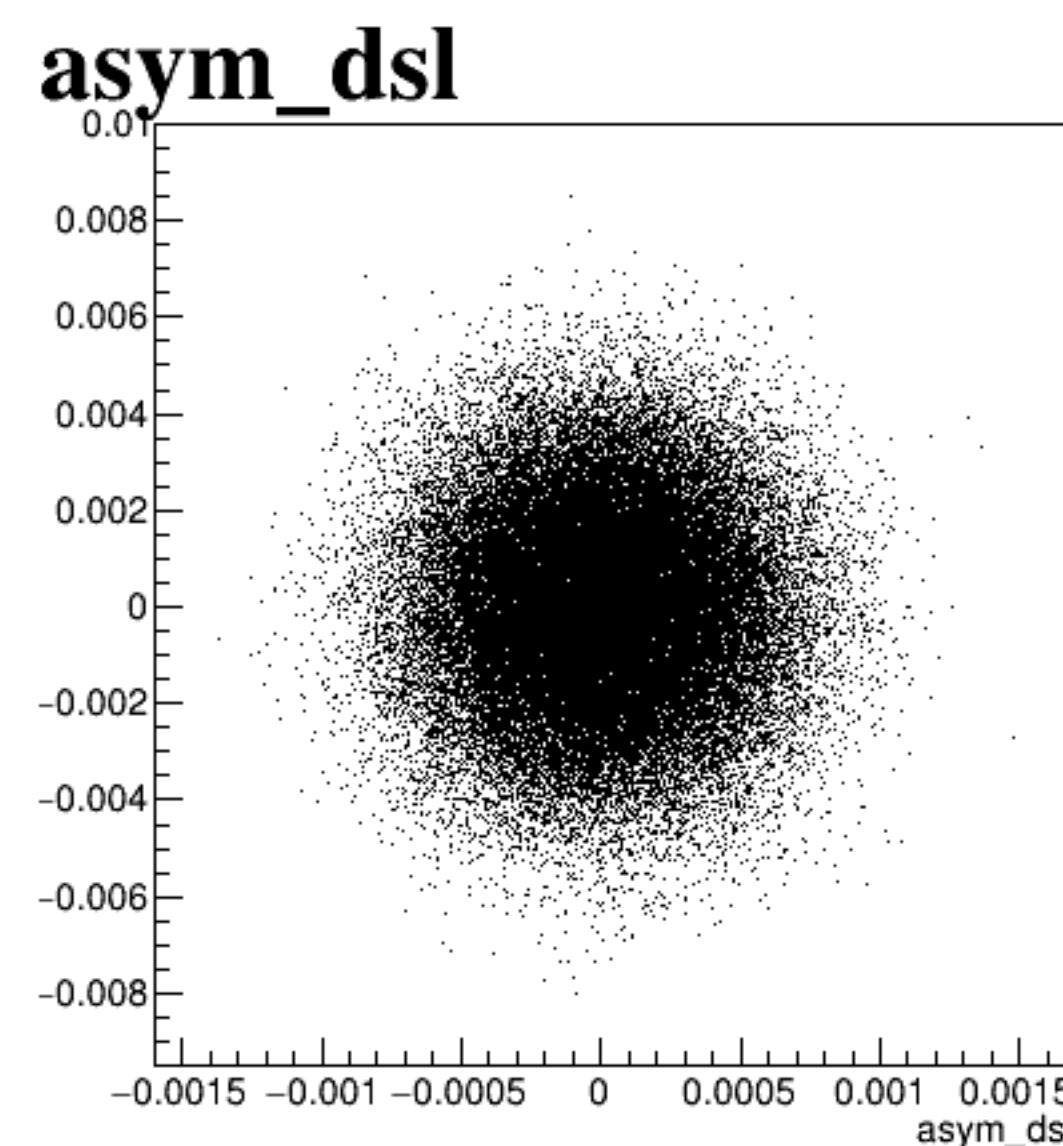
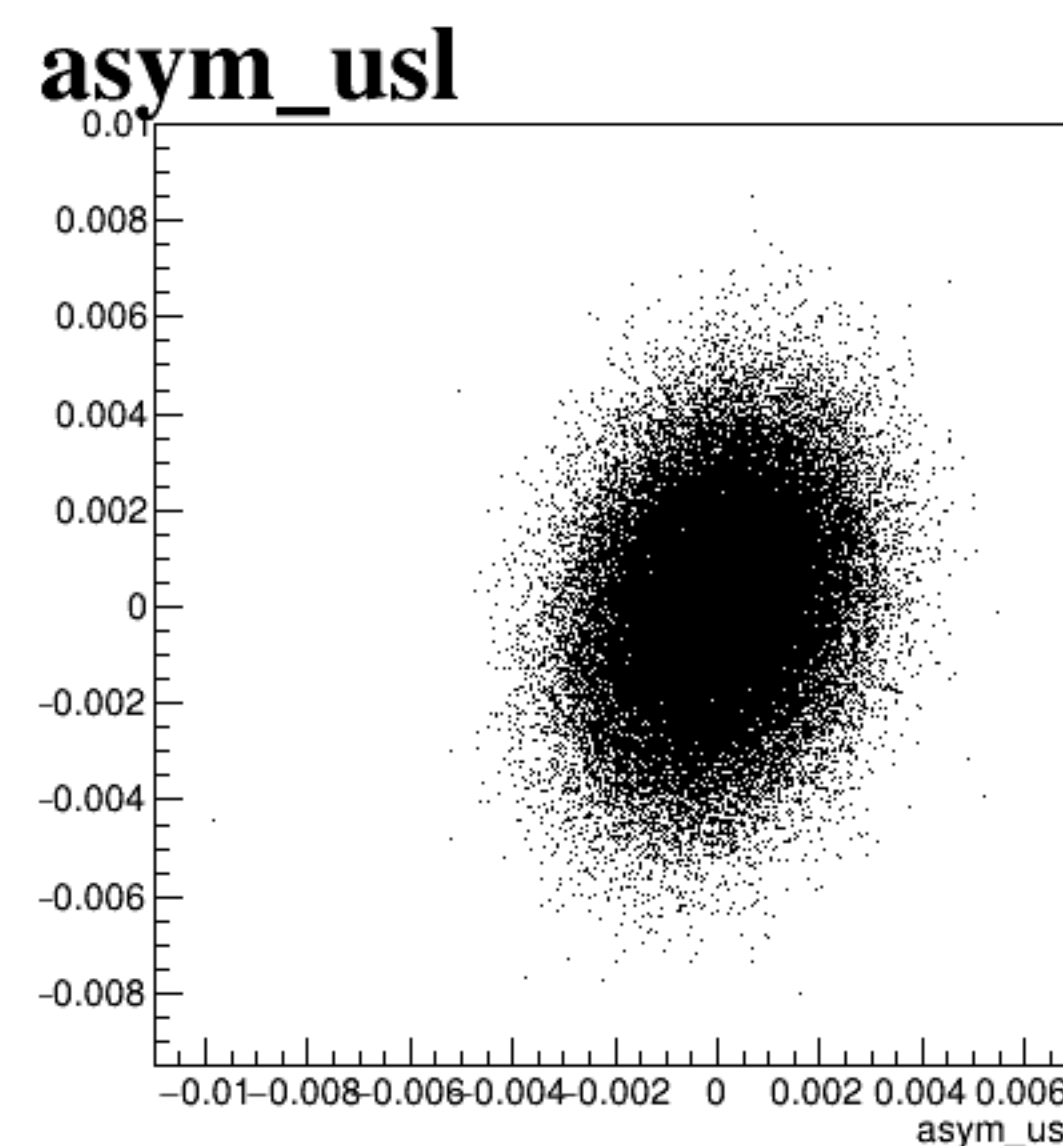
asym_dsr



asym_usl



asym_dsl

**asym_usr****asym_dsr**