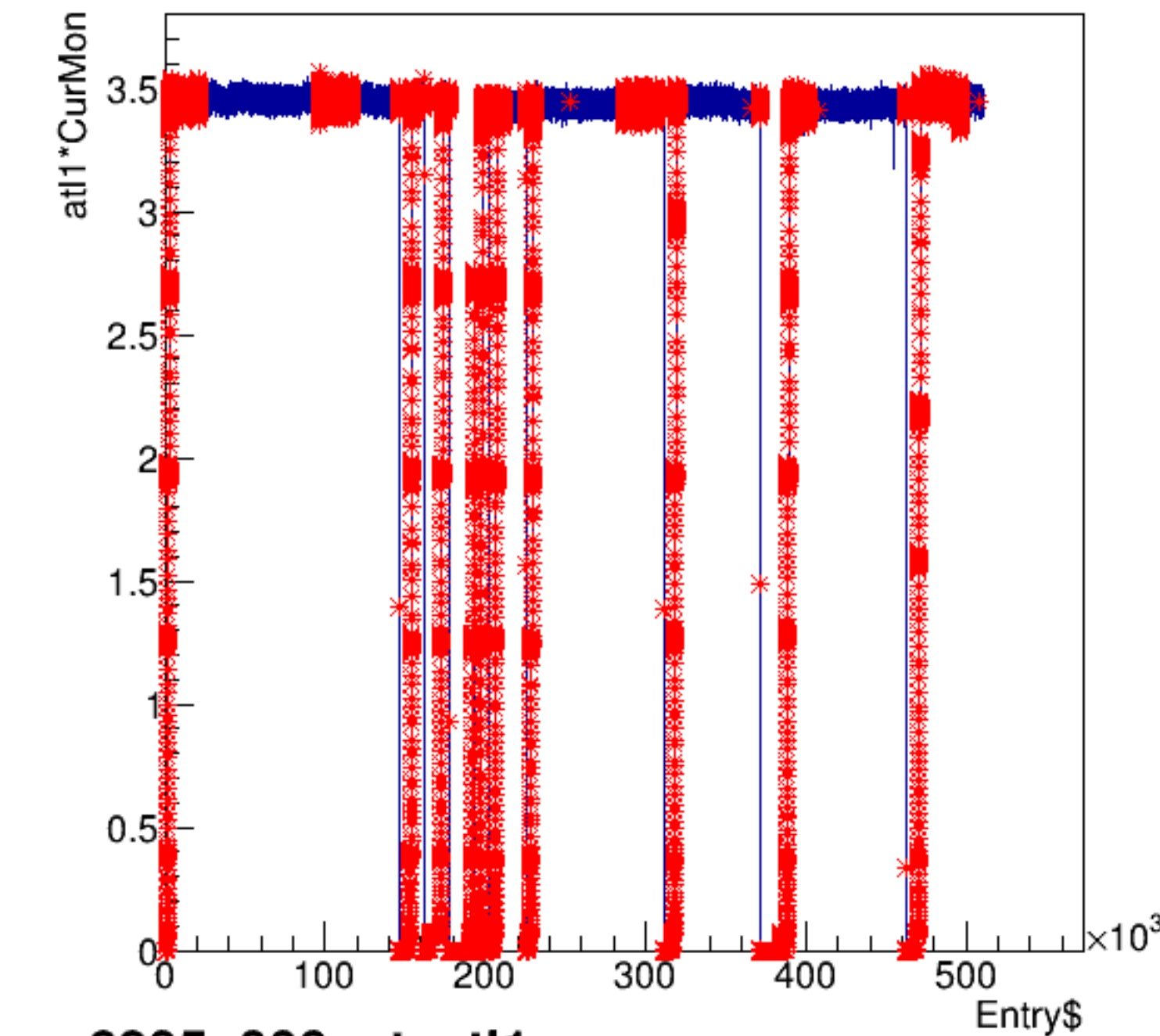
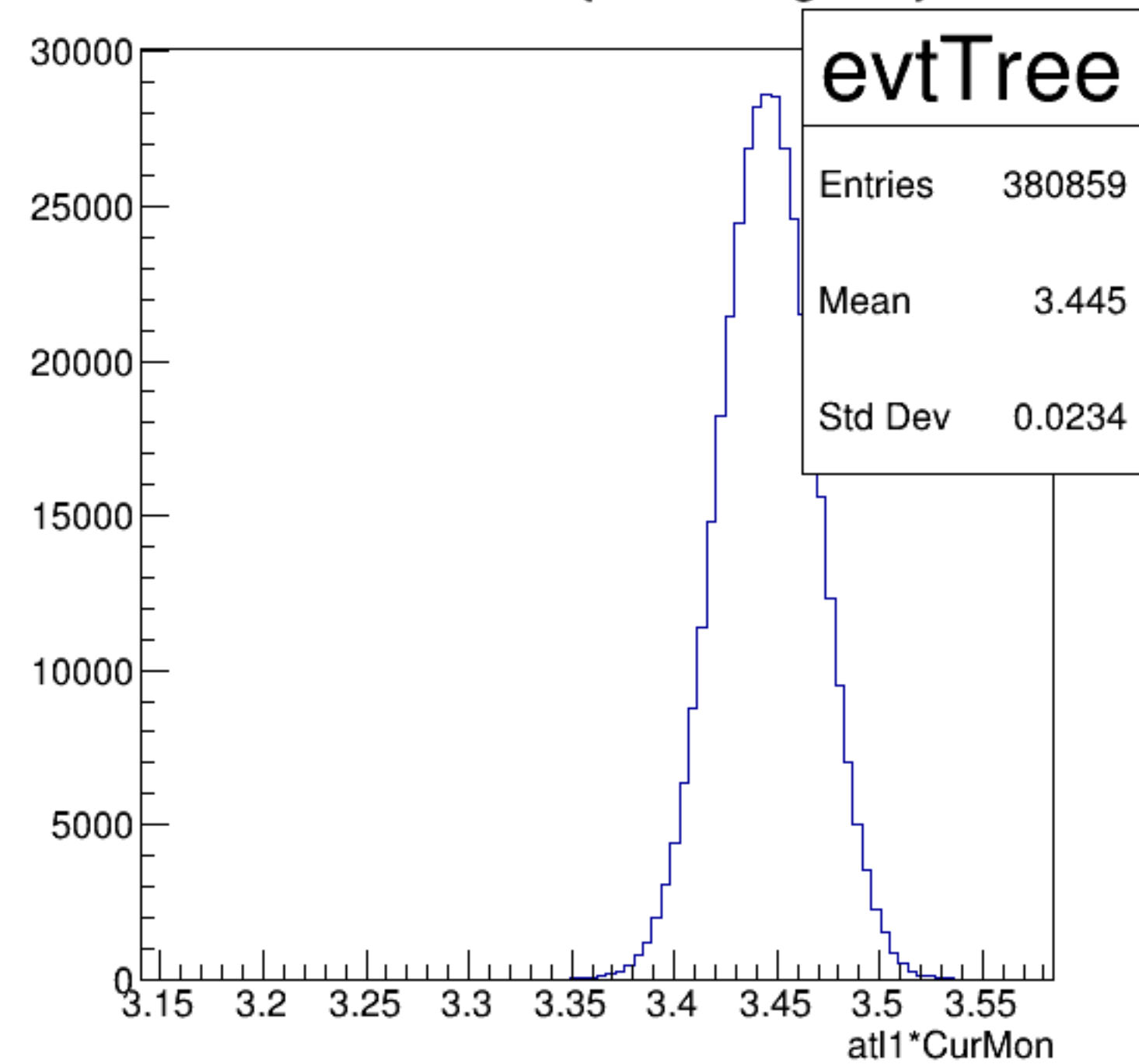


atl1\*CurMon:Entry\$

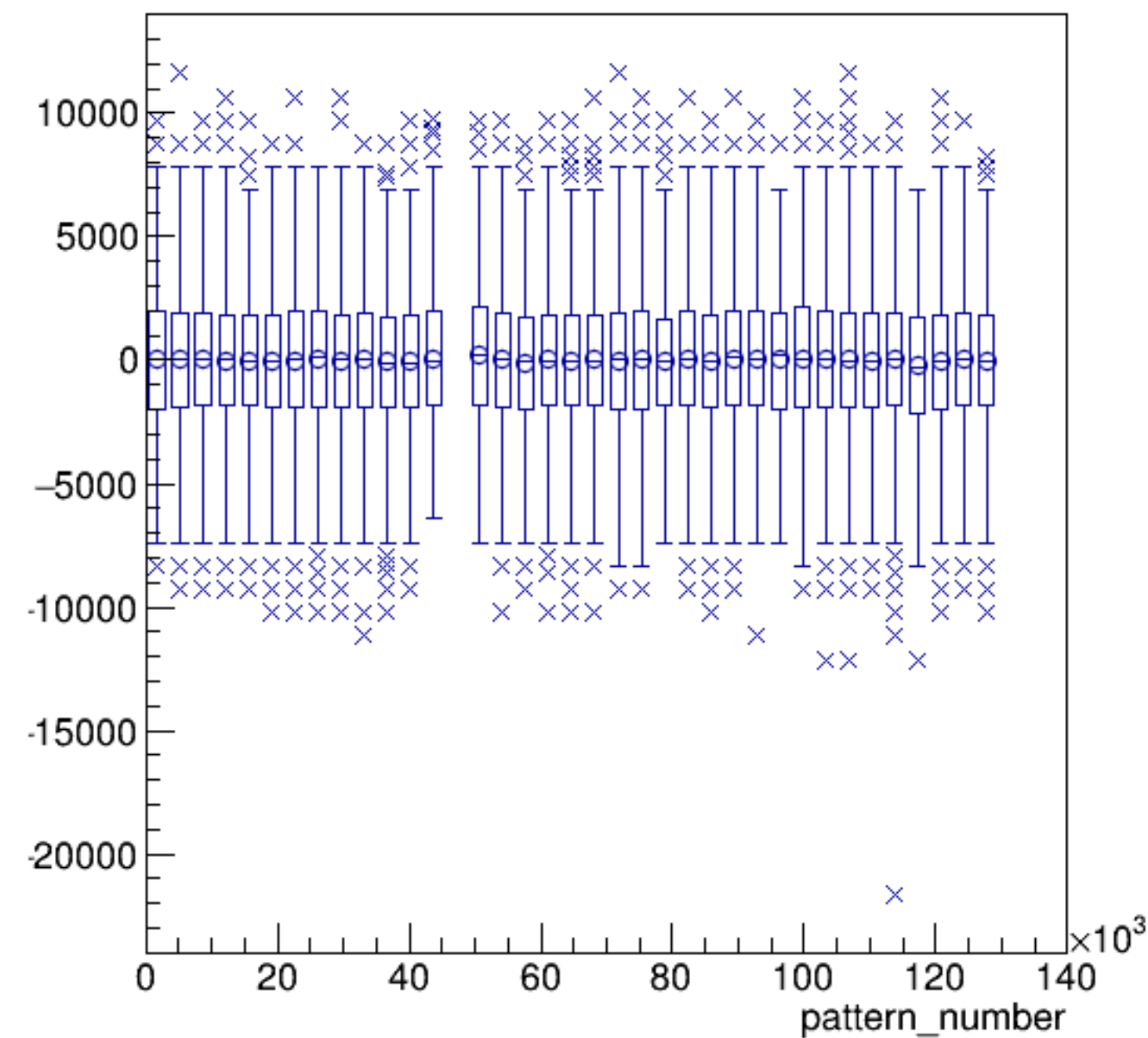


run6205\_000\_at\_atl1.png

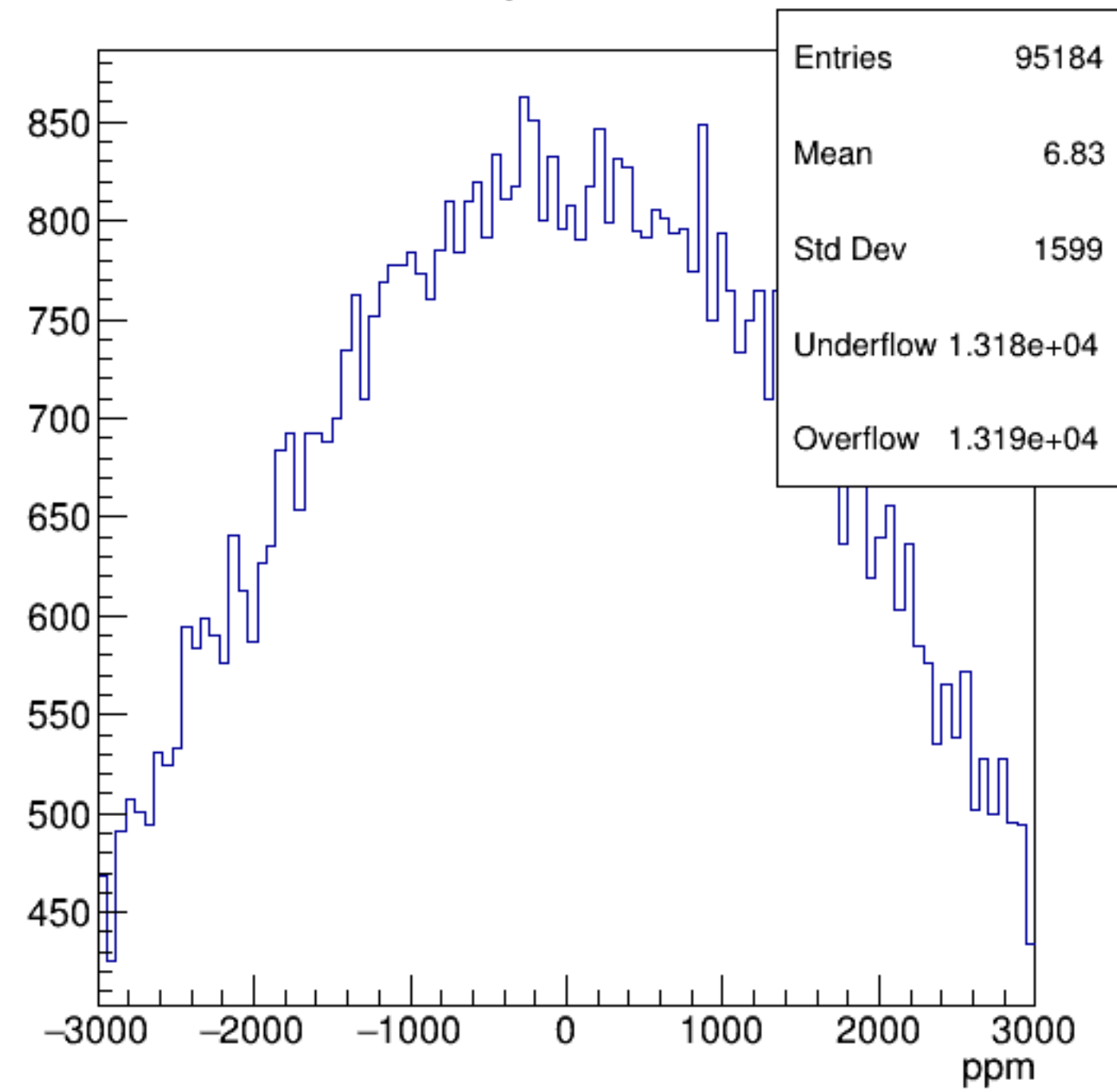
atl1\*CurMon {ErrorFlag==0}



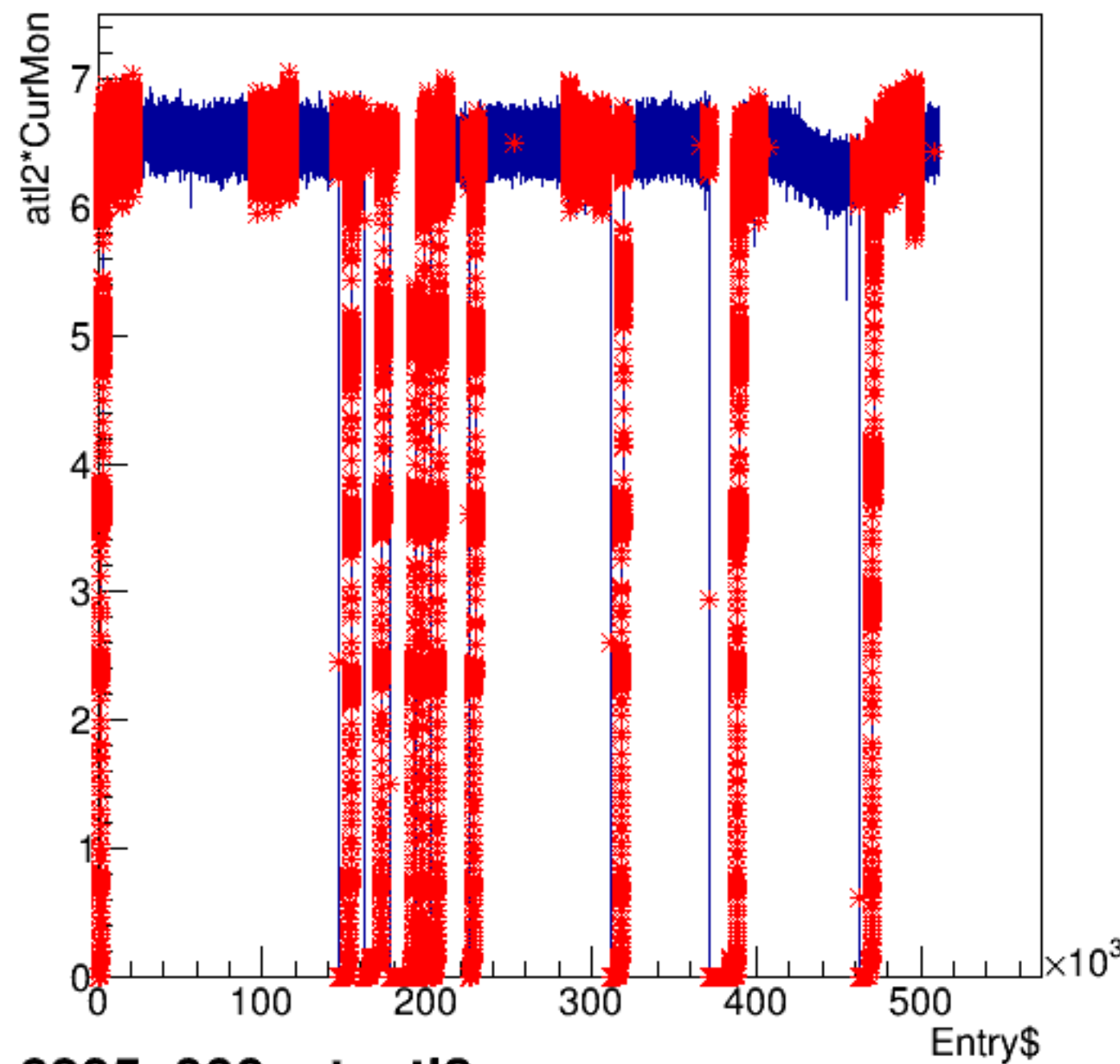
asym\_atl1/ppm:pattern\_number {ErrorFlag==0}



asym\_atl1

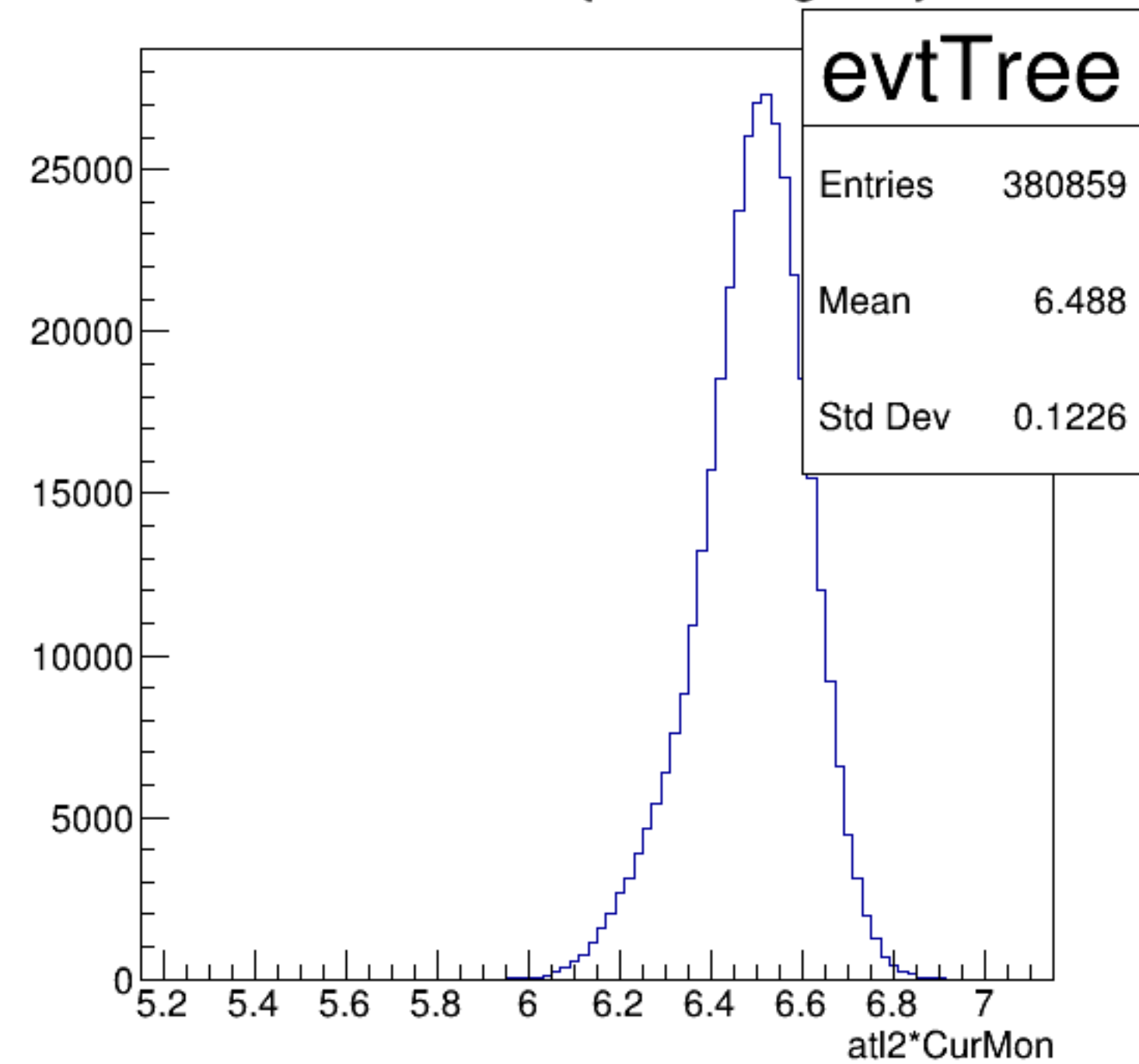


atl2\*CurMon:Entry\$

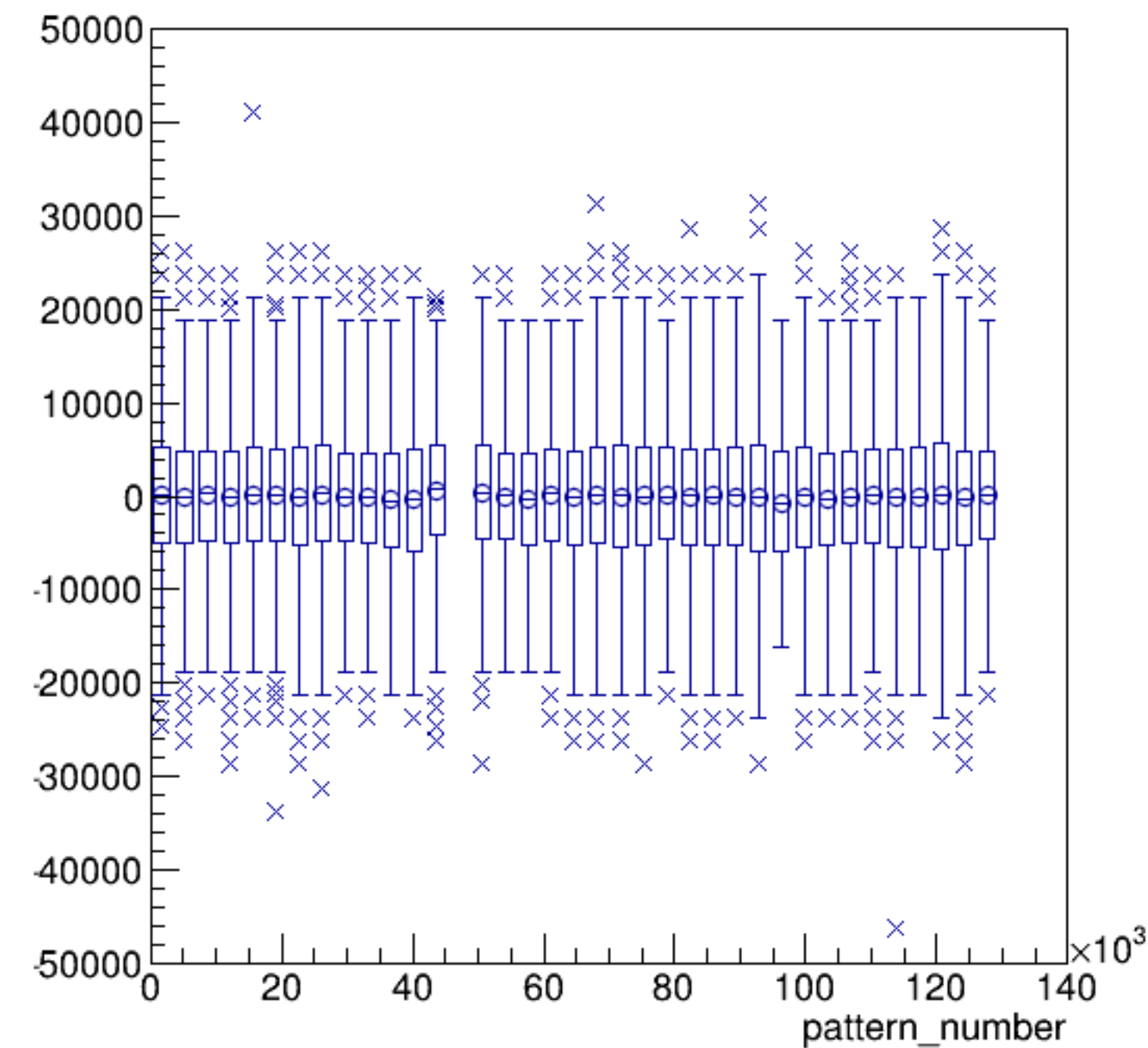


run6205\_000\_at\_atl2.png

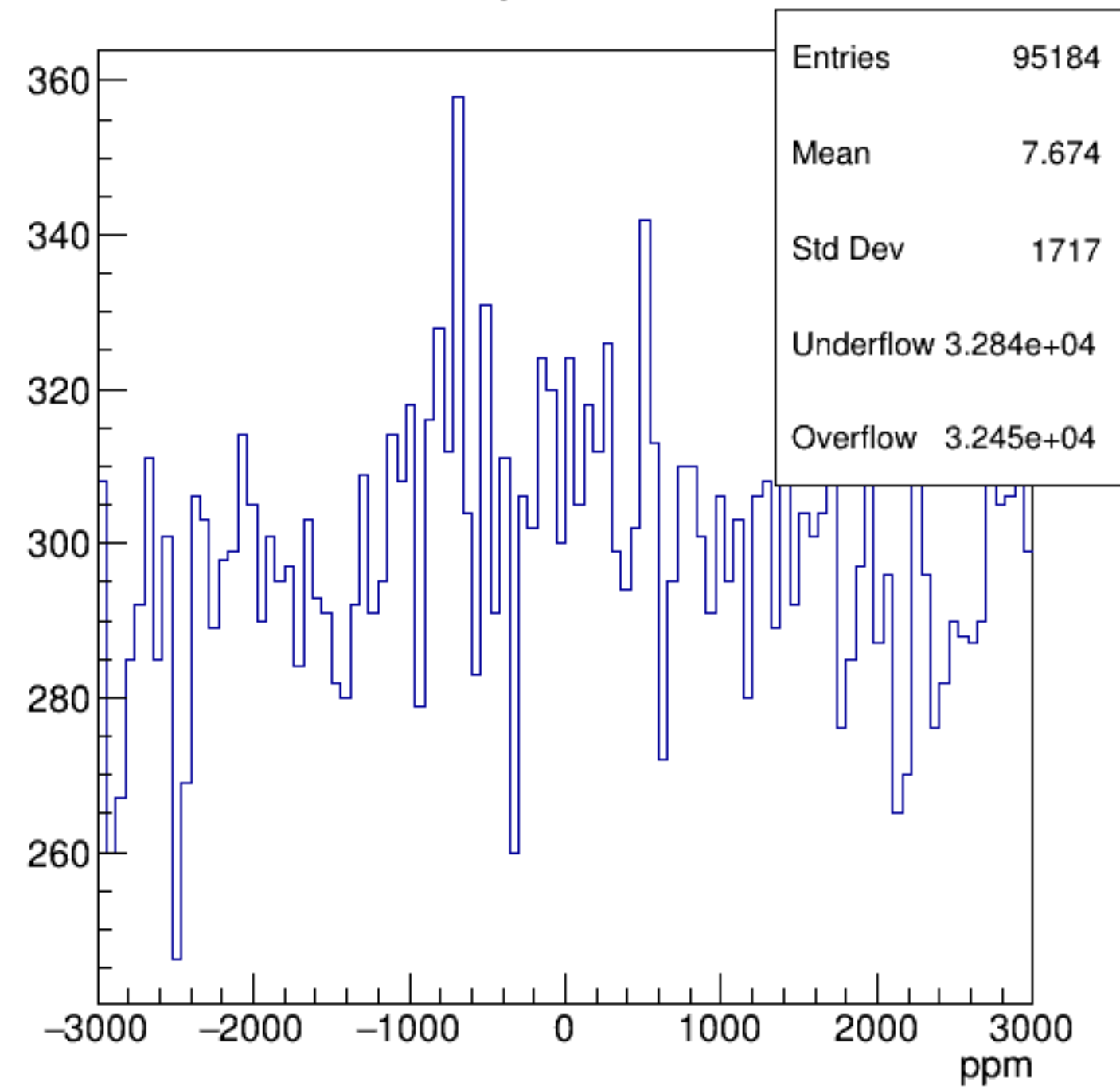
atl2\*CurMon {ErrorFlag==0}



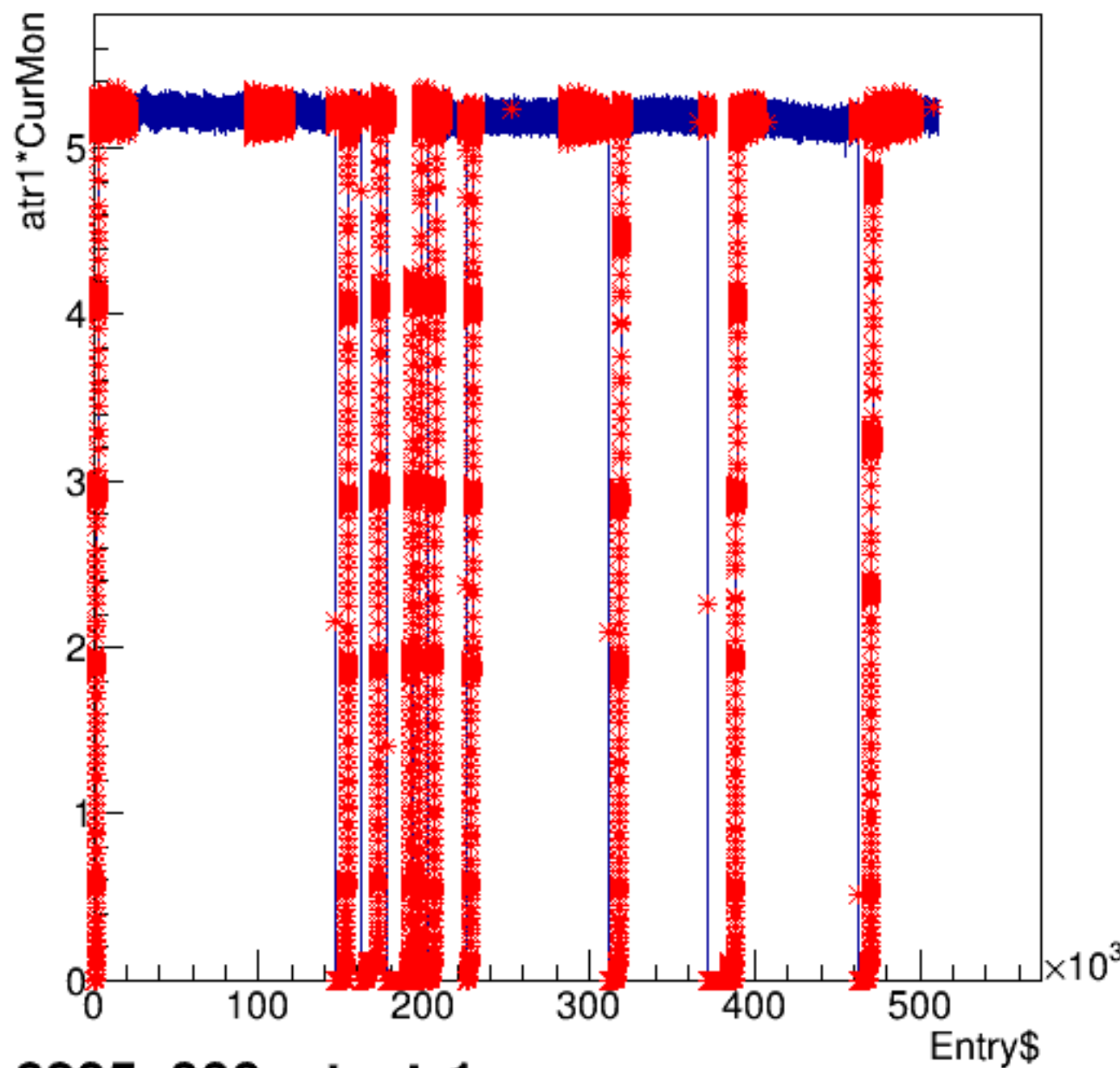
asym\_atl2/ppm:pattern\_number {ErrorFlag==0}



asym\_atl2

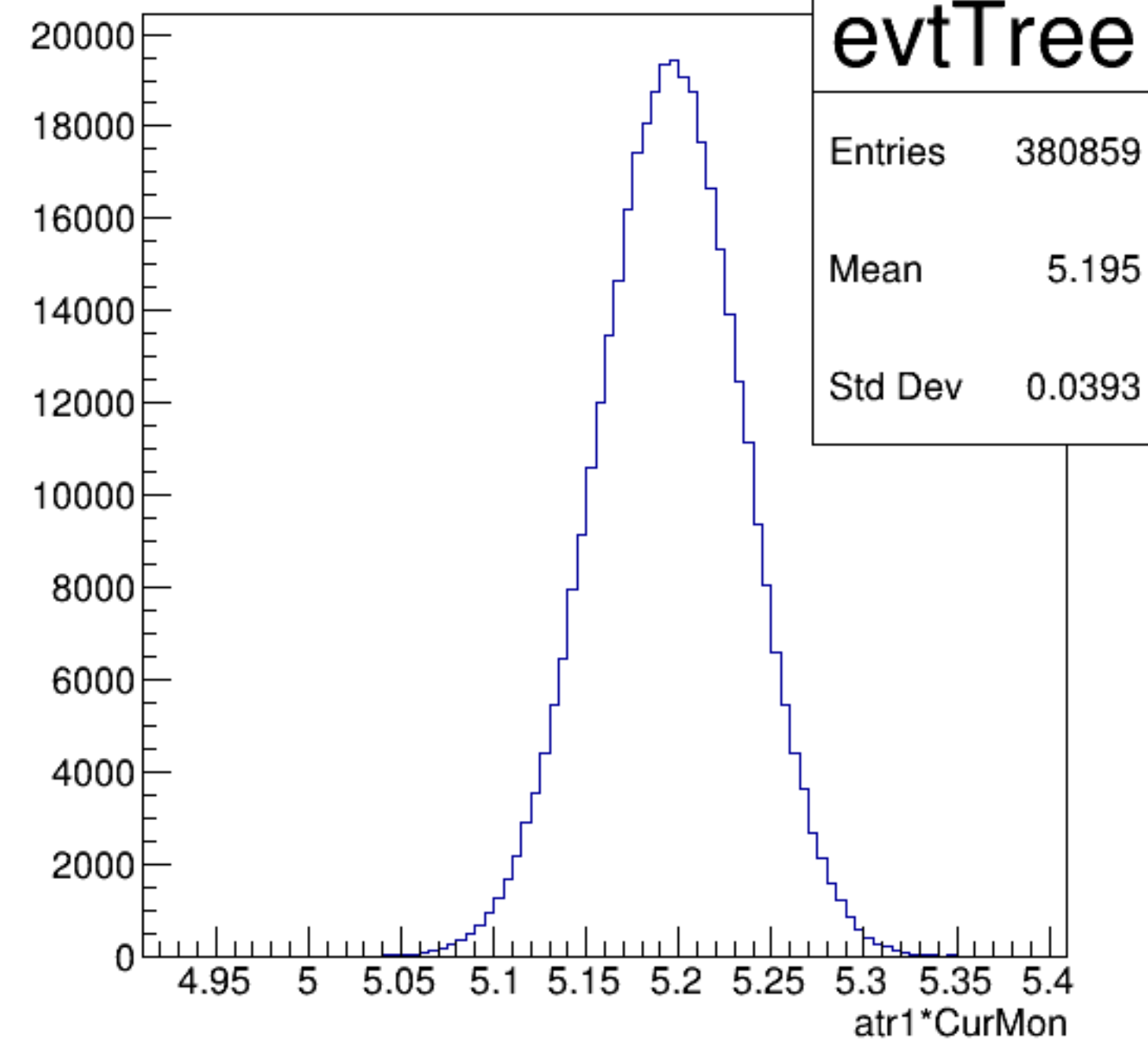


atr1\*CurMon:Entry\$

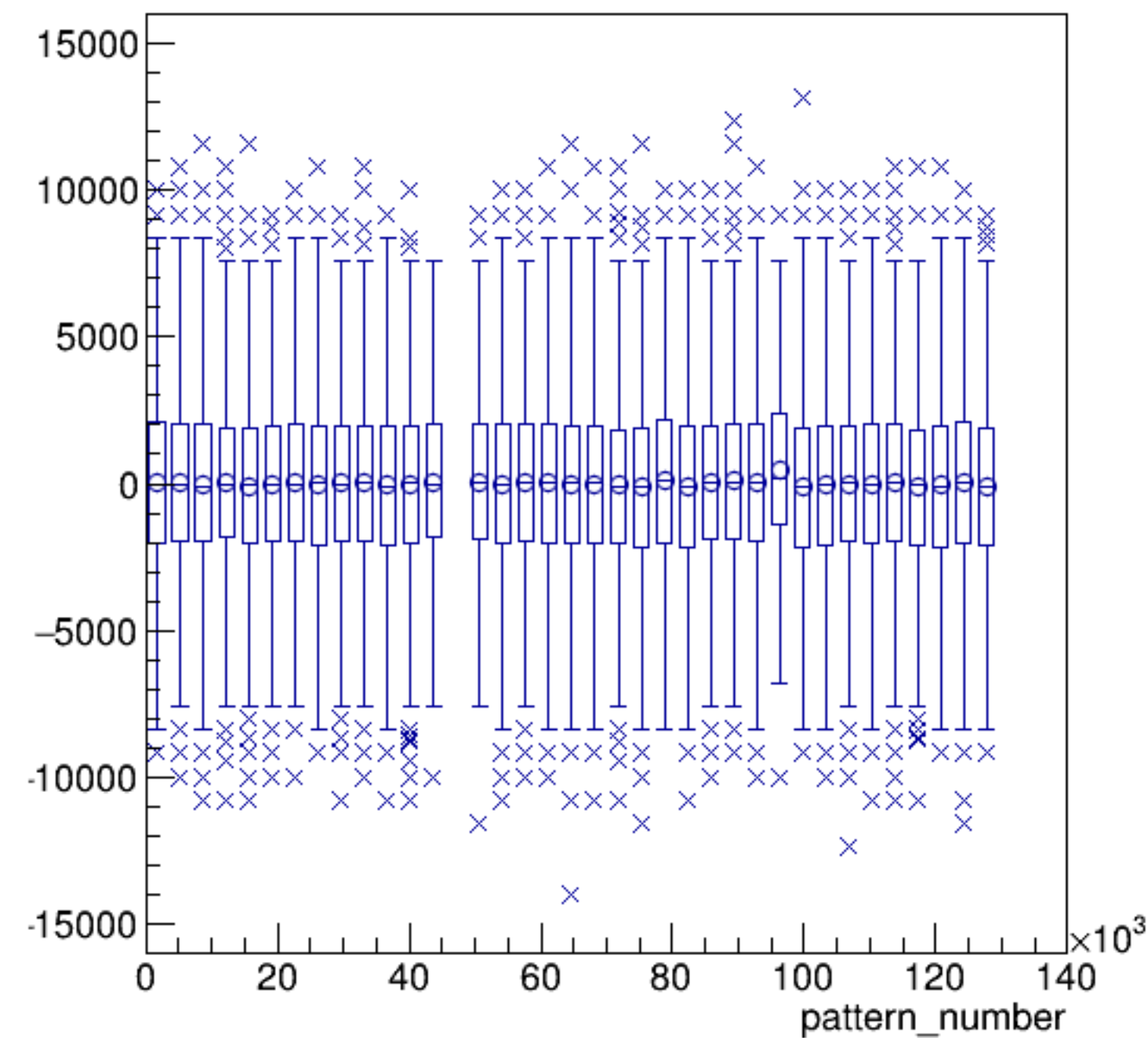


run6205\_000\_at\_atr1.png

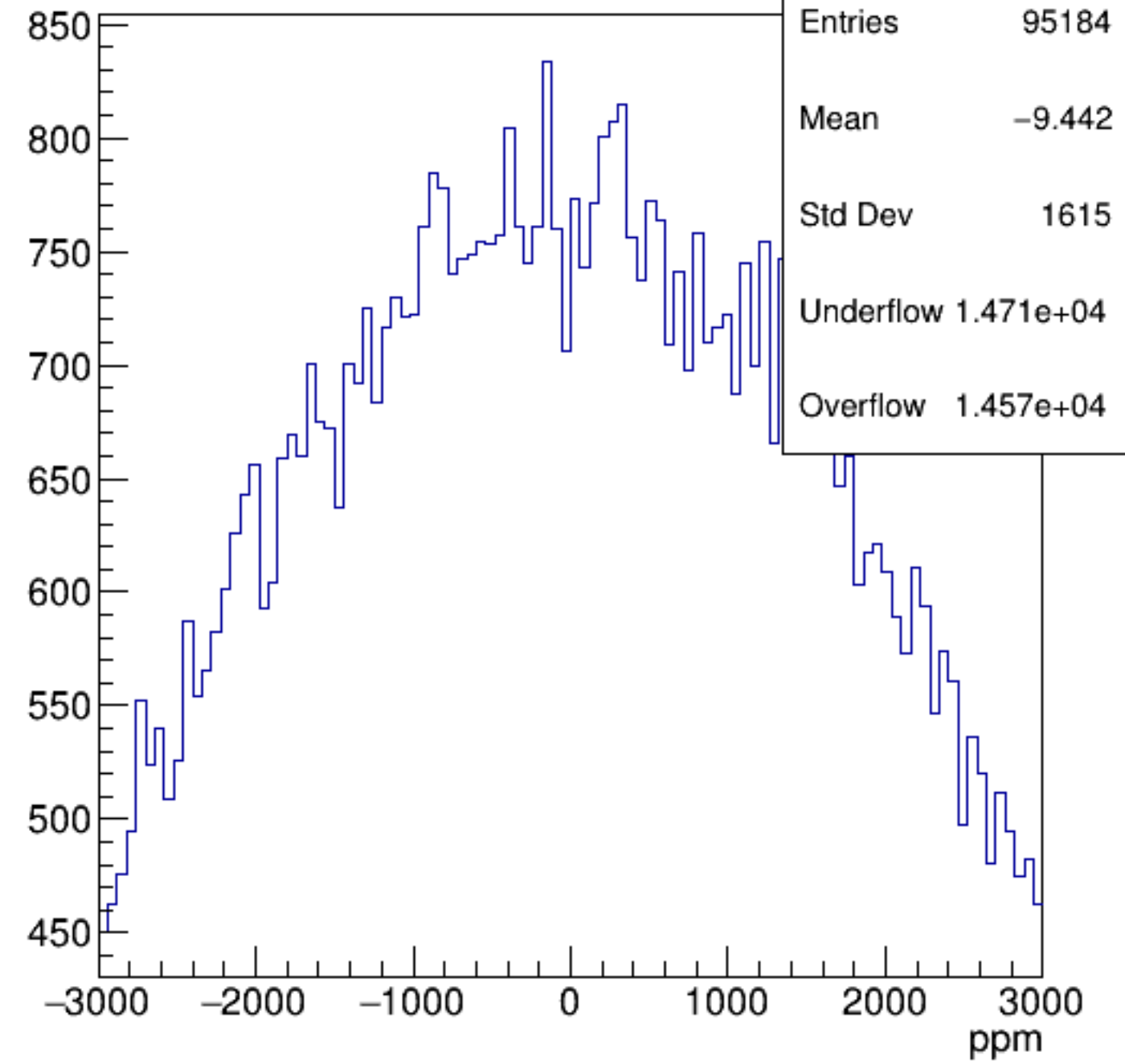
atr1\*CurMon {ErrorFlag==0}



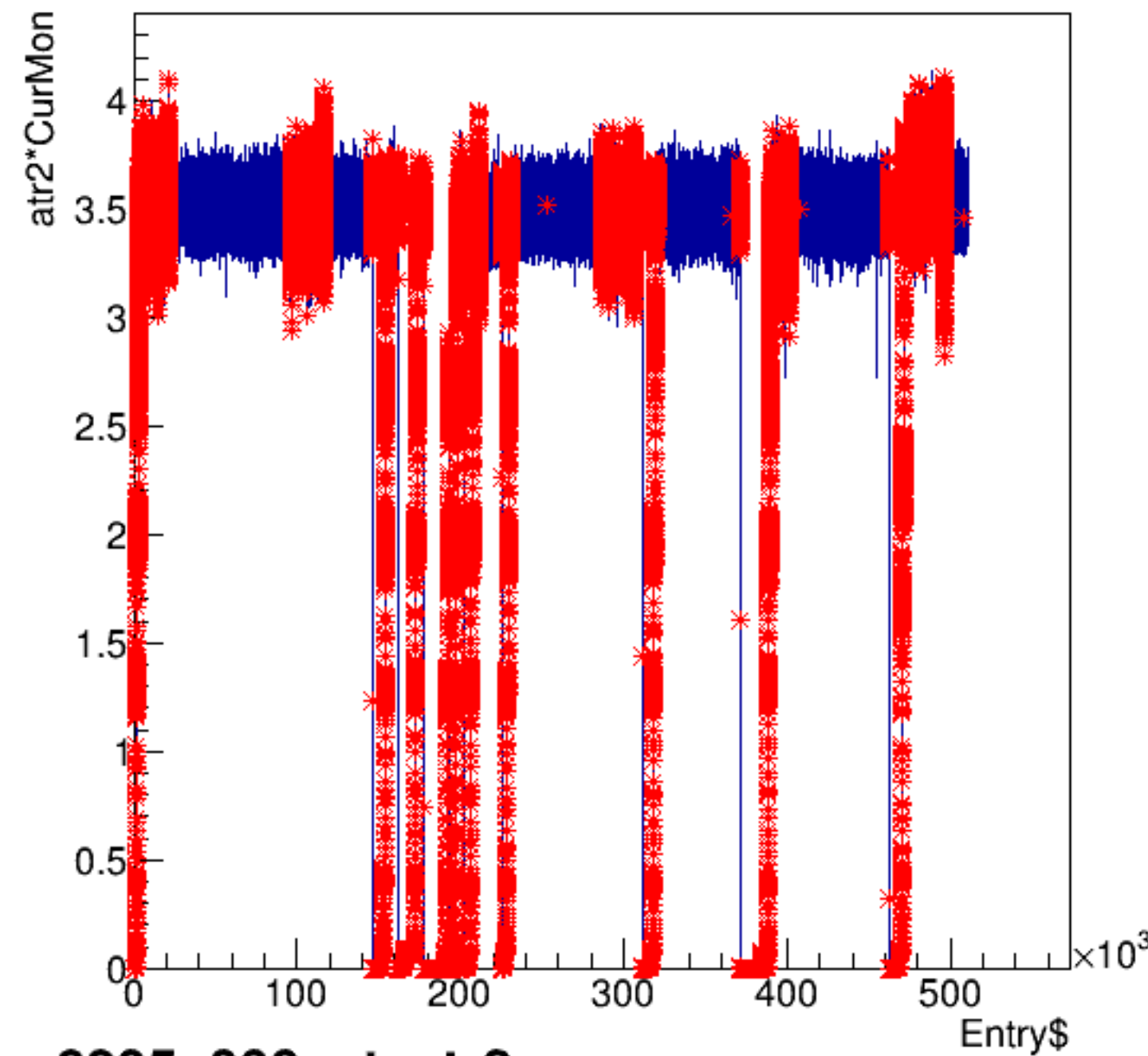
asym\_atr1/ppm:pattern\_number {ErrorFlag==0}



asym\_atr1

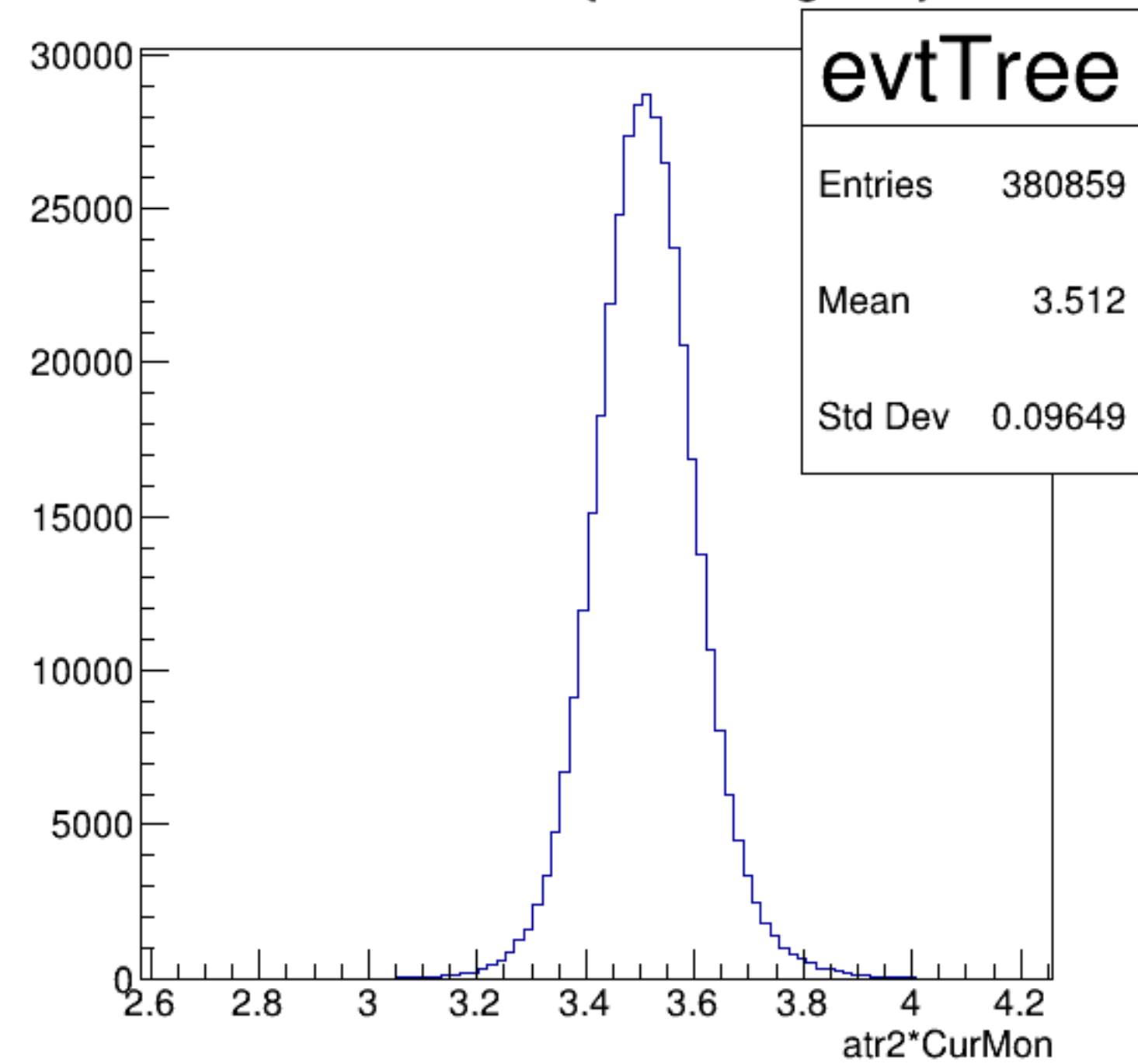


atr2\*CurMon:Entry\$

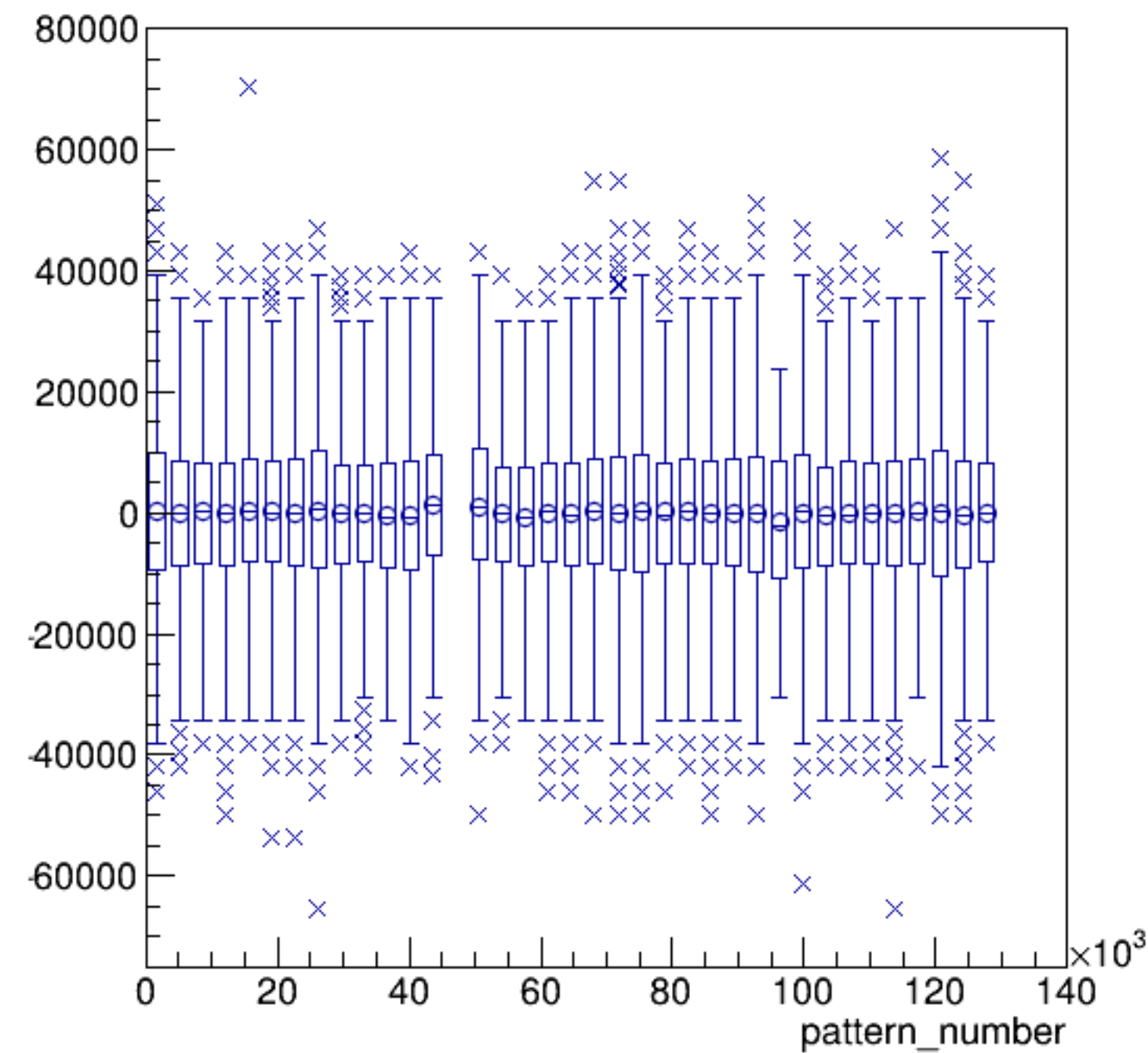


run6205\_000\_at\_atr2.png

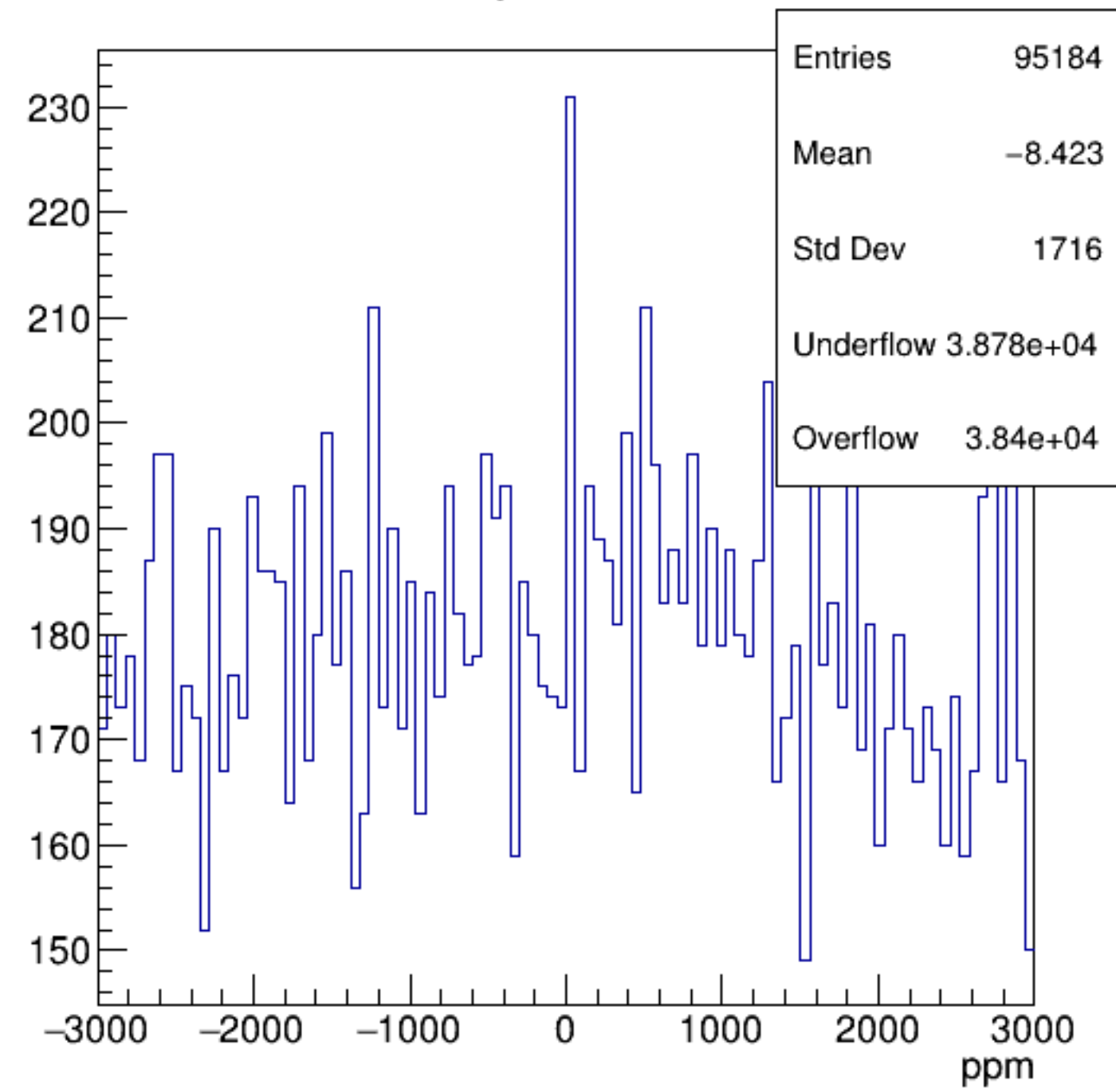
atr2\*CurMon {ErrorFlag==0}



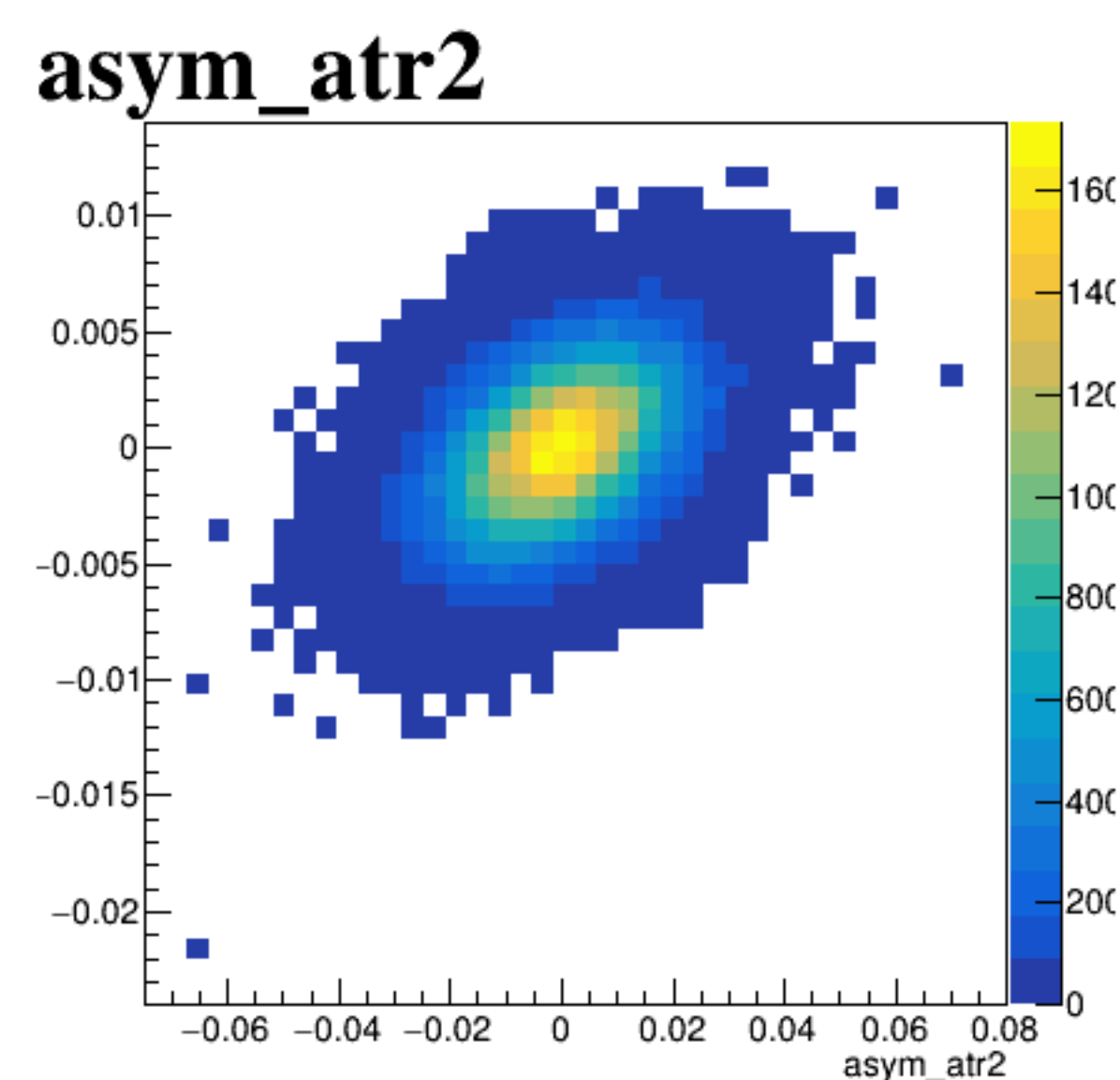
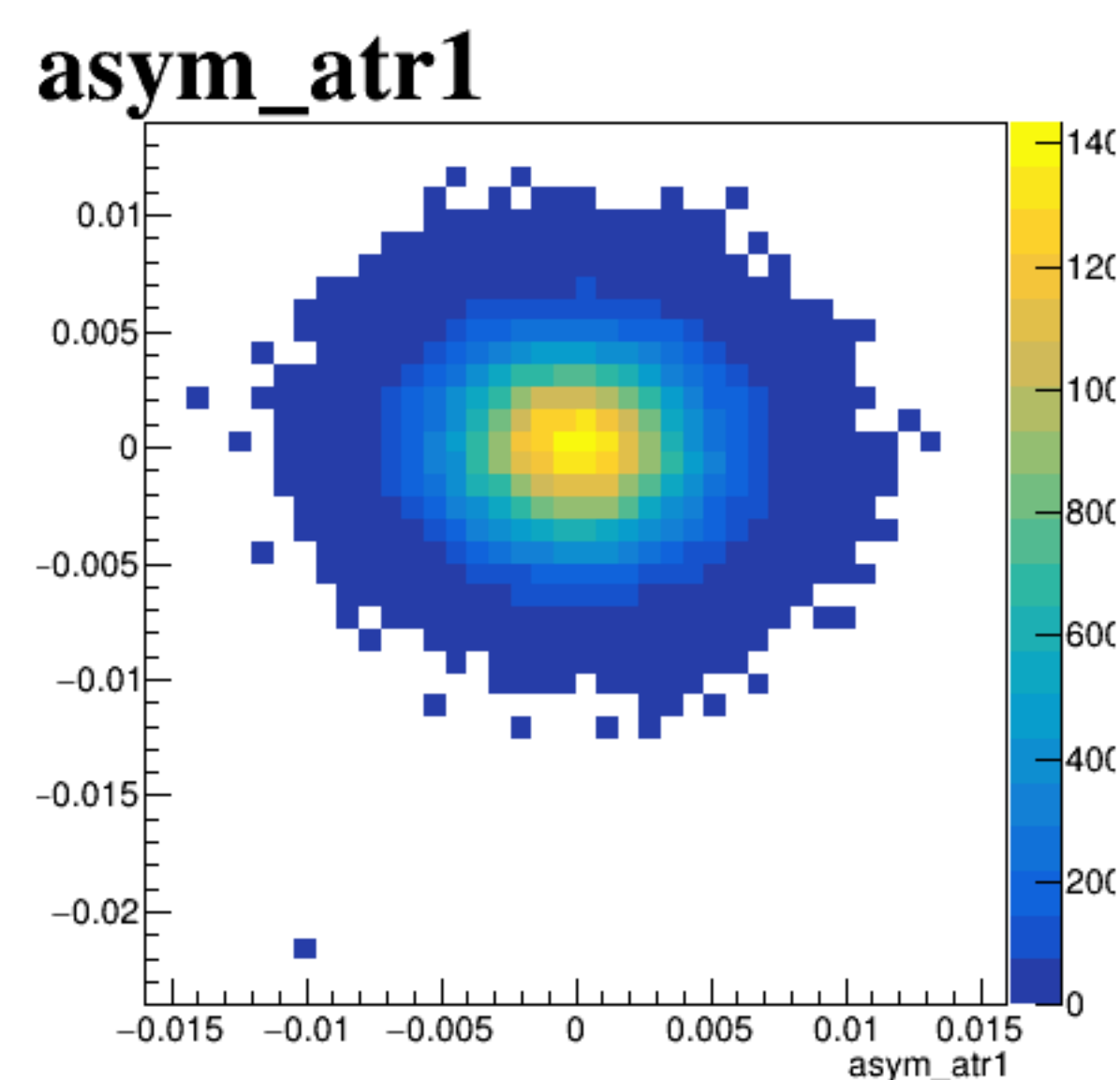
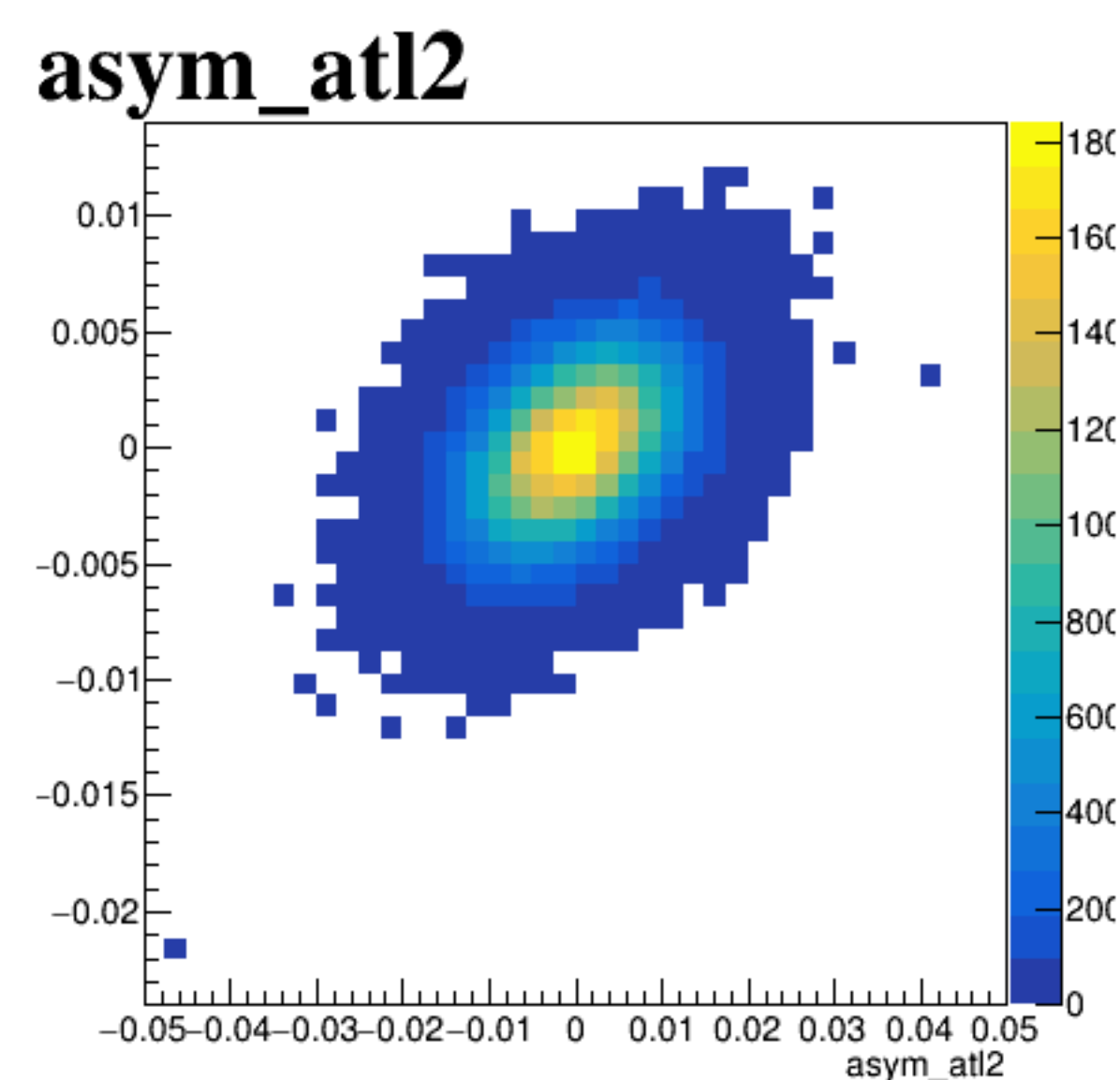
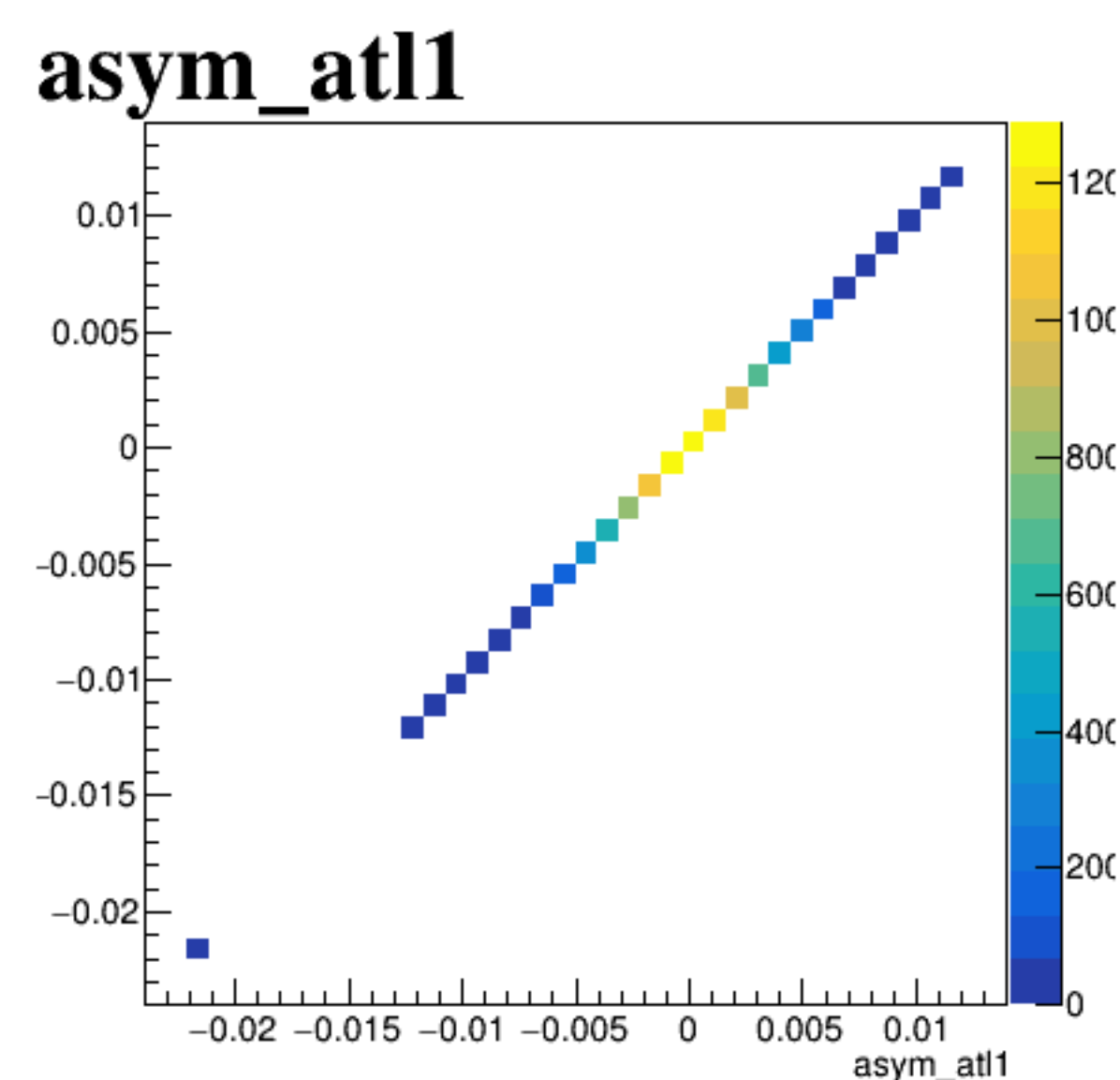
asym\_atr2/ppm:pattern\_number {ErrorFlag==0}



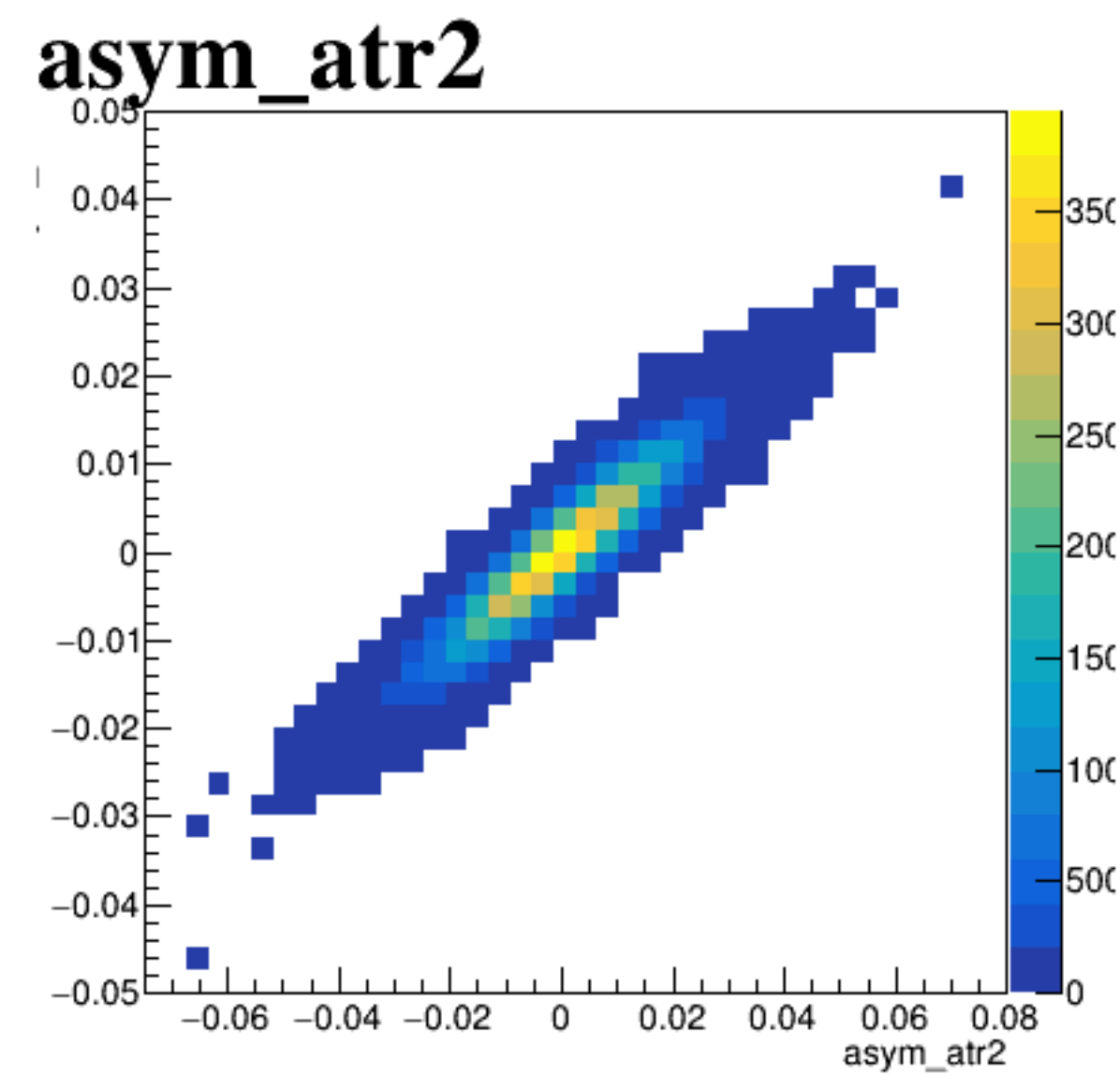
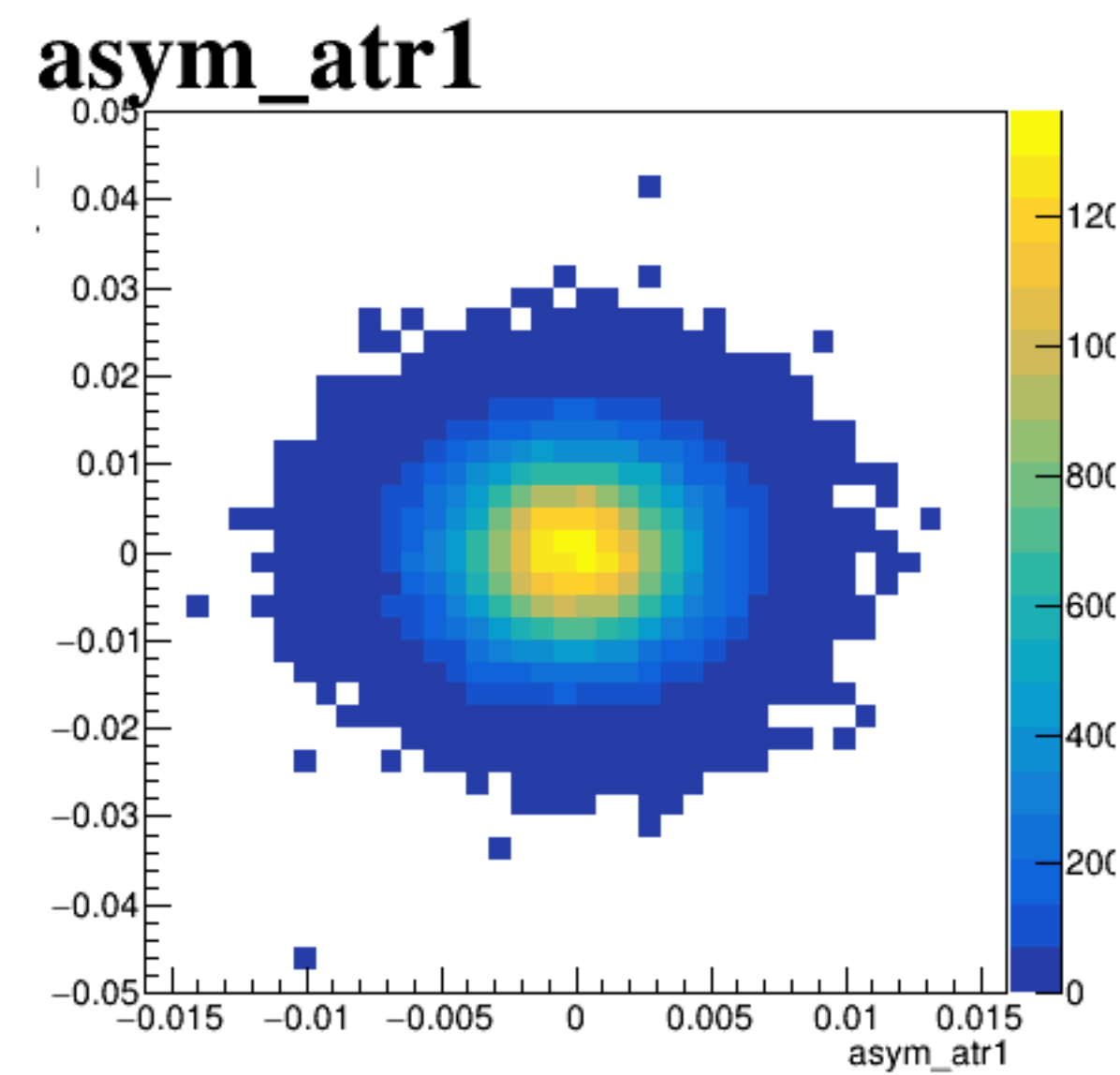
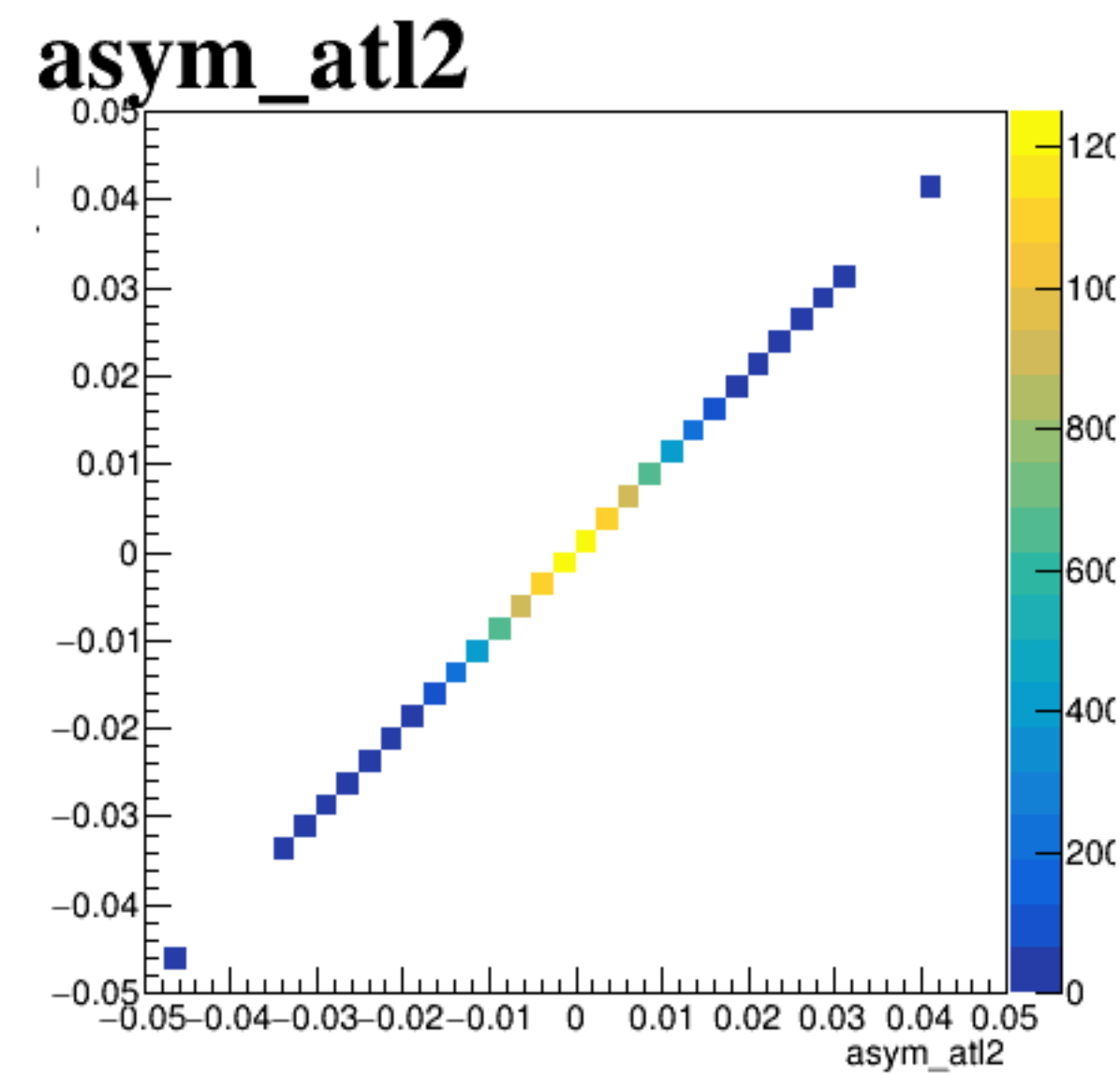
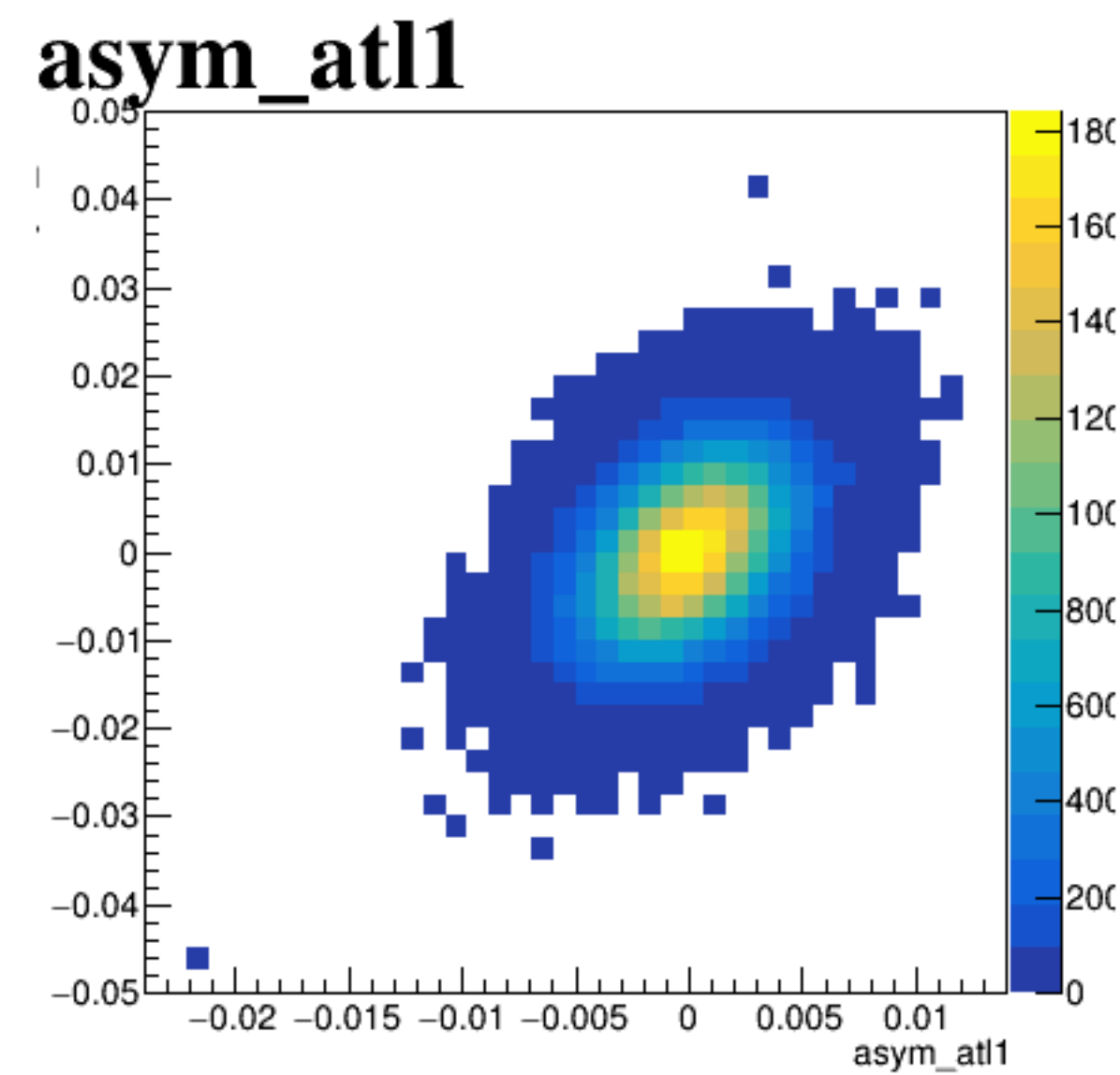
asym\_atr2



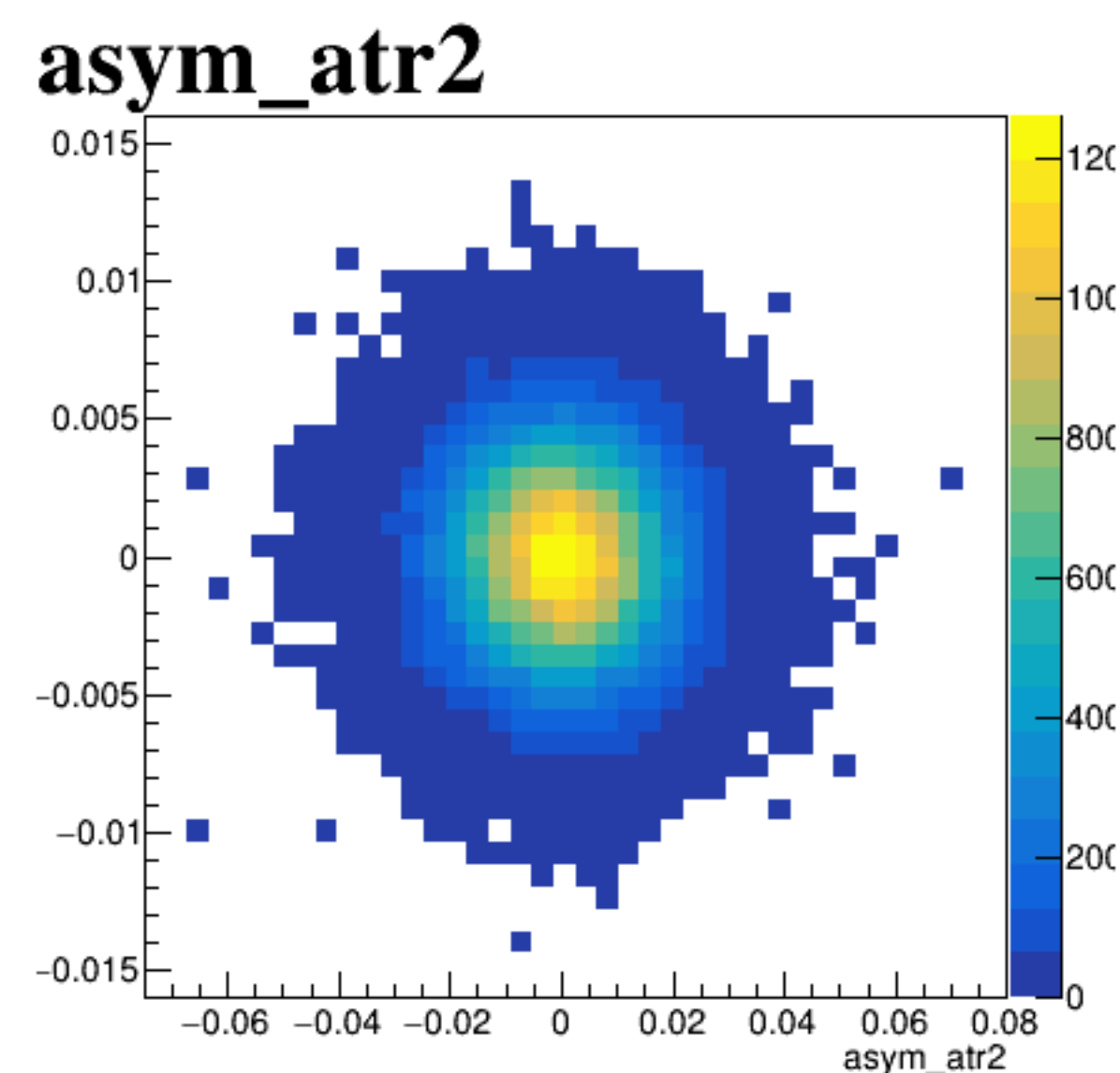
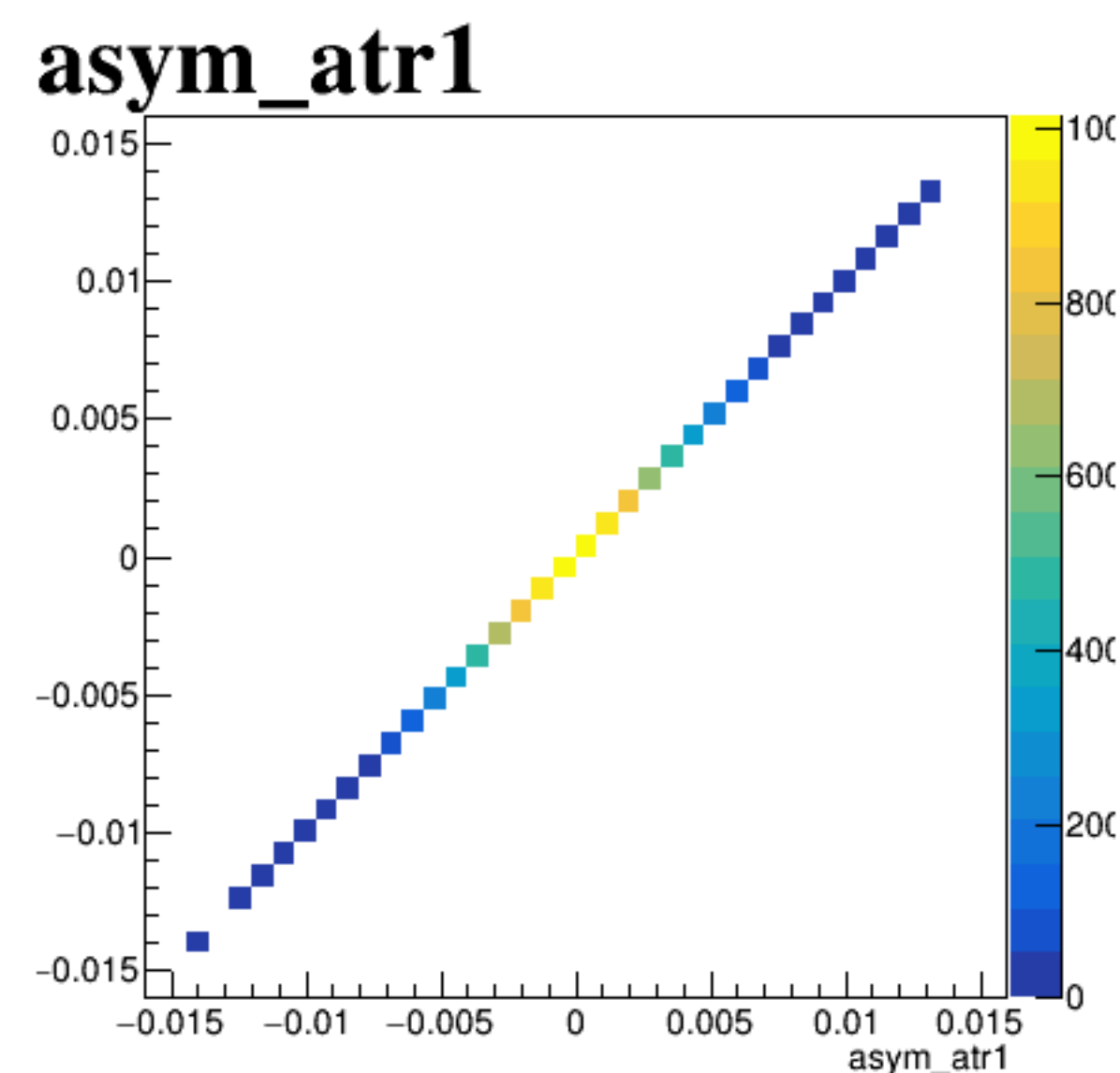
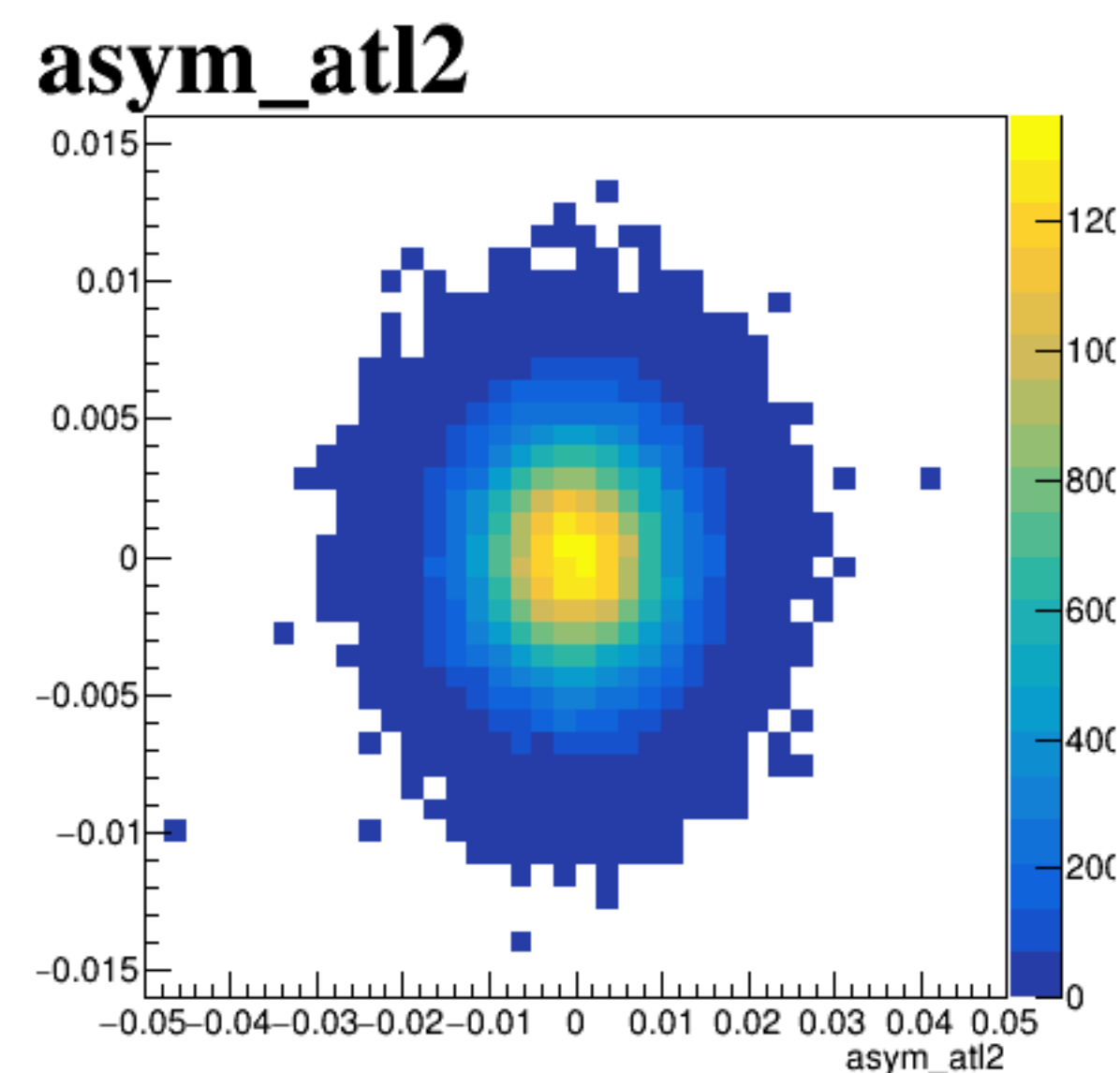
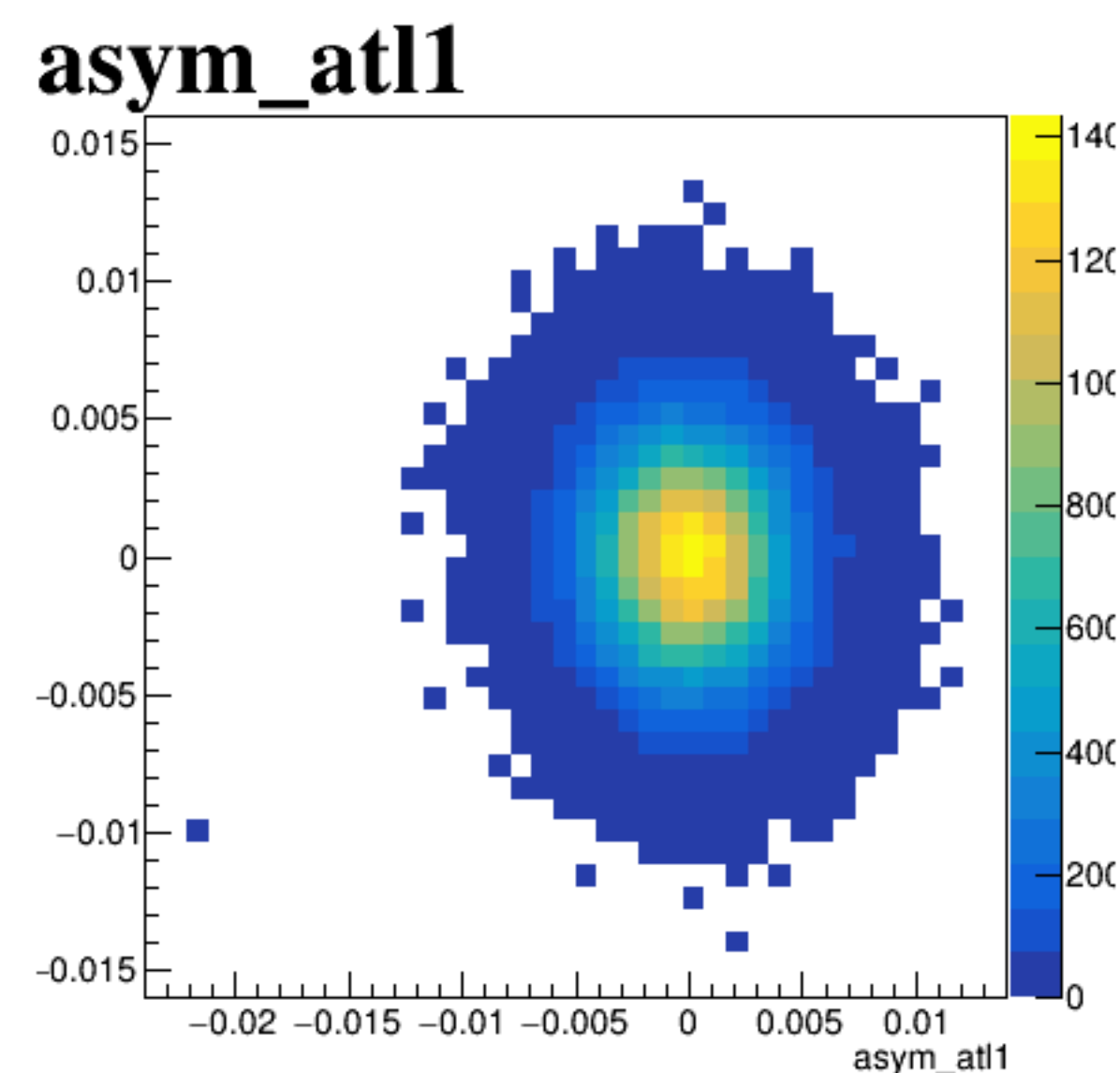
## asym\_atl1



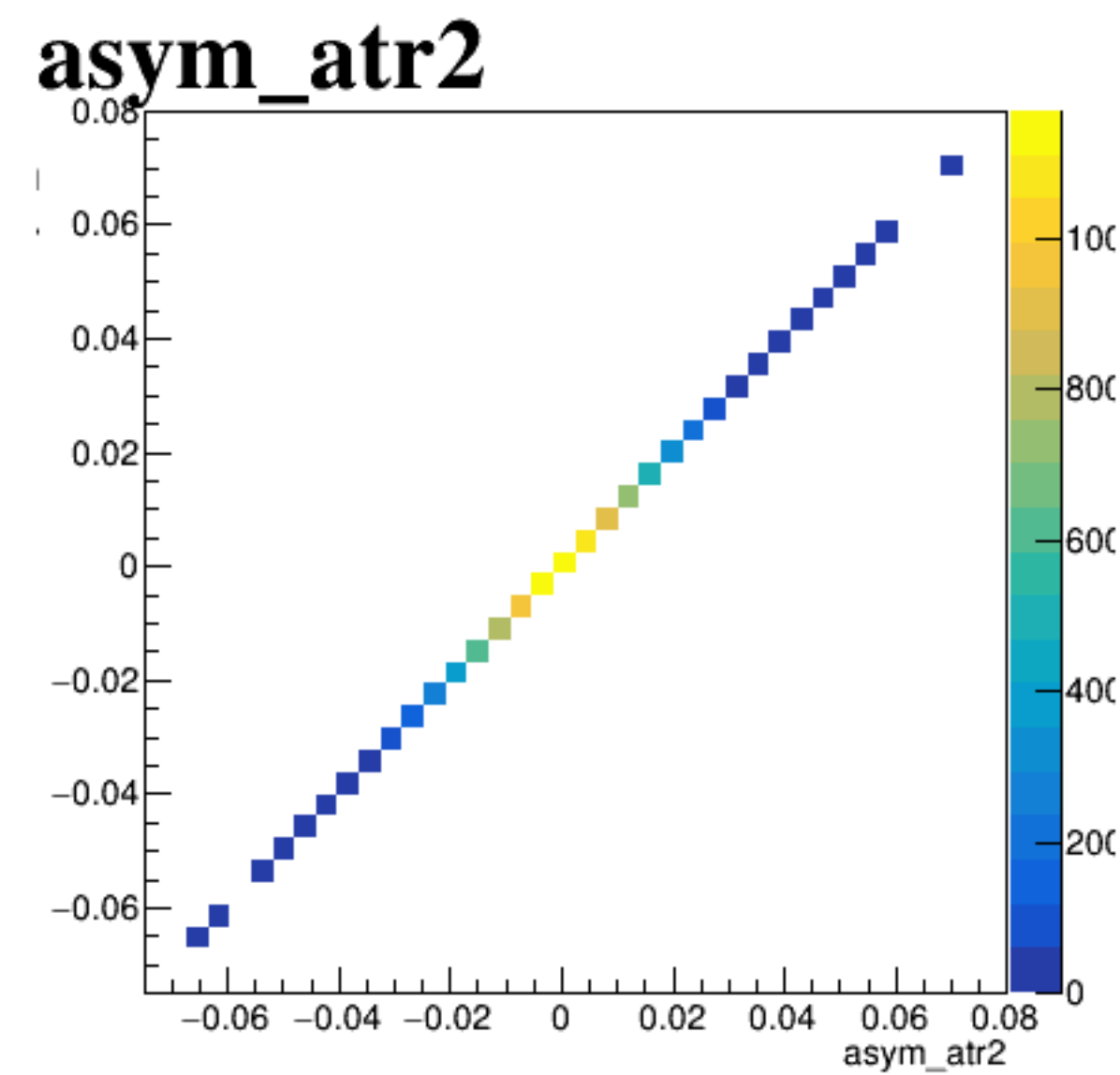
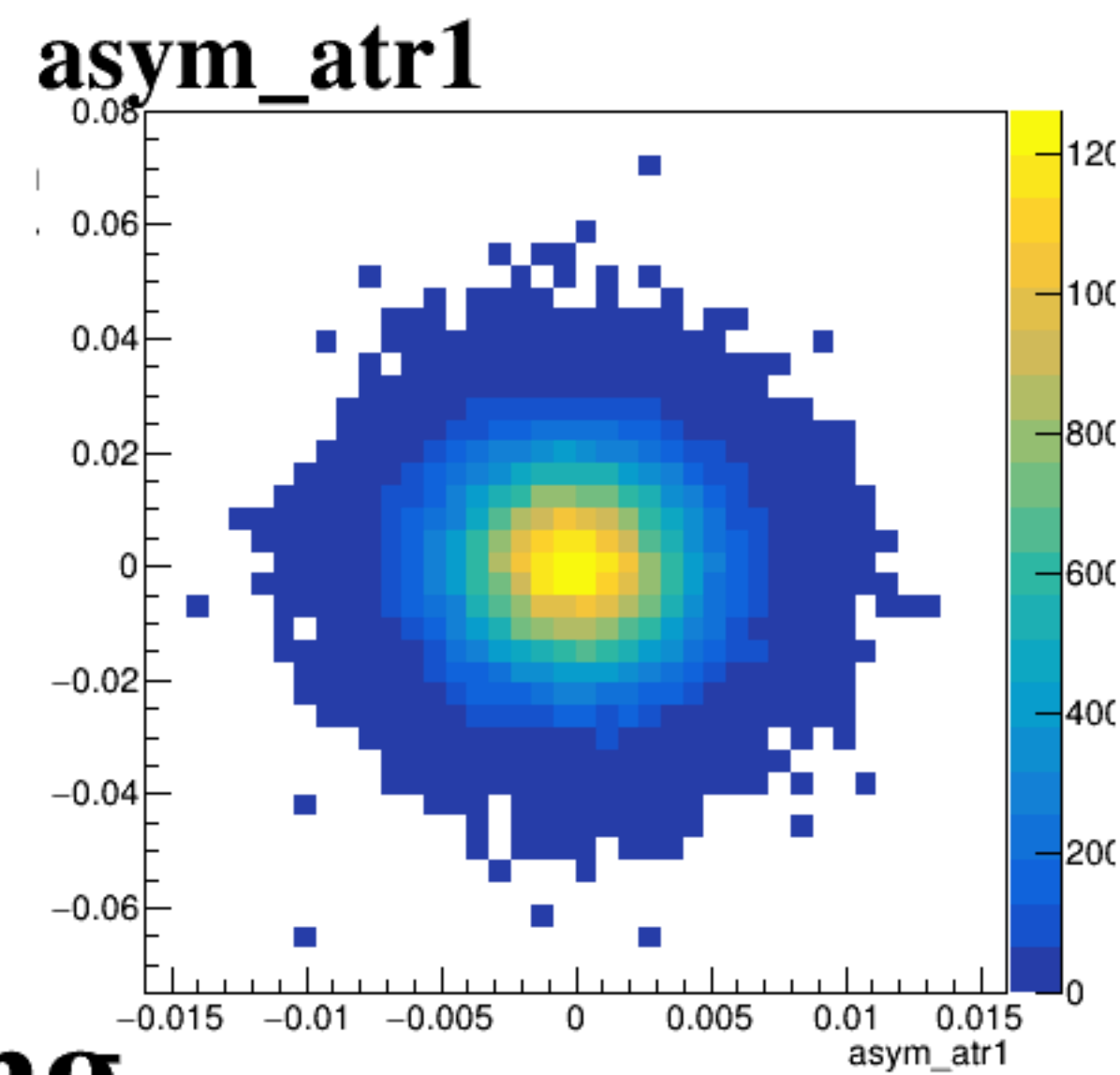
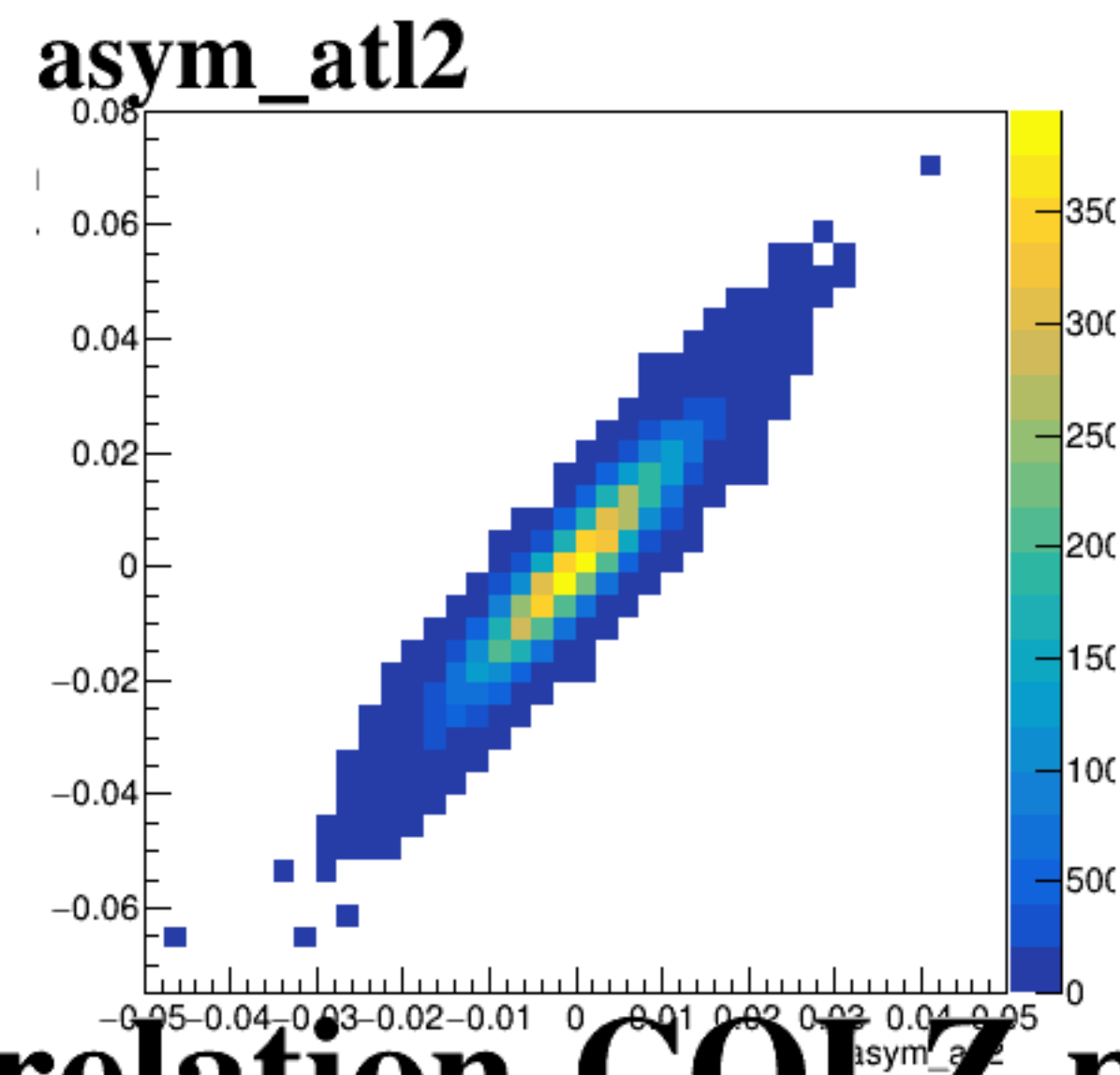
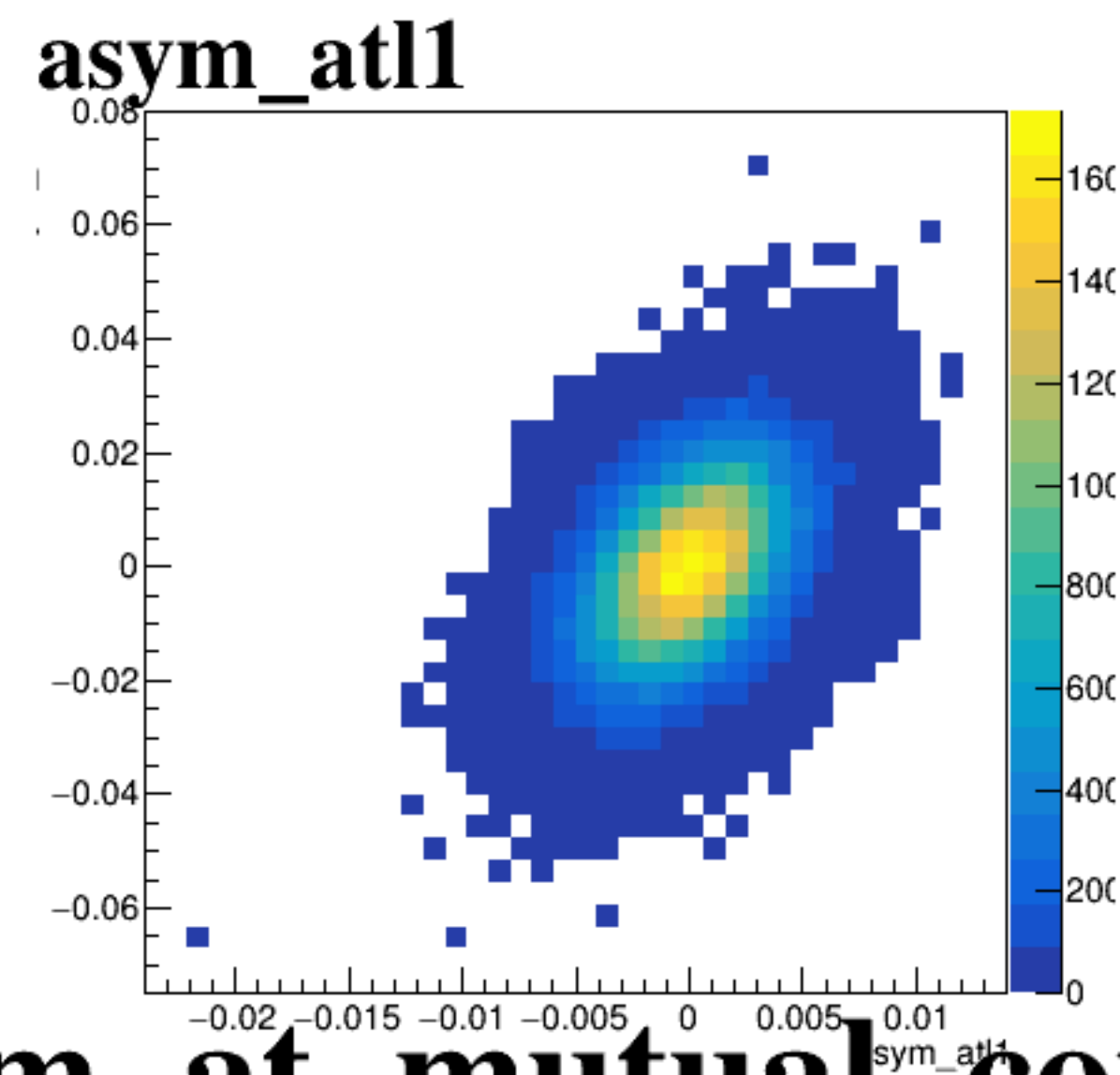
## asym\_atl2



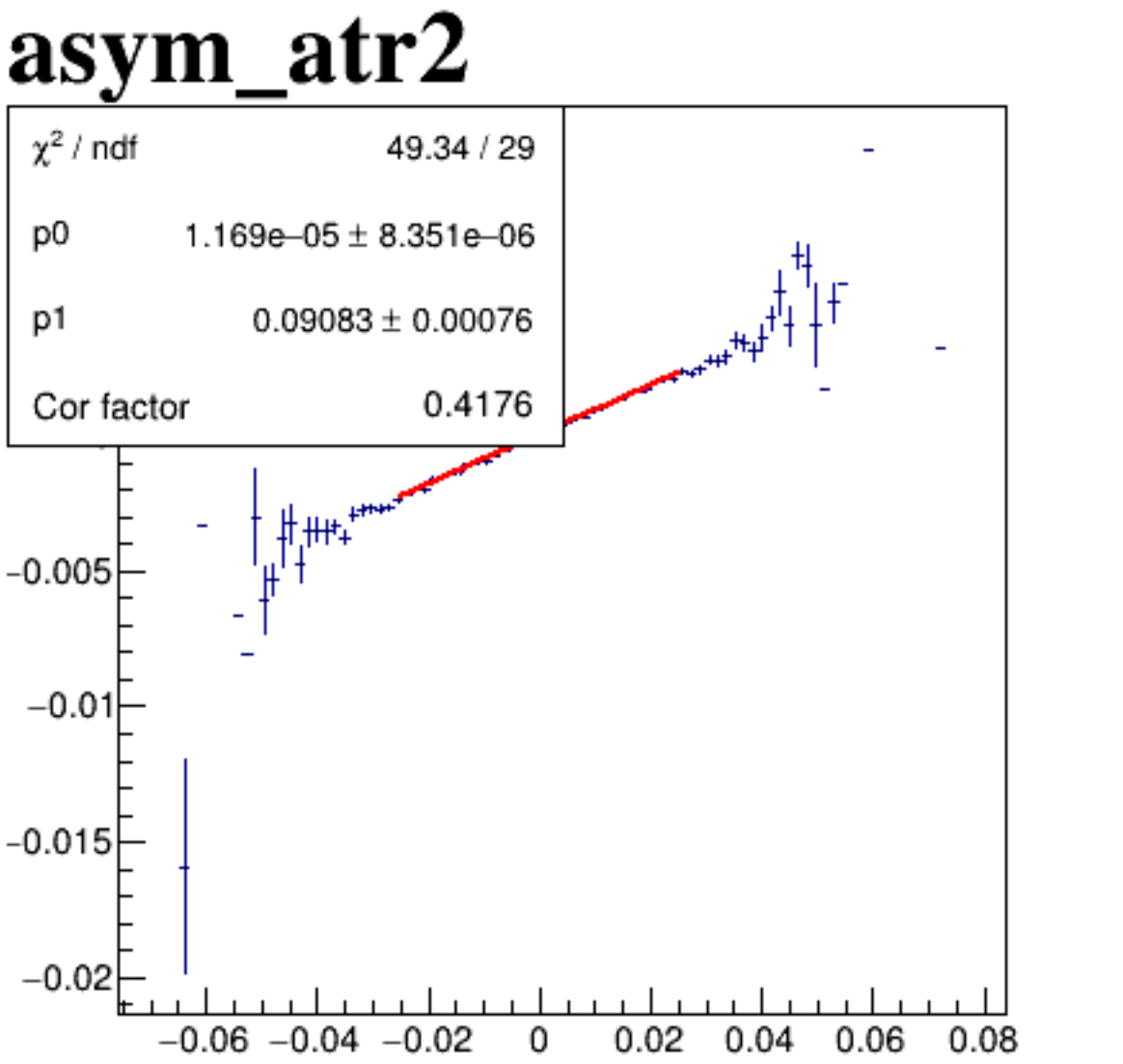
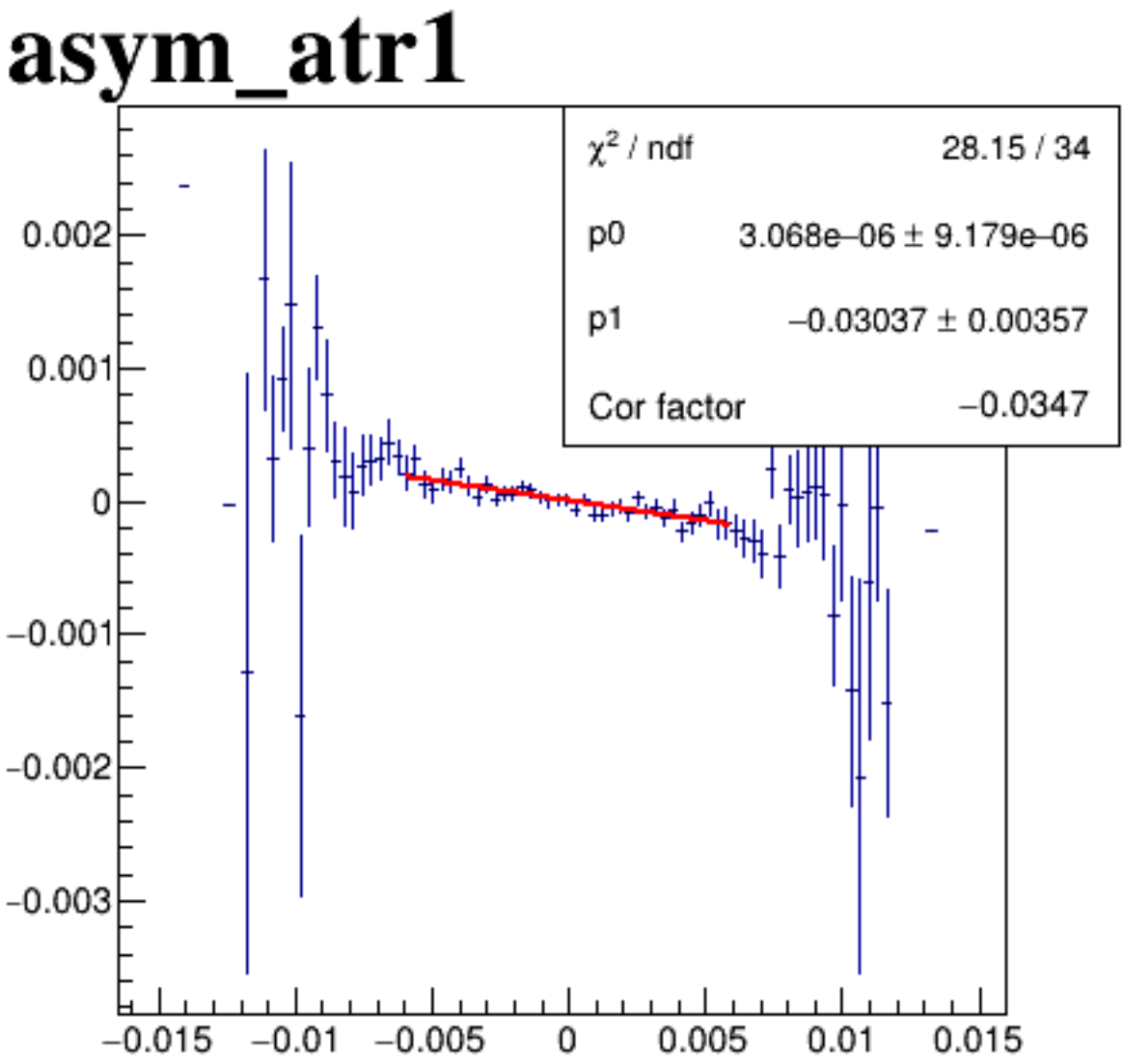
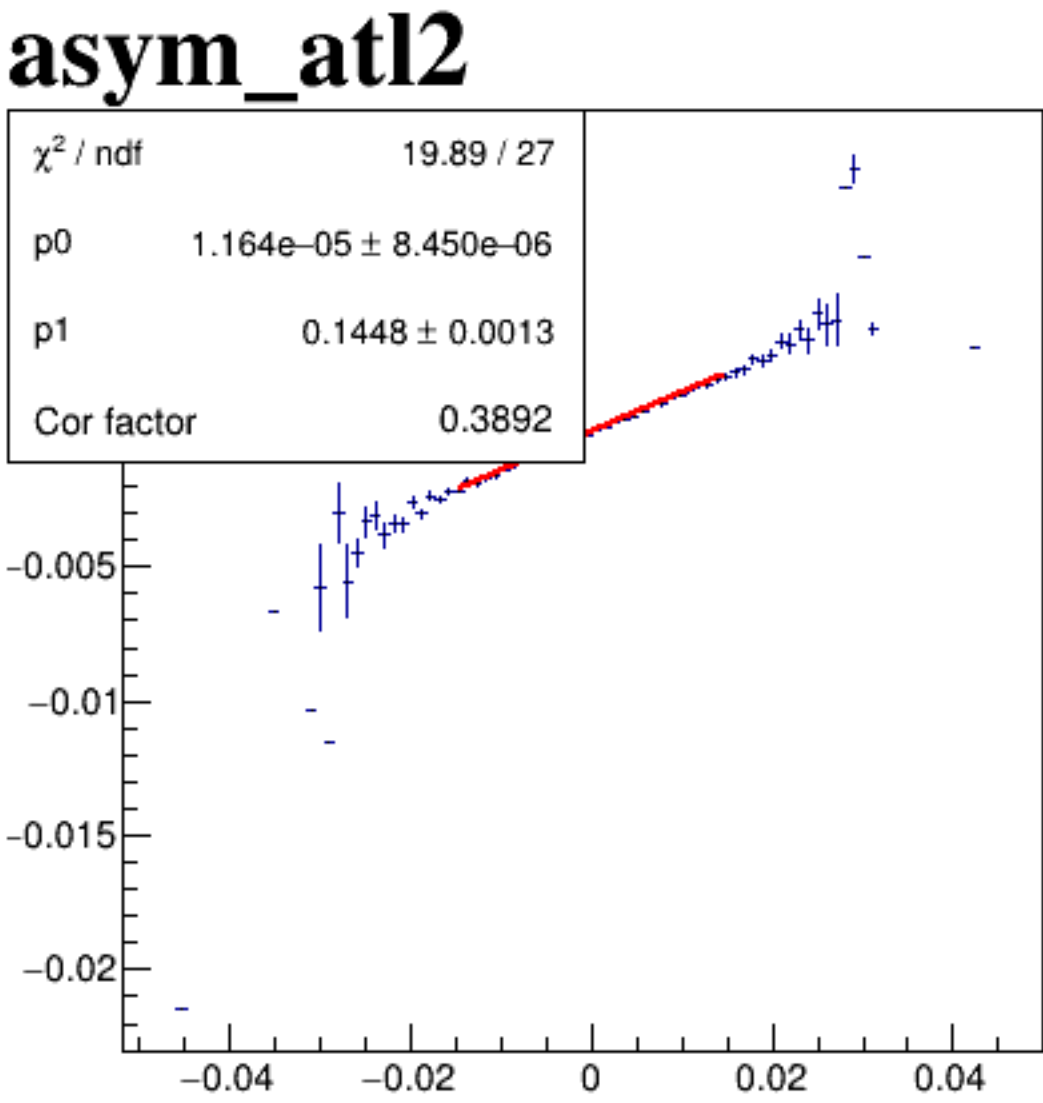
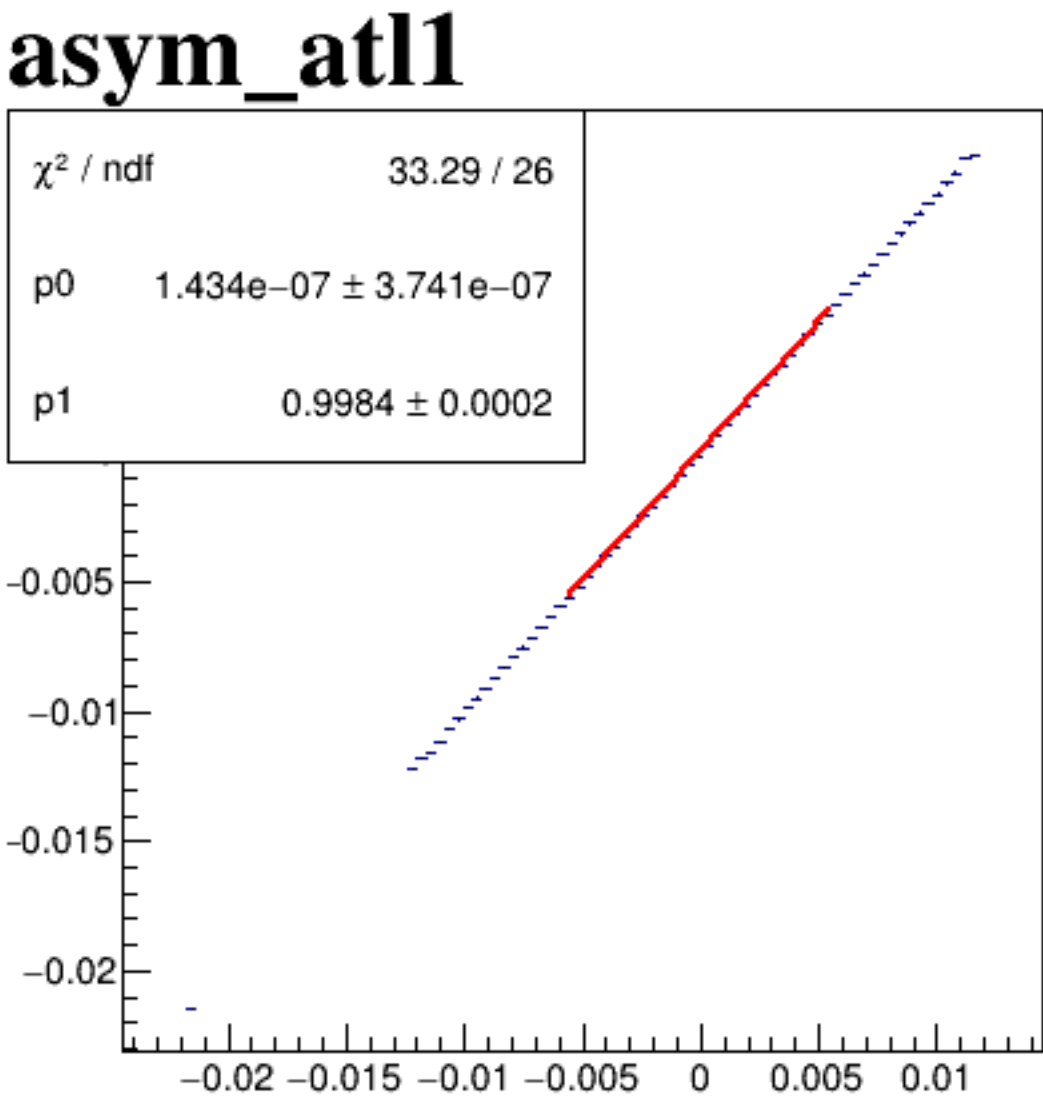
## asym\_atr1



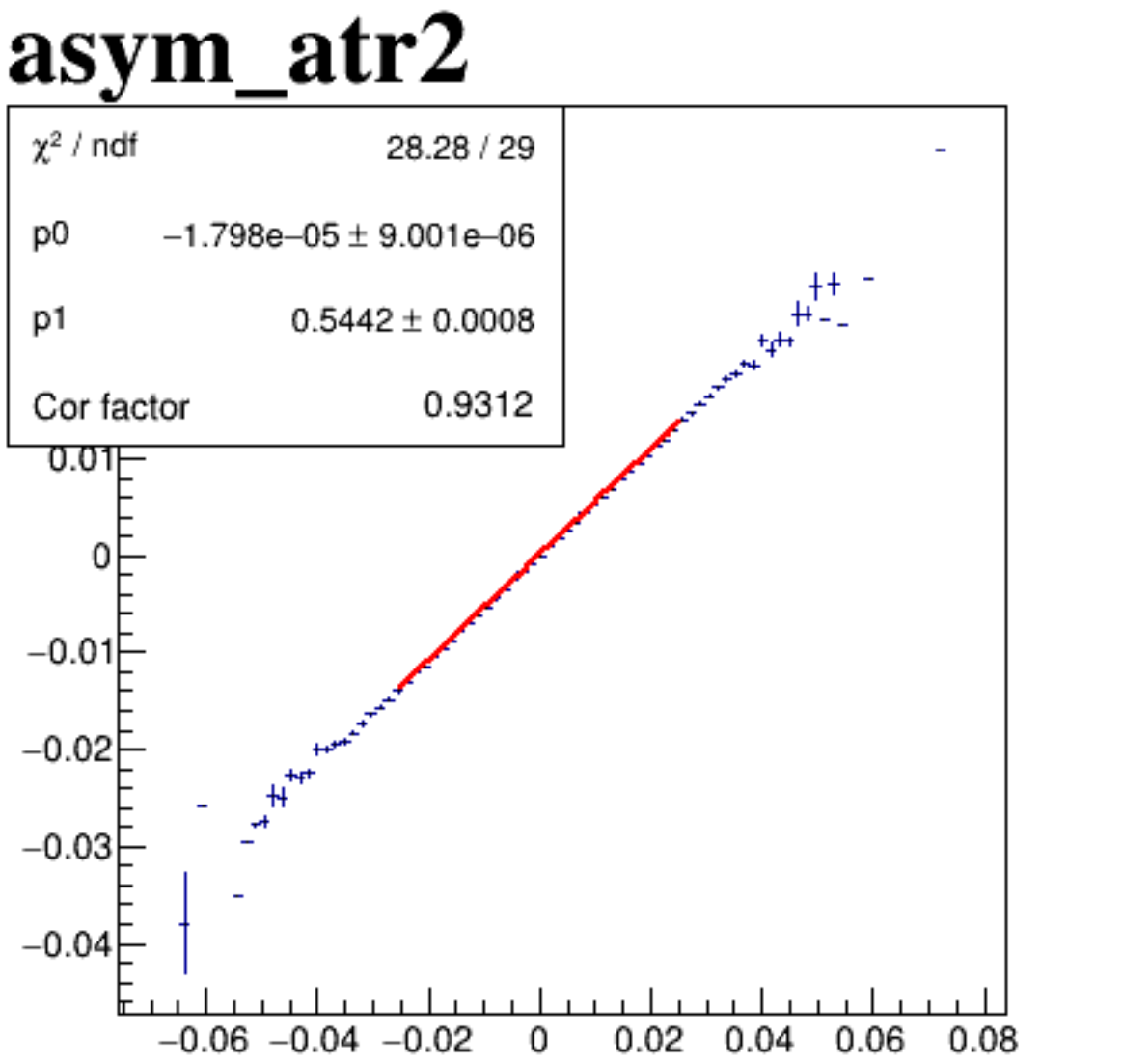
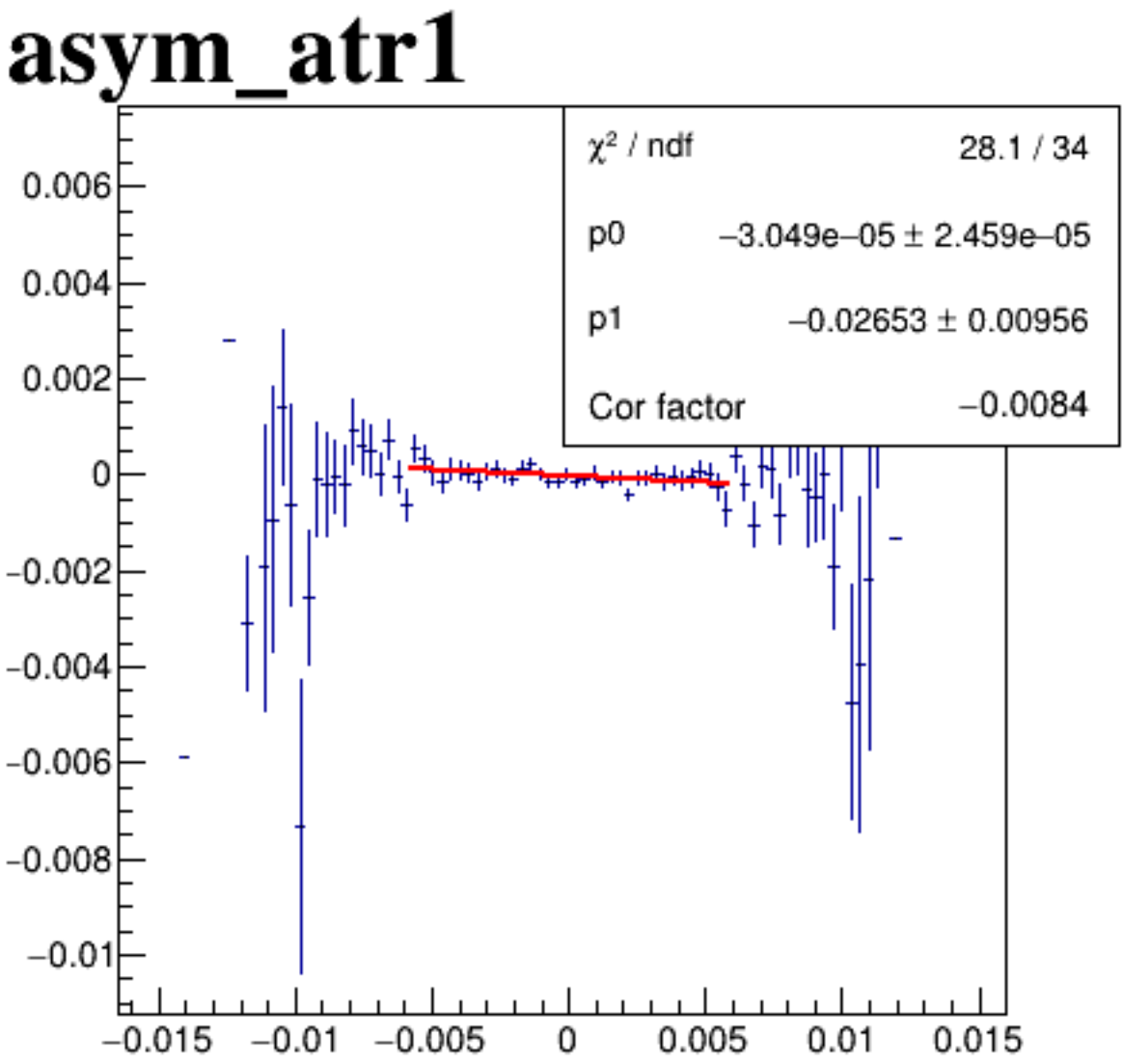
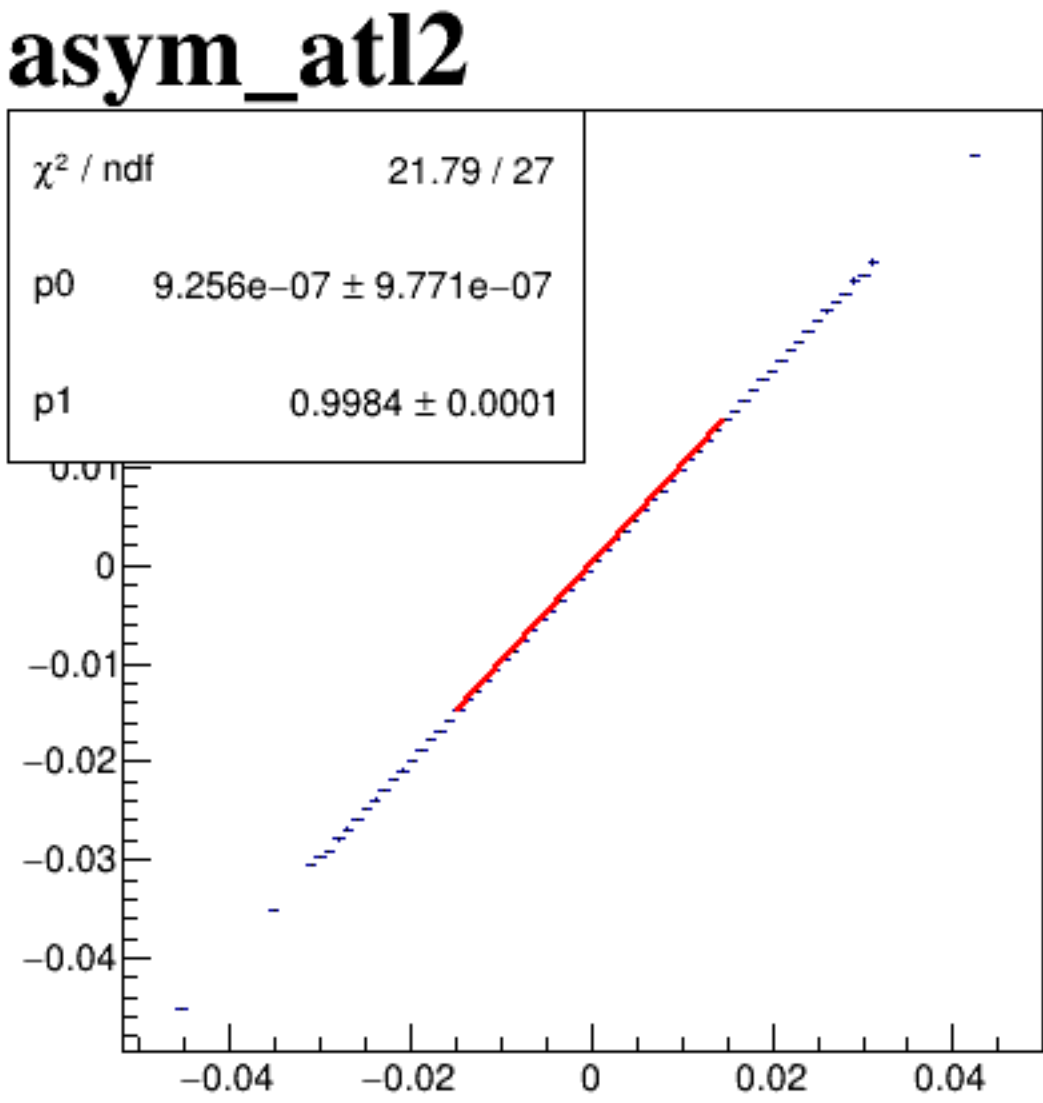
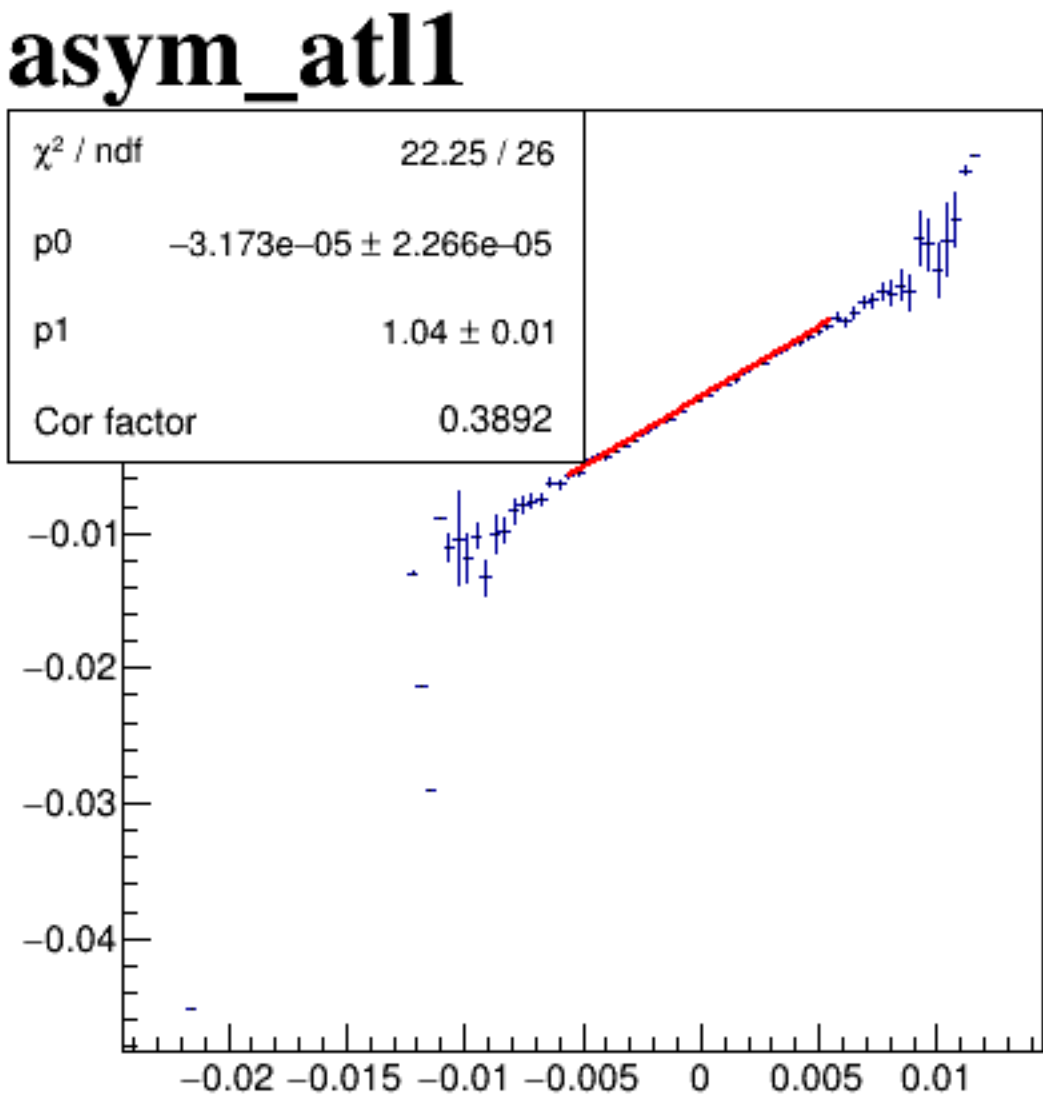
## asym\_atr2



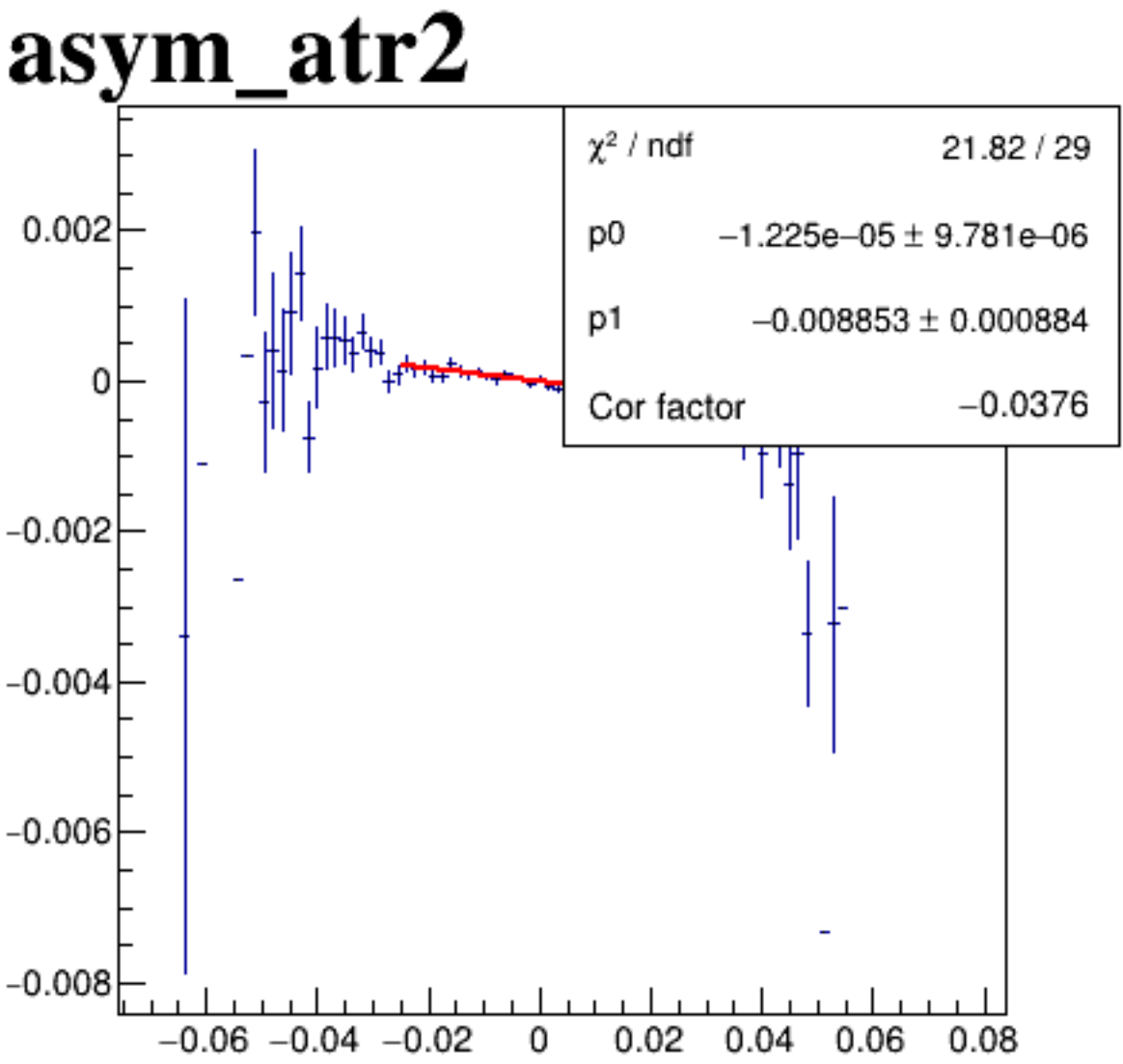
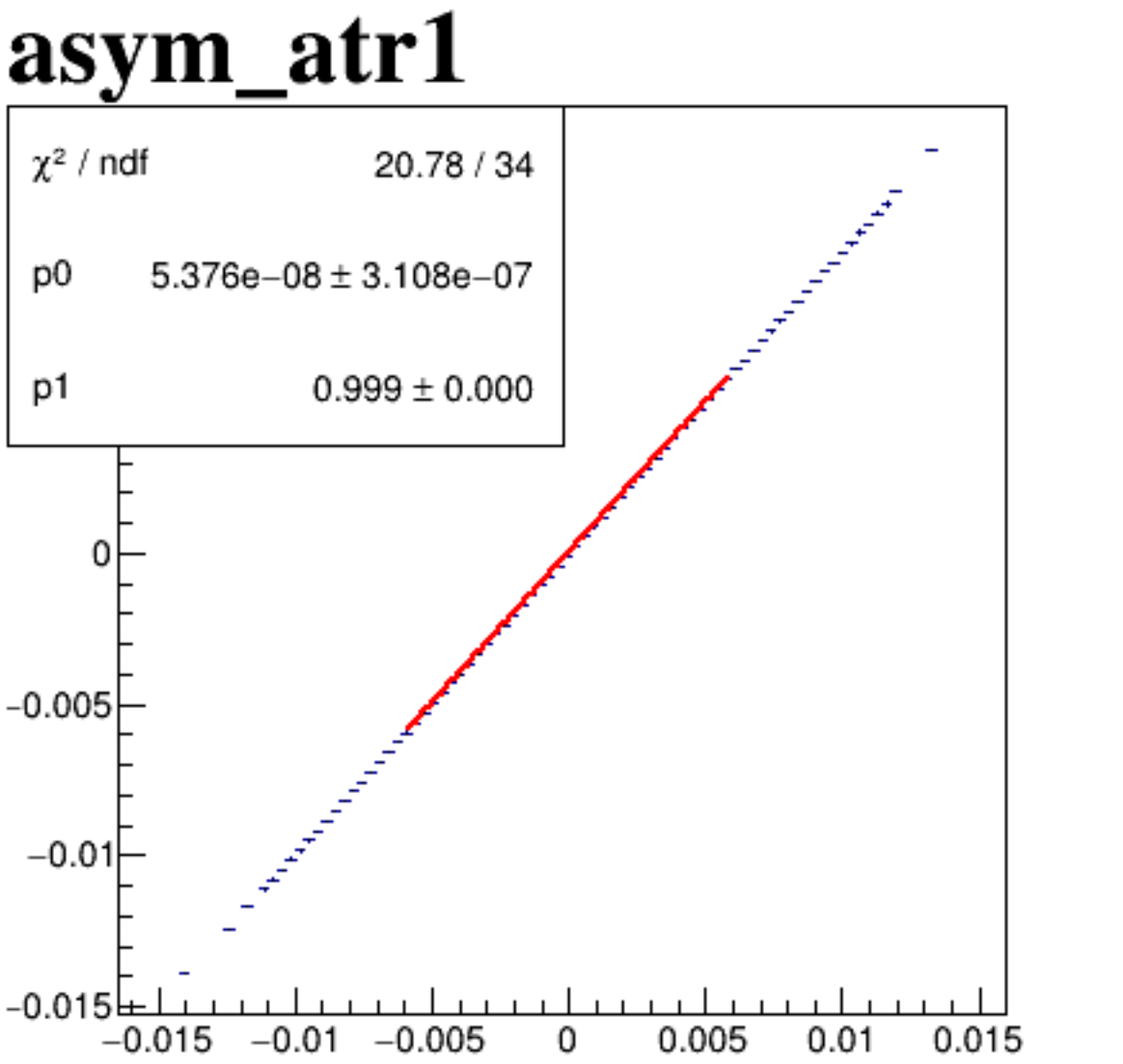
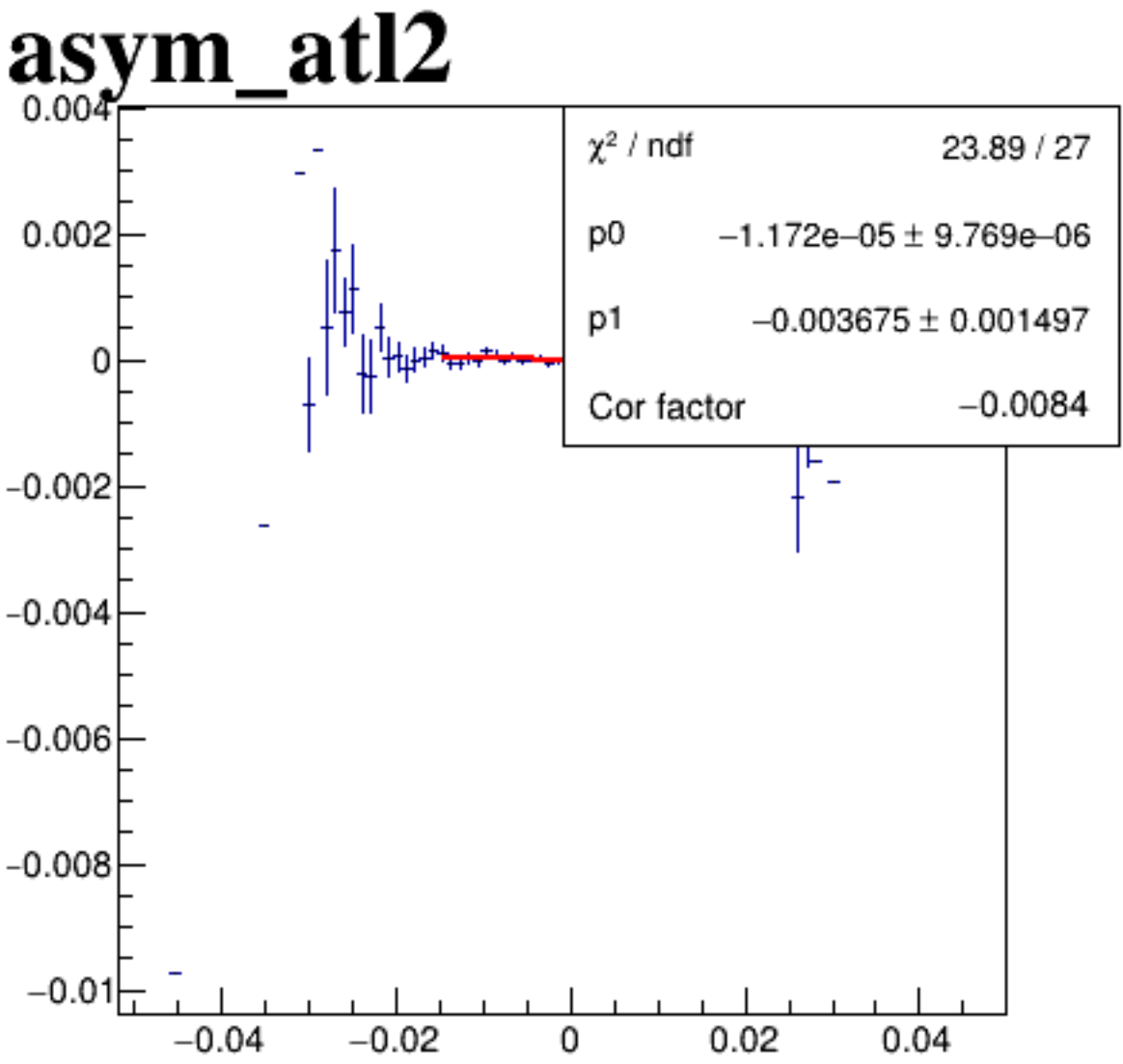
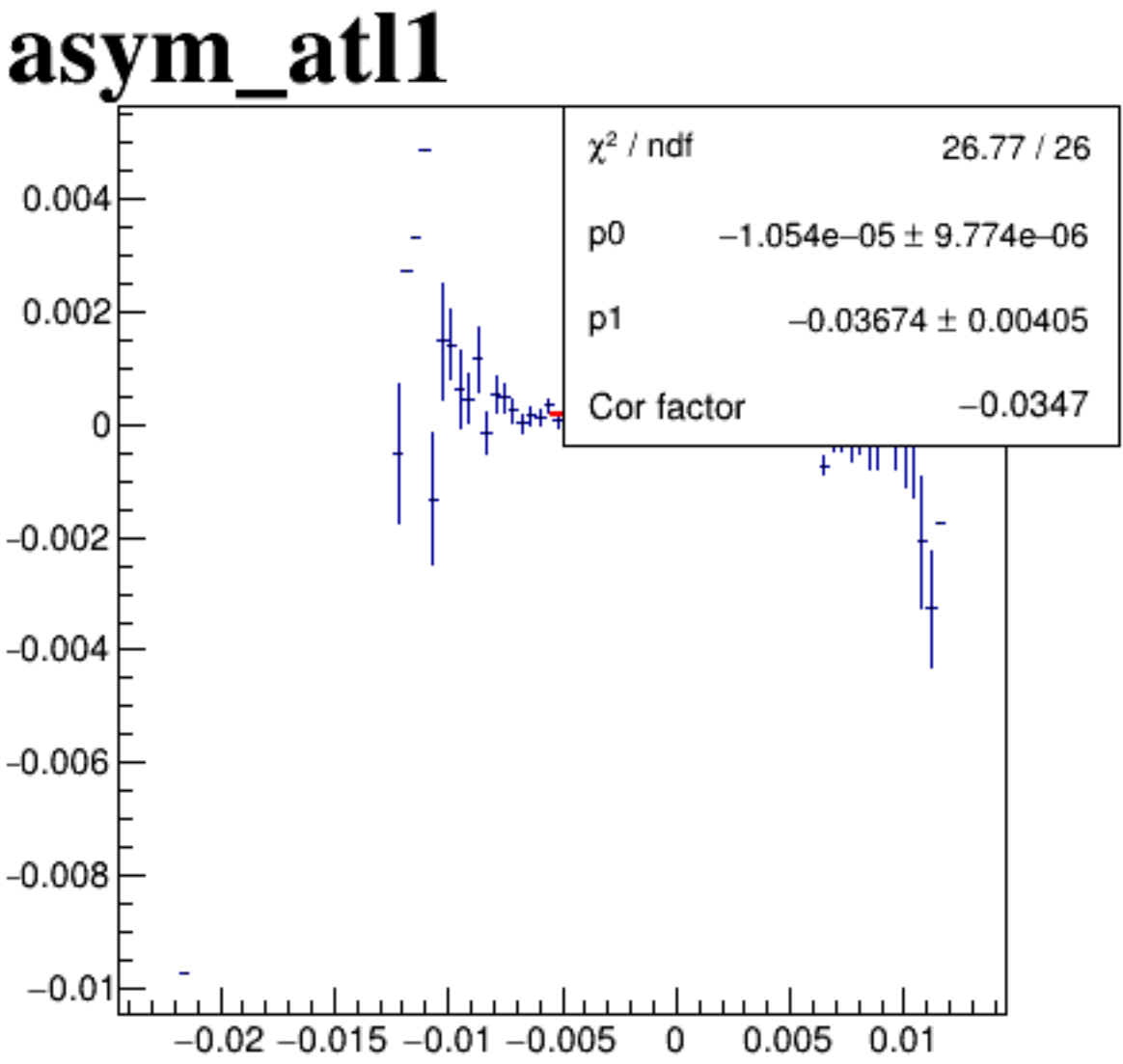
asym\_atl1



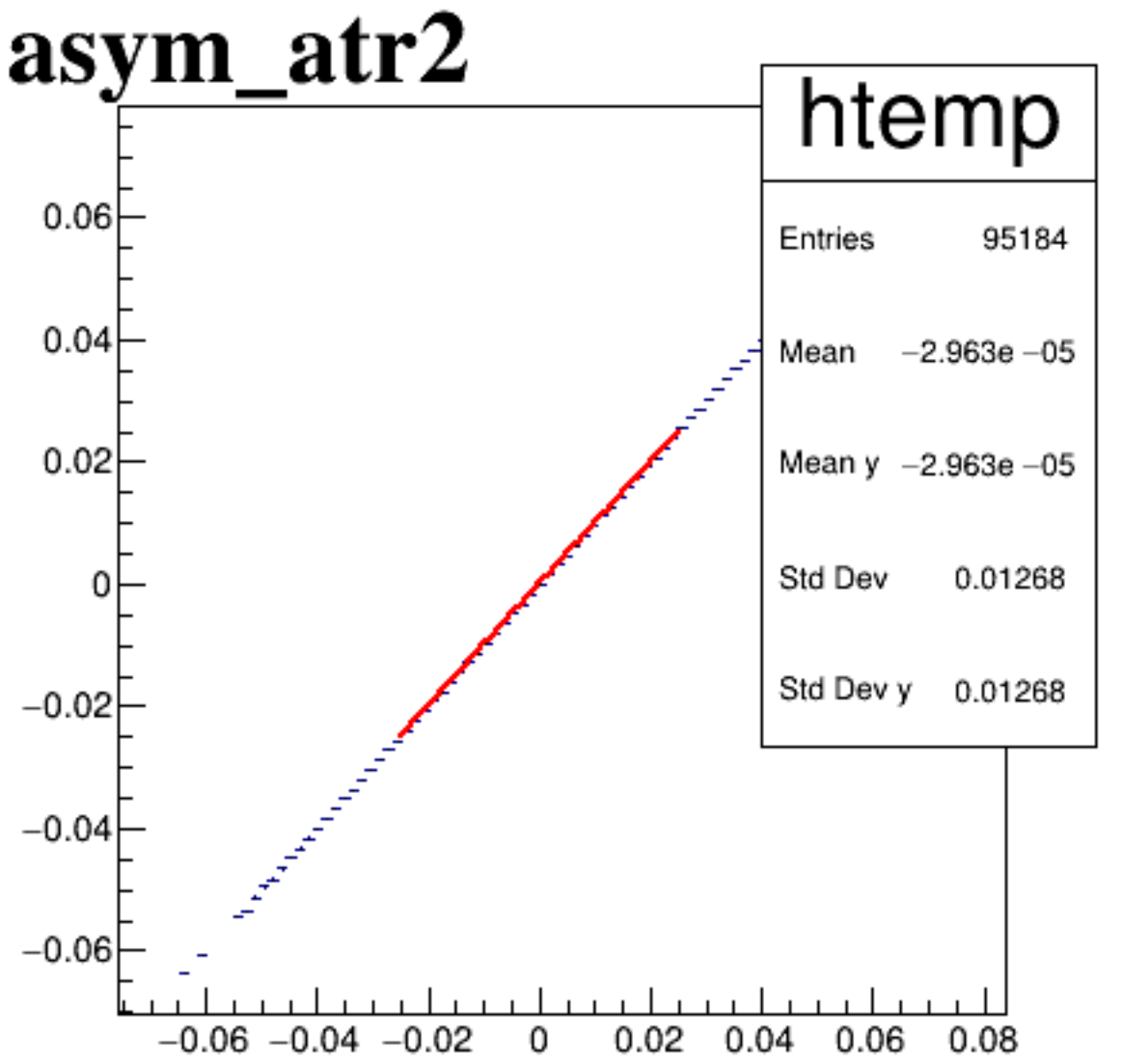
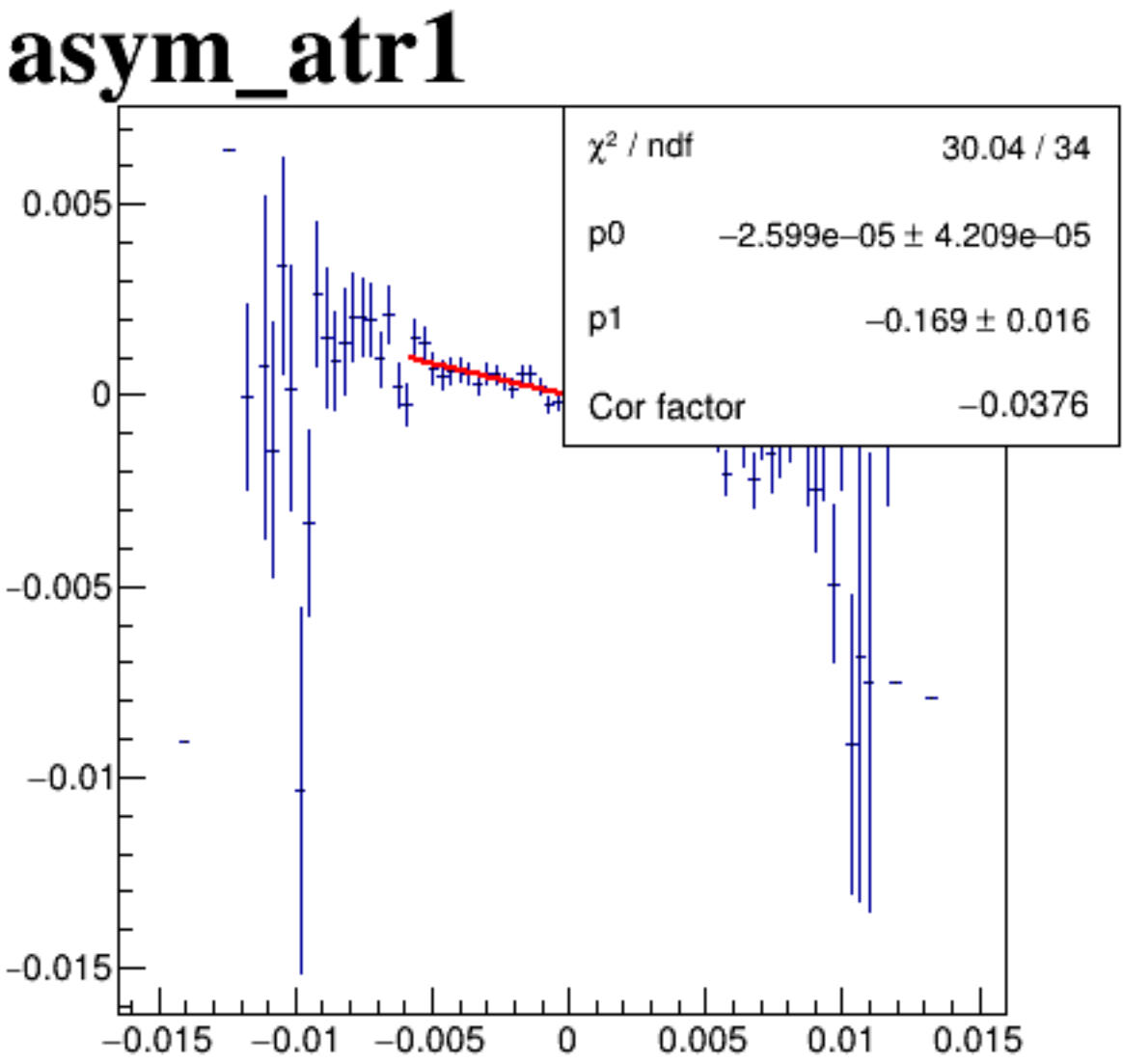
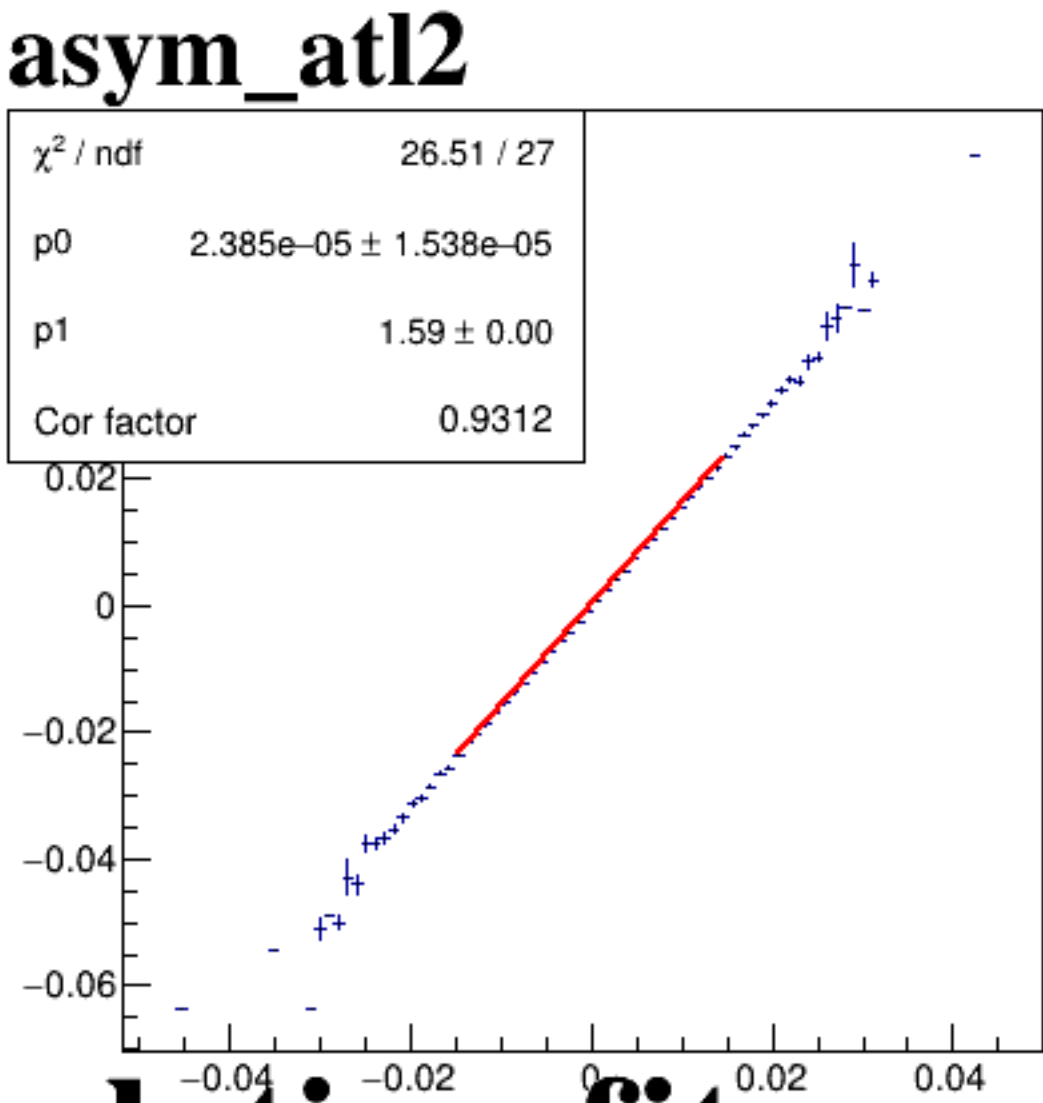
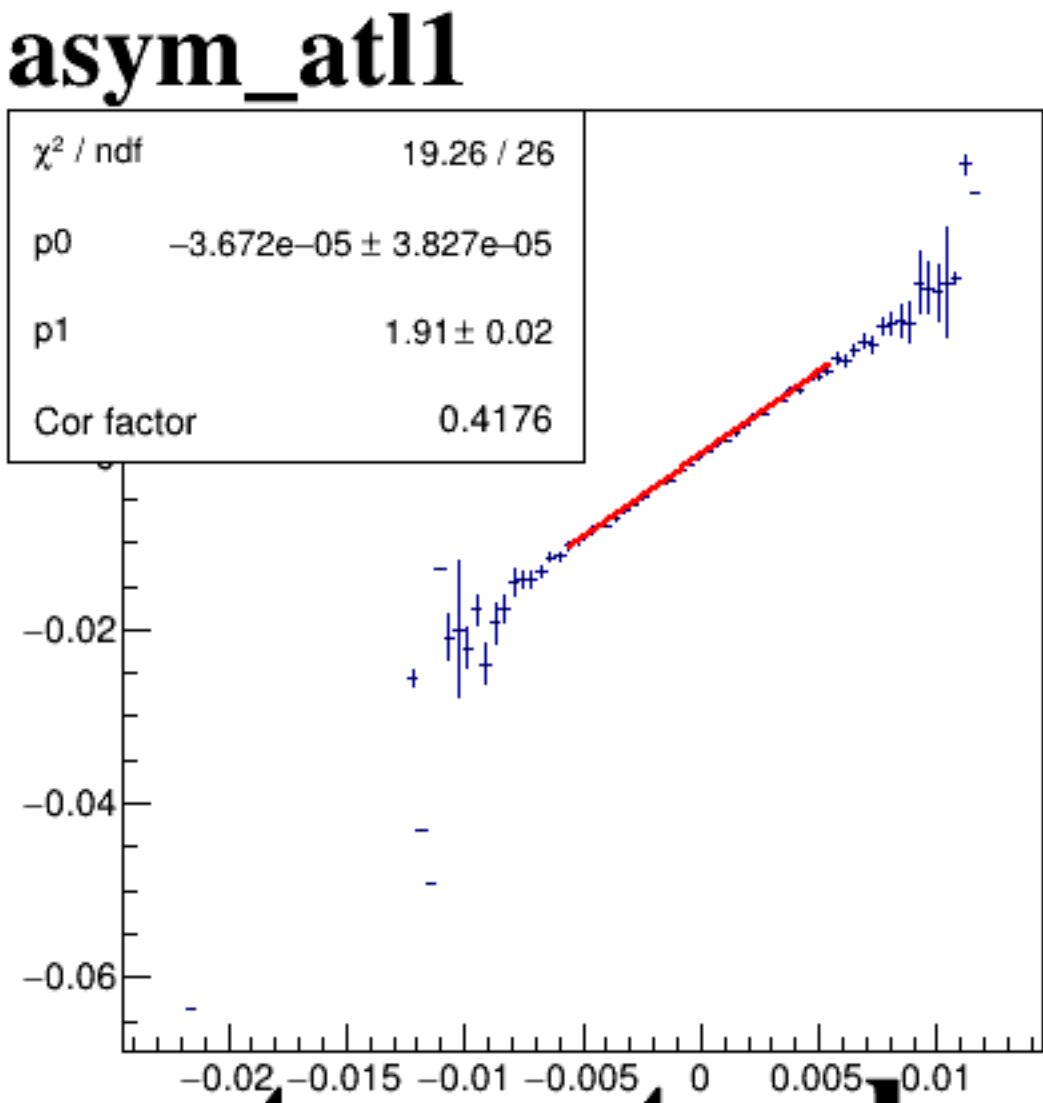
asym\_atl2



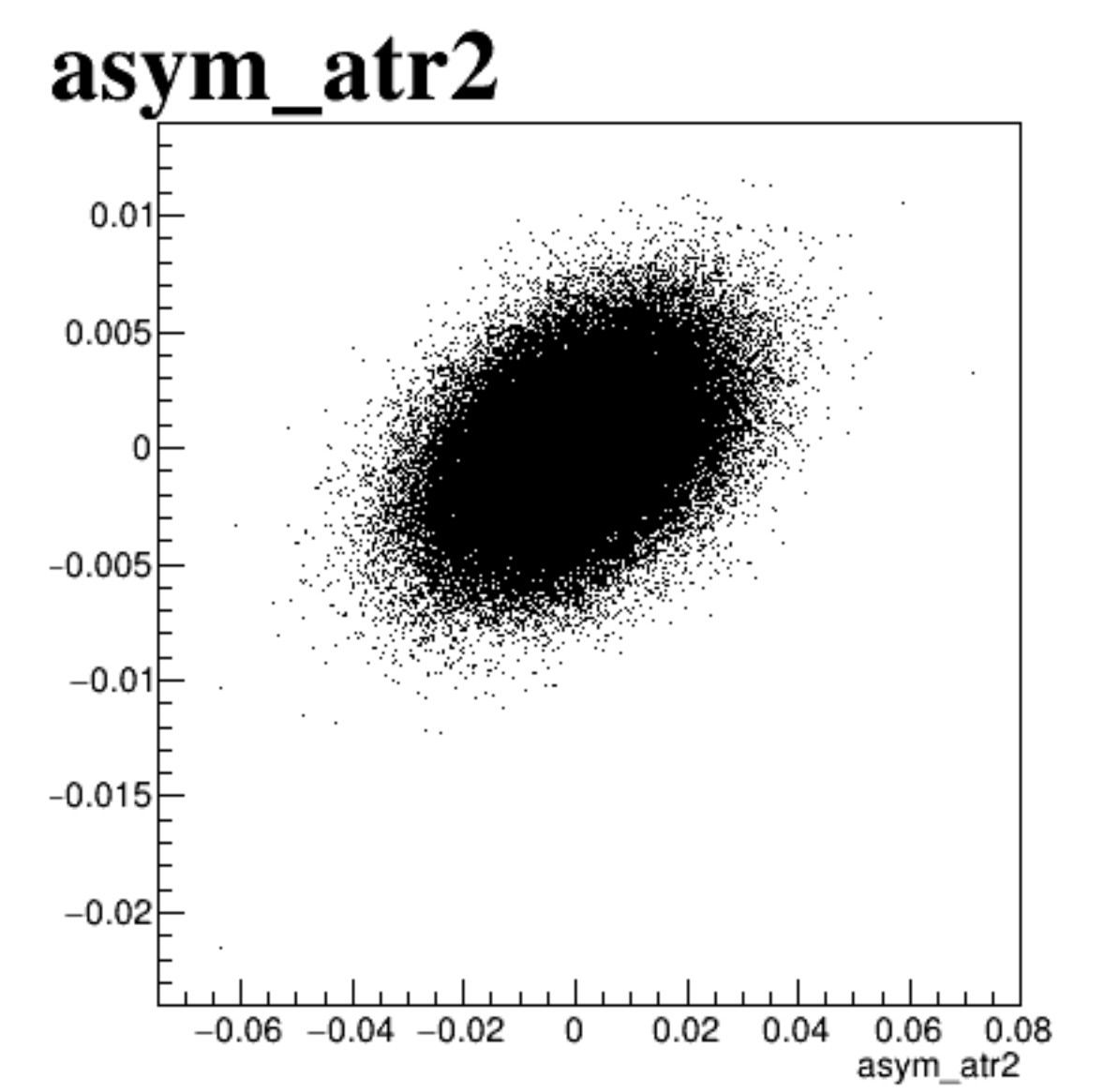
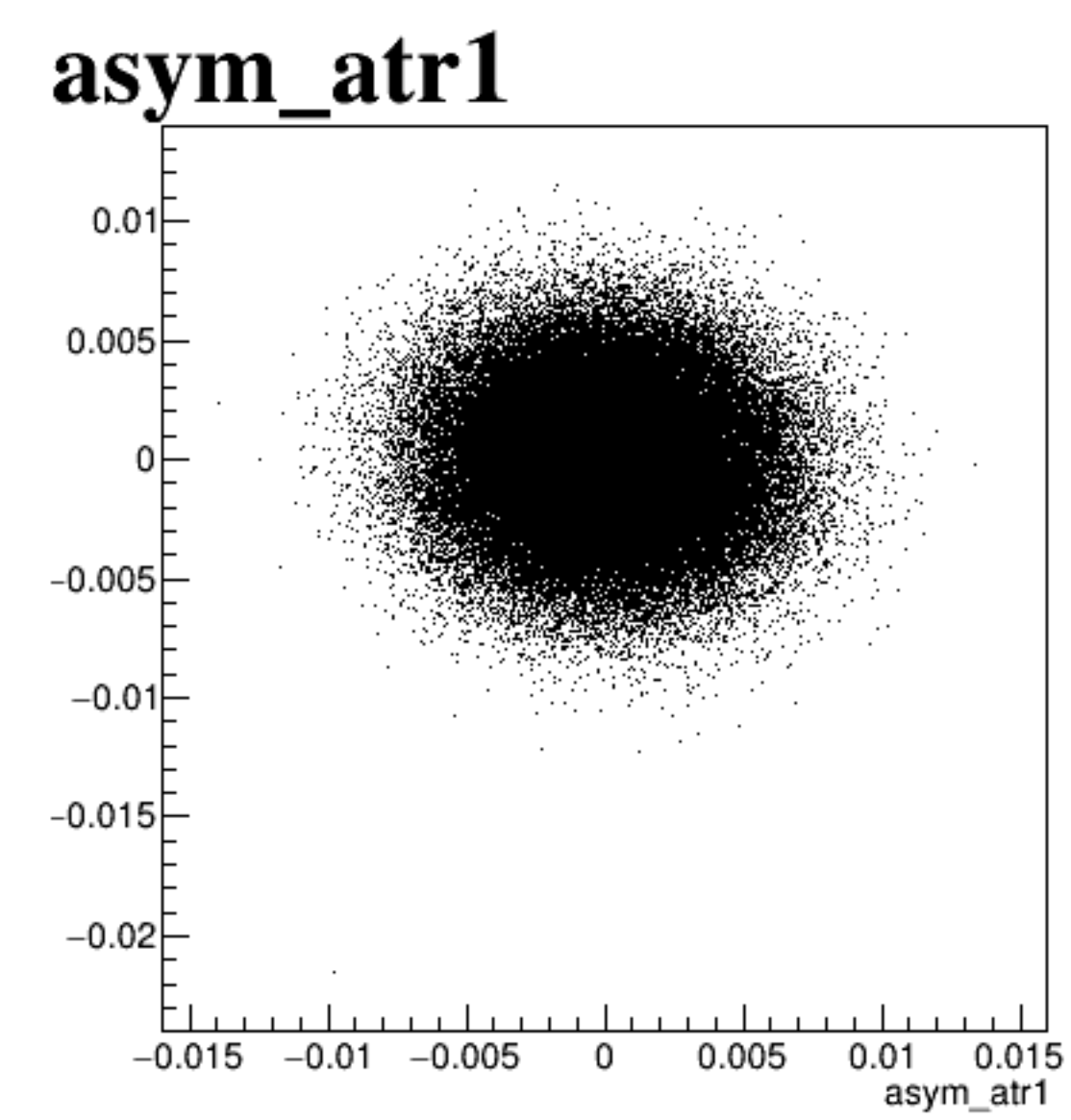
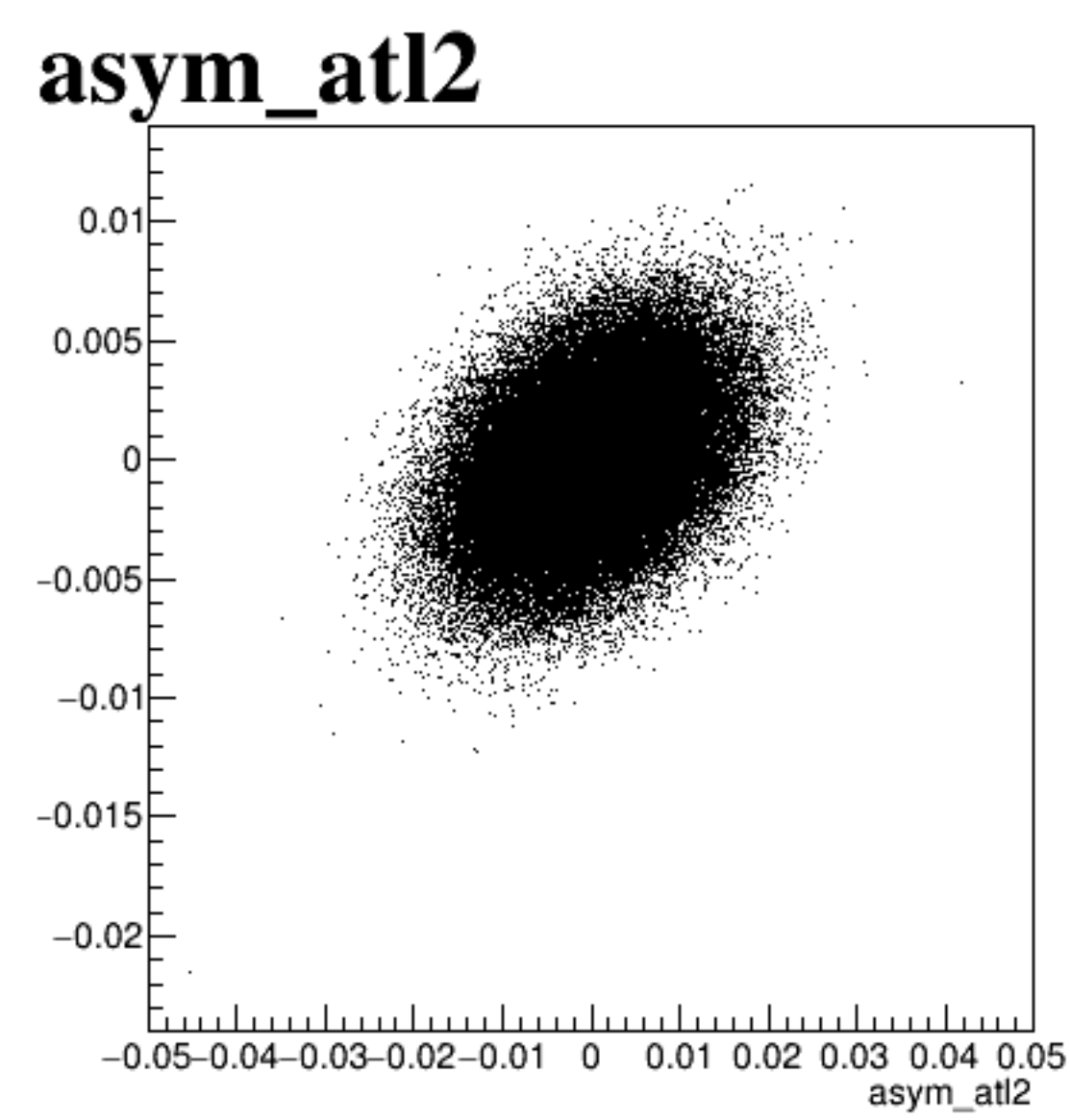
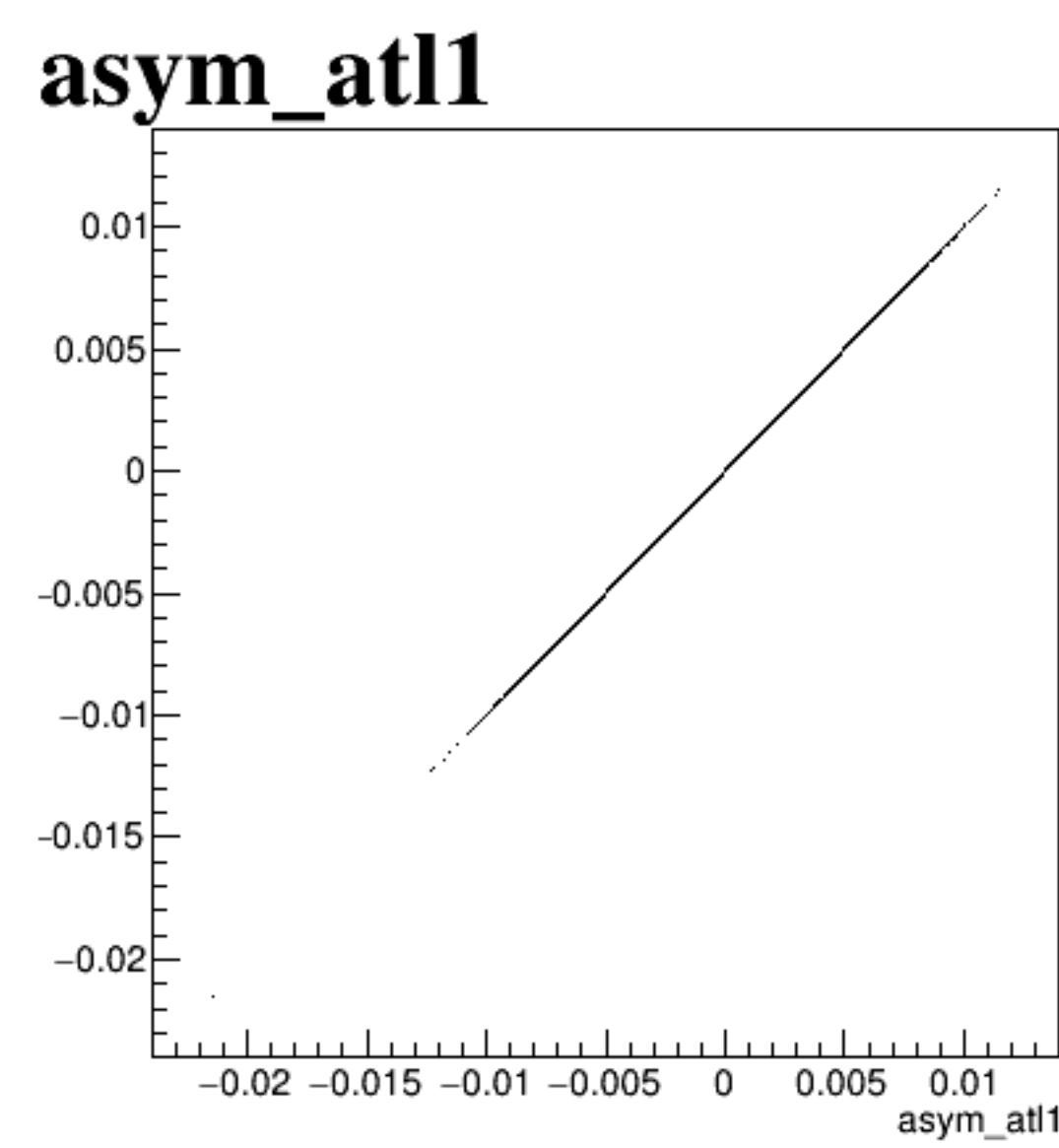
asym\_atr1



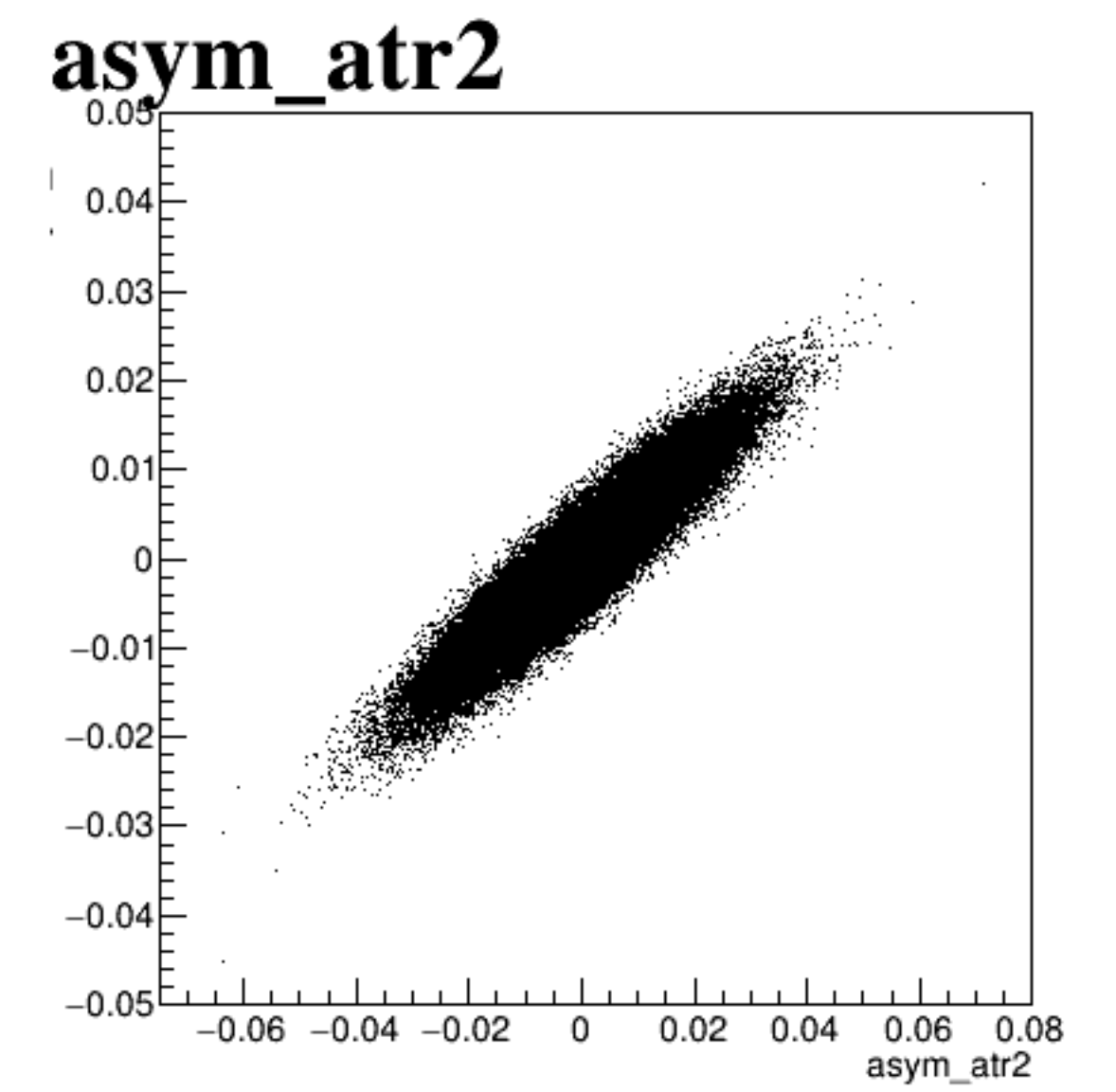
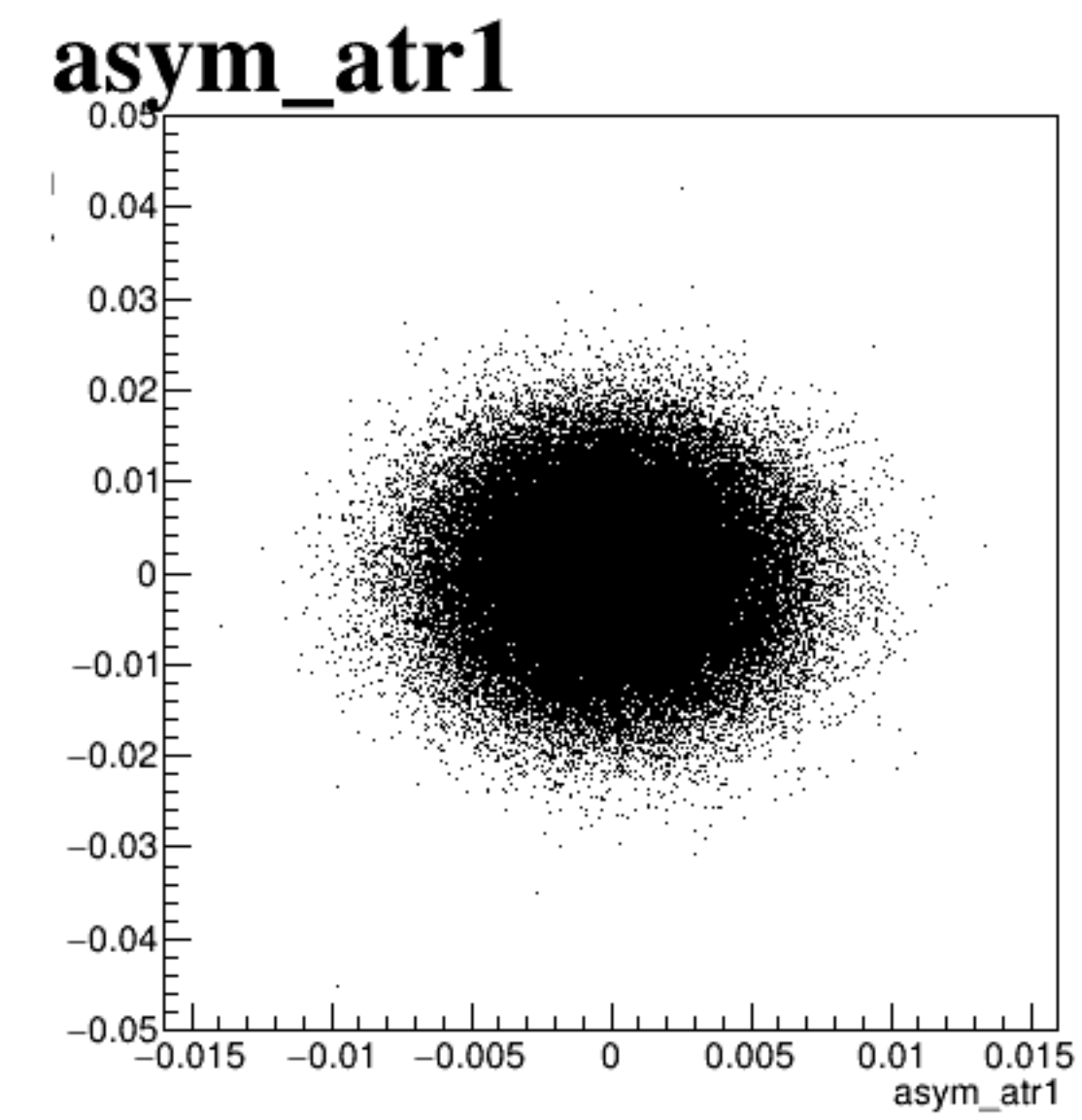
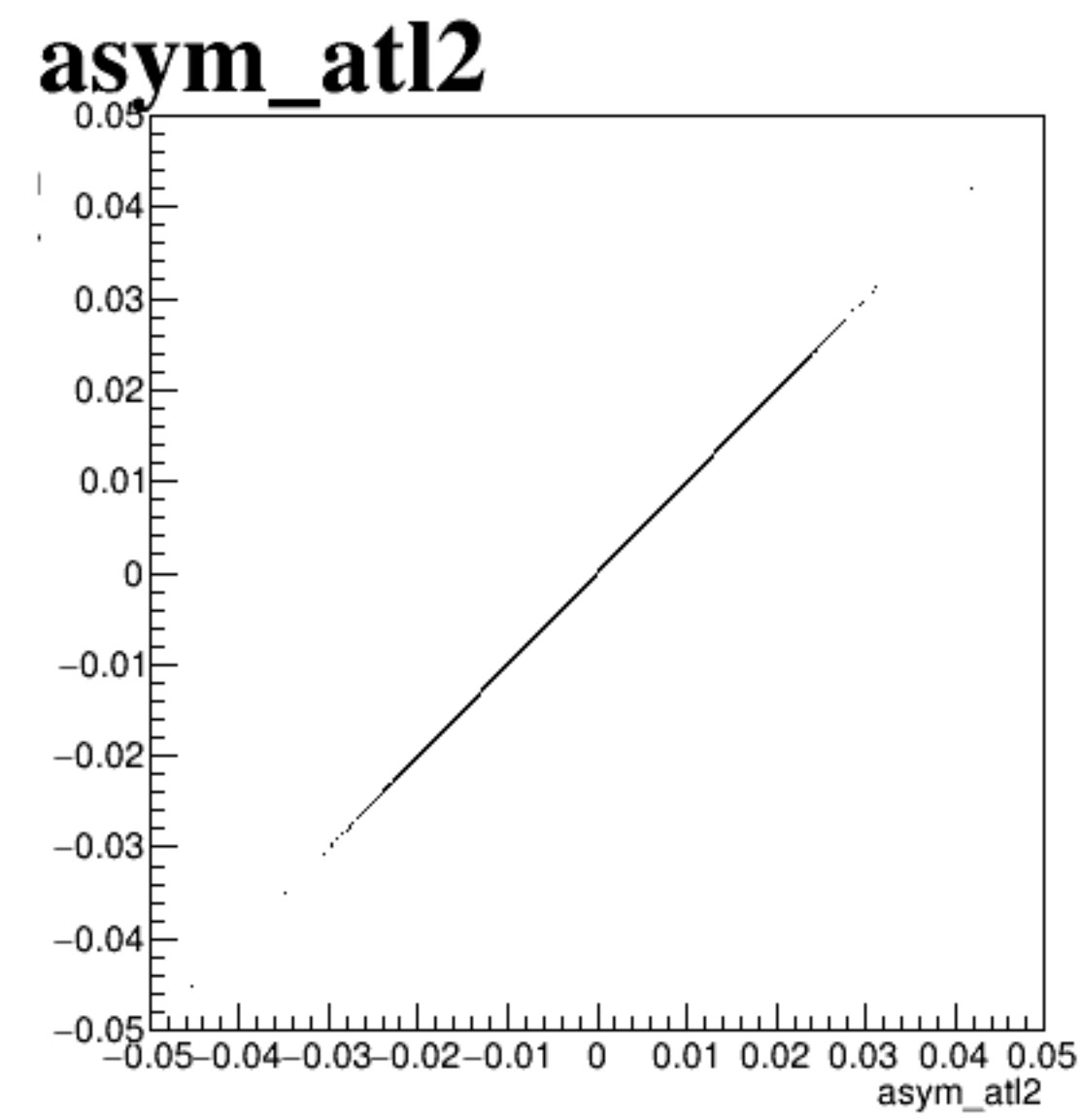
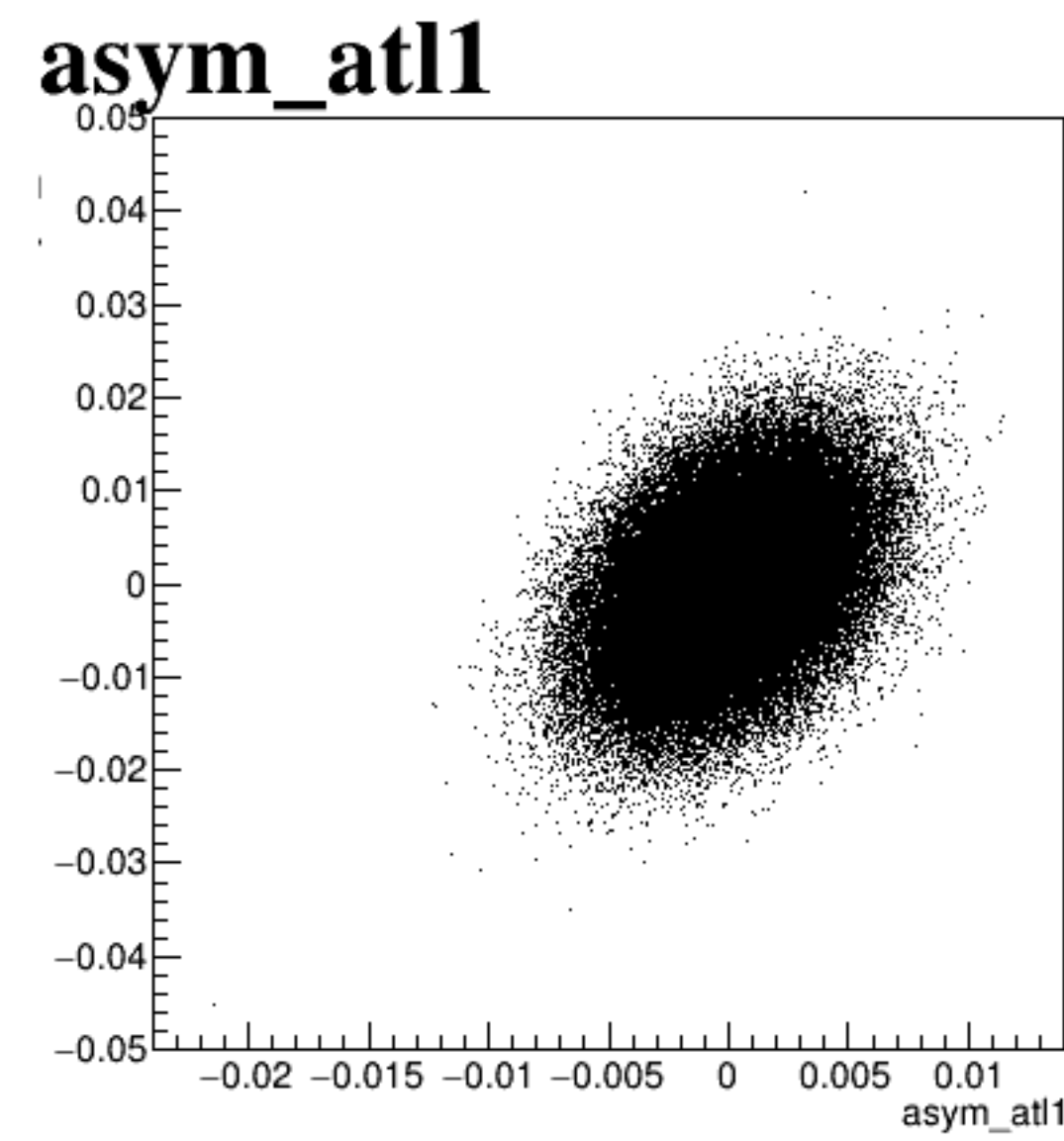
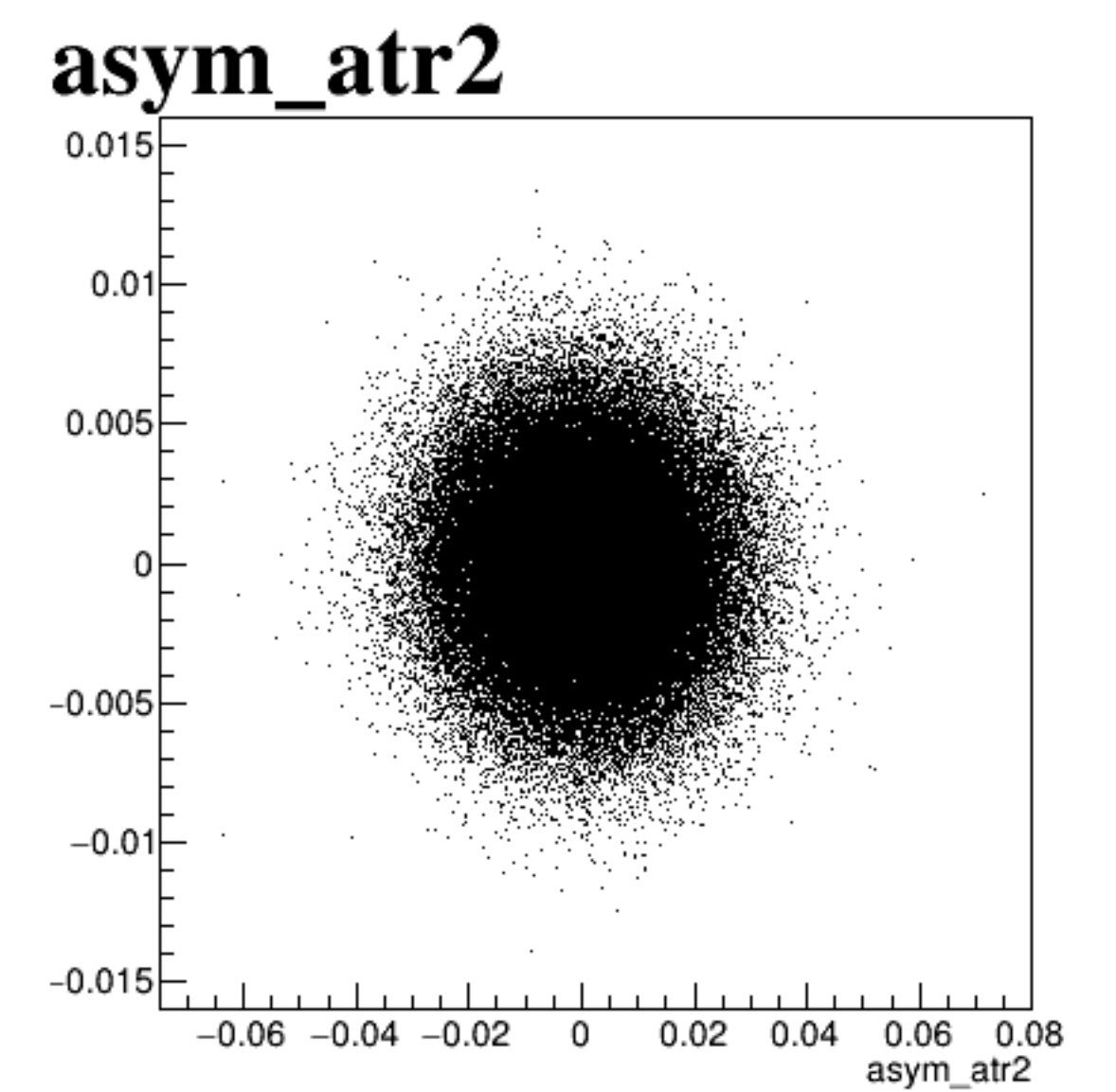
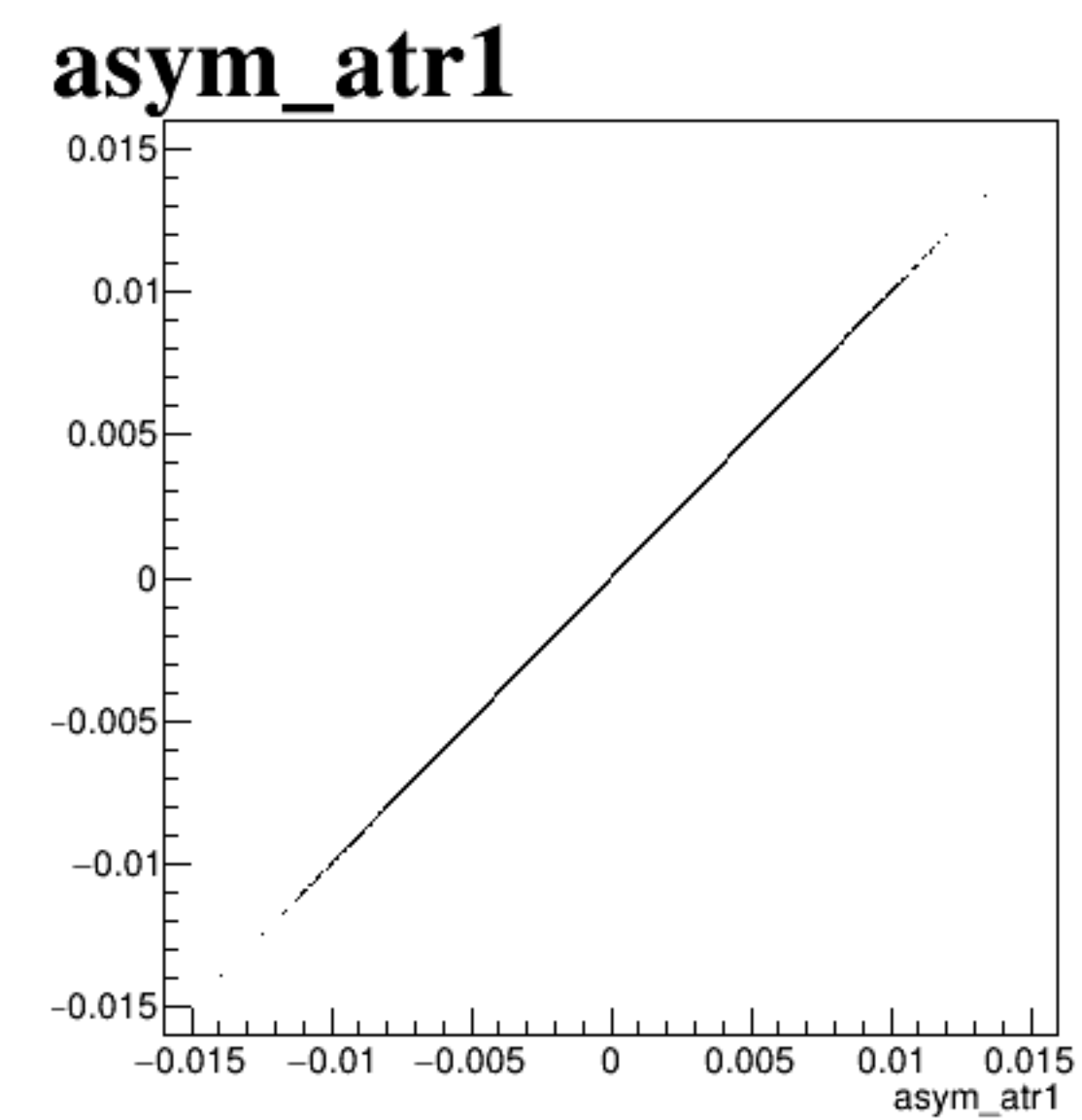
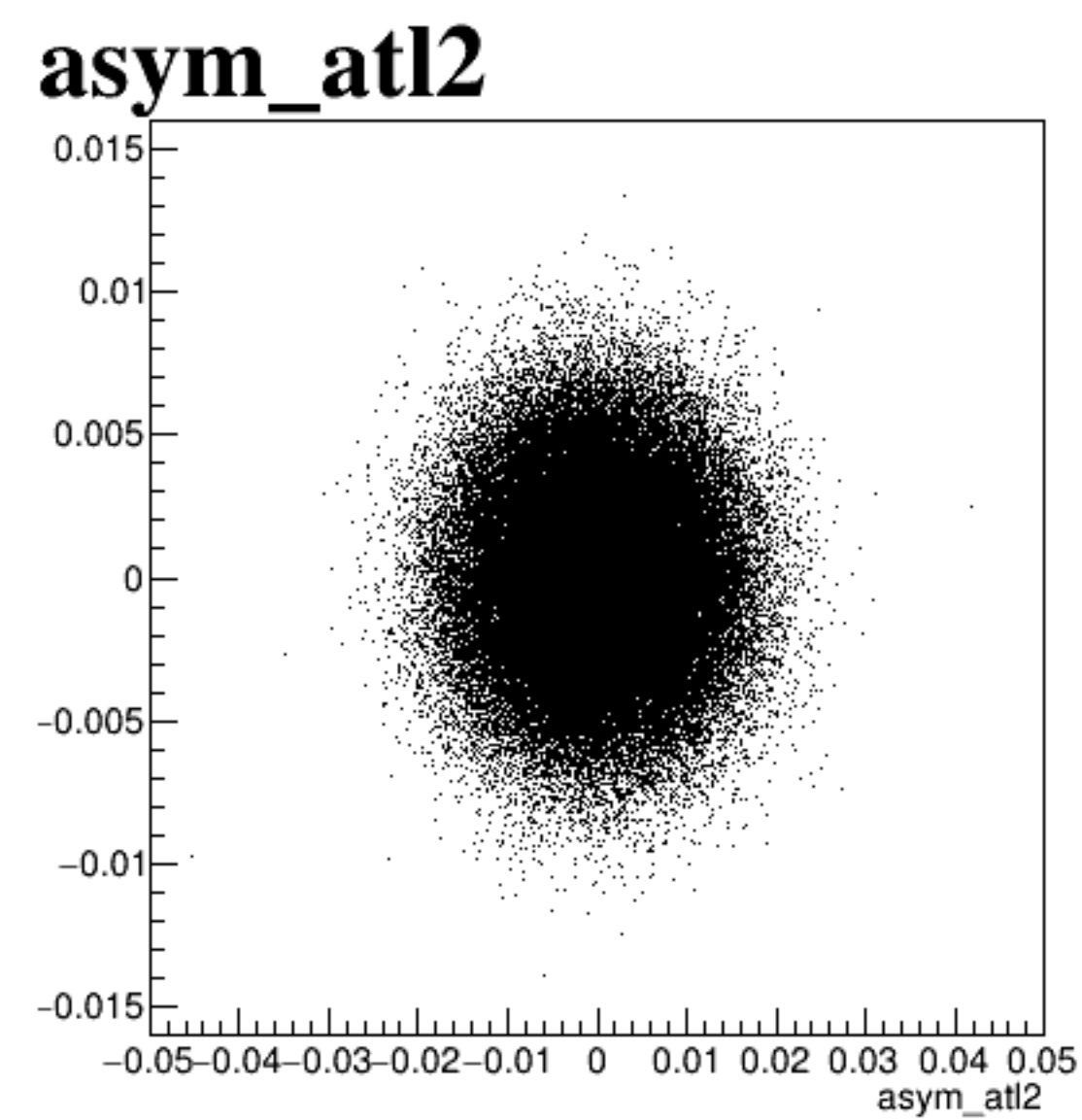
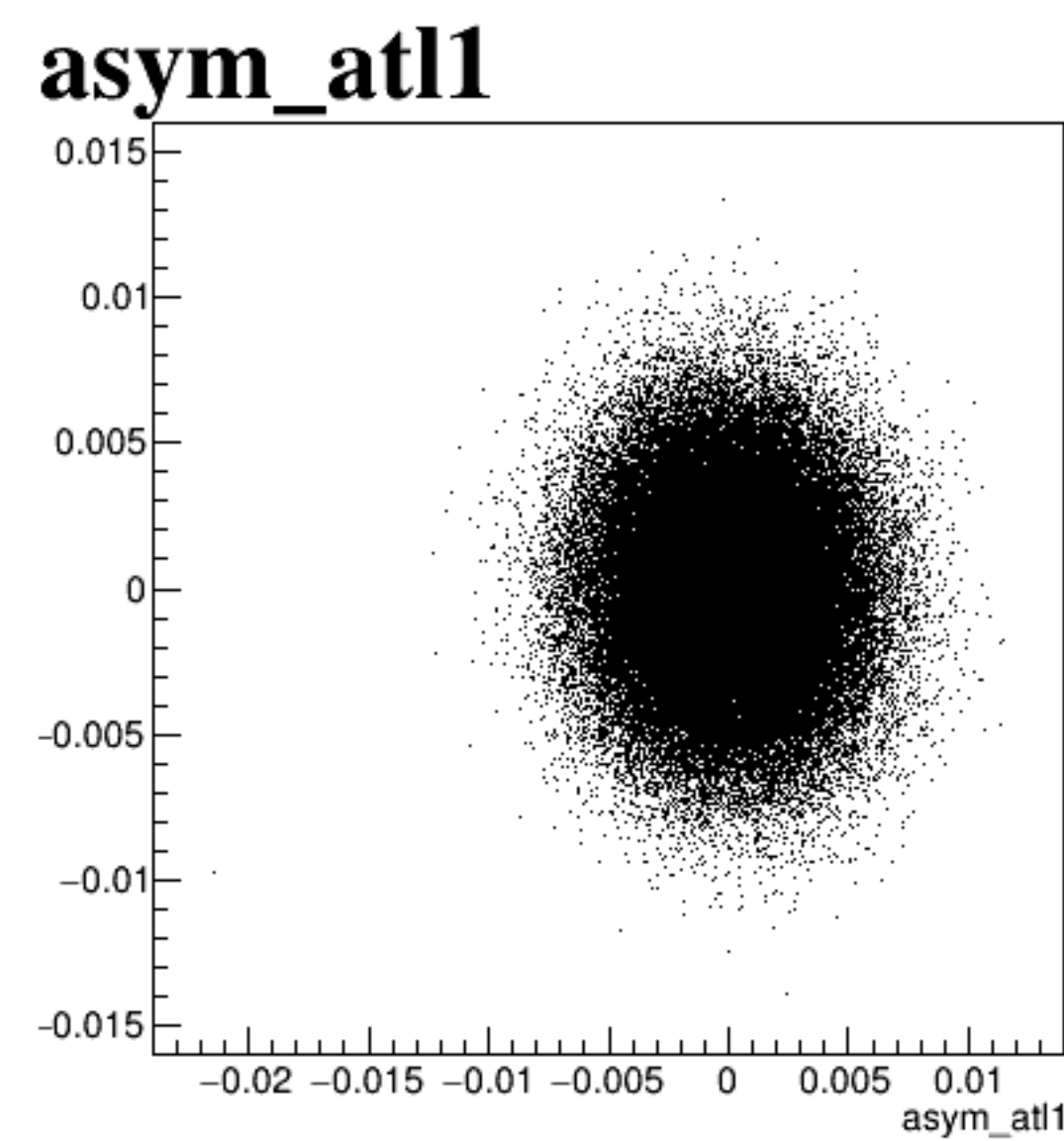
asym\_atr2



## asym\_atl1



## asym\_atl2

**asym\_atr1****asym\_atr2**