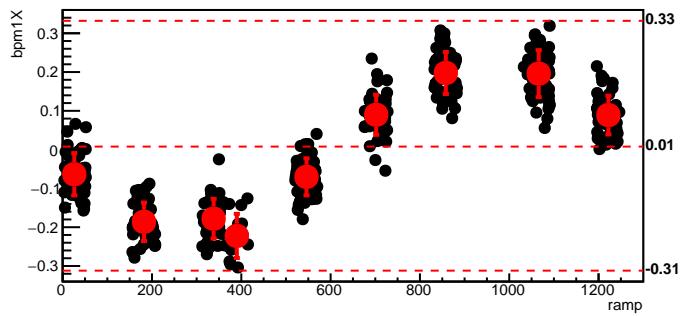
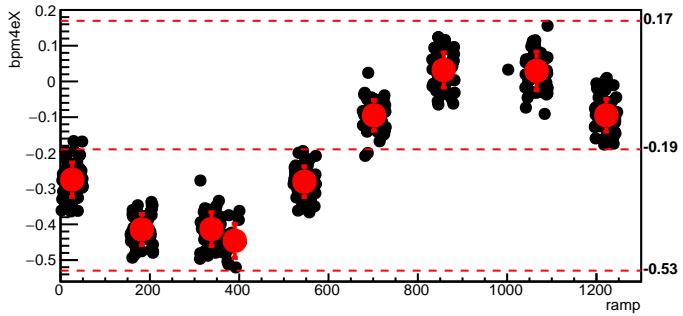


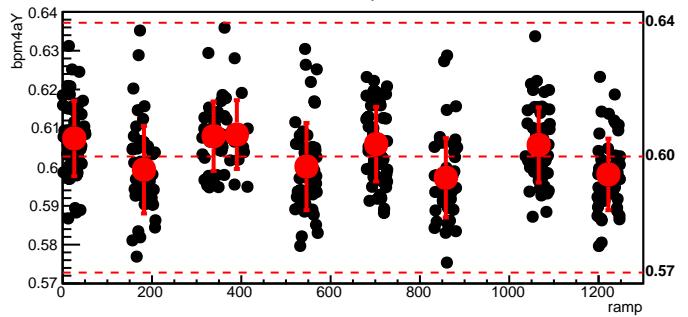
bmod Trim1,cyc=5956



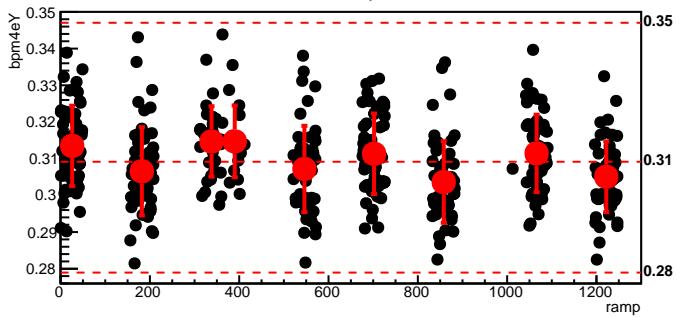
bmod Trim1,cyc=5956



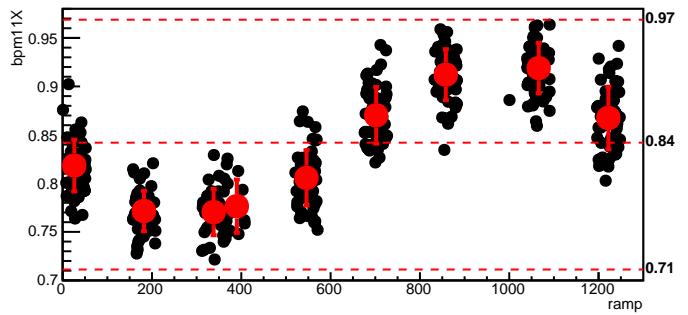
bmod Trim1,cyc=5956



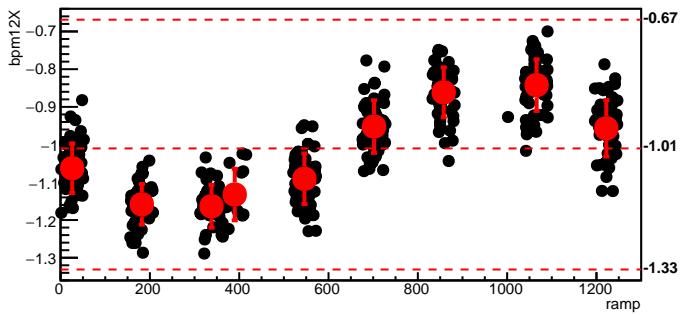
bmod Trim1,cyc=5956



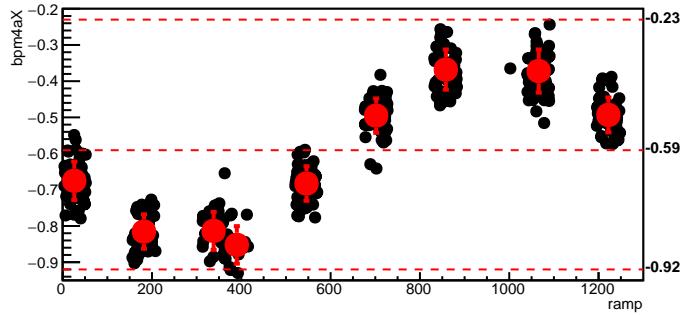
bmod Trim1,cyc=5956



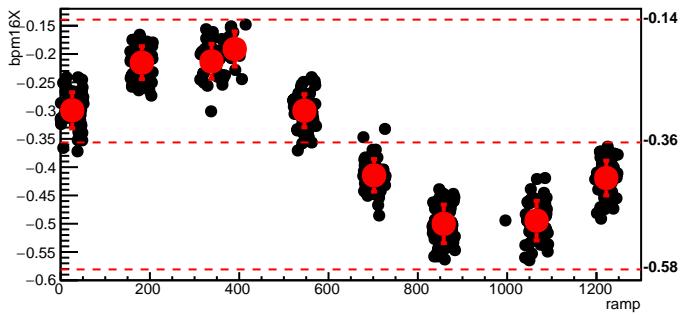
bmod Trim1,cyc=5956



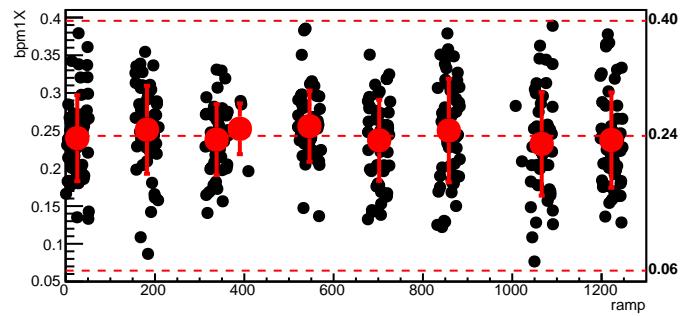
bmod Trim1,cyc=5956



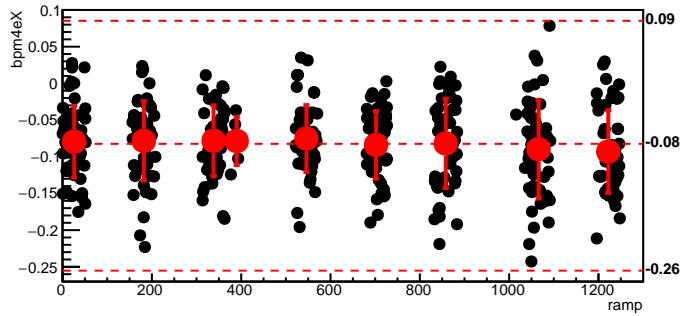
bmod Trim1,cyc=5956



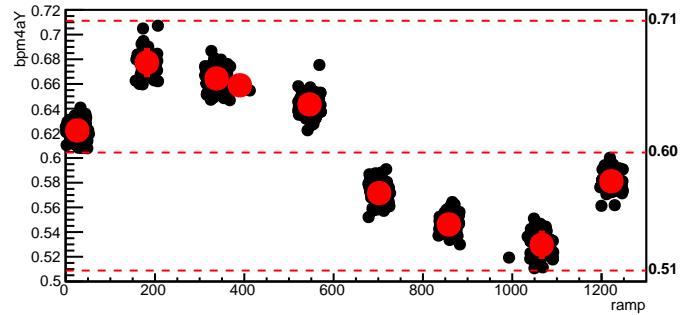
bmod Trim2,cyc=5956



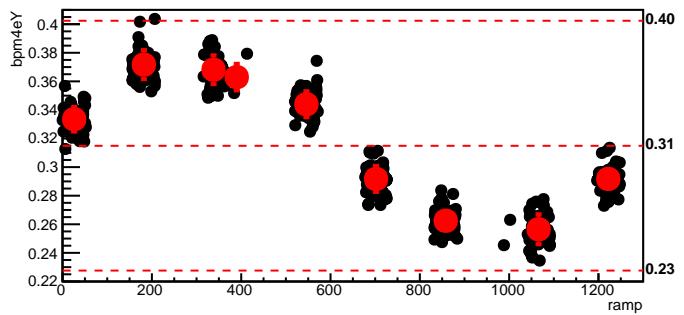
bmod Trim2,cyc=5956



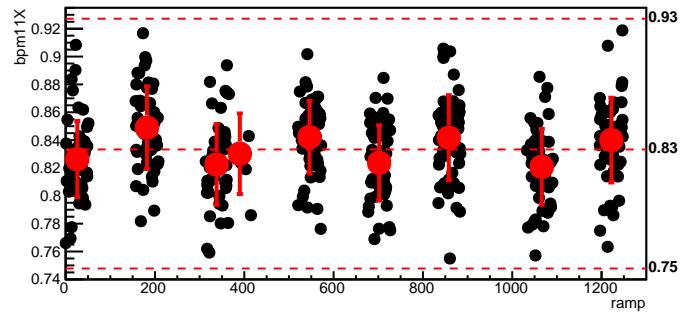
bmod Trim2,cyc=5956



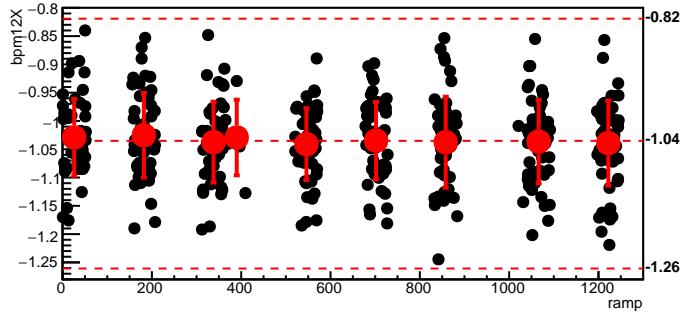
bmod Trim2,cyc=5956



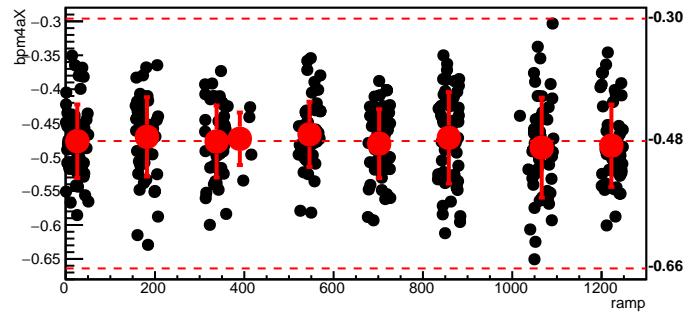
bmod Trim2,cyc=5956



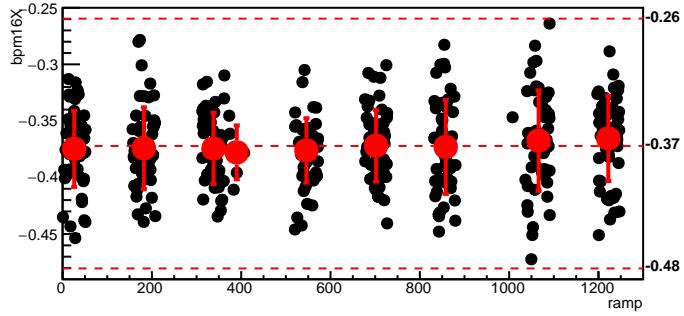
bmod Trim2,cyc=5956



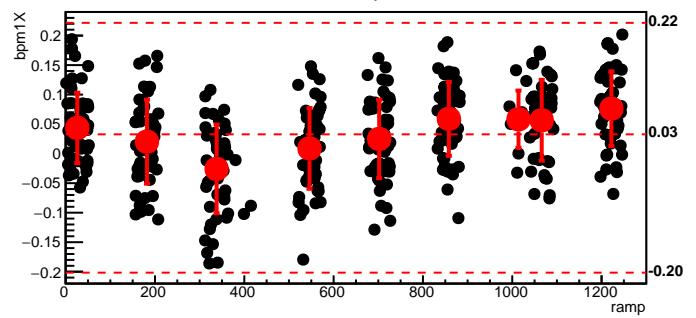
bmod Trim2,cyc=5956



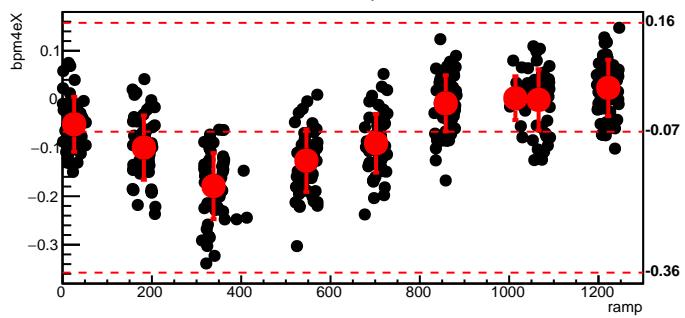
bmod Trim2,cyc=5956



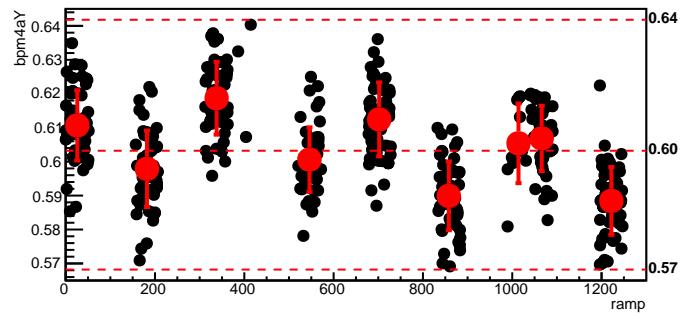
bmod Trim3,cyc=5956



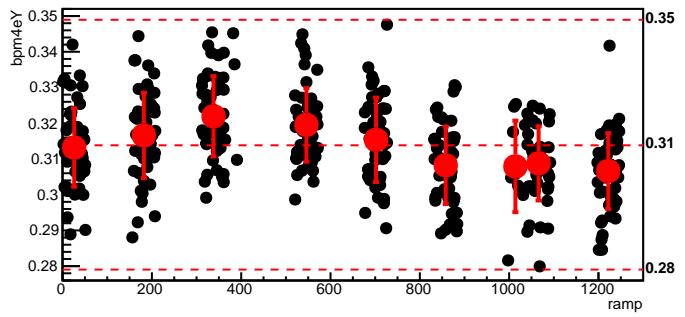
bmod Trim3,cyc=5956



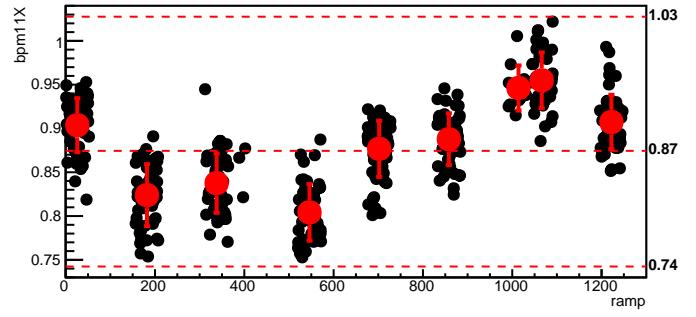
bmod Trim3,cyc=5956



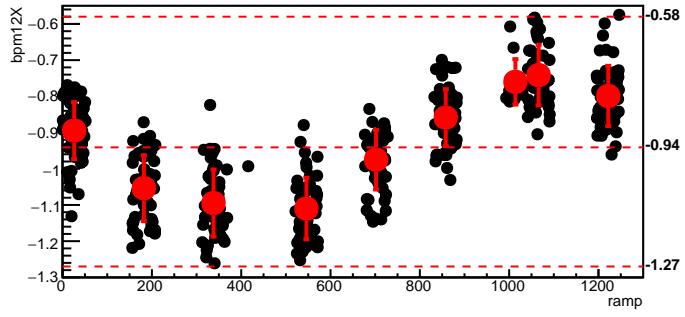
bmod Trim3,cyc=5956



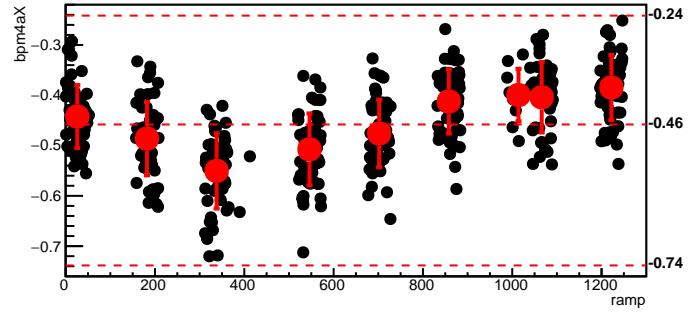
bmod Trim3,cyc=5956



bmod Trim3,cyc=5956



bmod Trim3,cyc=5956



bmod Trim3,cyc=5956

