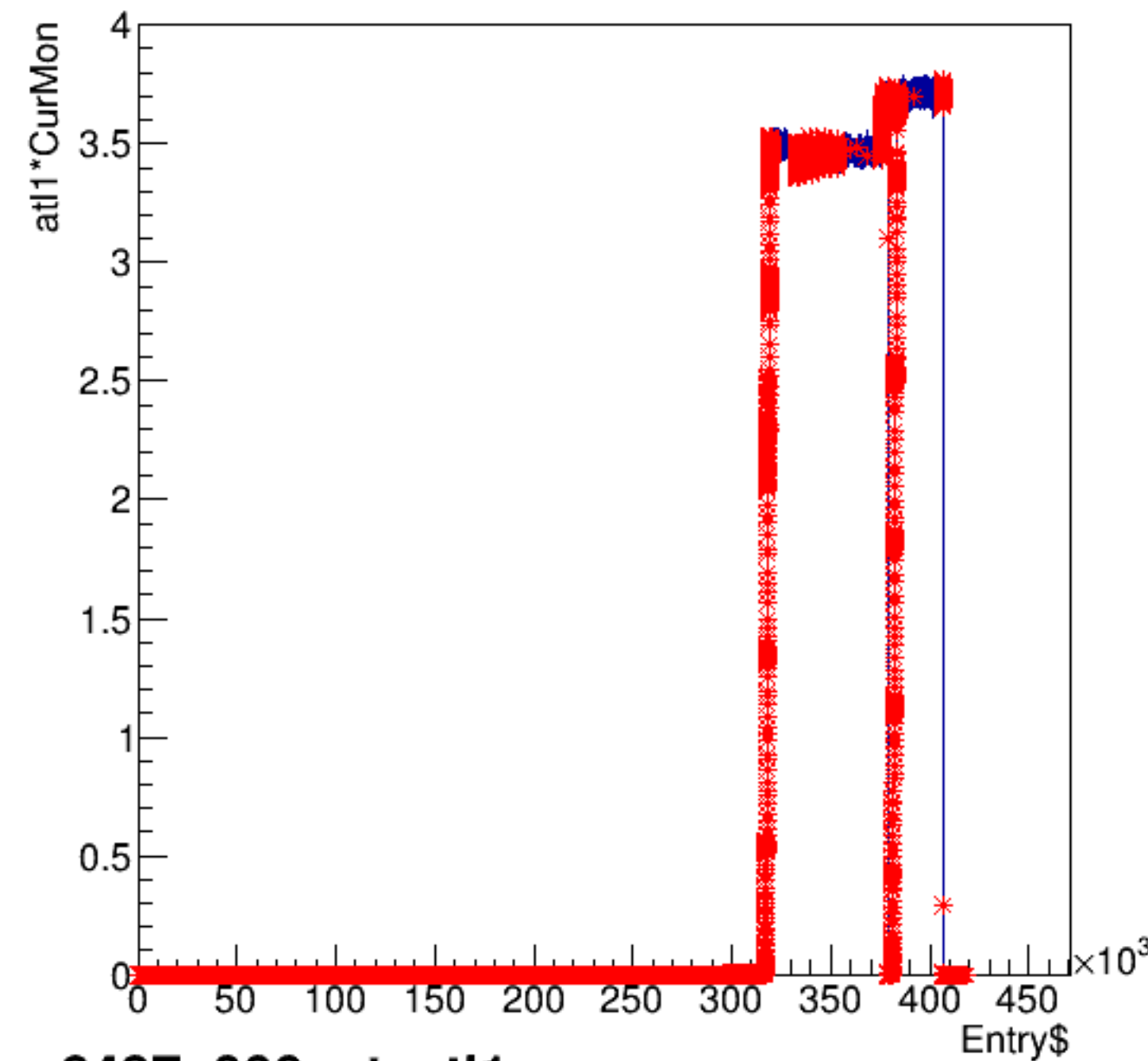
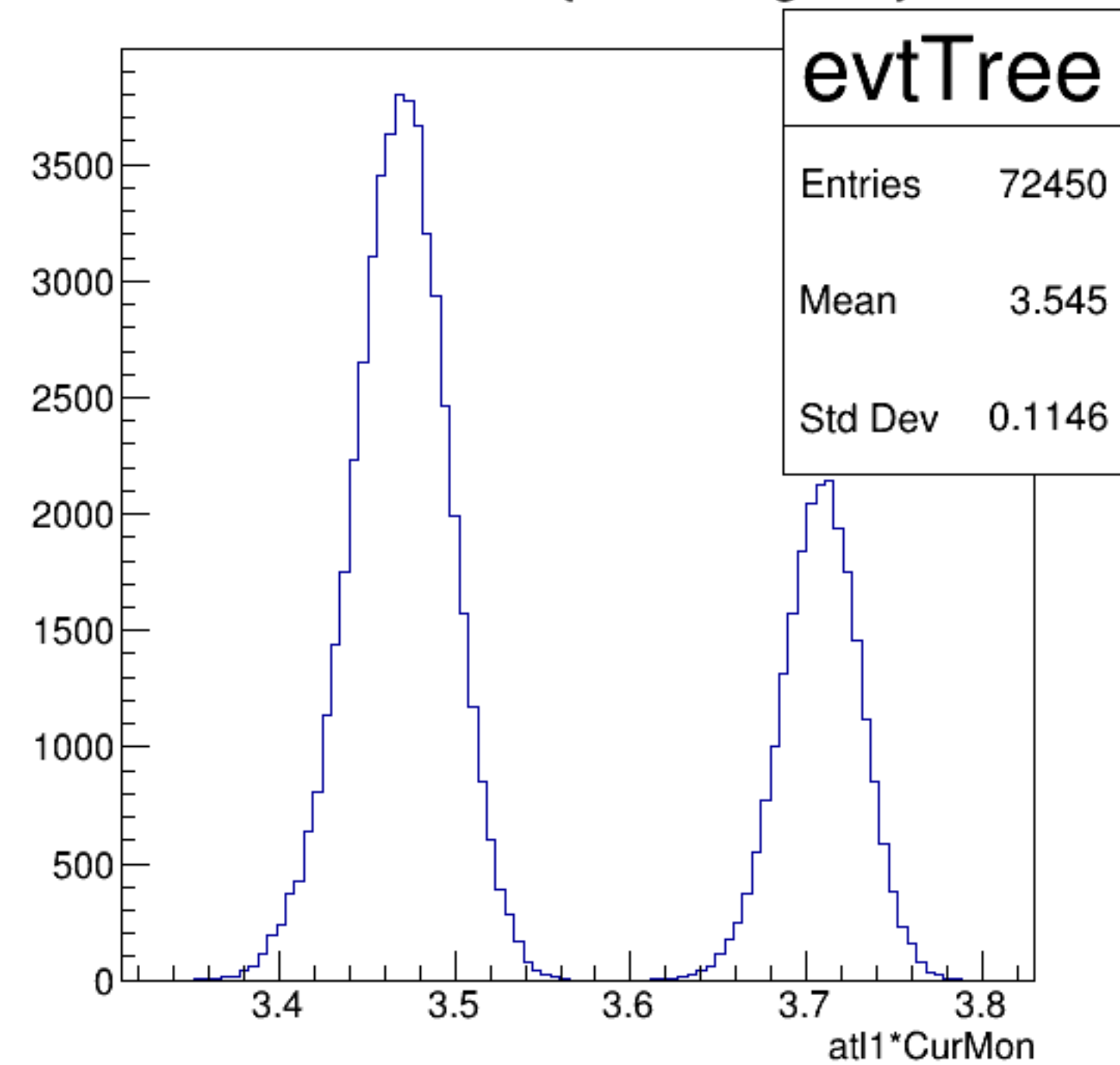


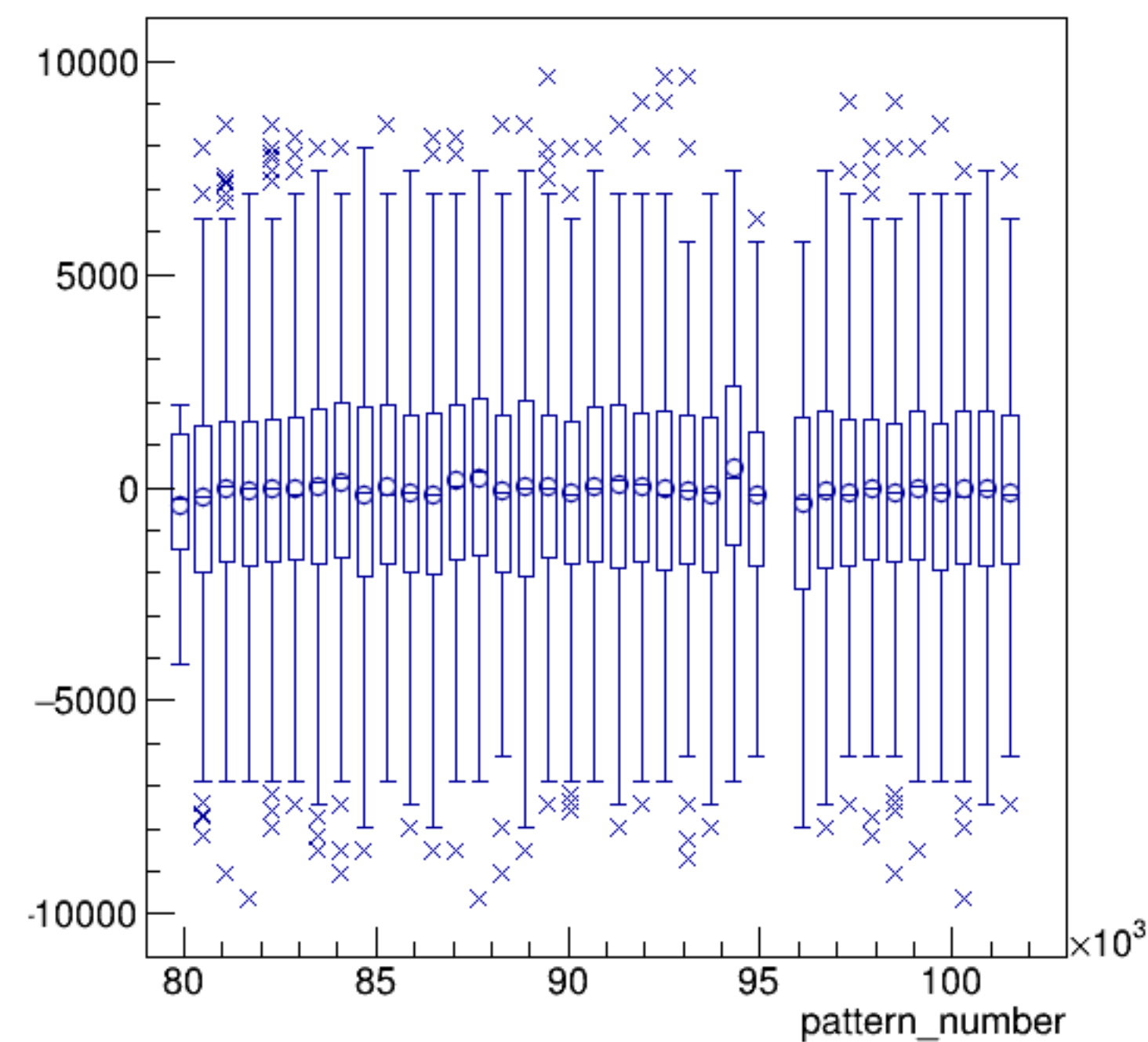
atl1*CurMon:Entry\$



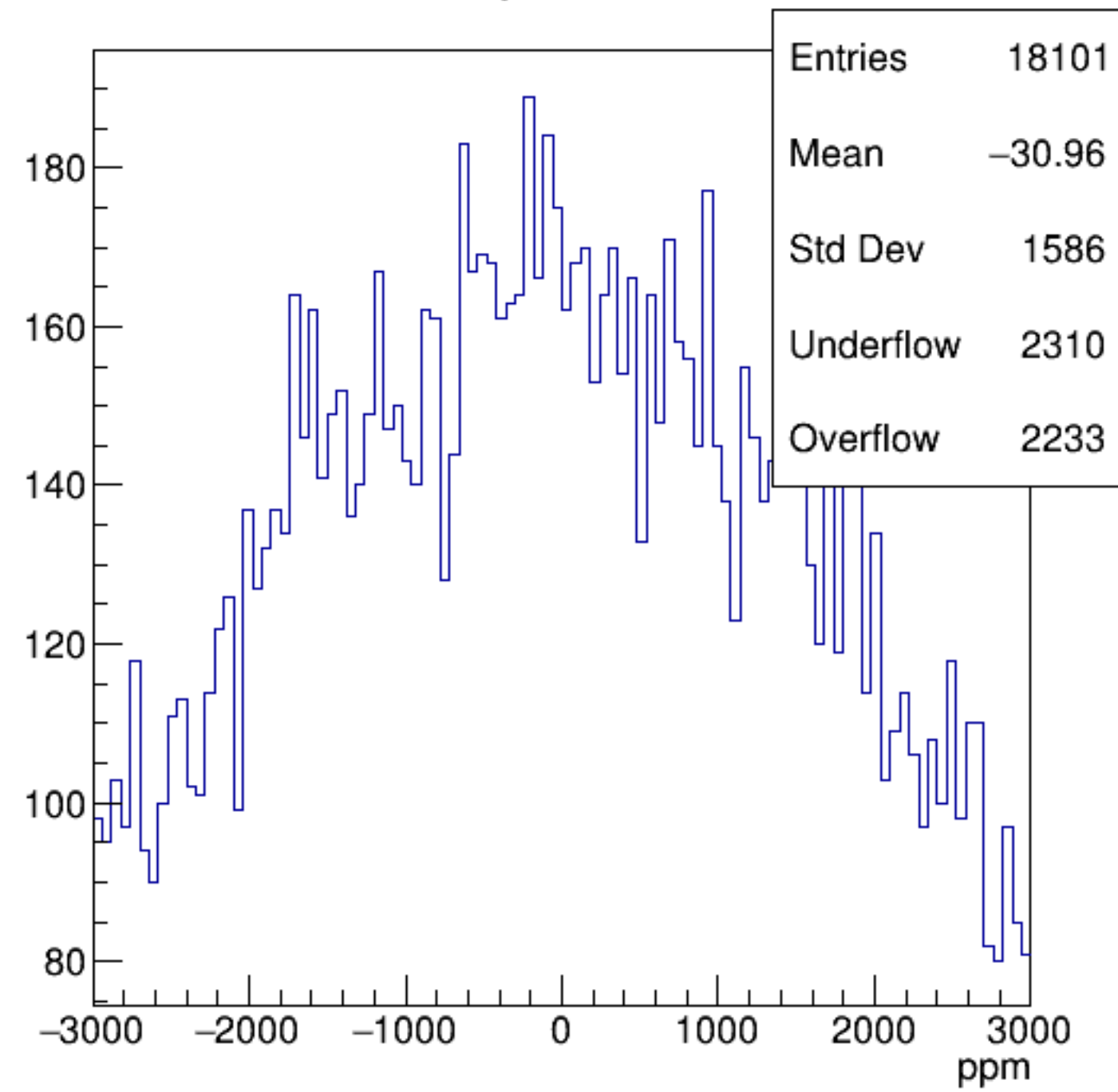
atl1*CurMon {ErrorFlag==0}



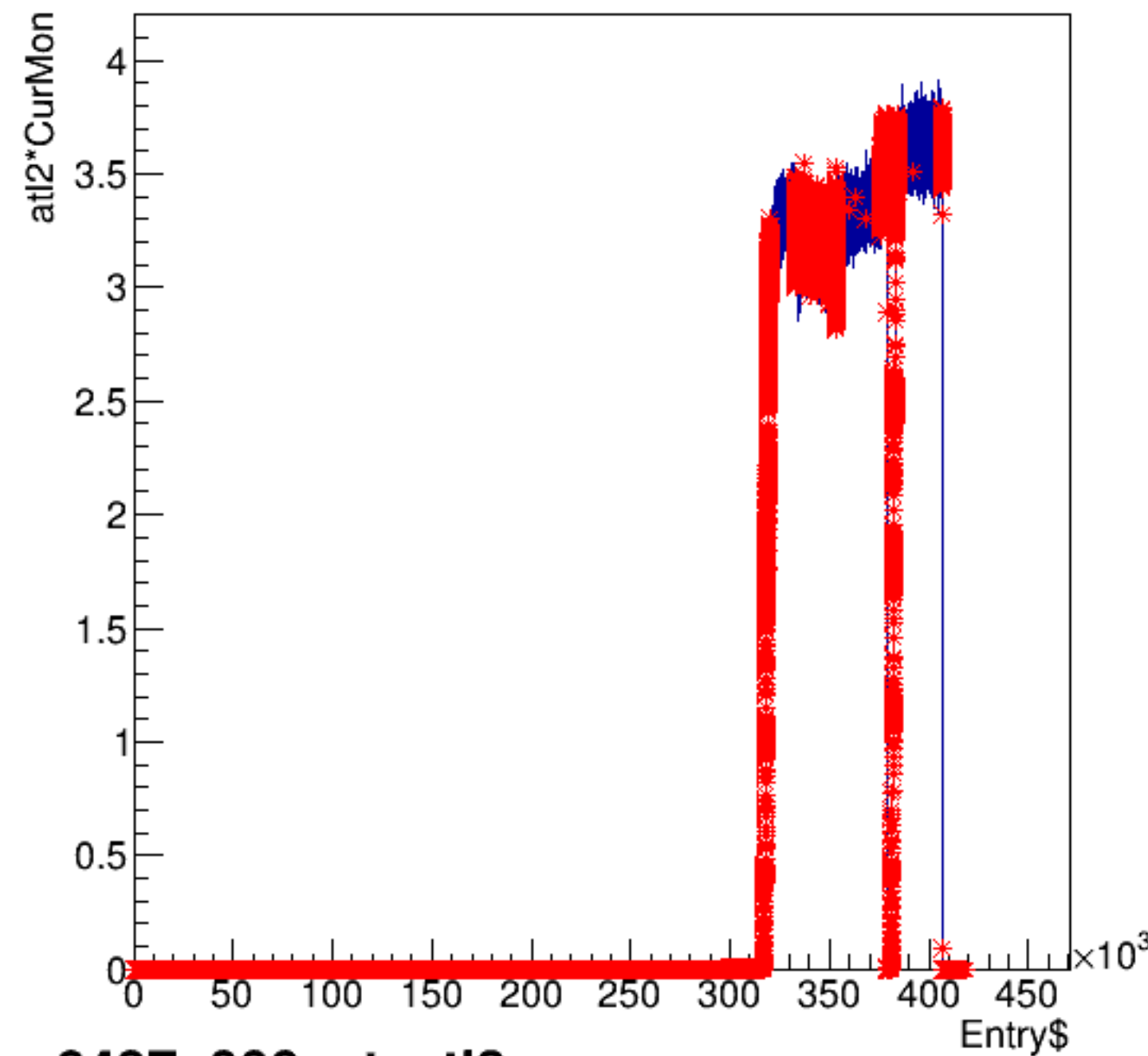
asym_atl1/ppm:pattern_number {ErrorFlag==0}



asym_atl1

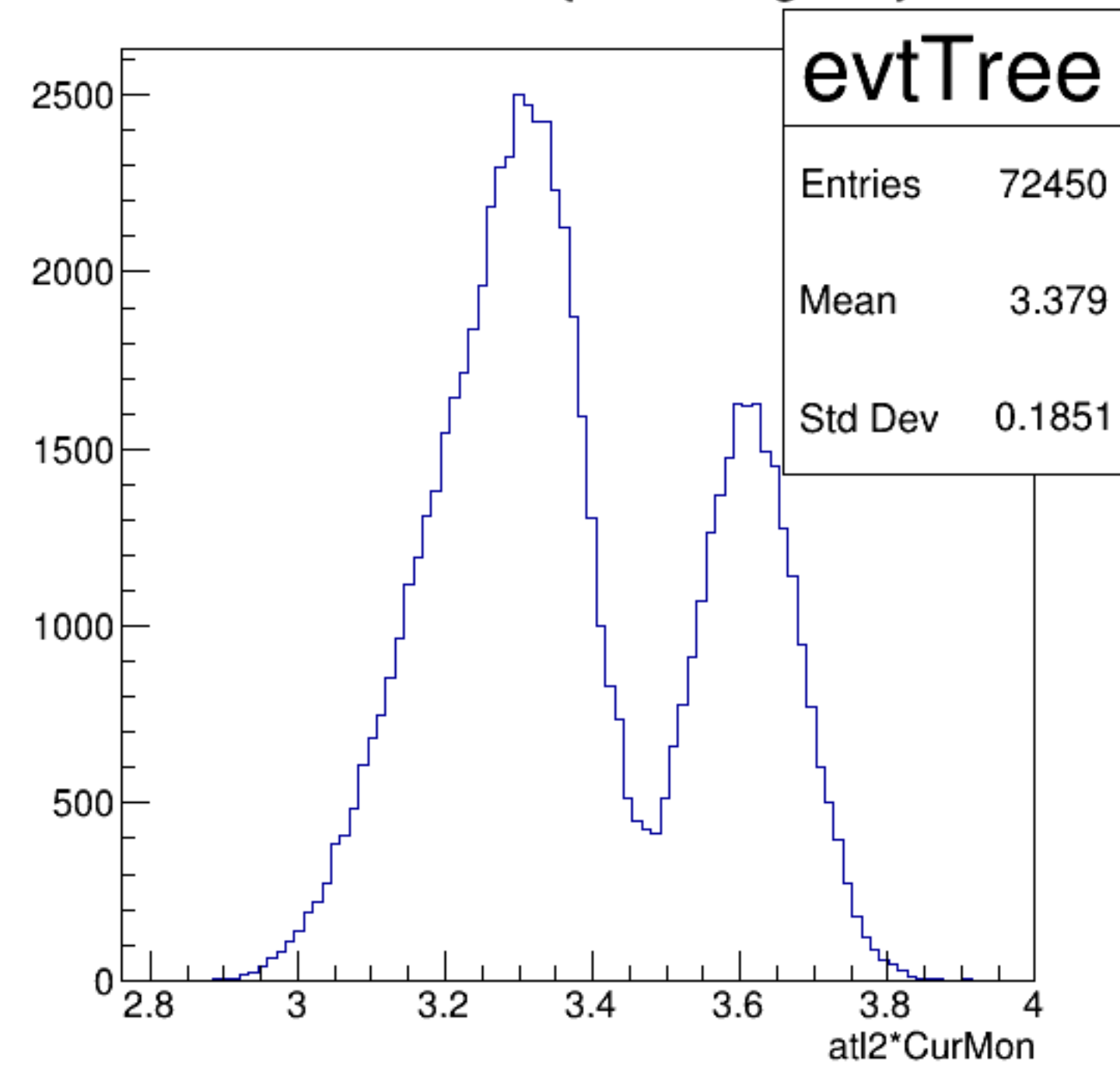


atl2*CurMon:Entry\$

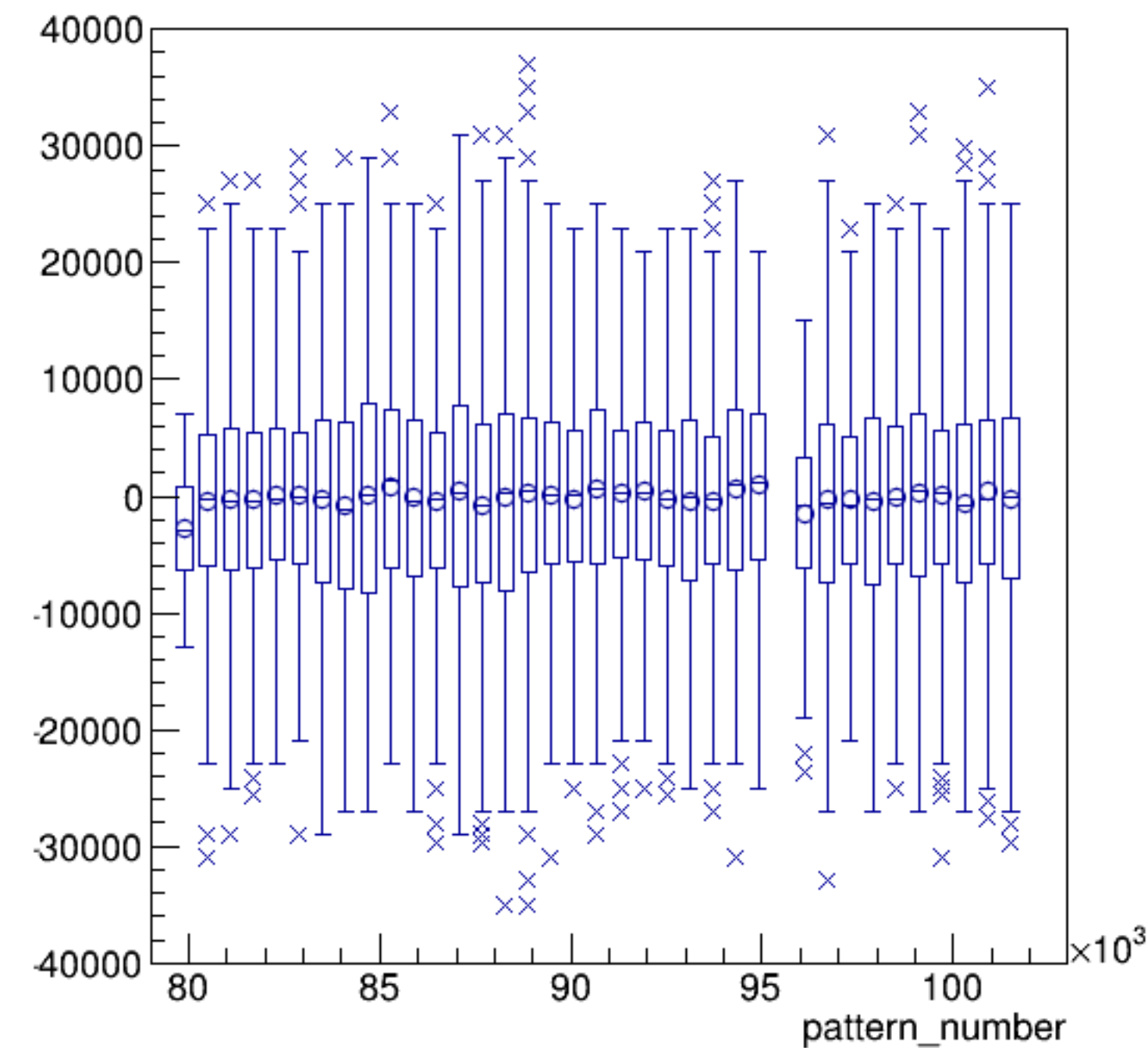


run6437_000_at_atl2.png

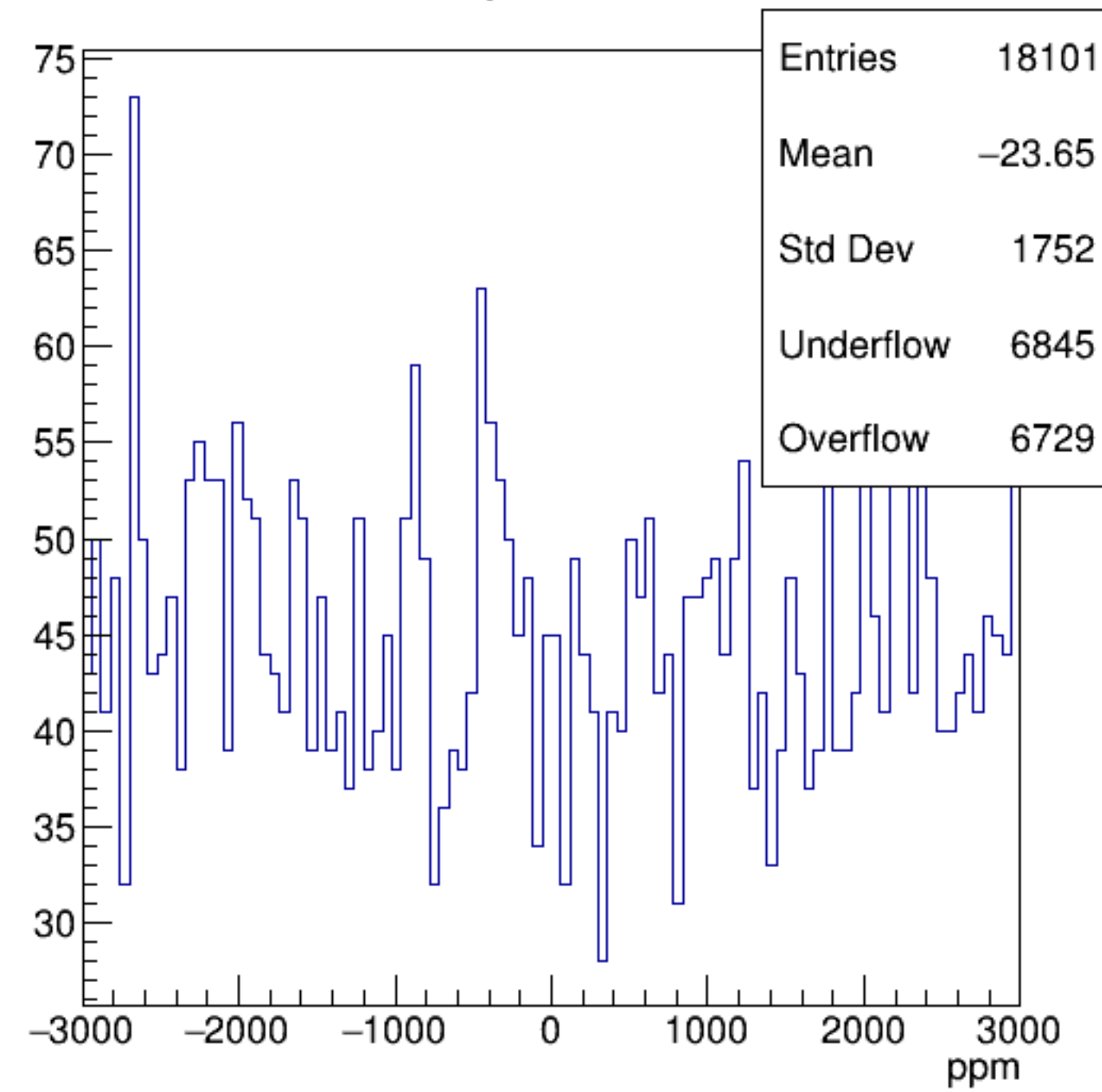
atl2*CurMon {ErrorFlag==0}



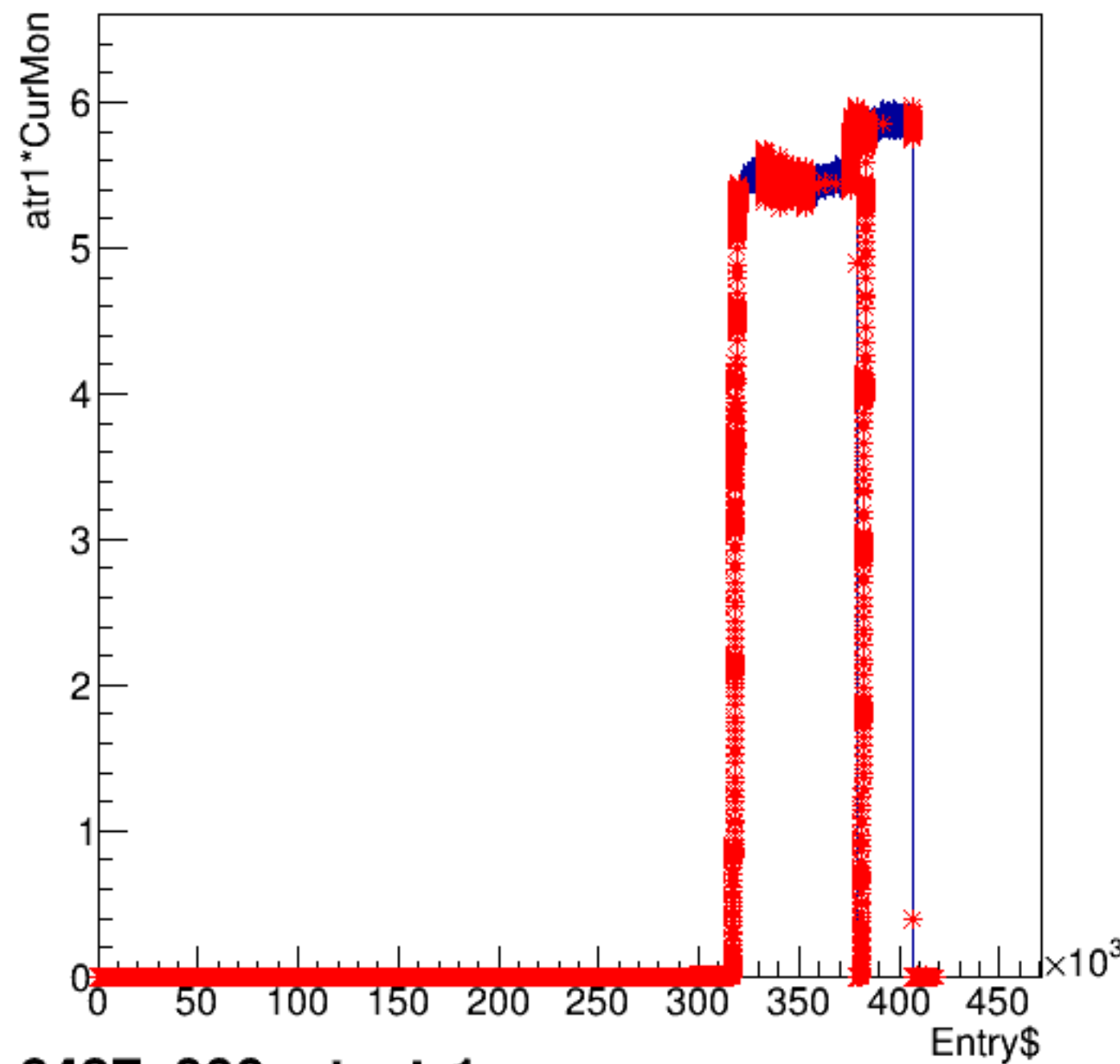
asym_atl2/ppm:pattern_number {ErrorFlag==0}



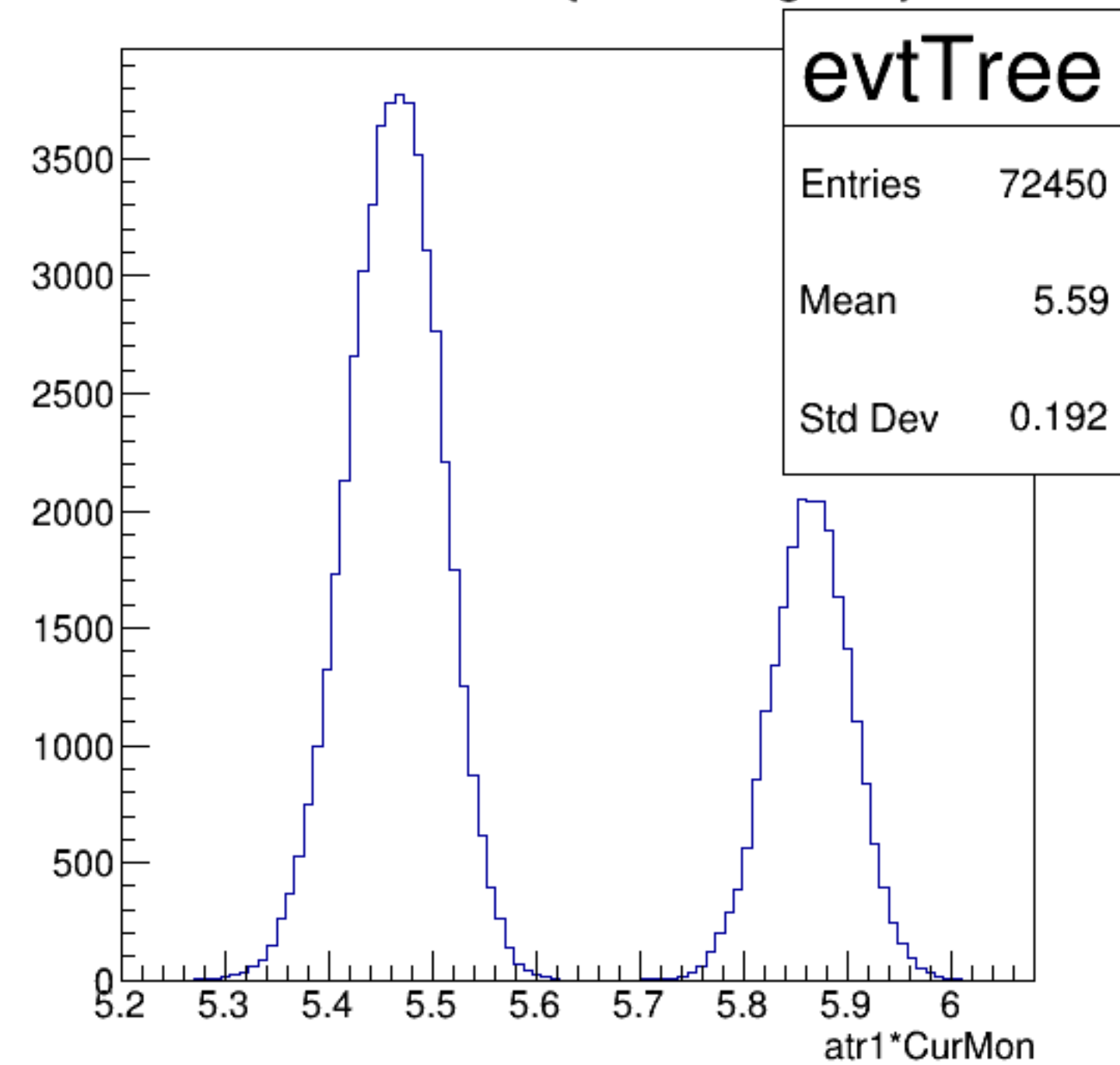
asym_atl2



atr1*CurMon:Entry\$

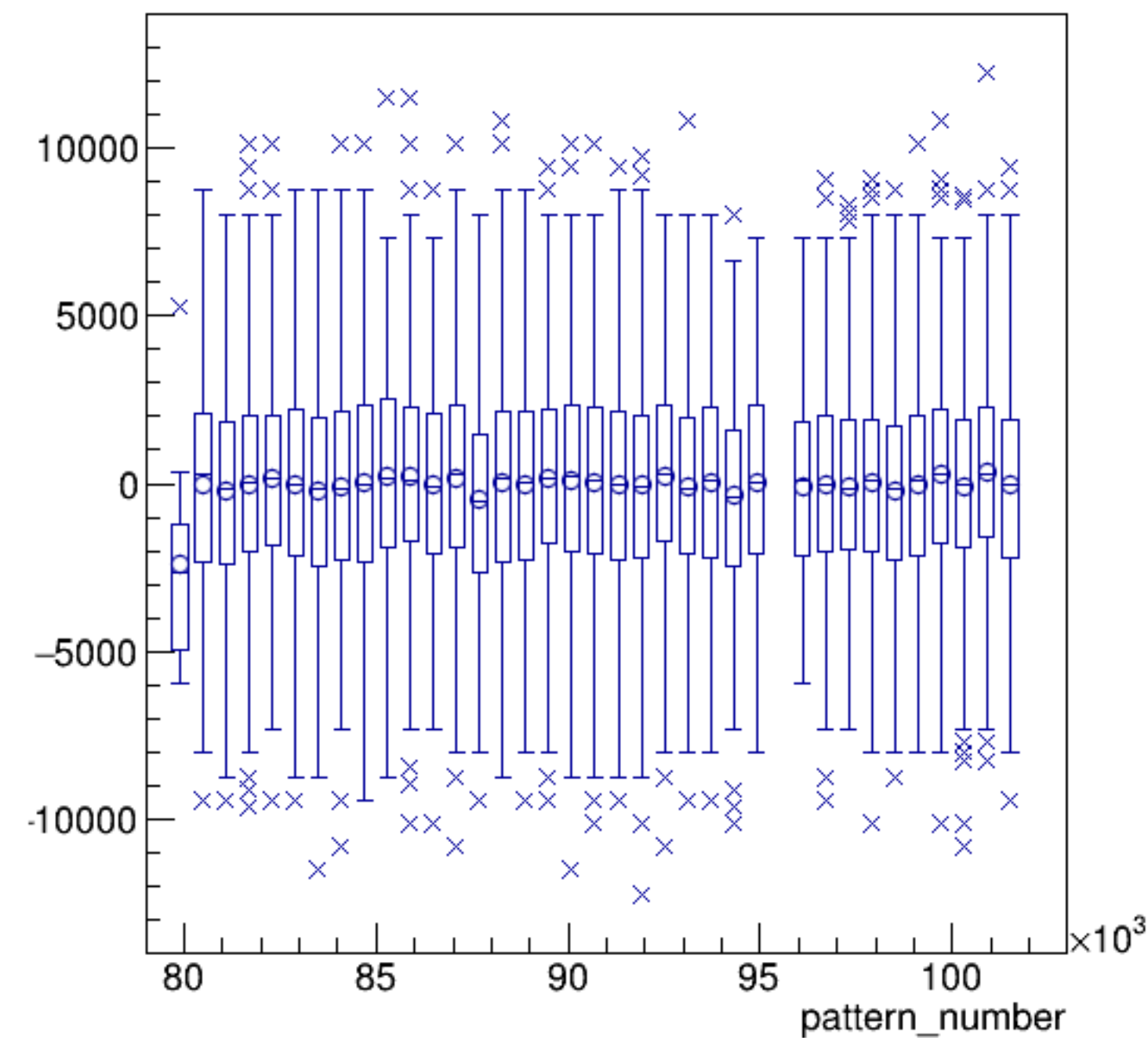


atr1*CurMon {ErrorFlag==0}

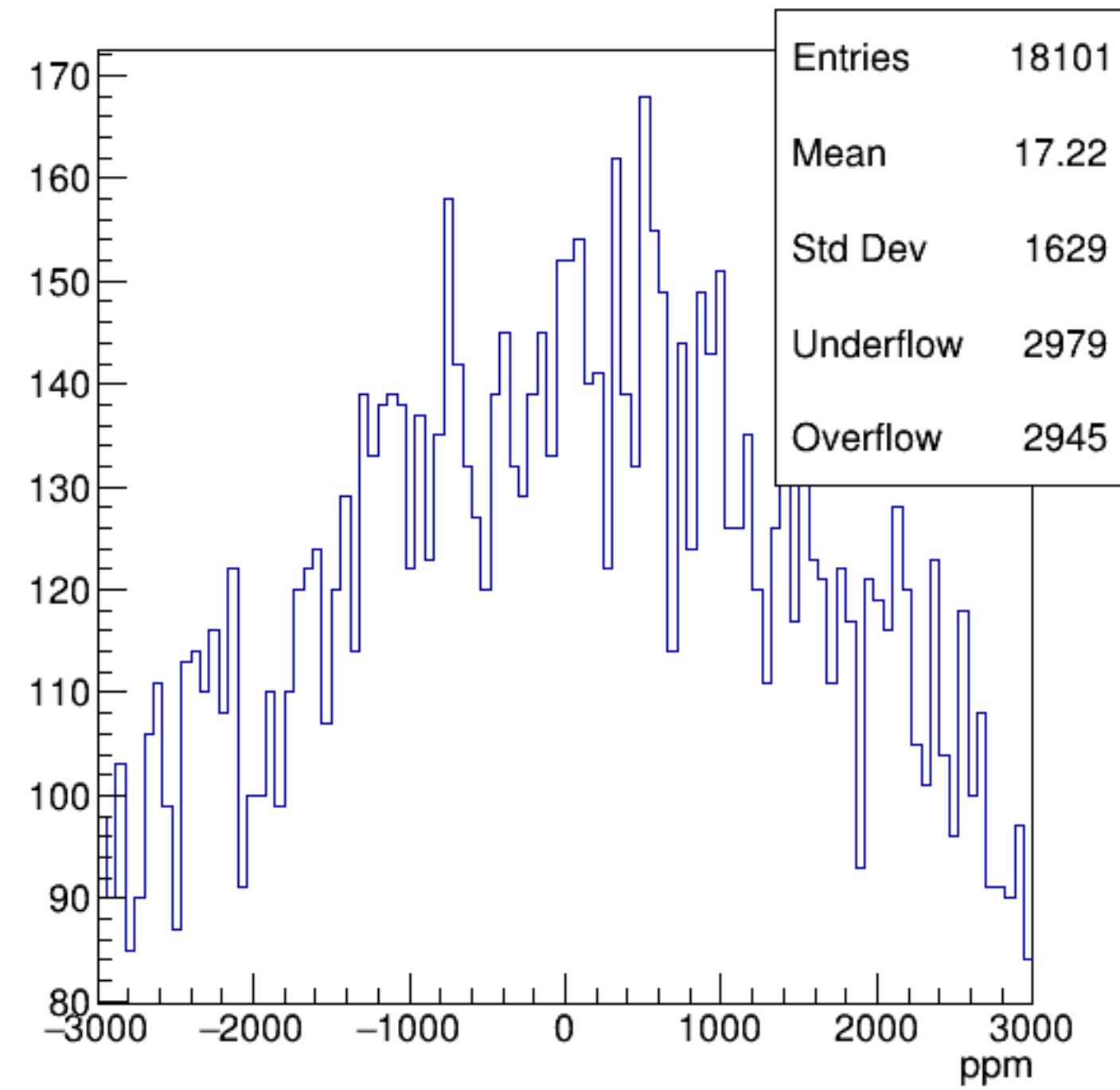


evtTree	
Entries	72450
Mean	5.59
Std Dev	0.192

asym_atr1/ppm:pattern_number {ErrorFlag==0}

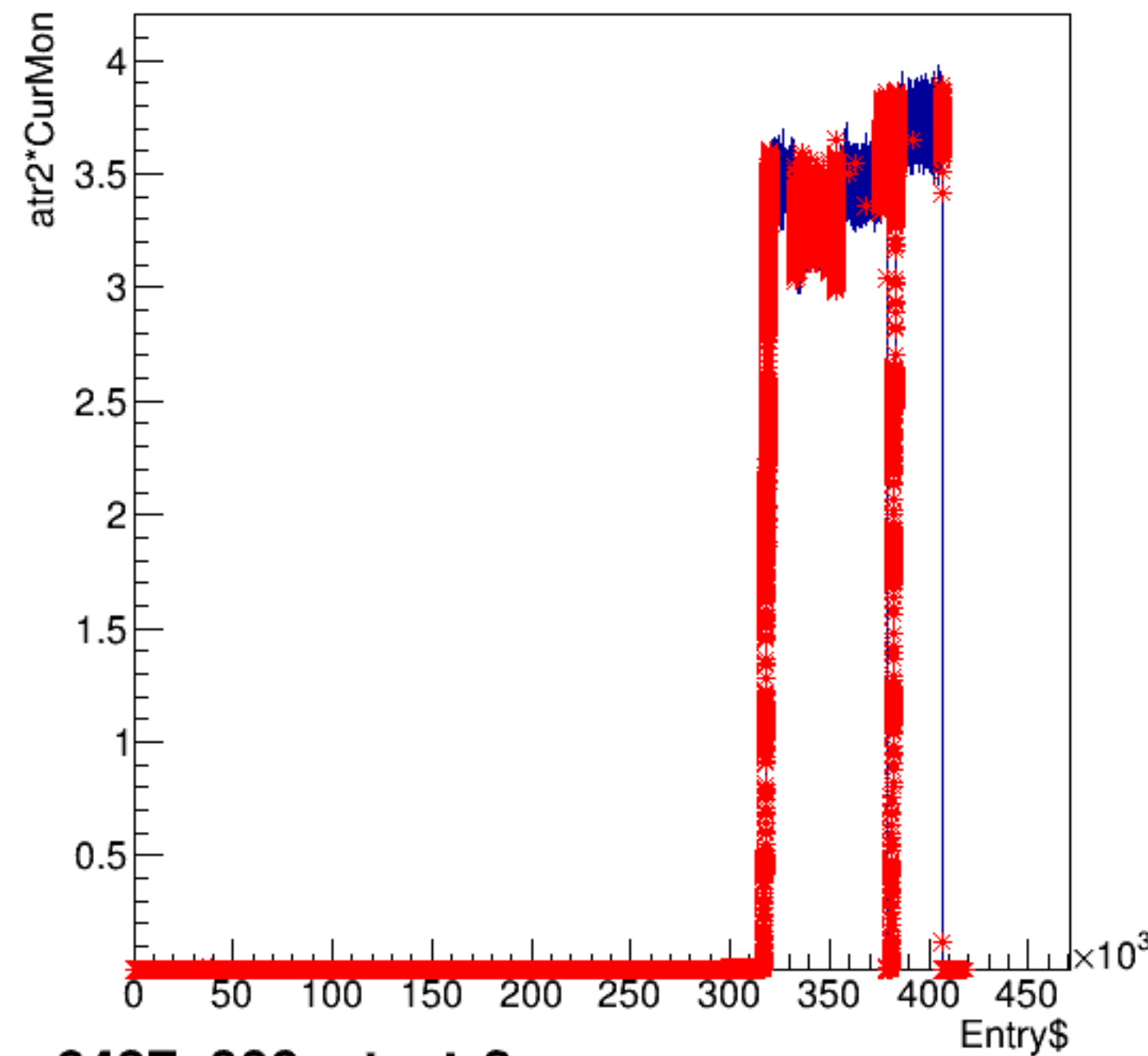


asym_atr1



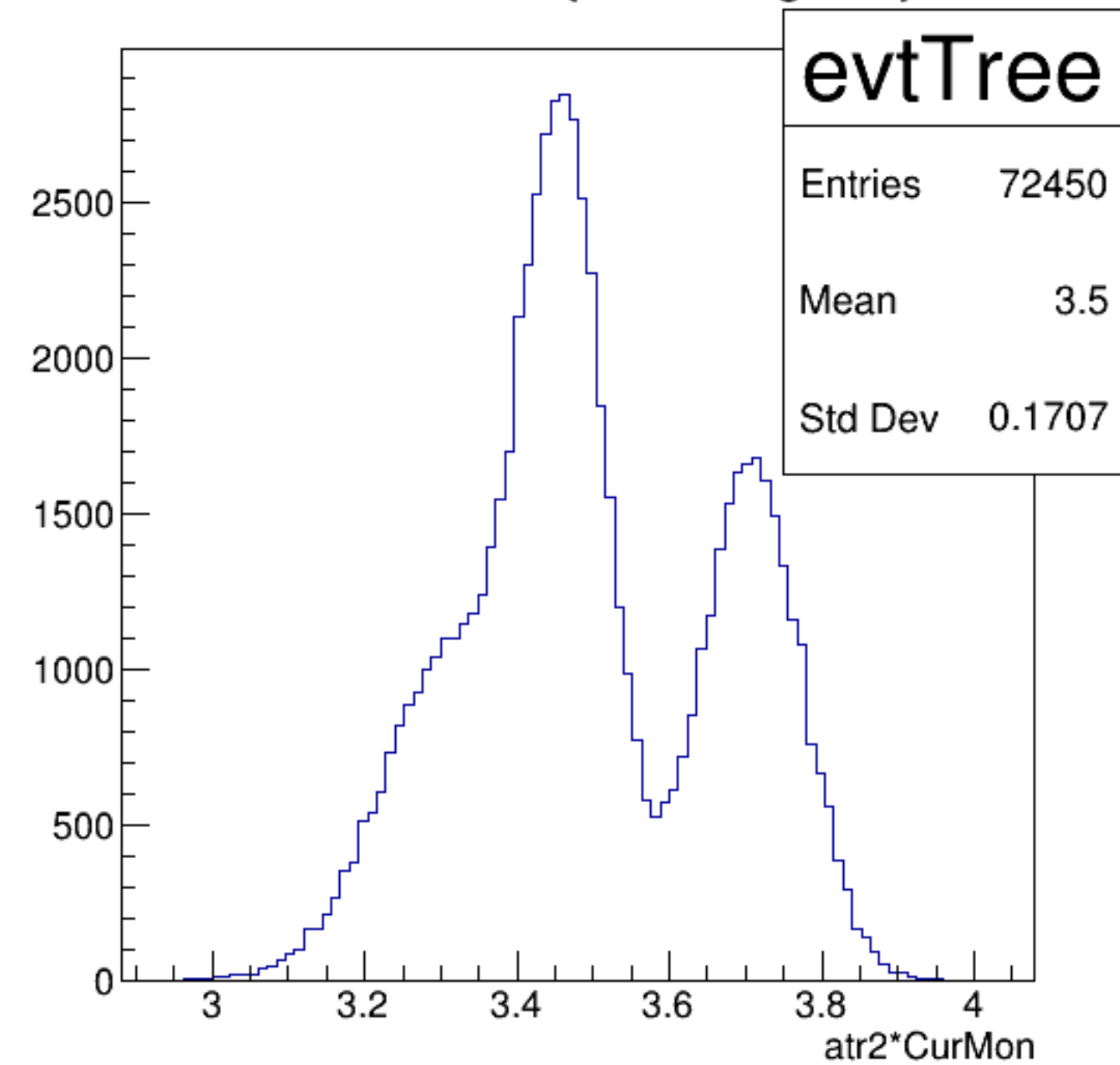
Entries	18101
Mean	17.22
Std Dev	1629
Underflow	2979
Overflow	2945

atr2*CurMon:Entry\$

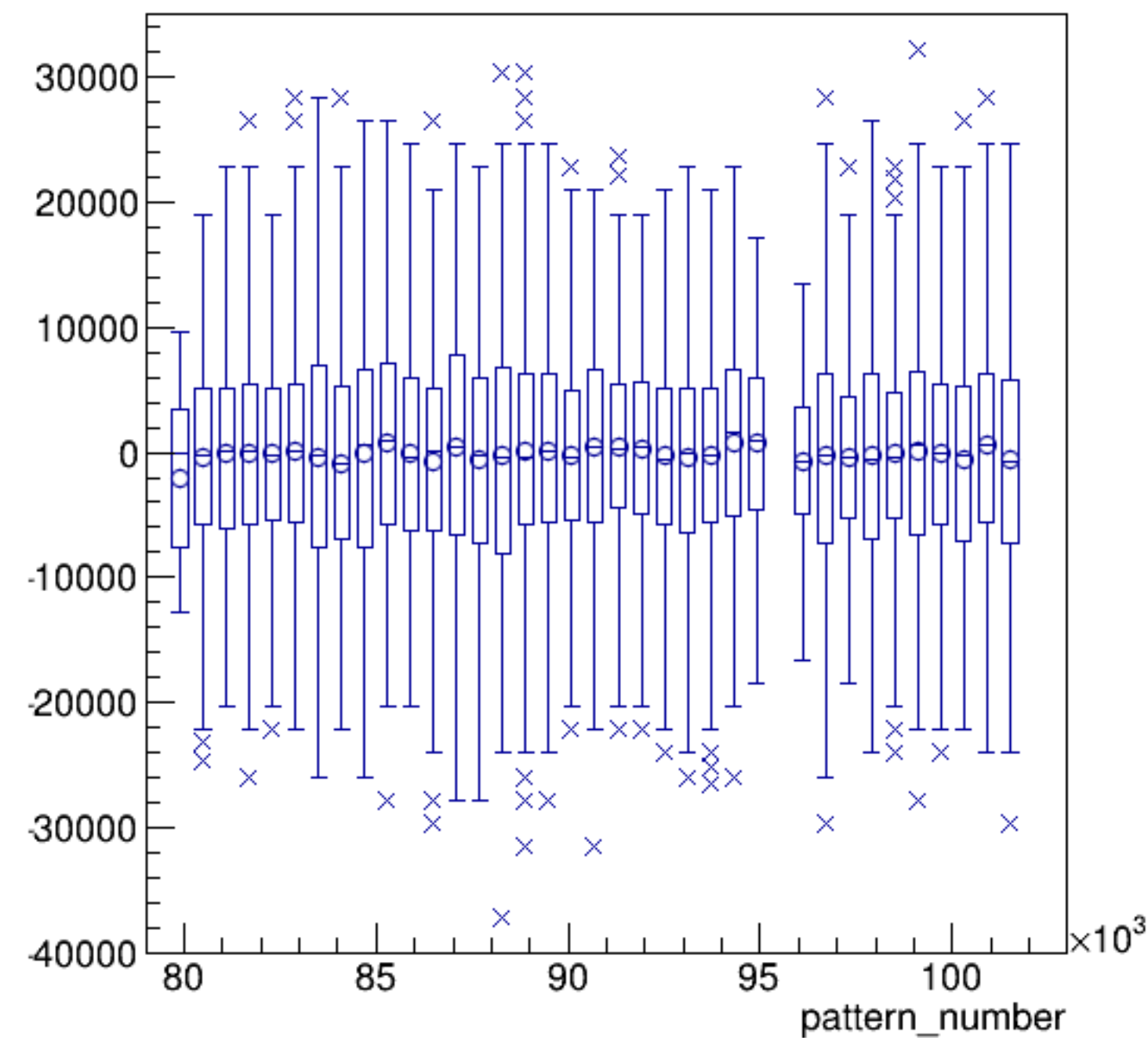


run6437_000_at_atr2.png

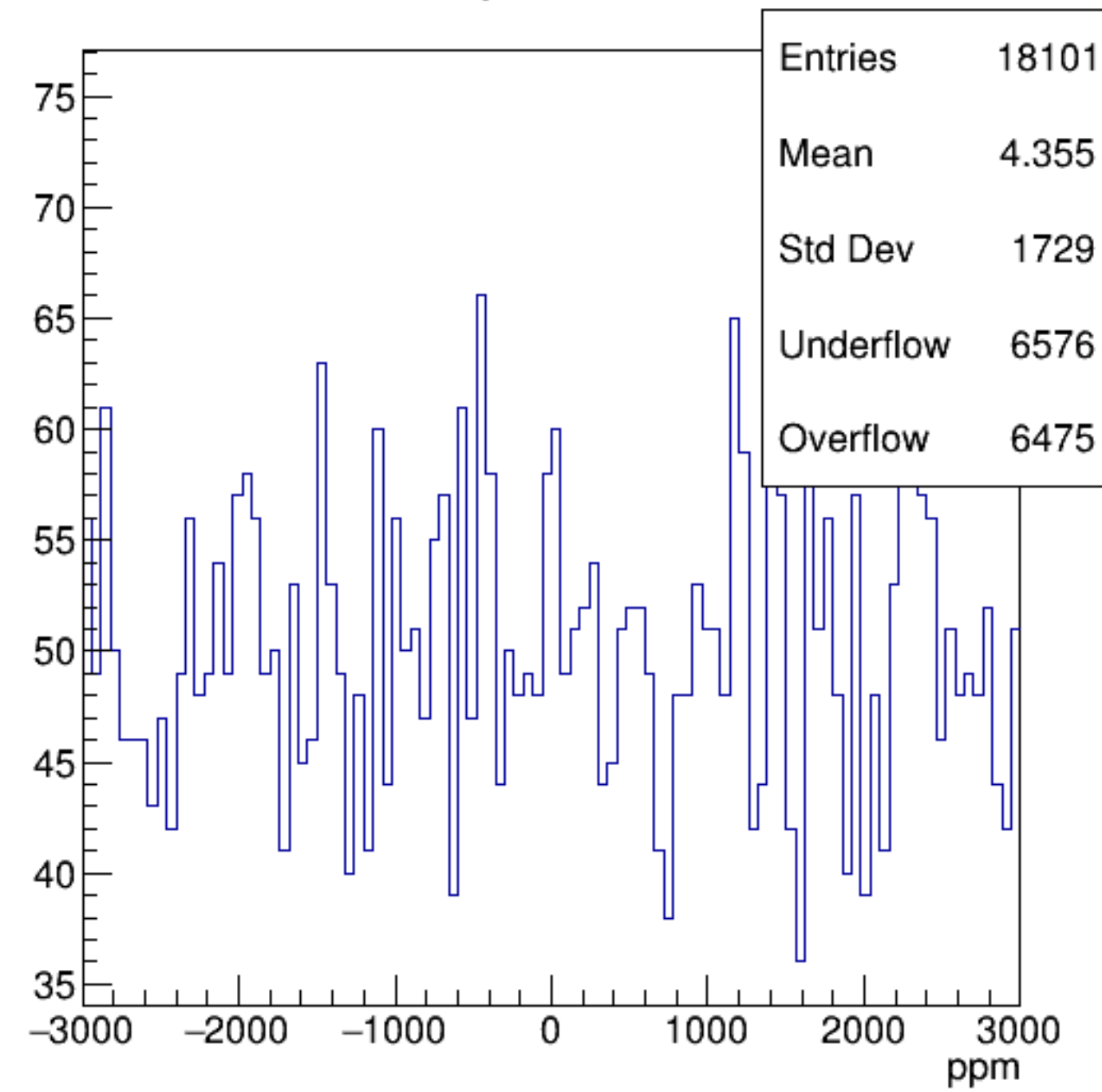
atr2*CurMon {ErrorFlag==0}



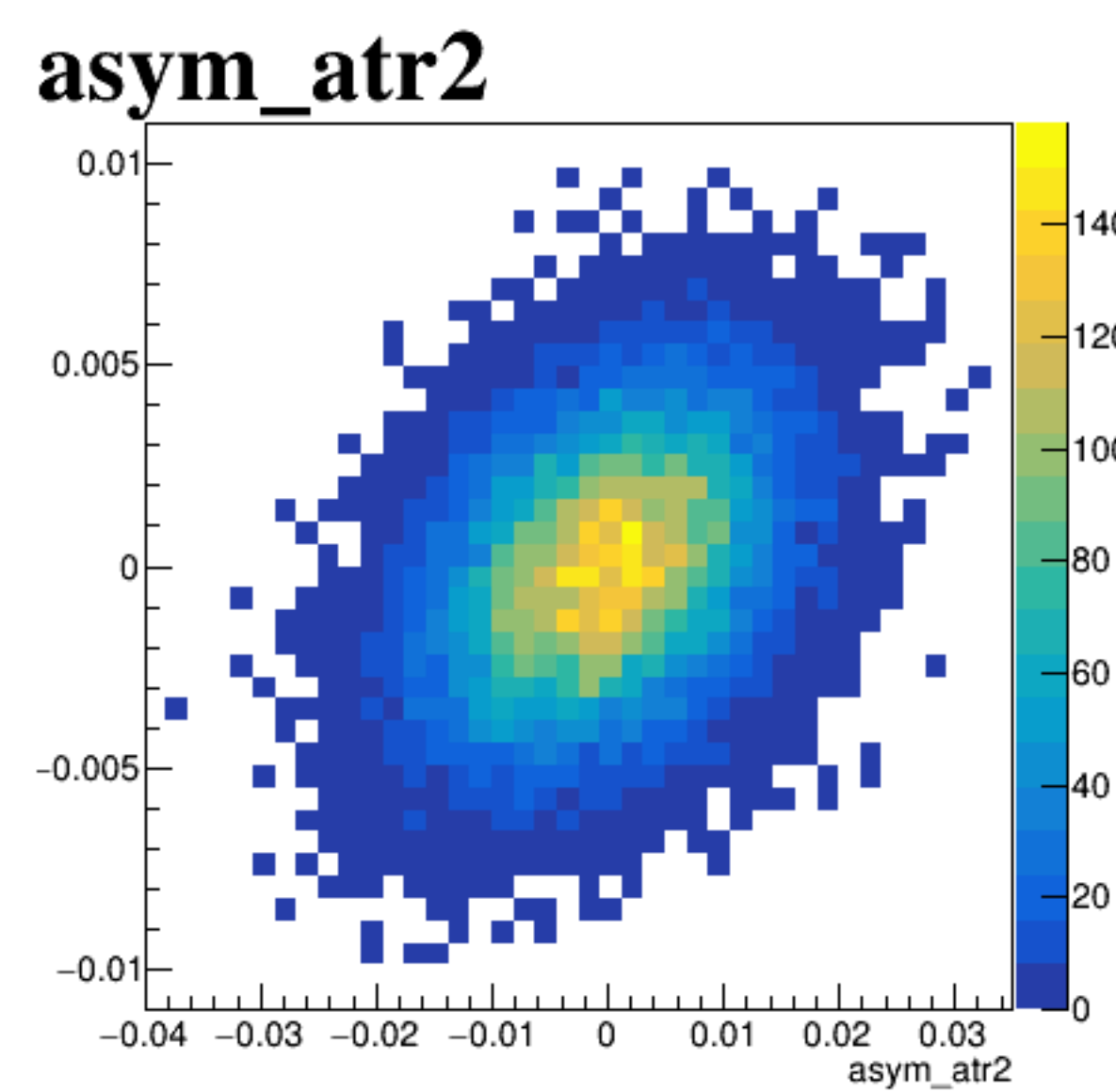
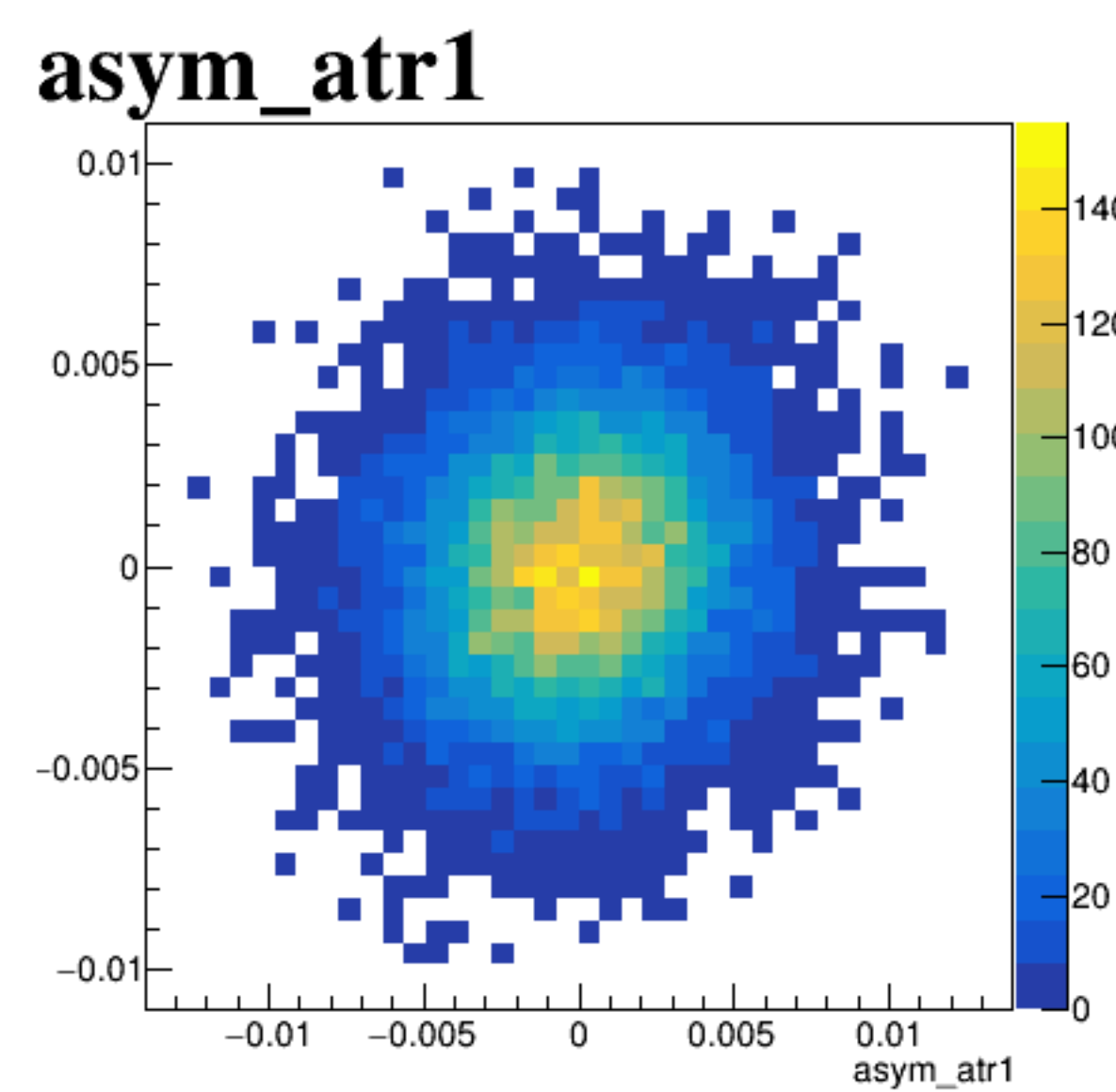
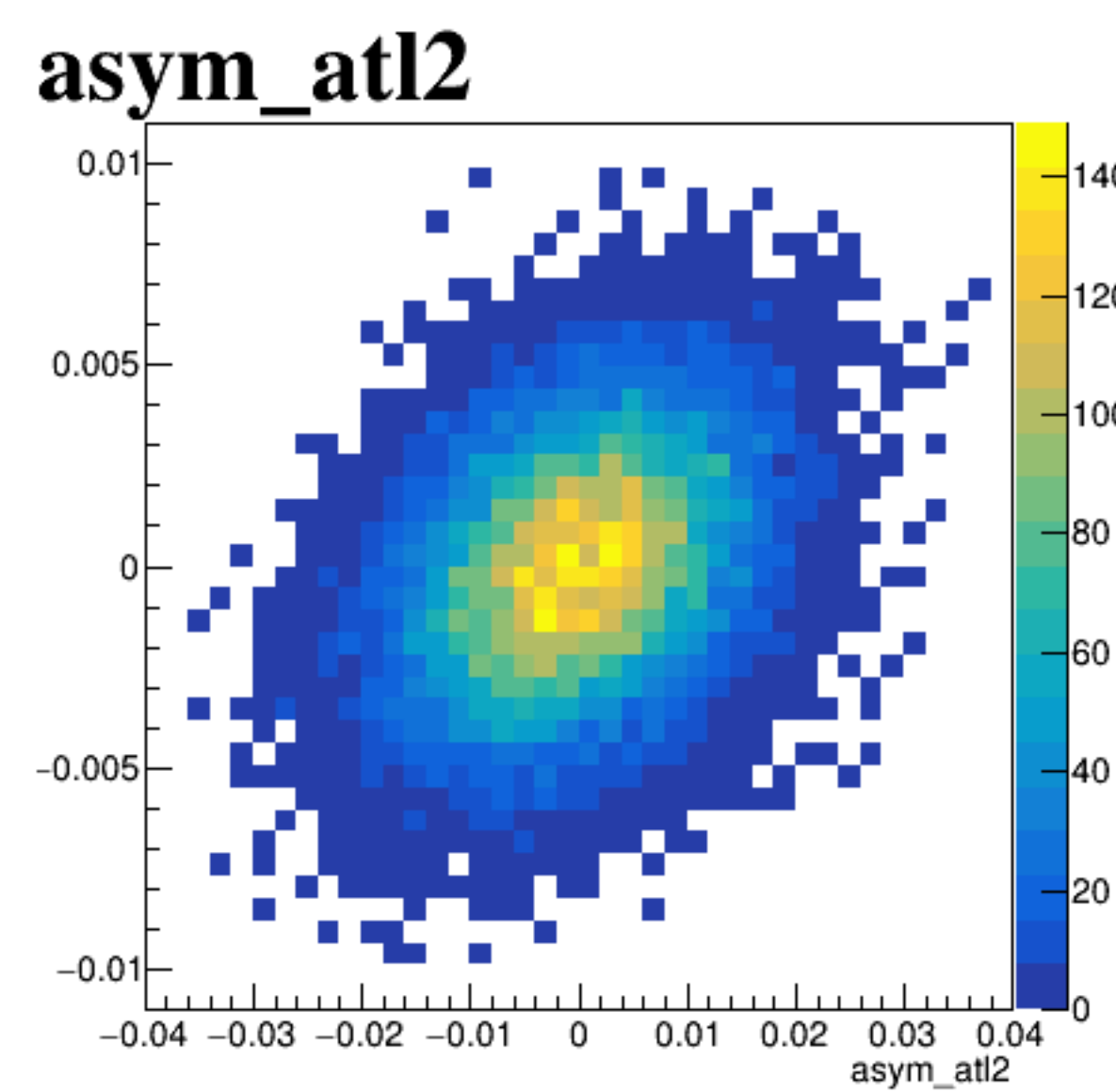
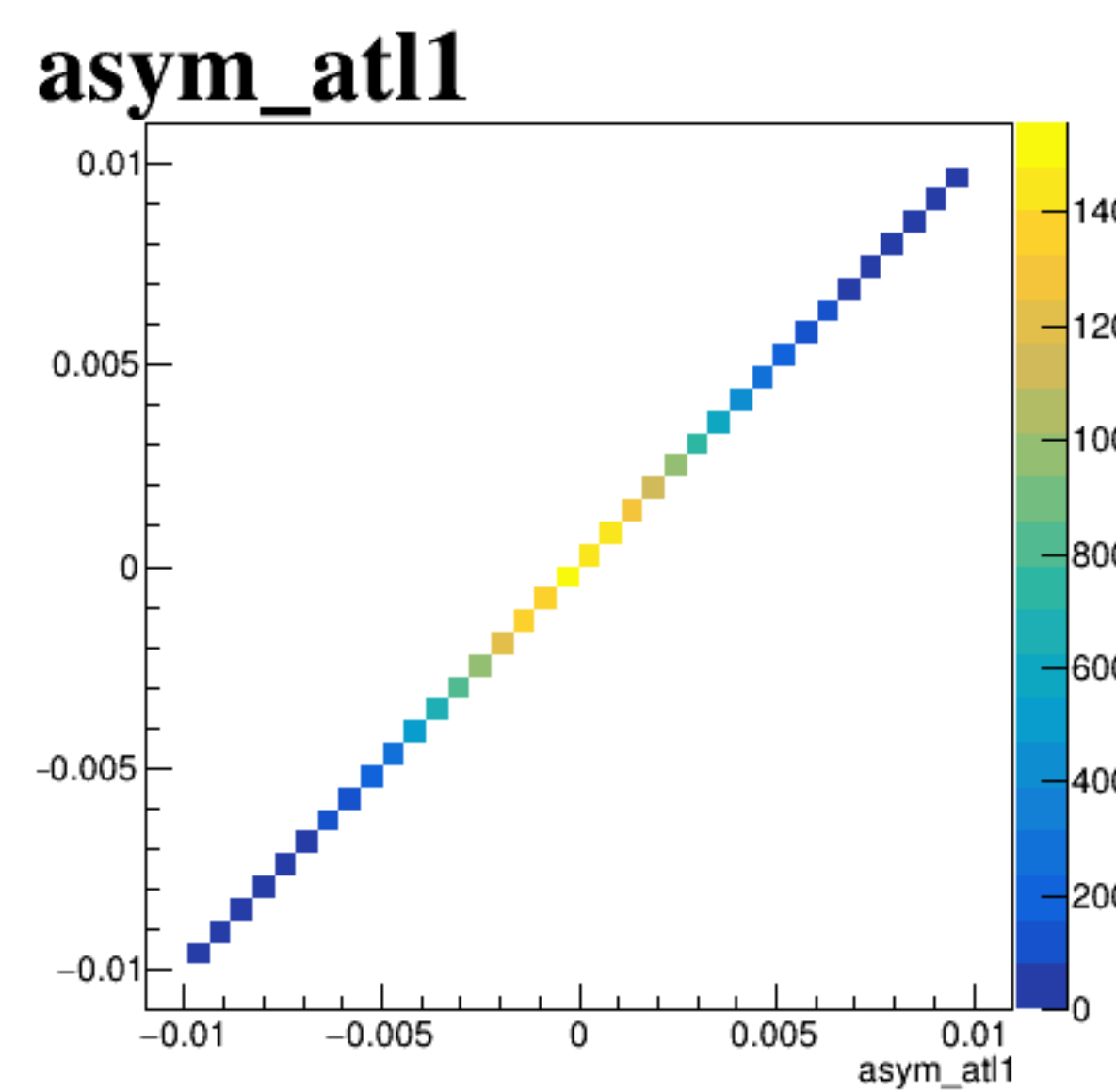
asym_atr2/ppm:pattern_number {ErrorFlag==0}



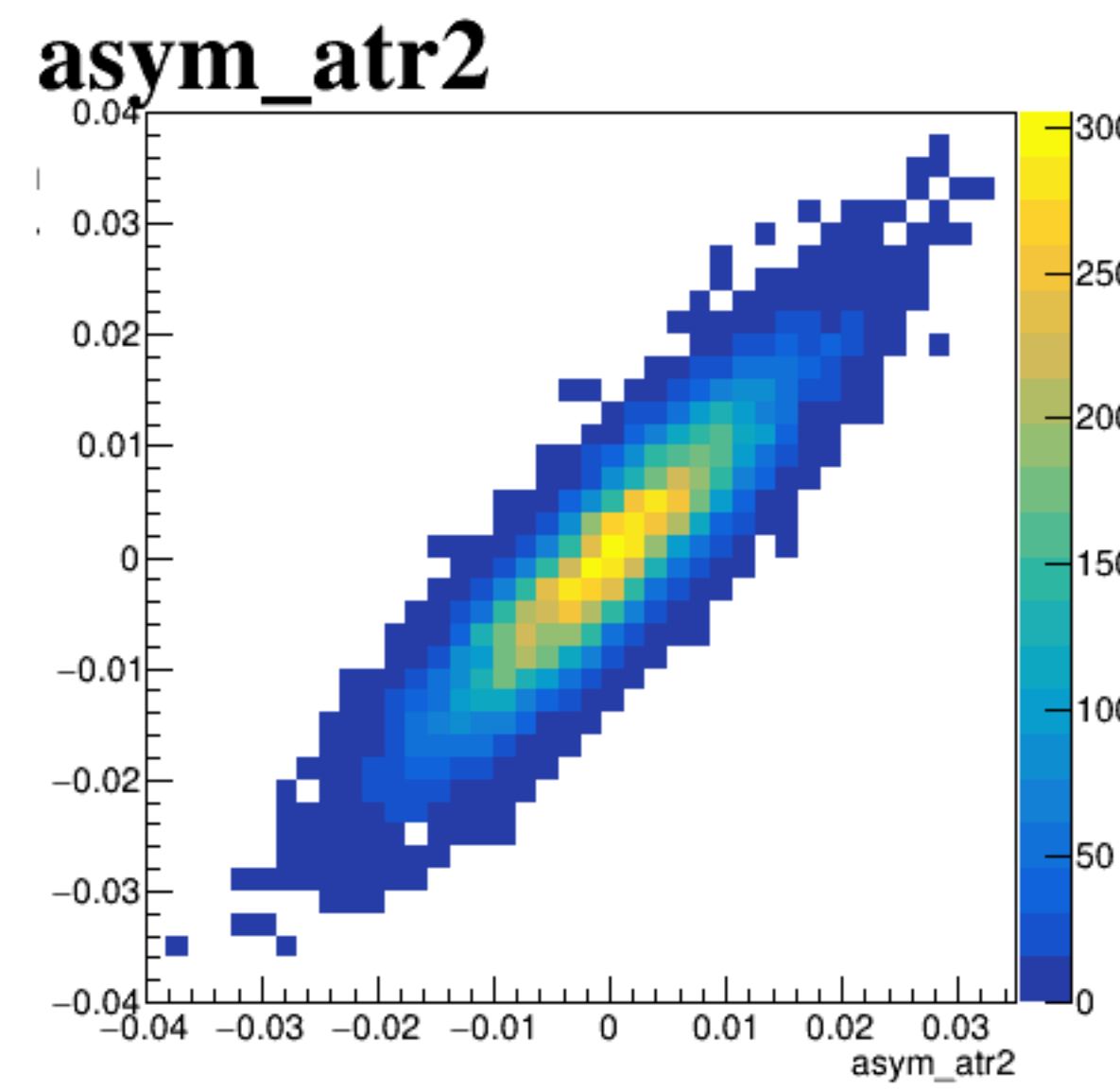
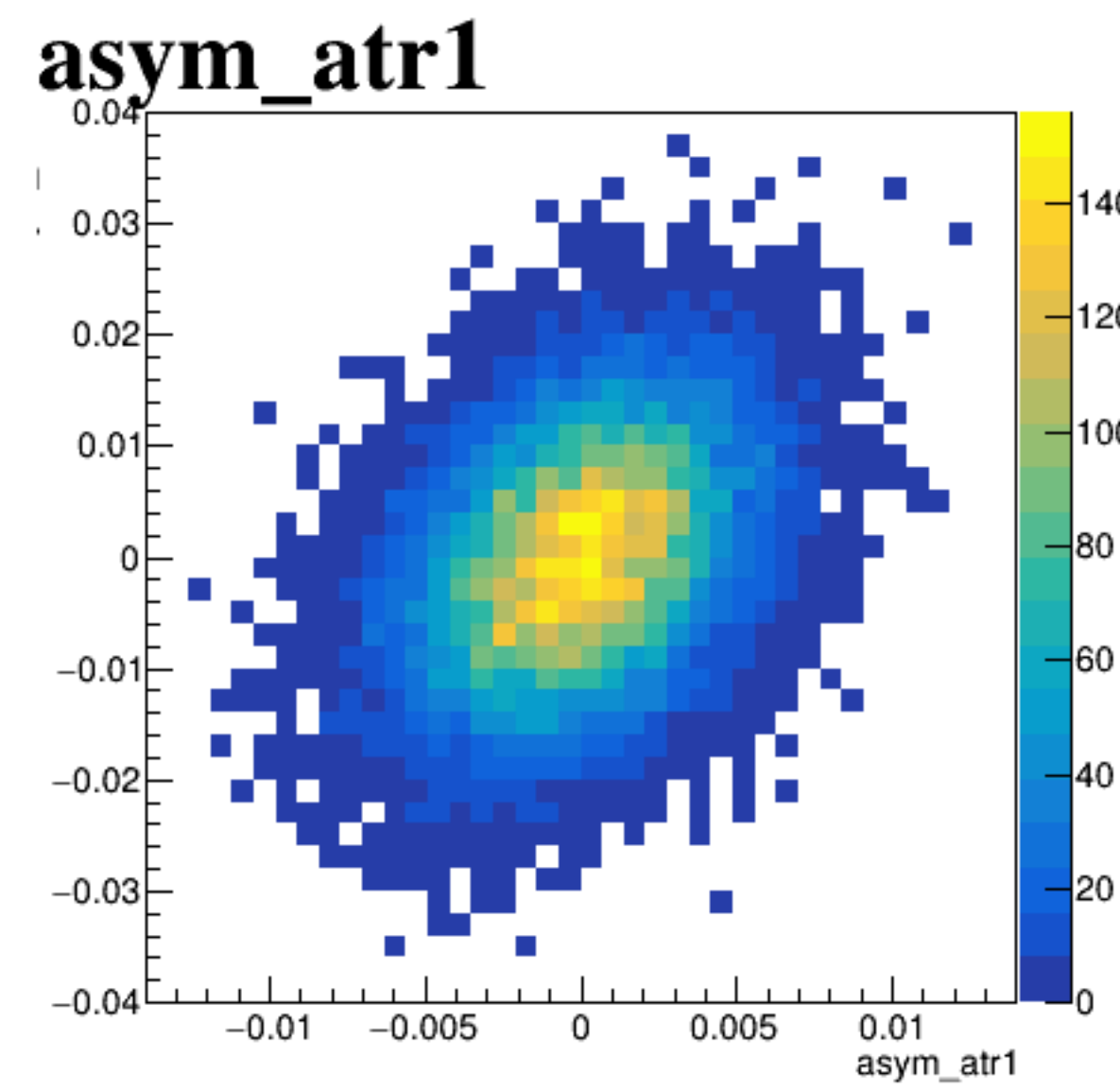
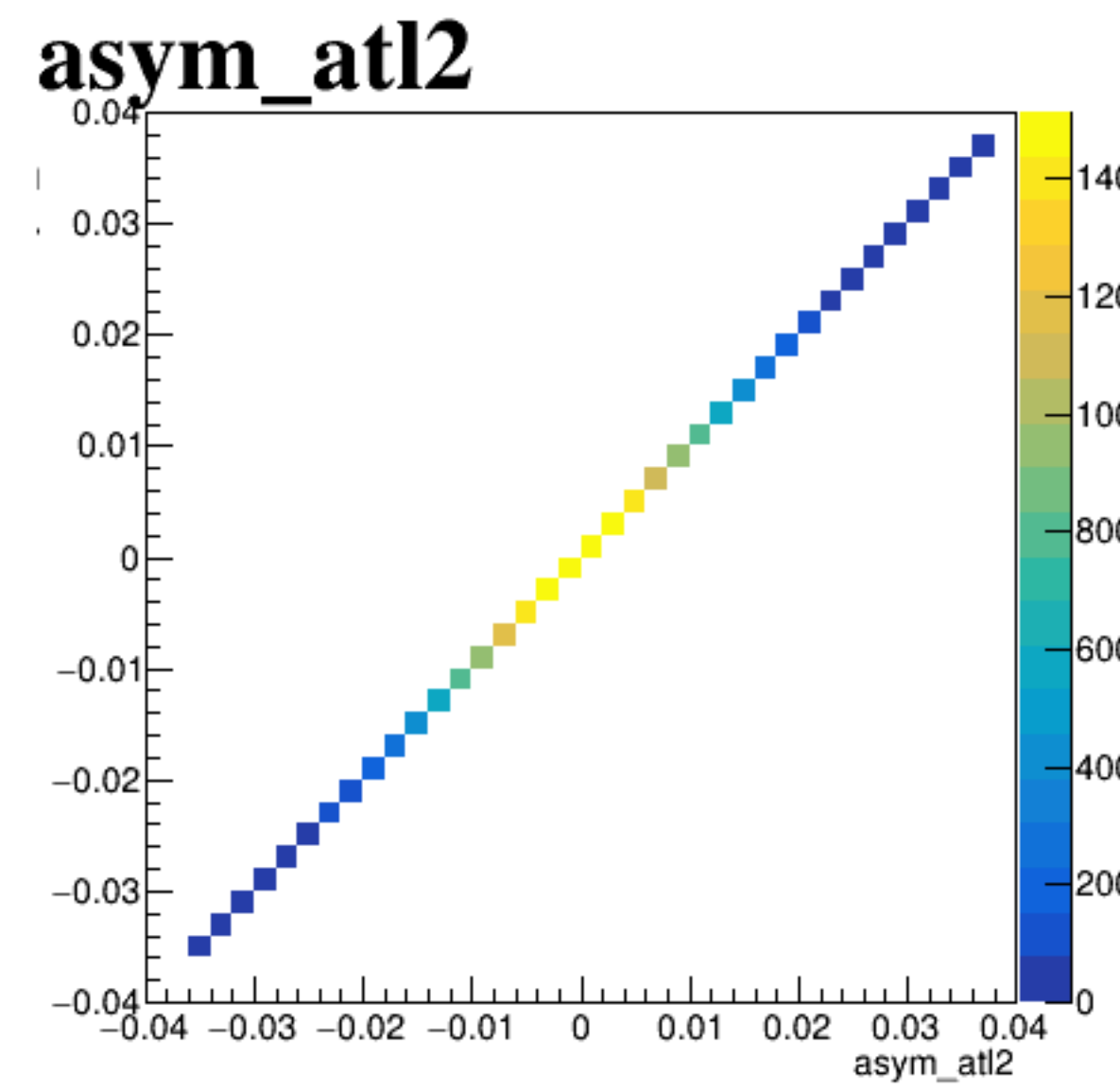
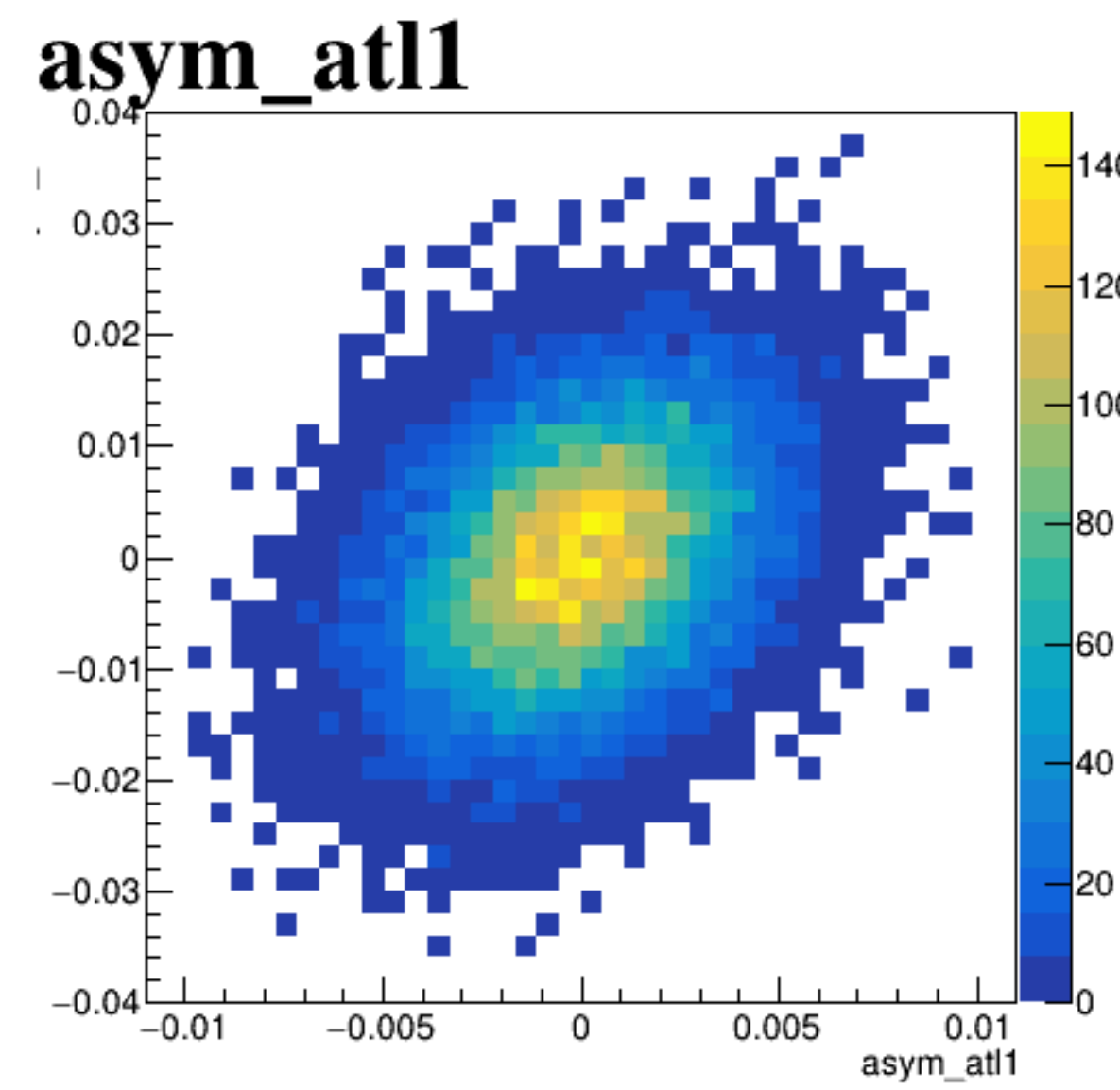
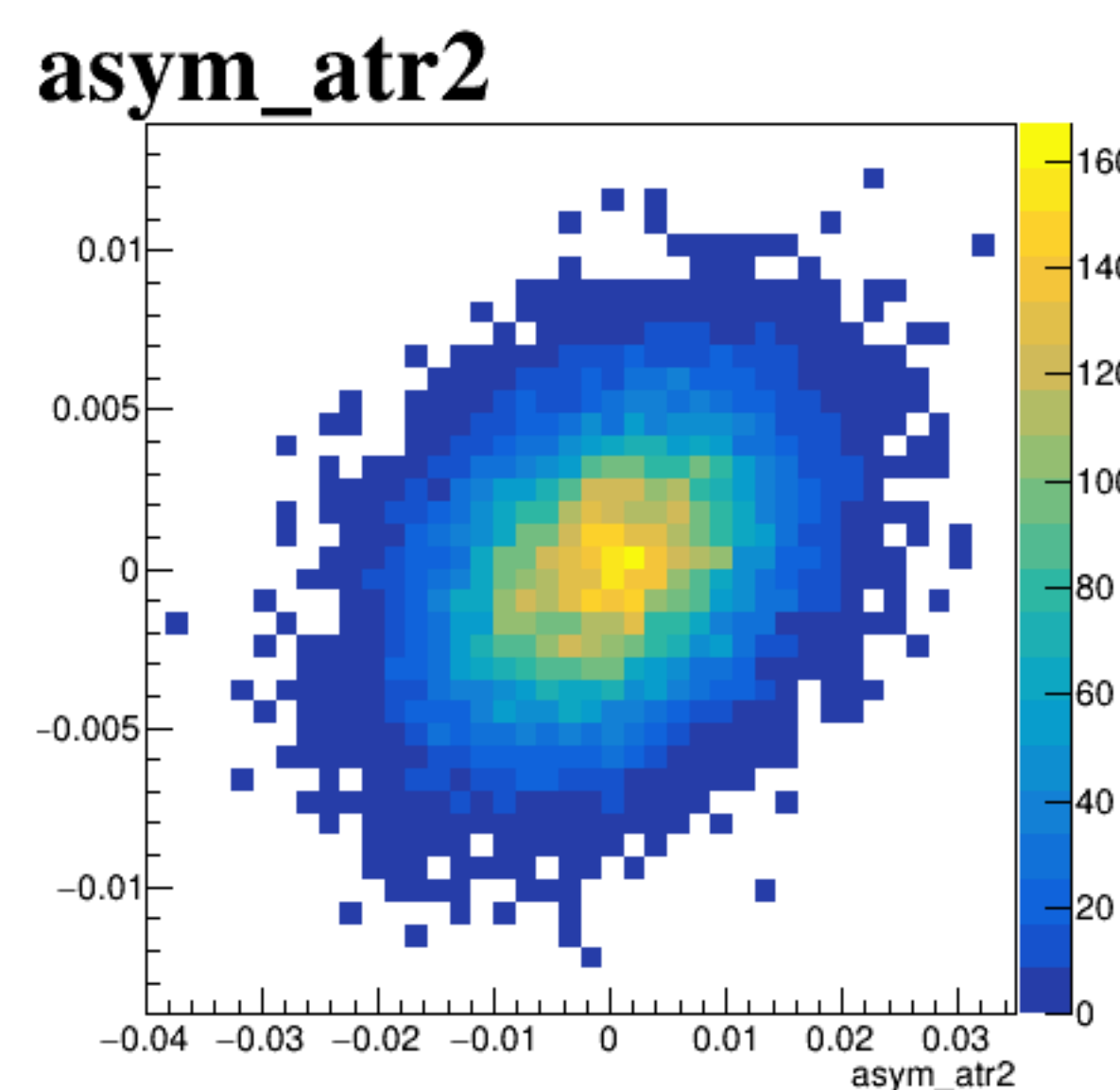
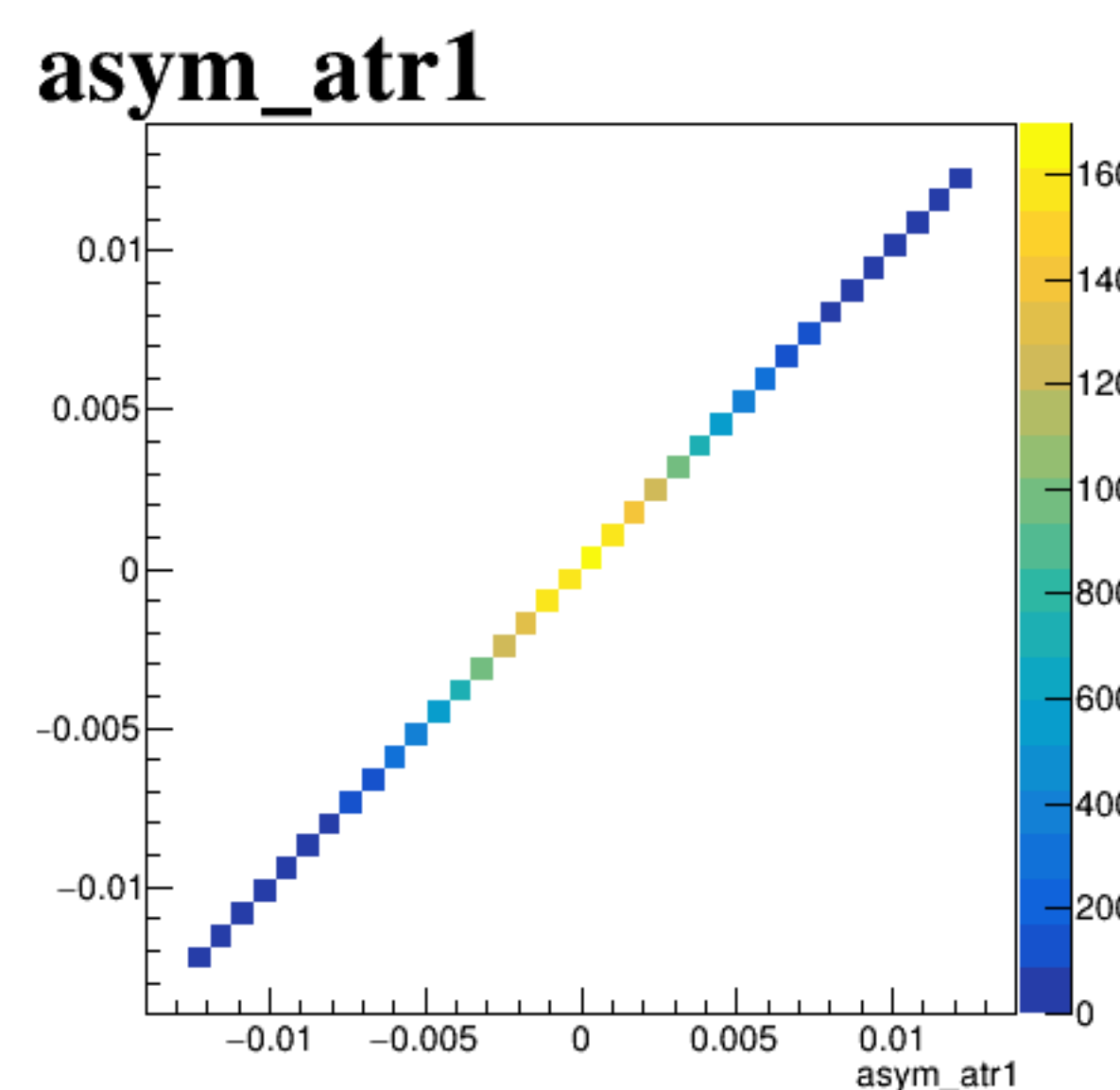
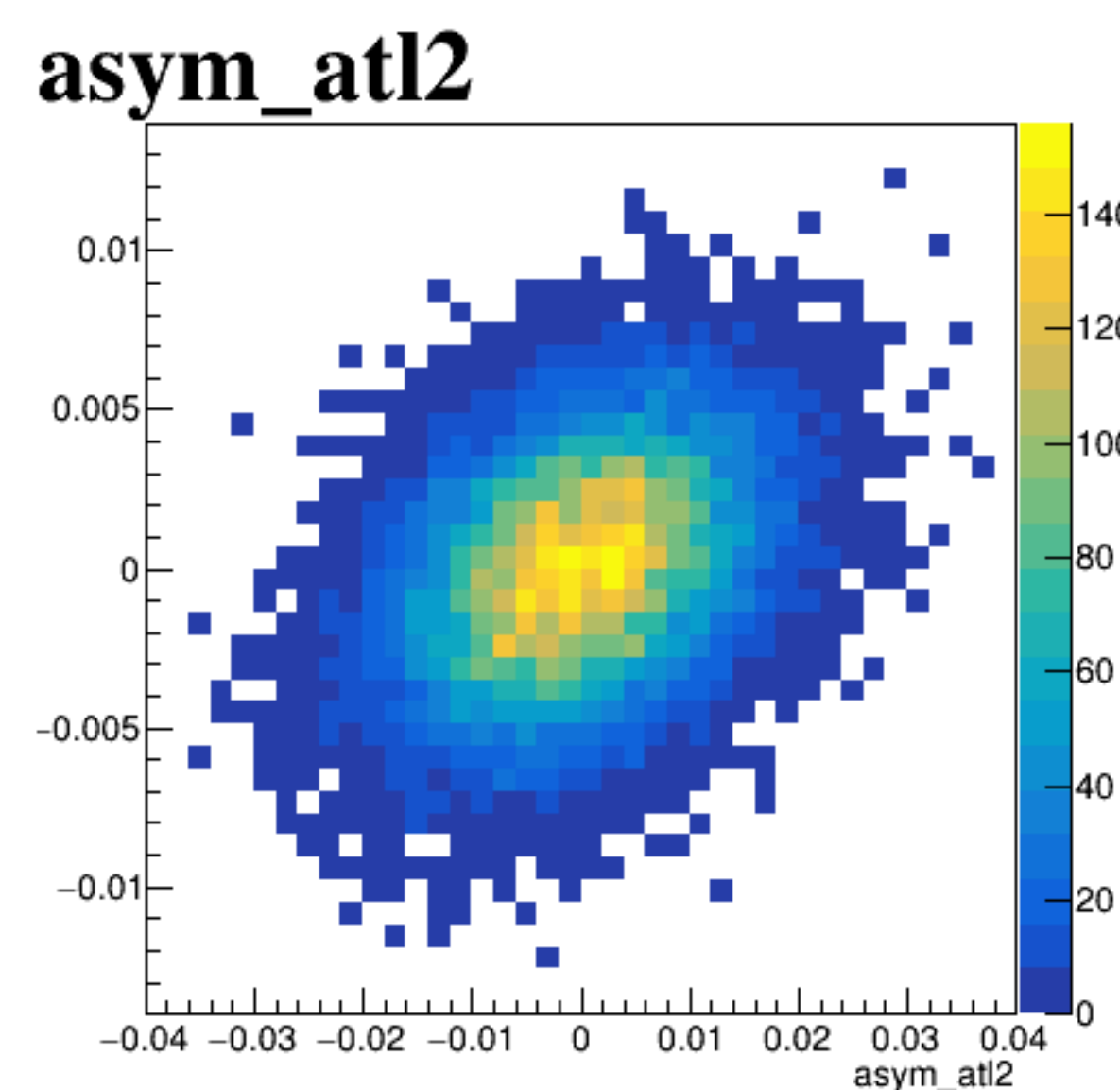
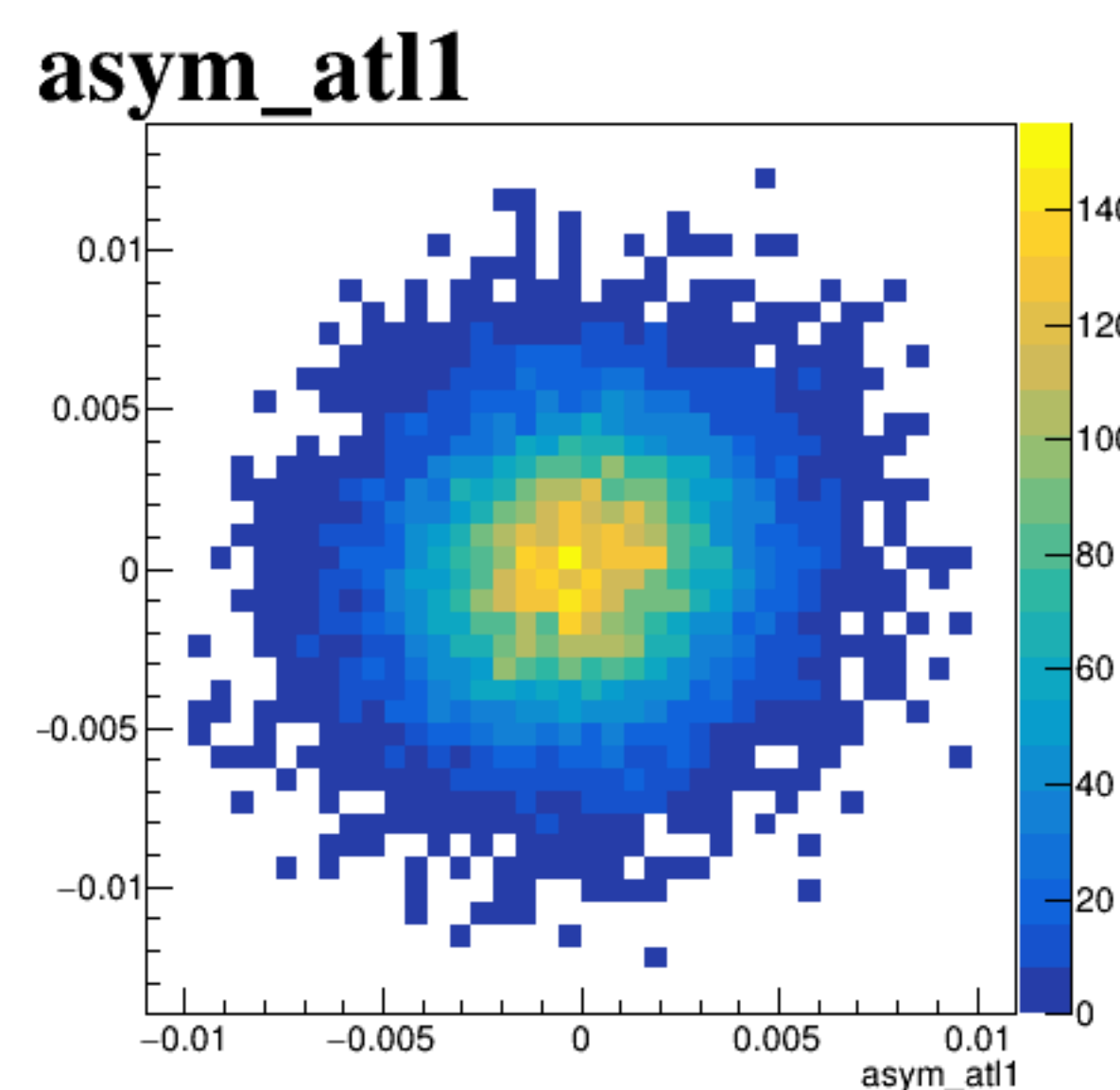
asym_atr2



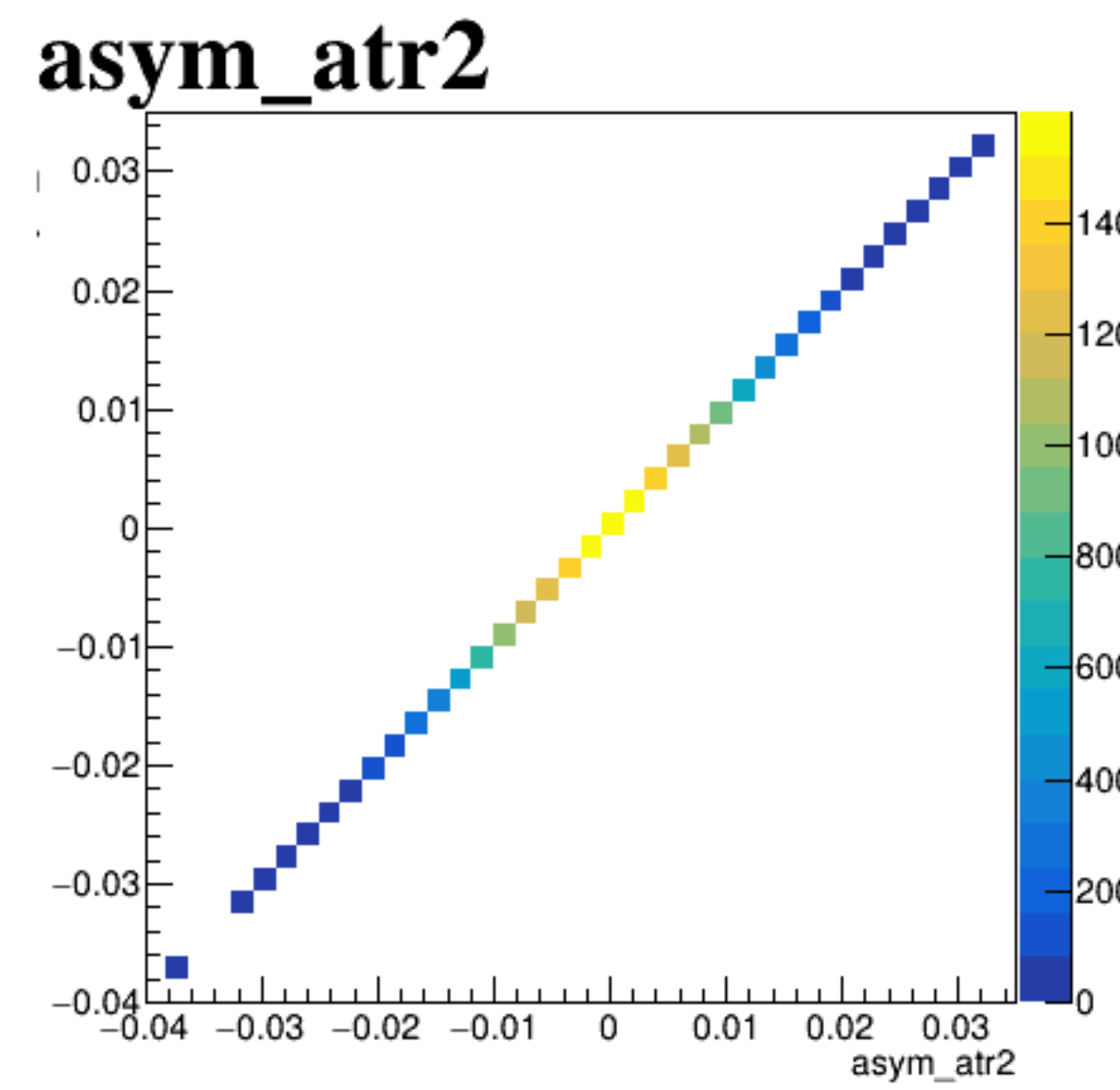
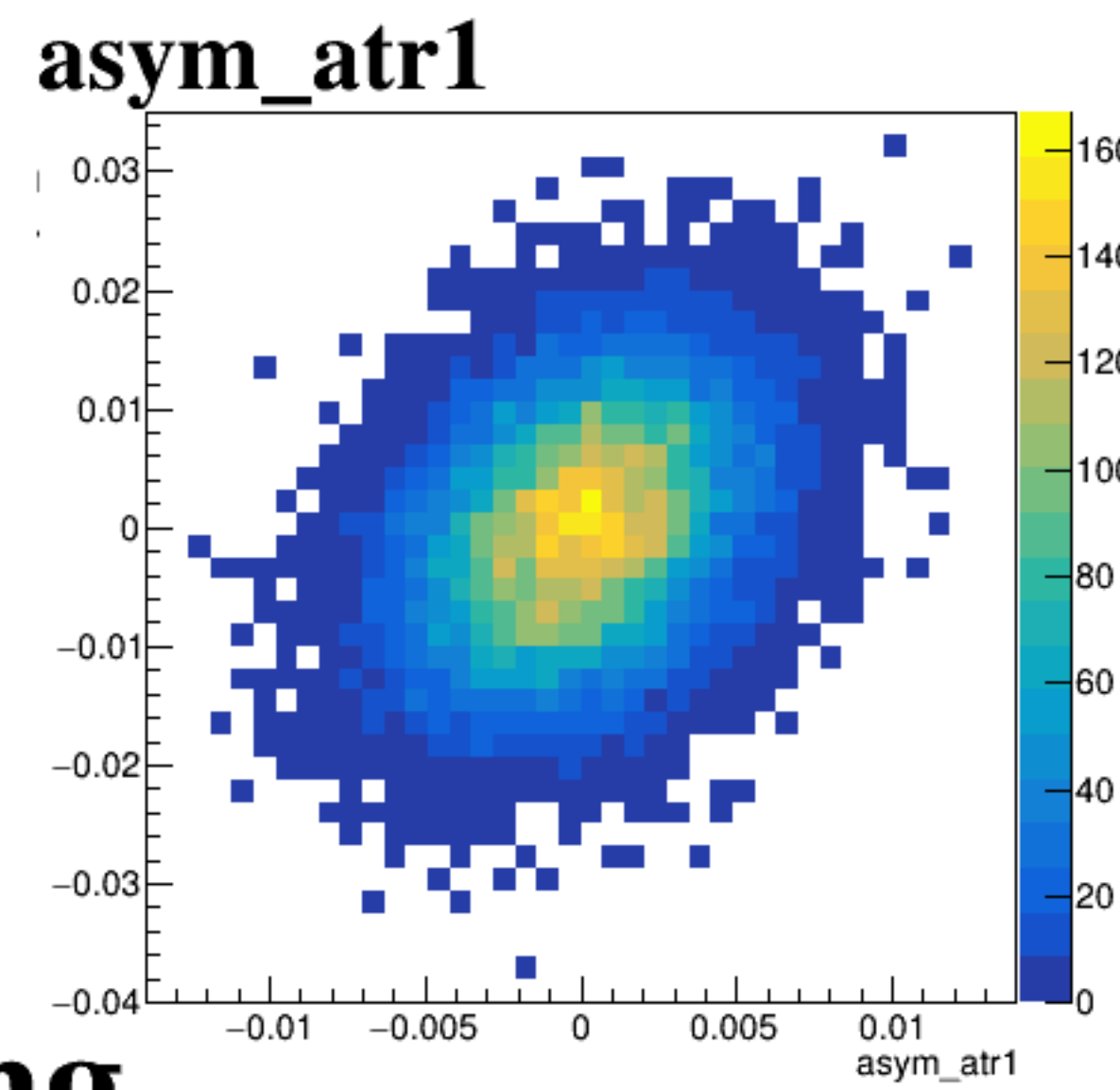
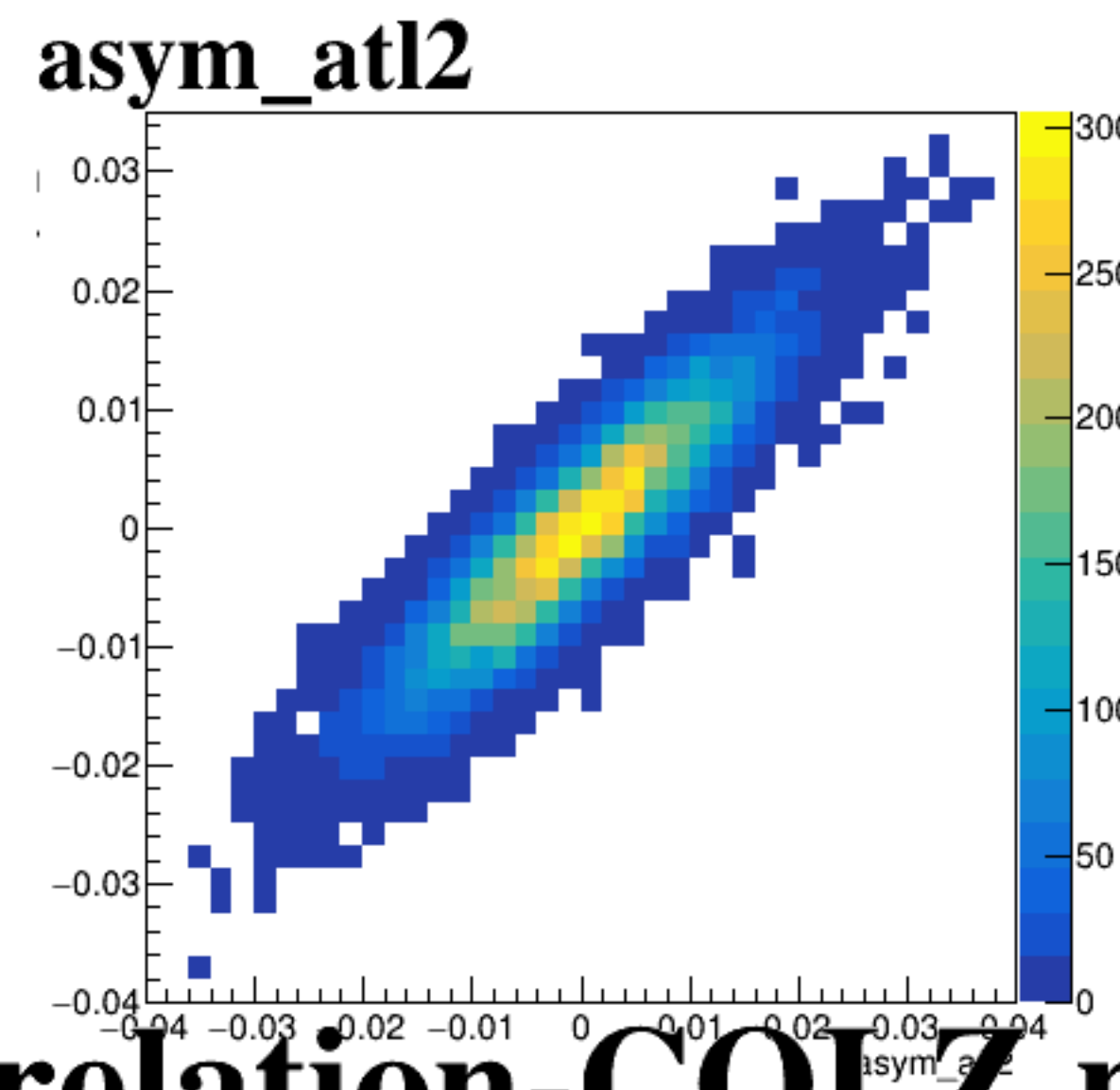
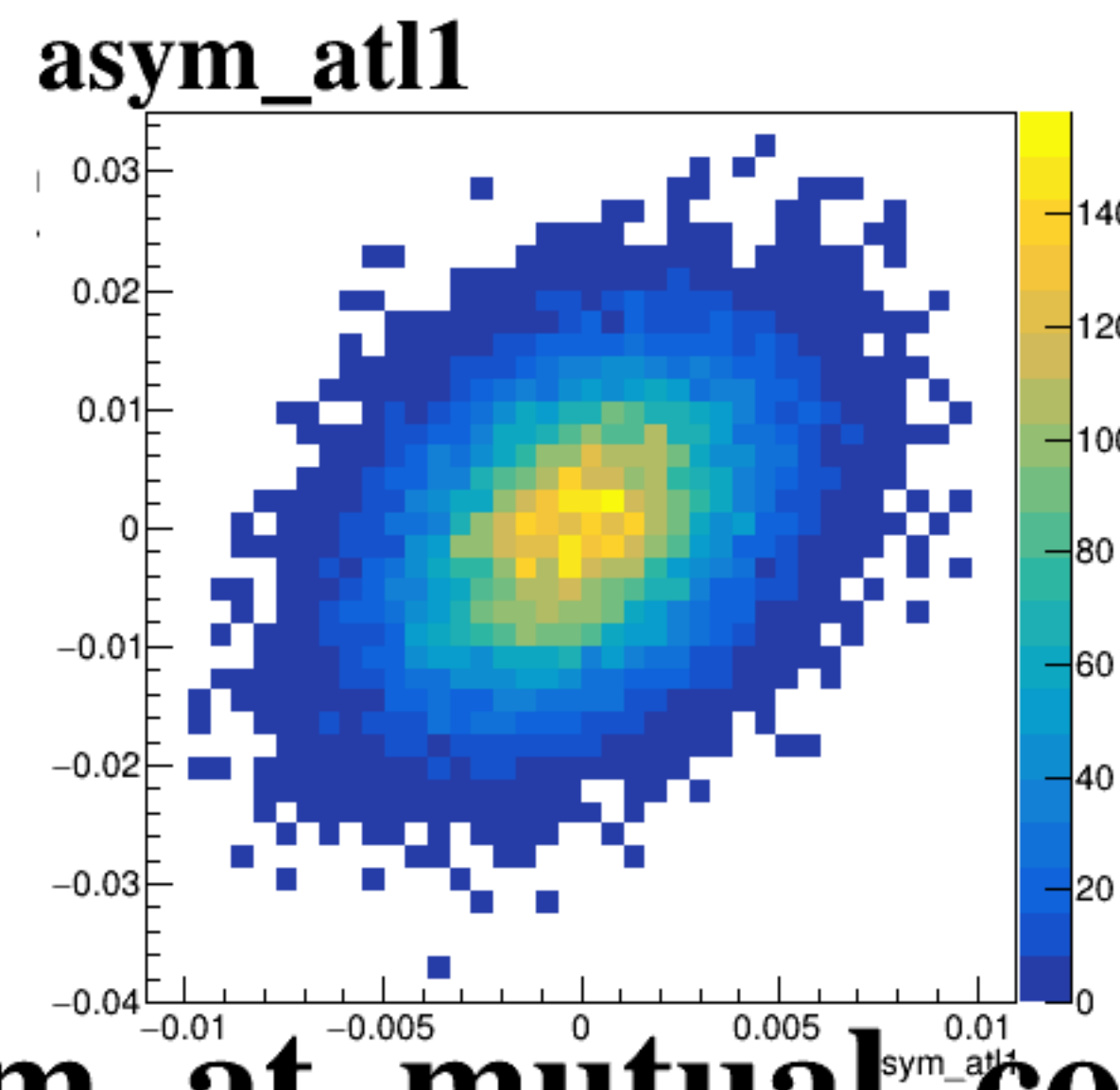
asym_atl1



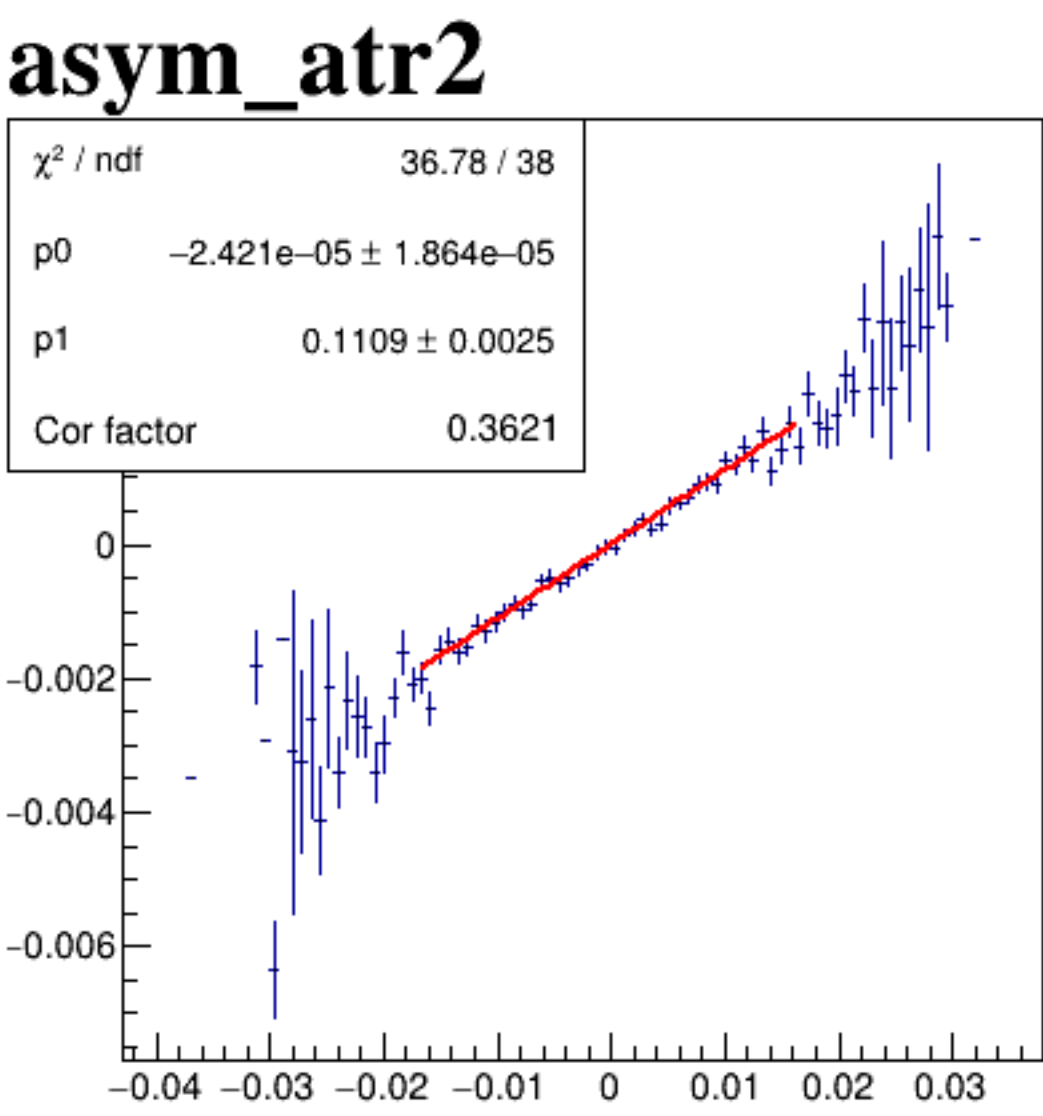
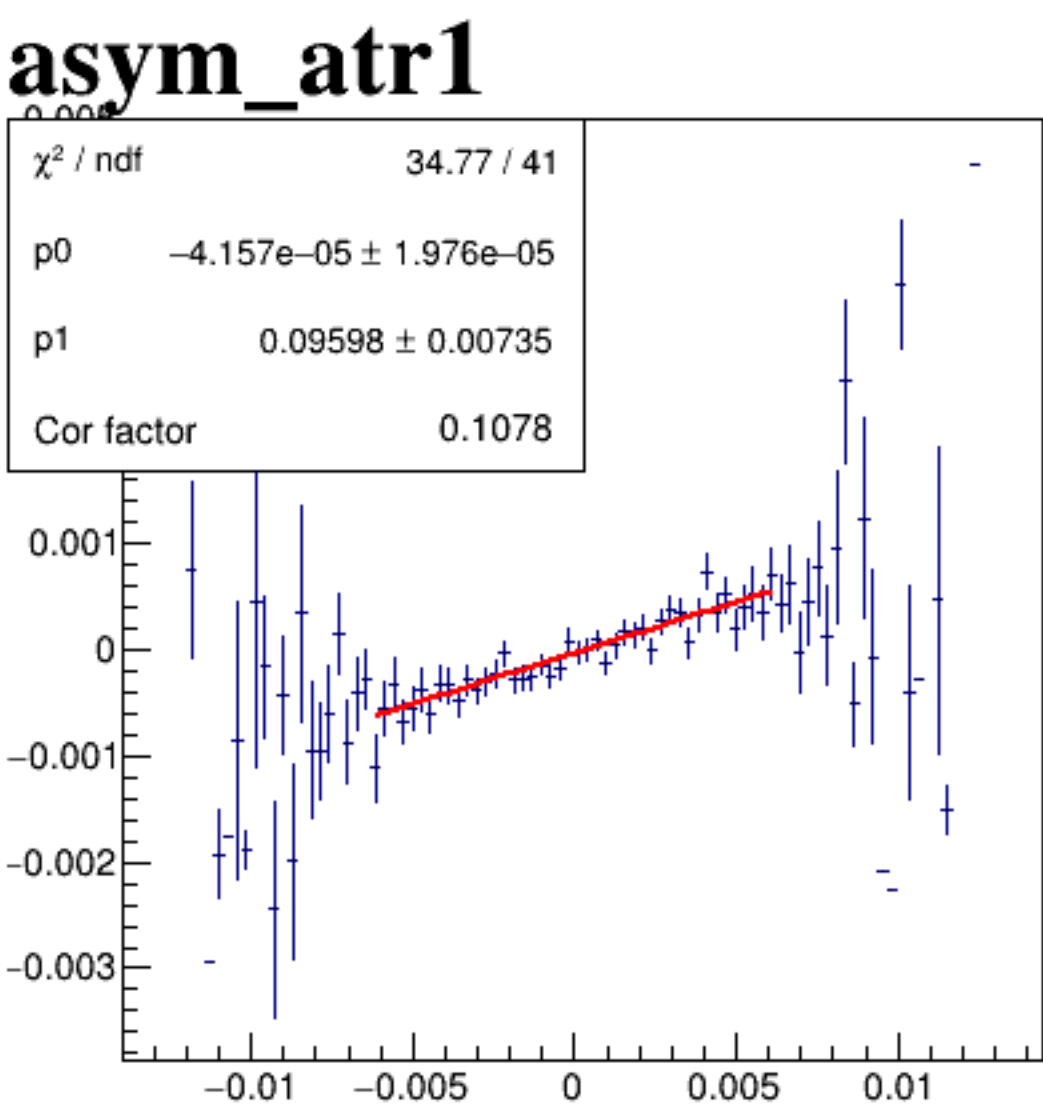
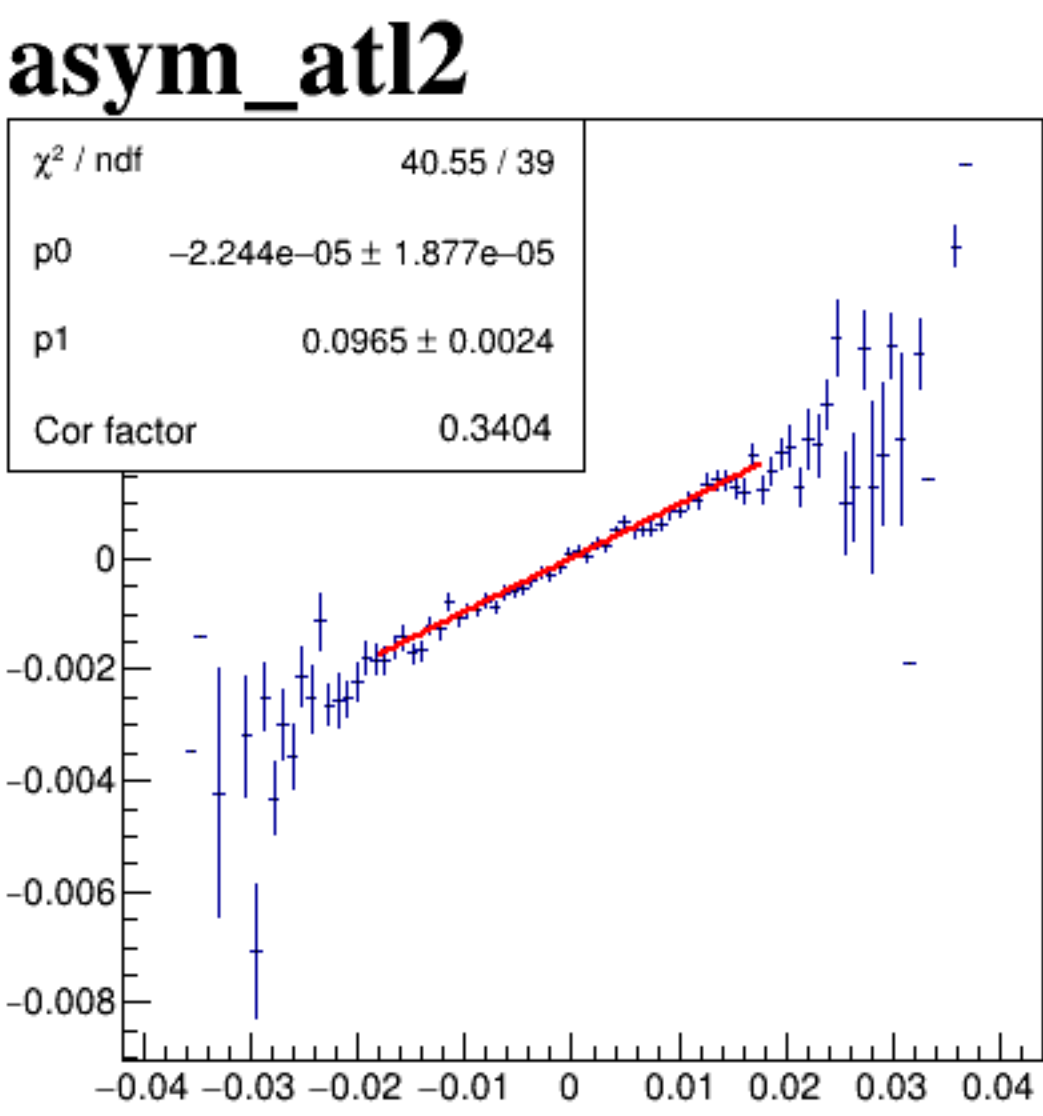
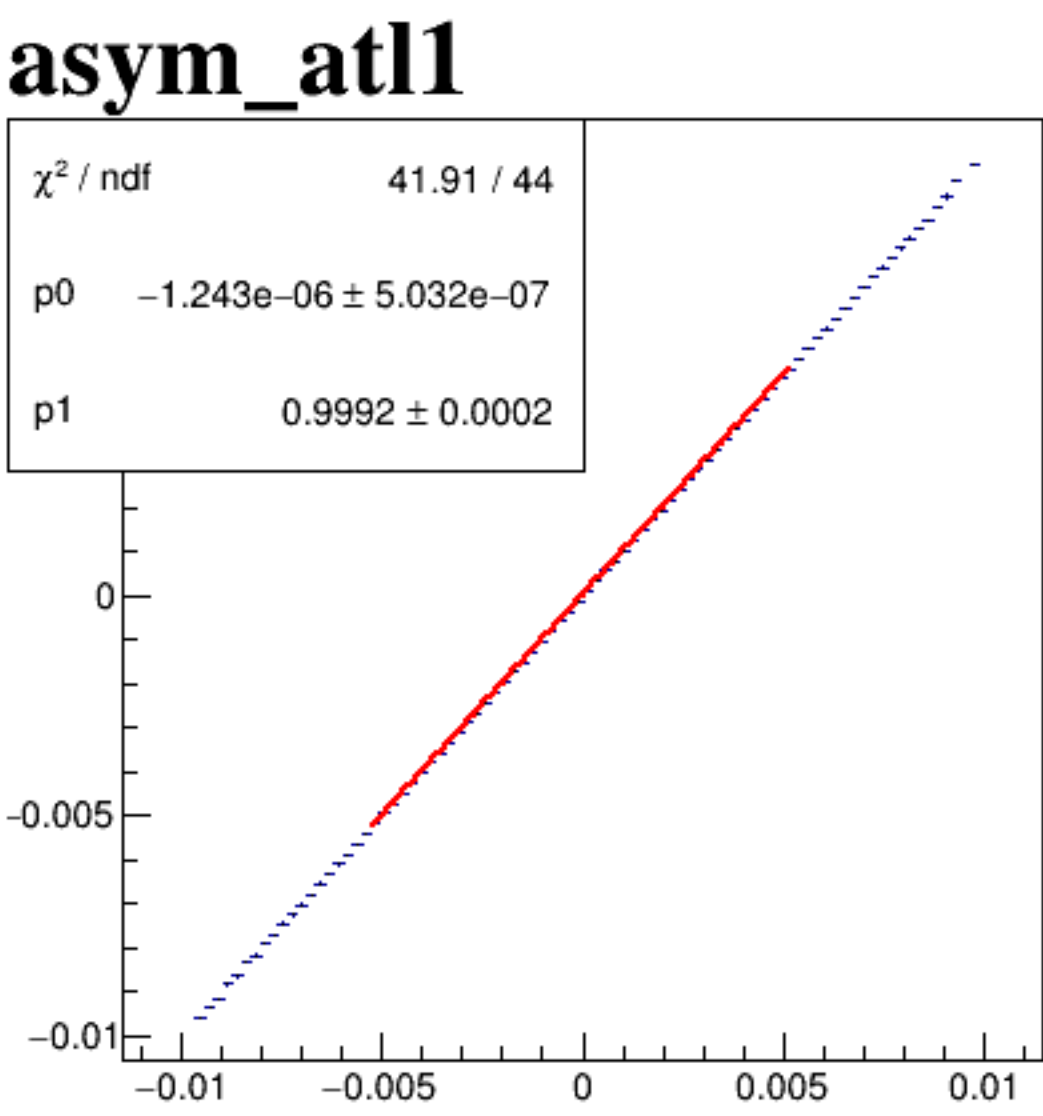
asym_atl2

**asym_atr1**

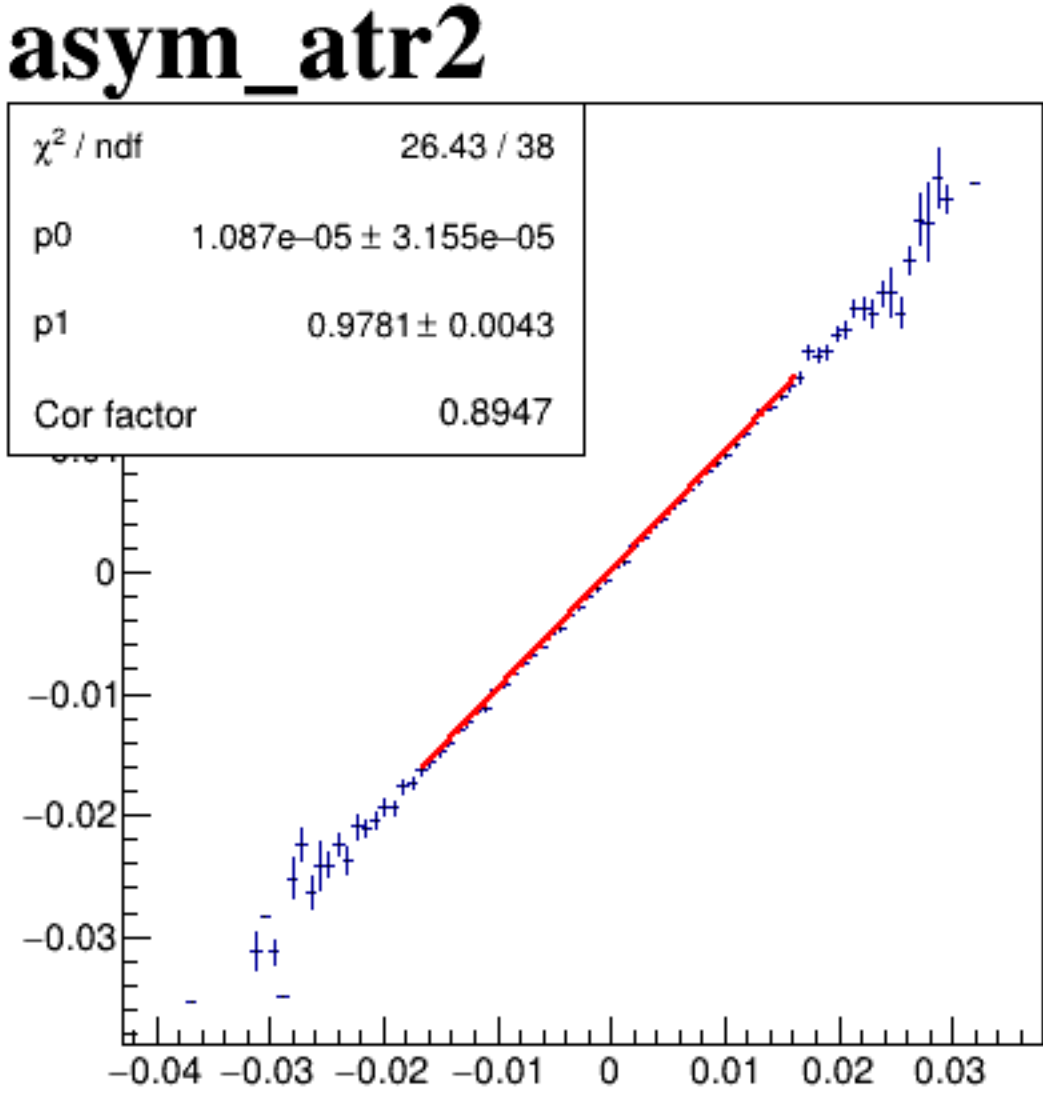
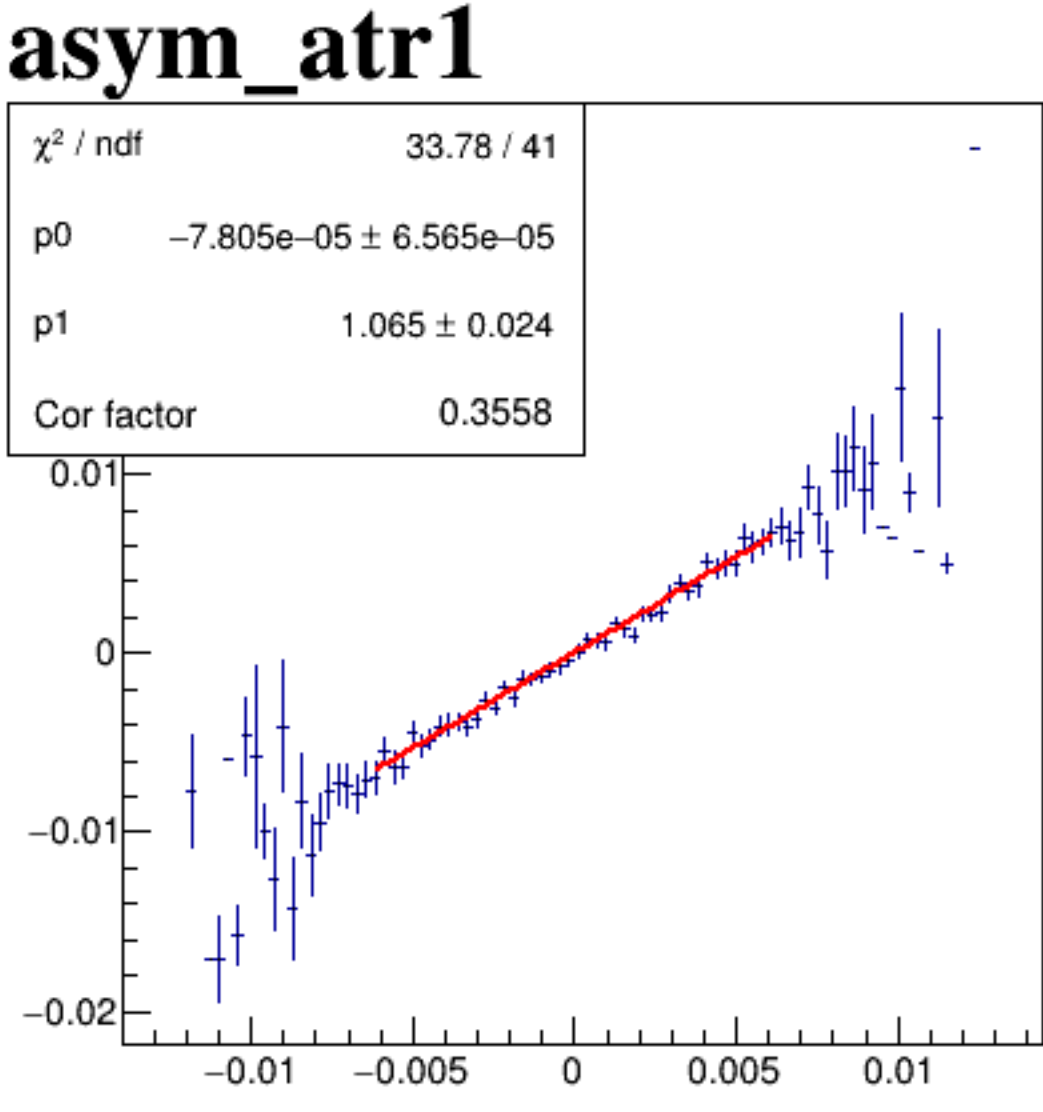
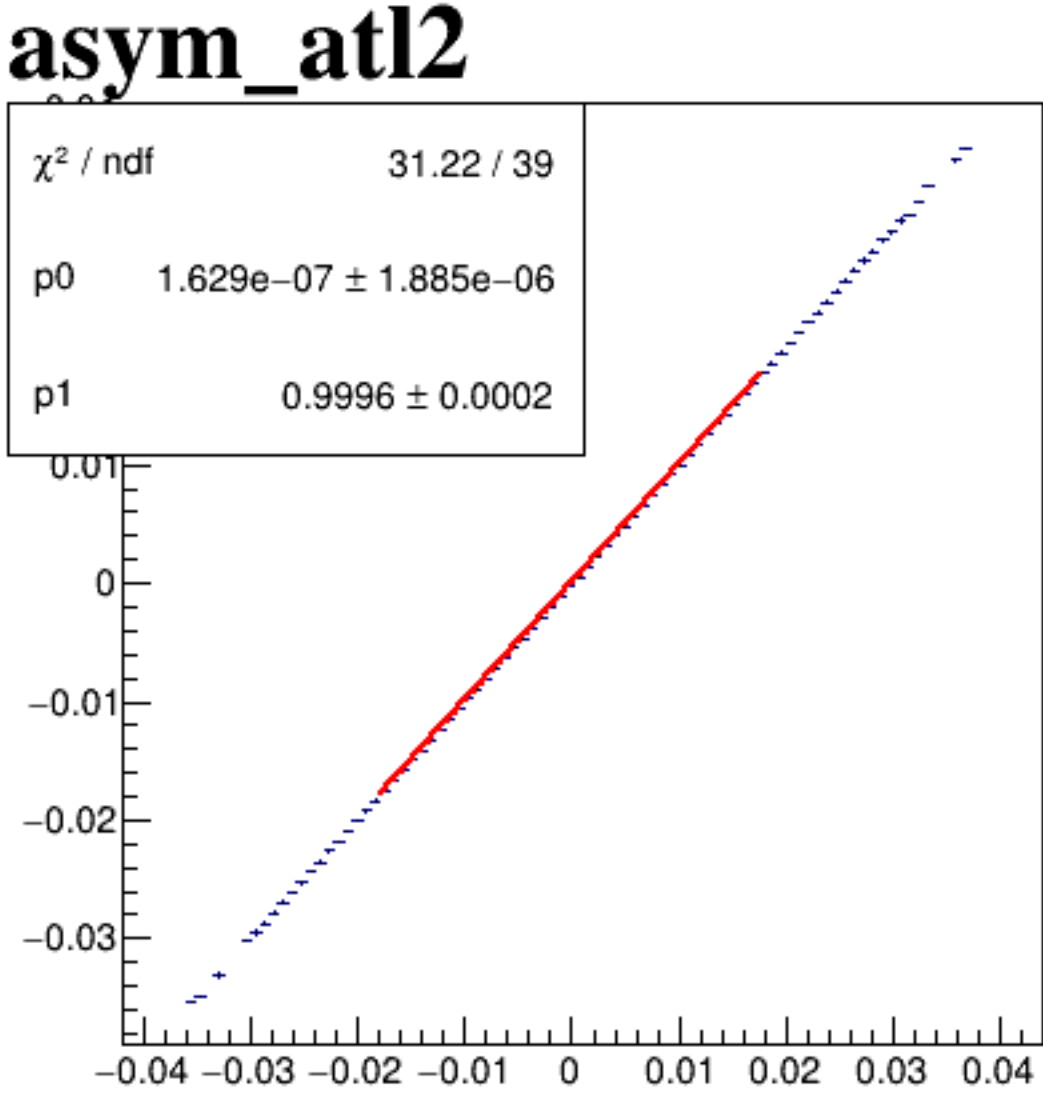
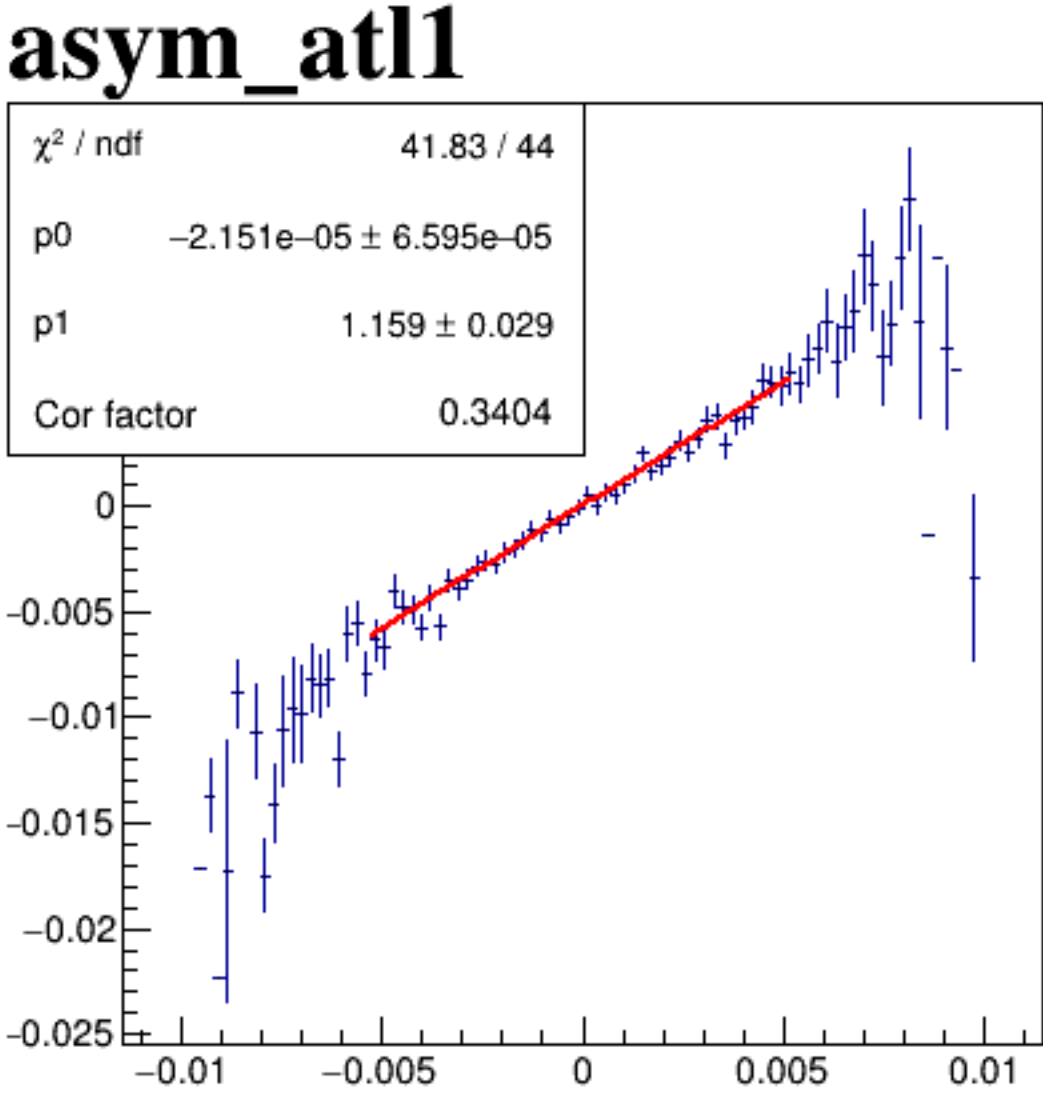
asym_atr2



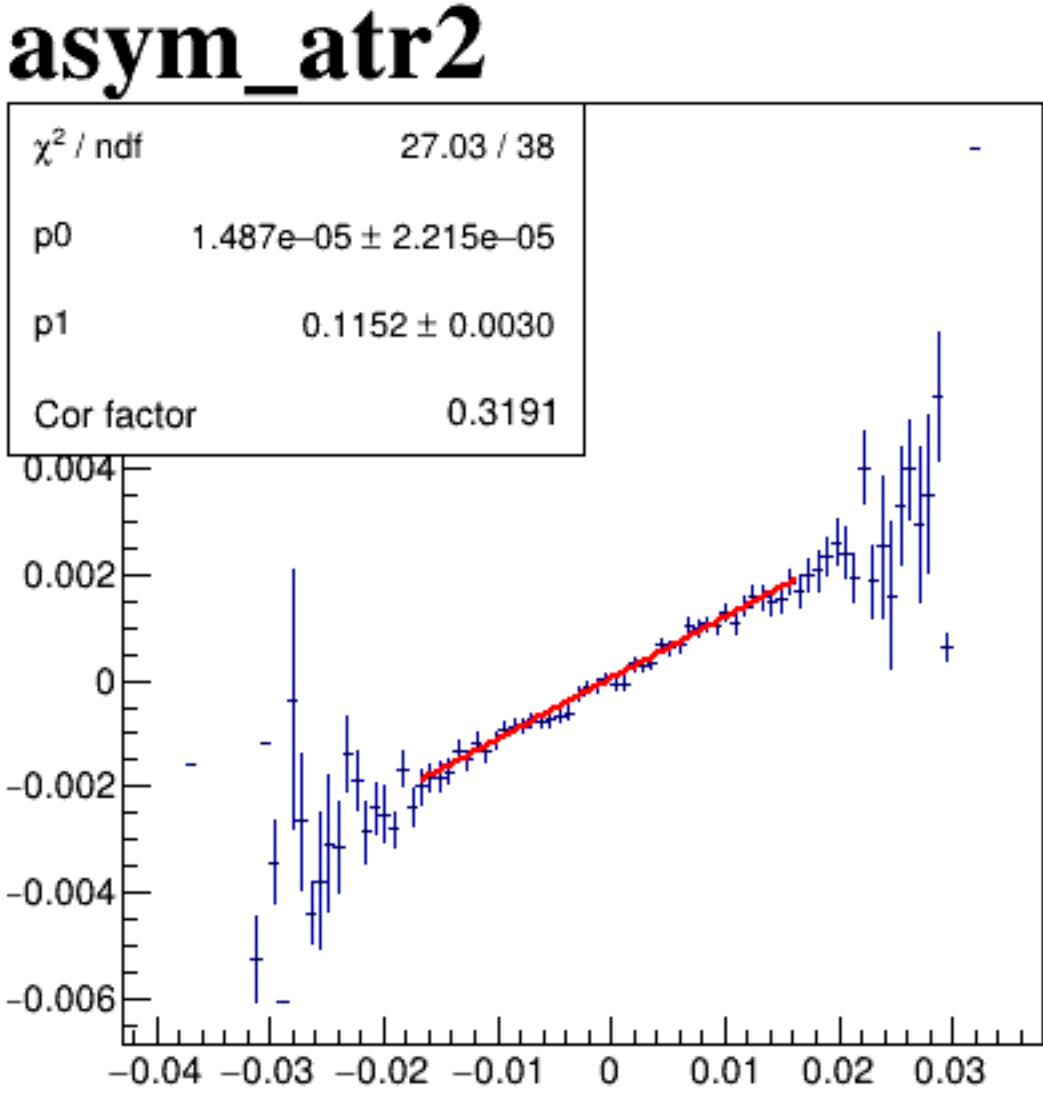
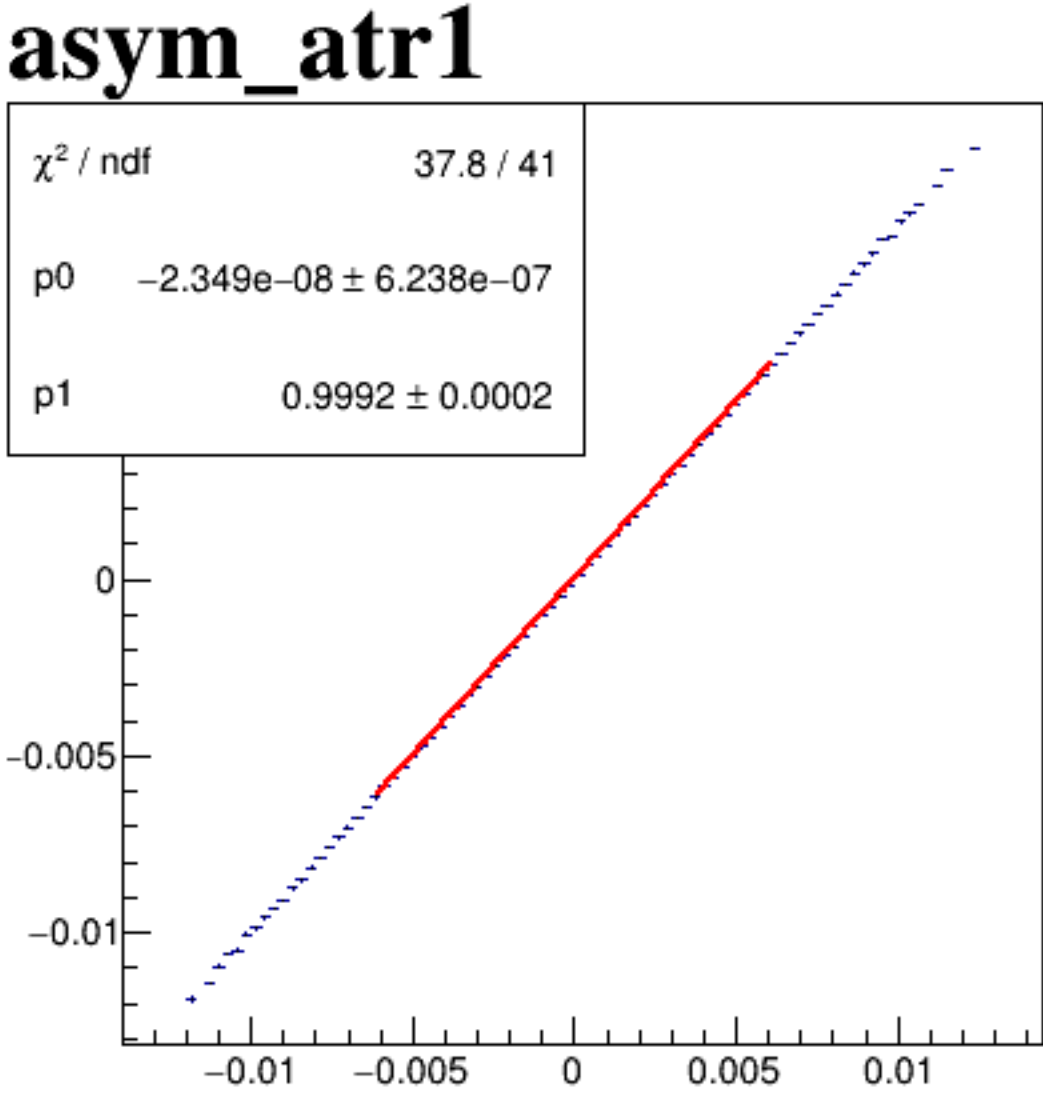
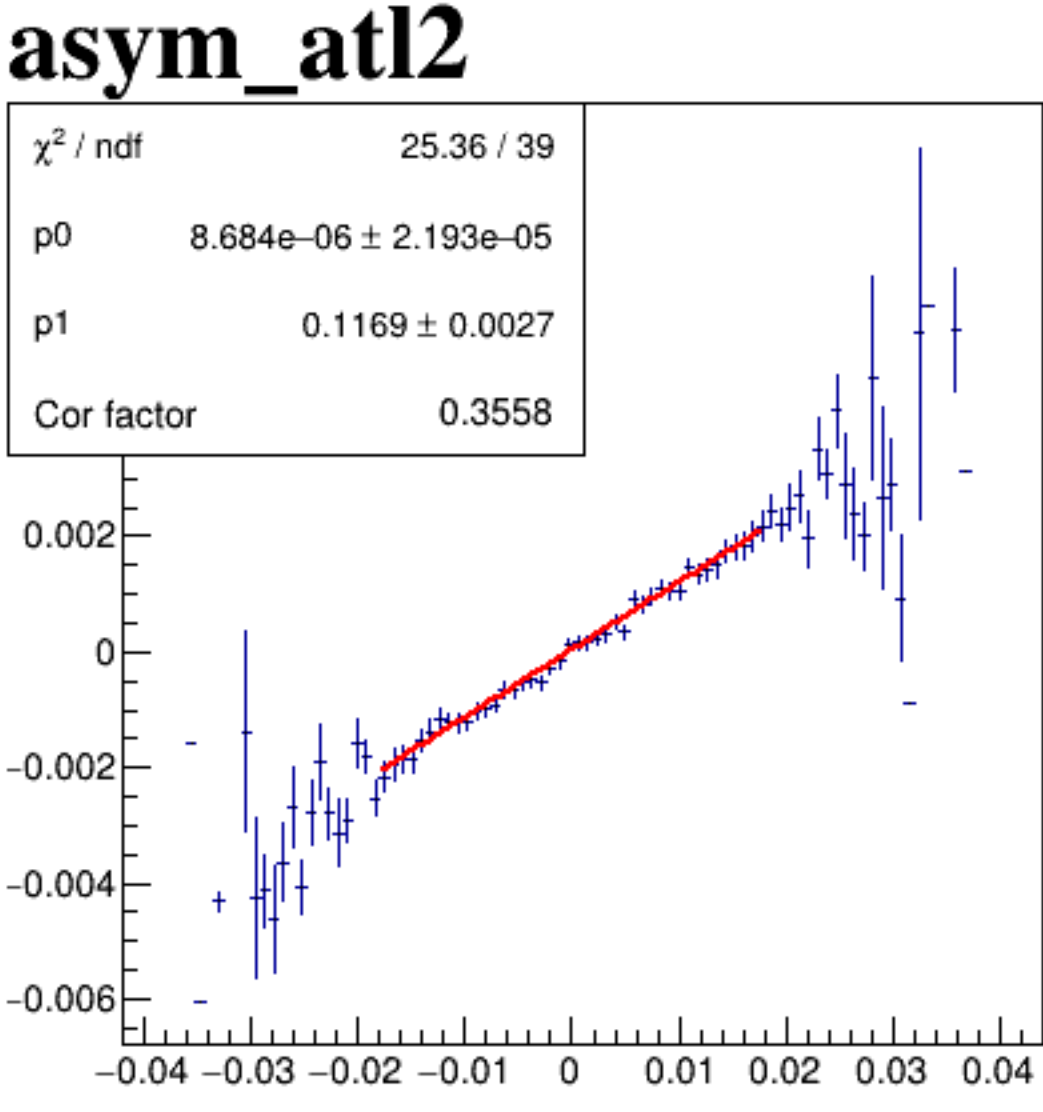
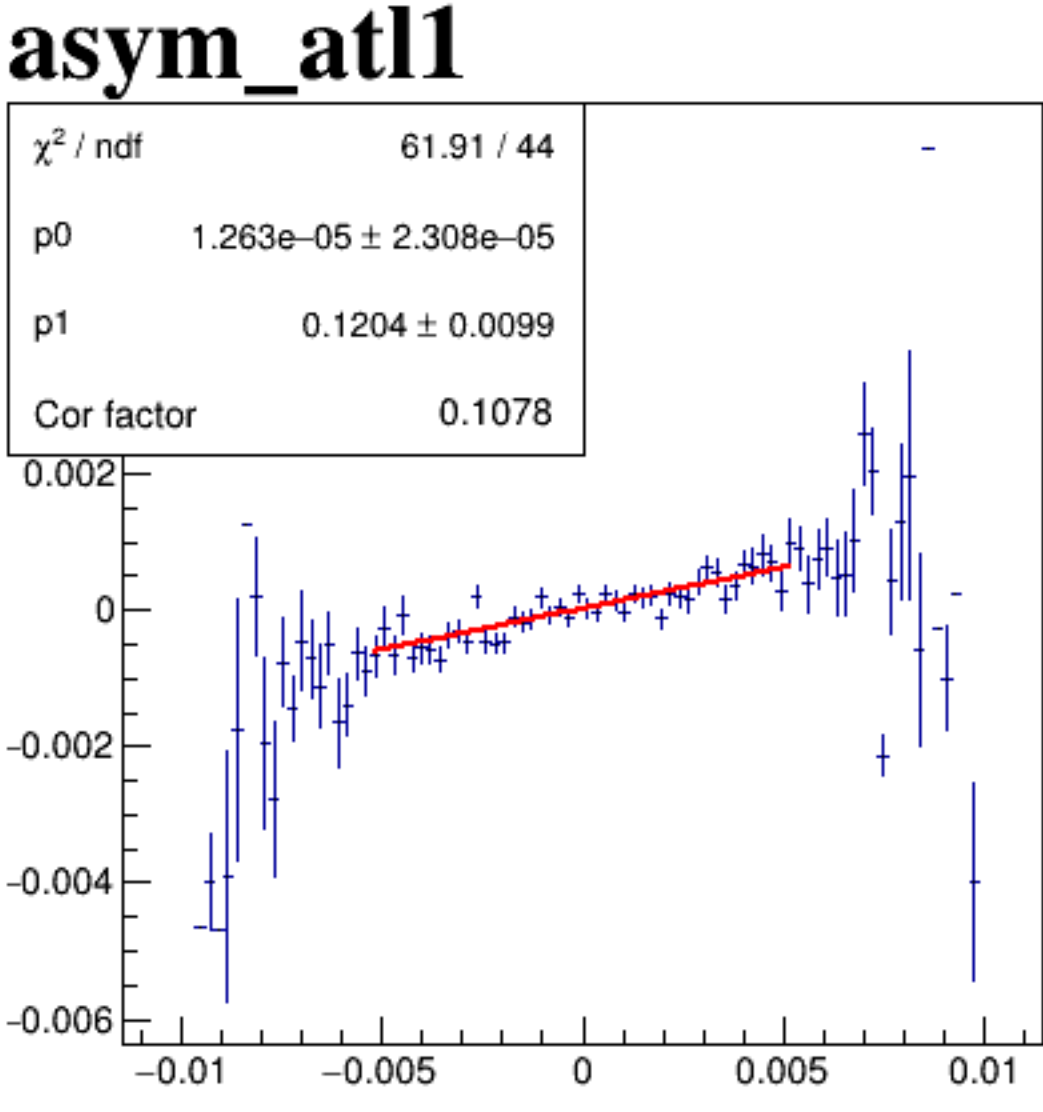
asym_atl1



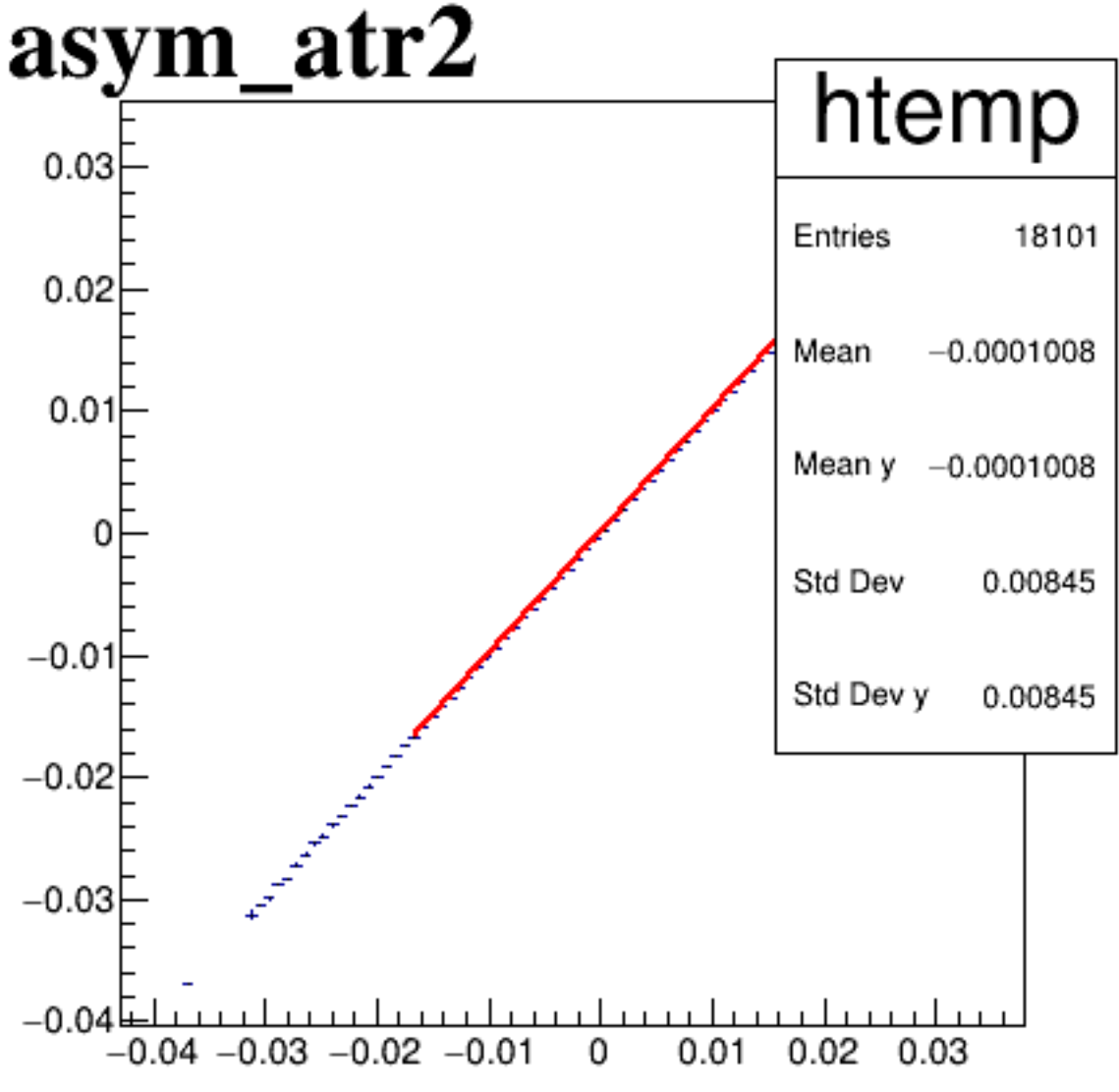
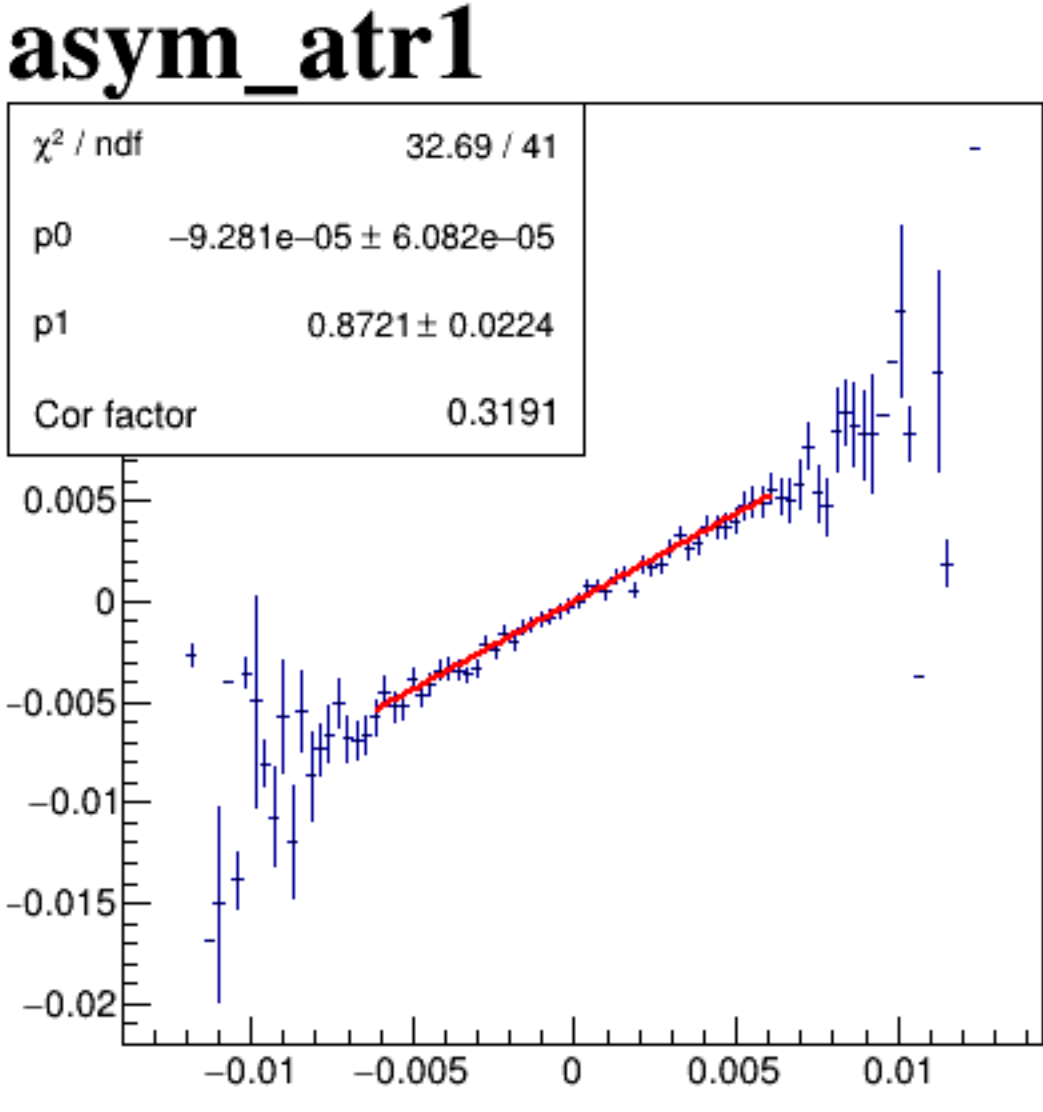
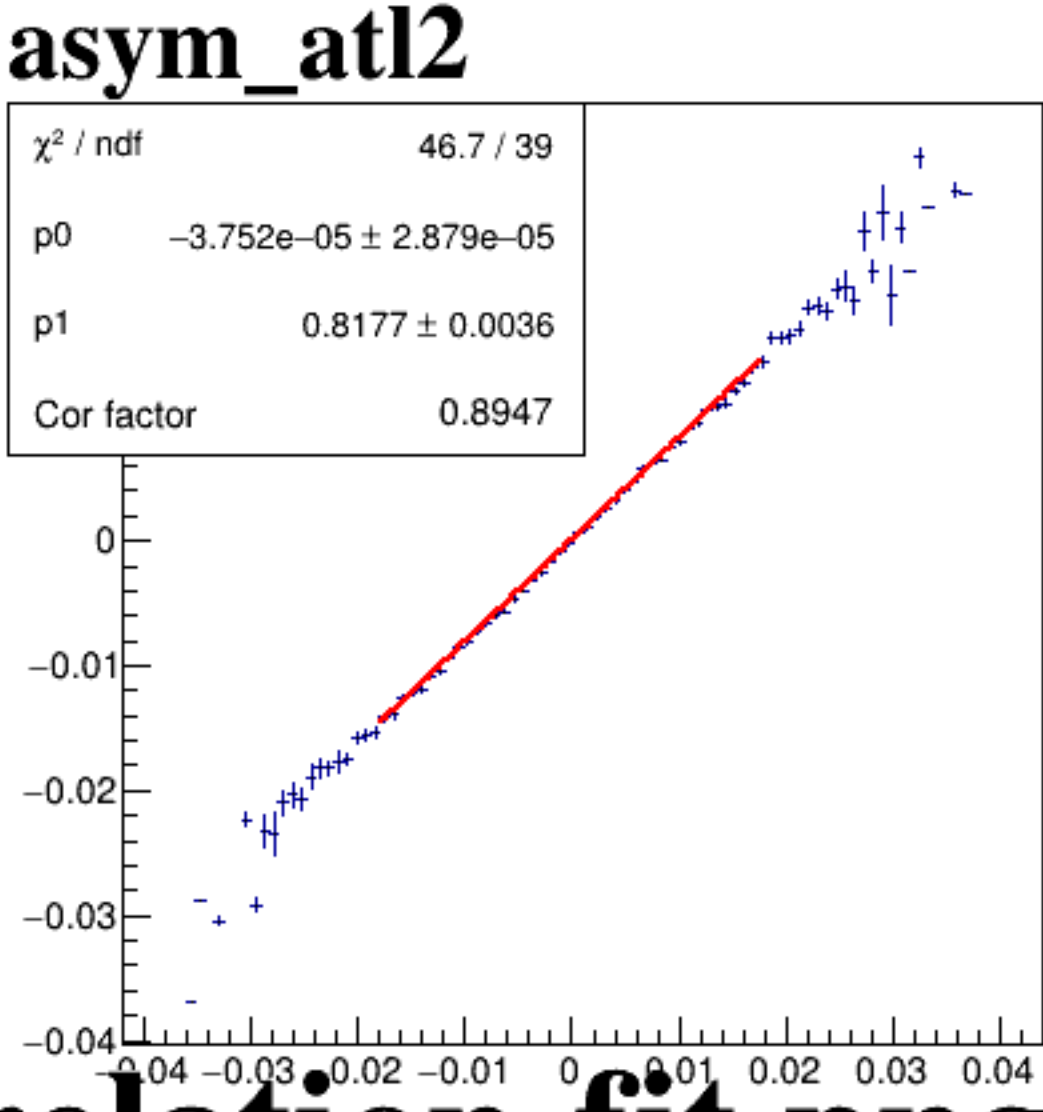
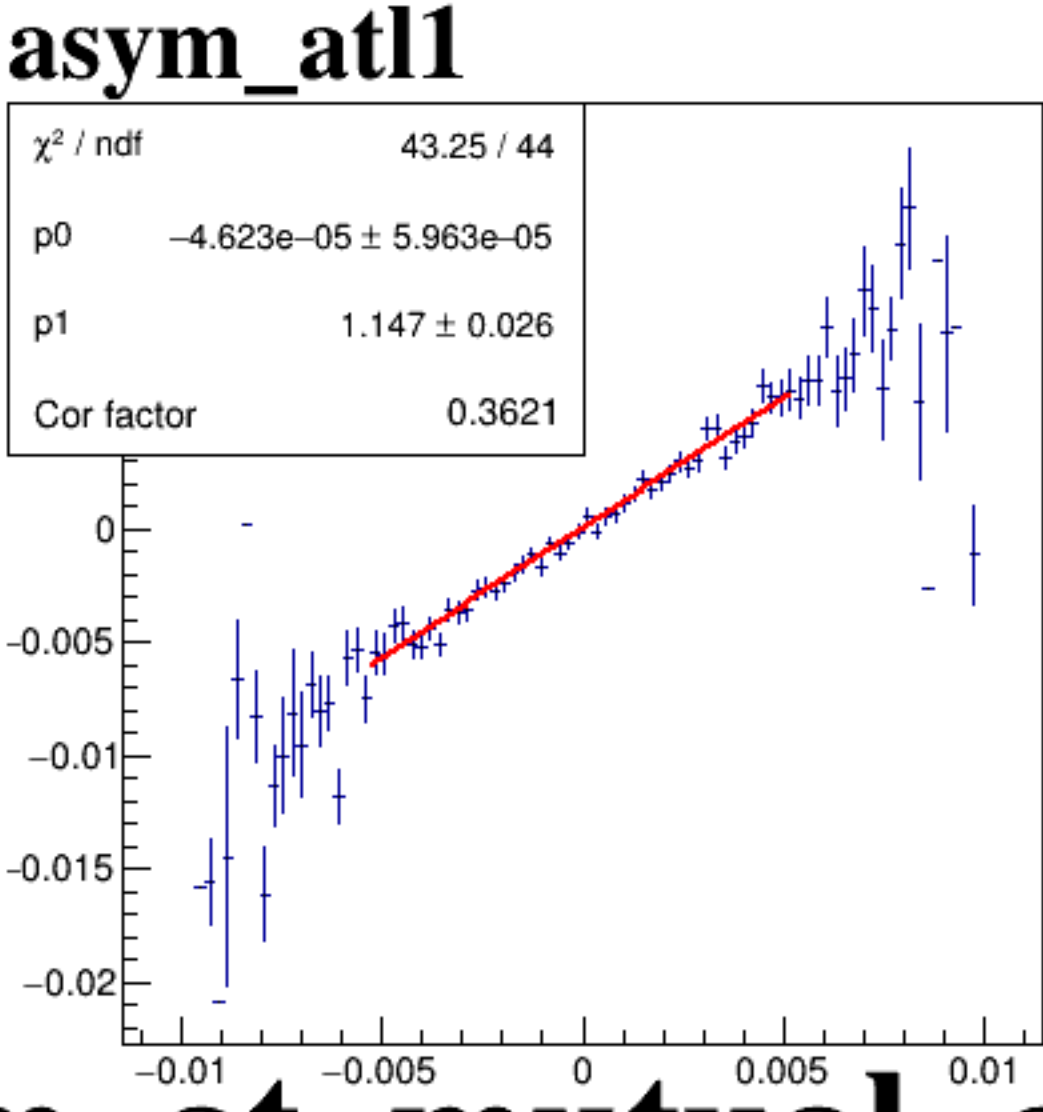
asym_atl2



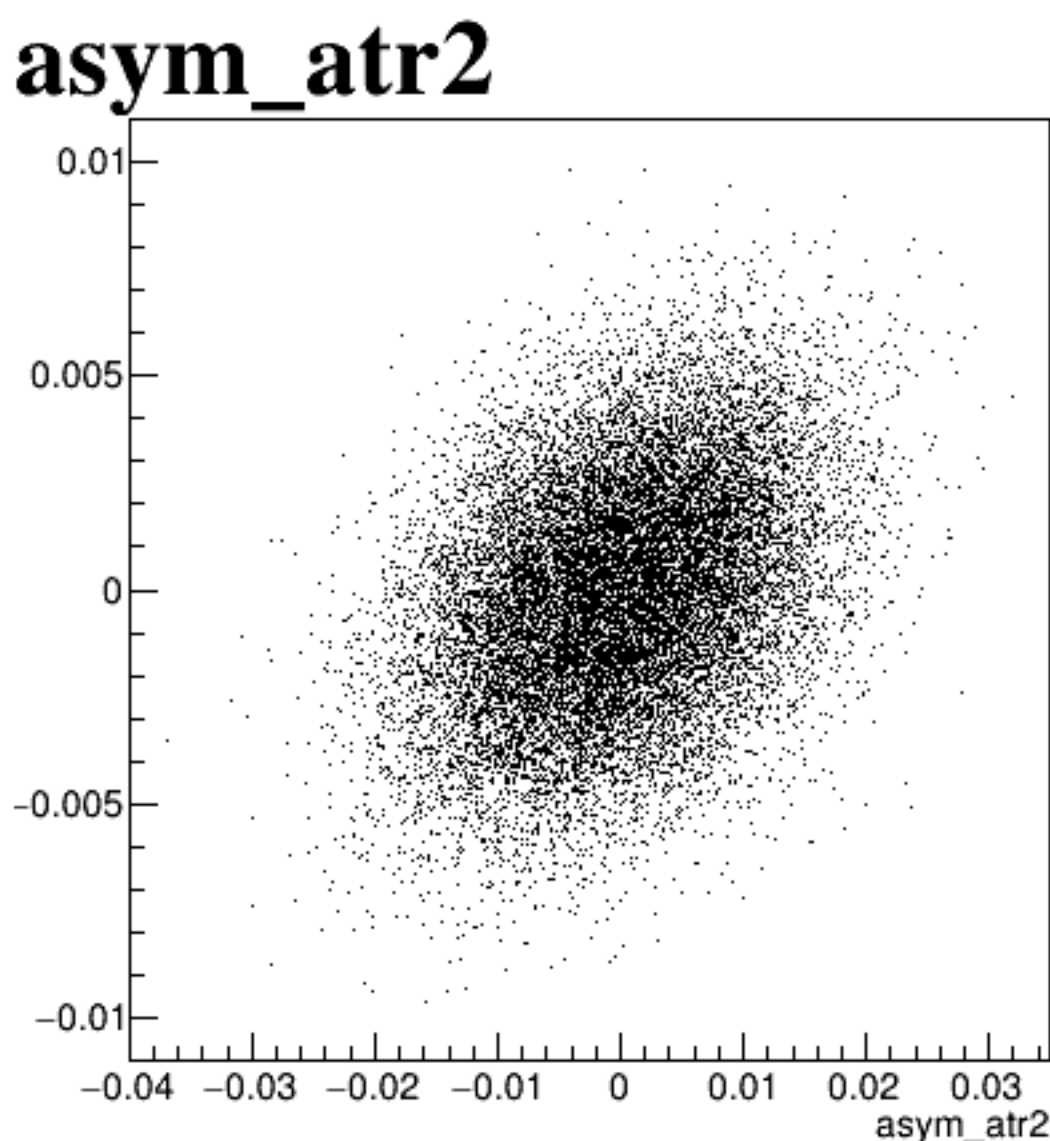
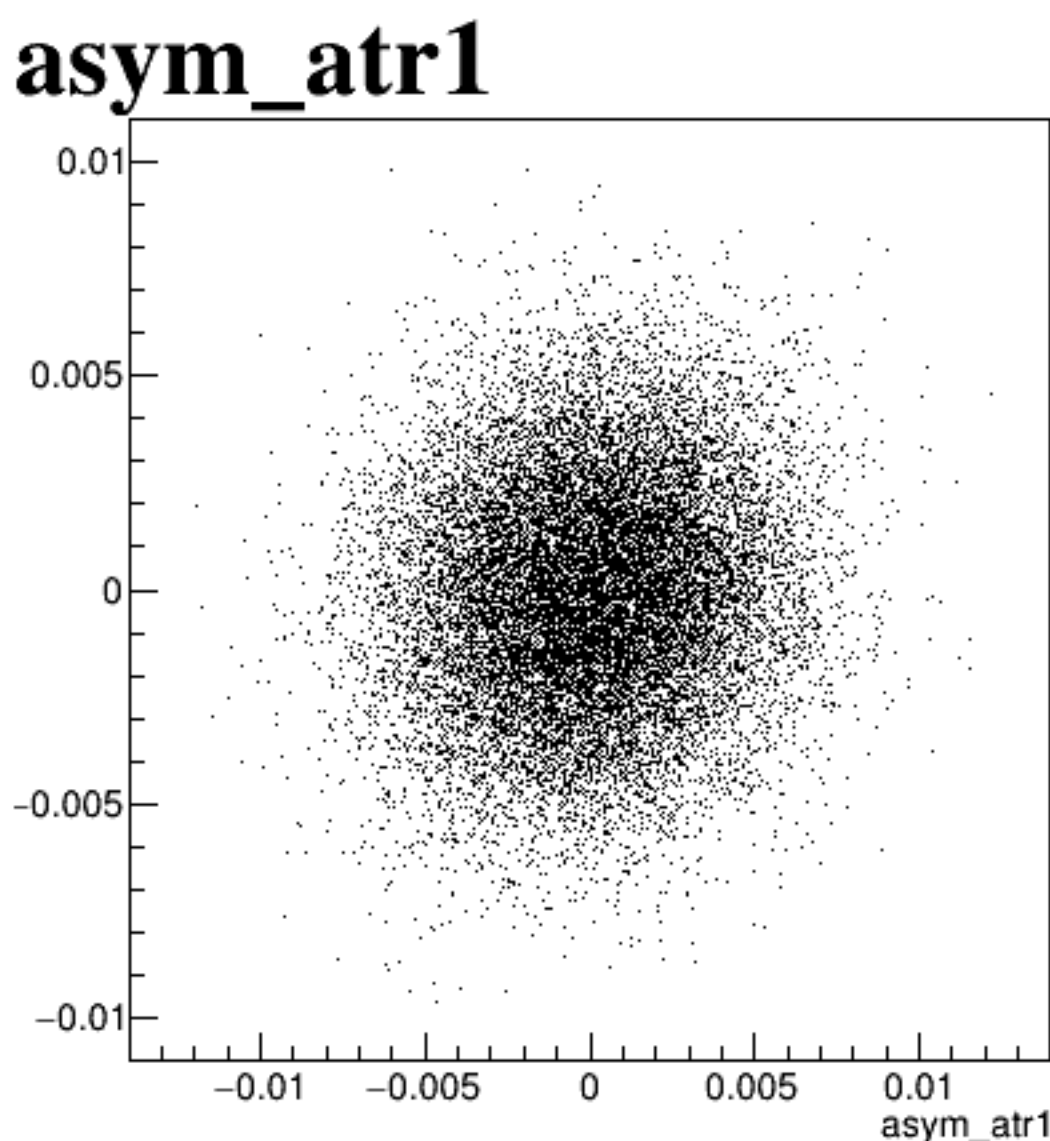
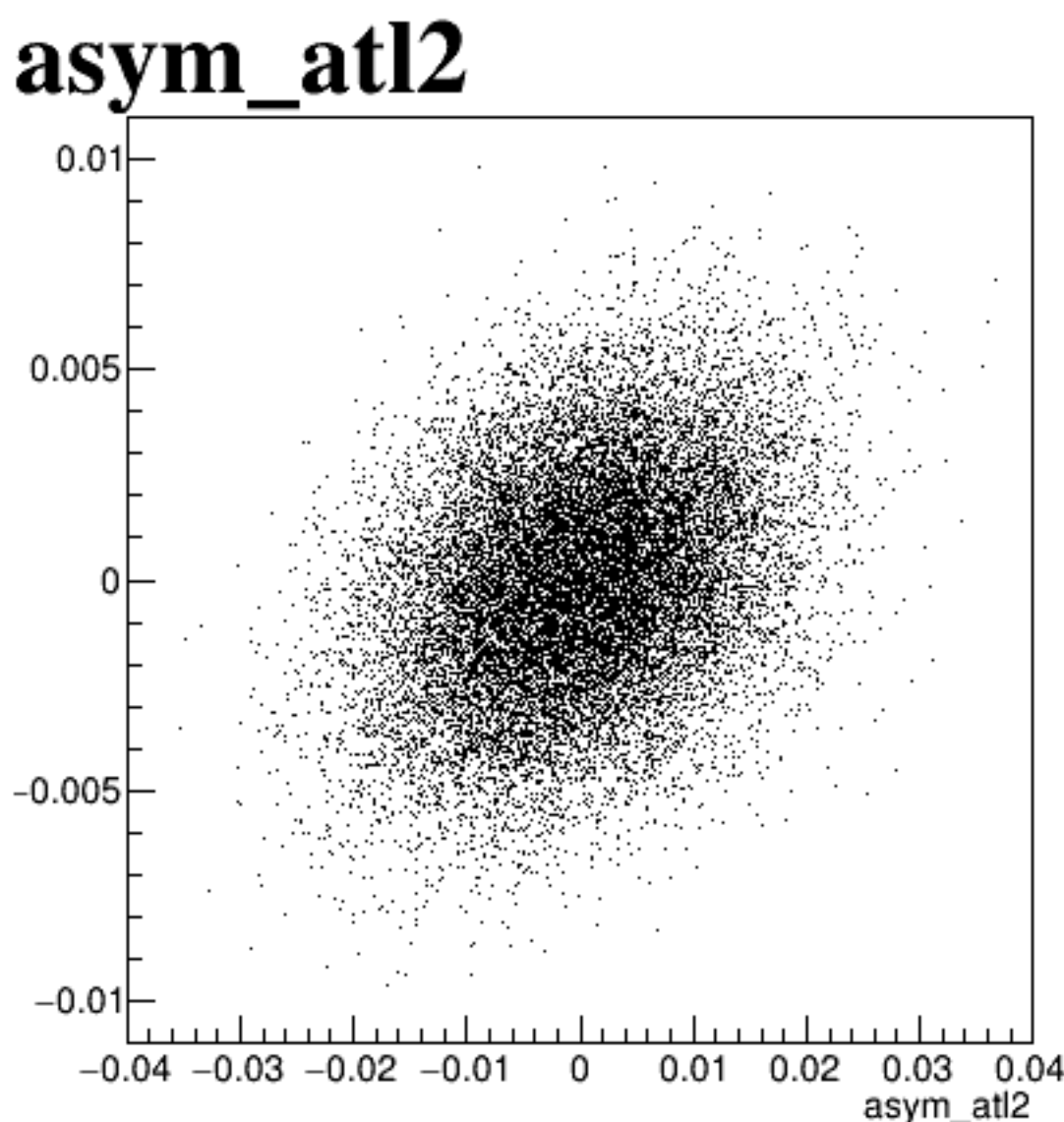
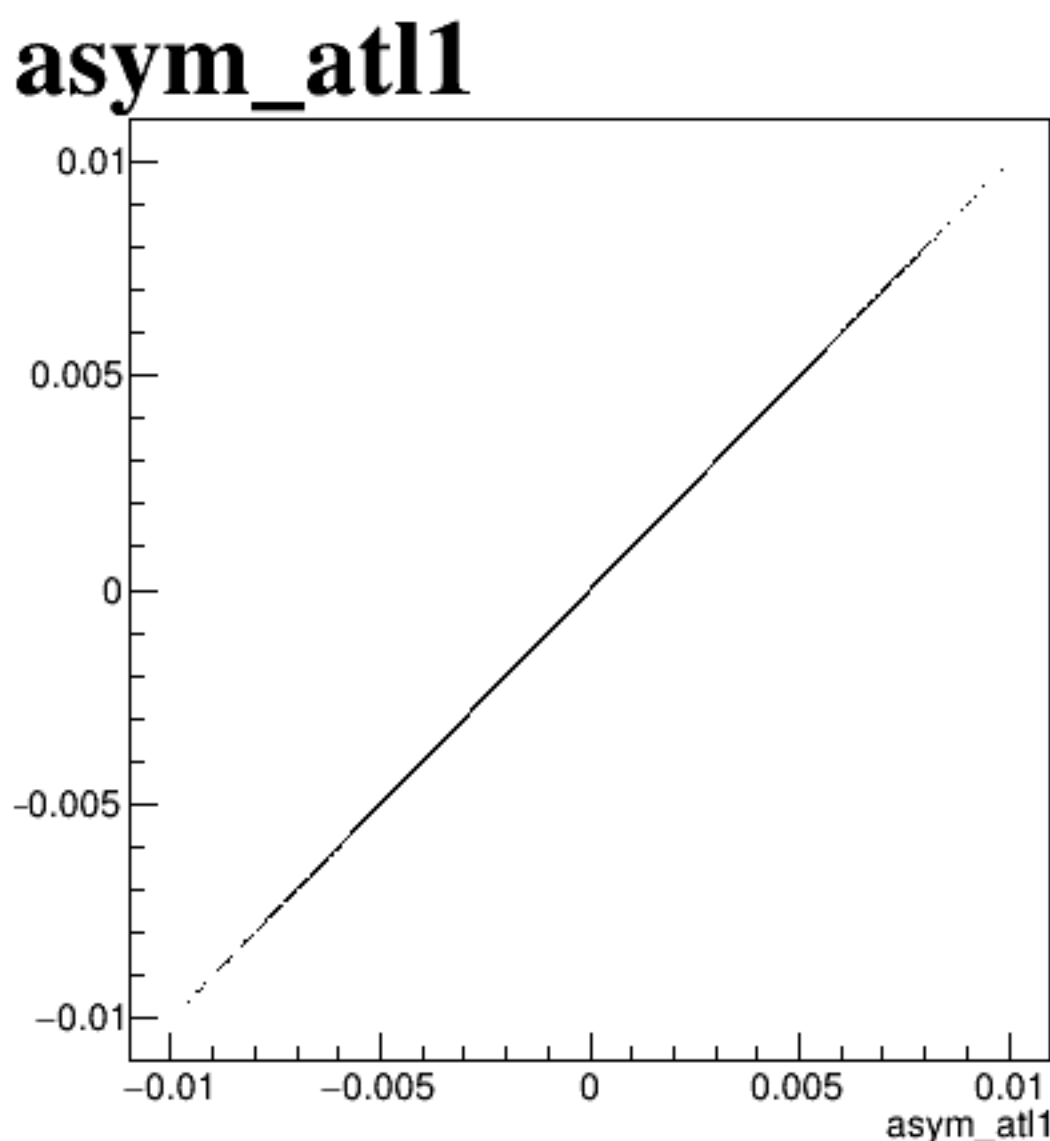
asym_atr1



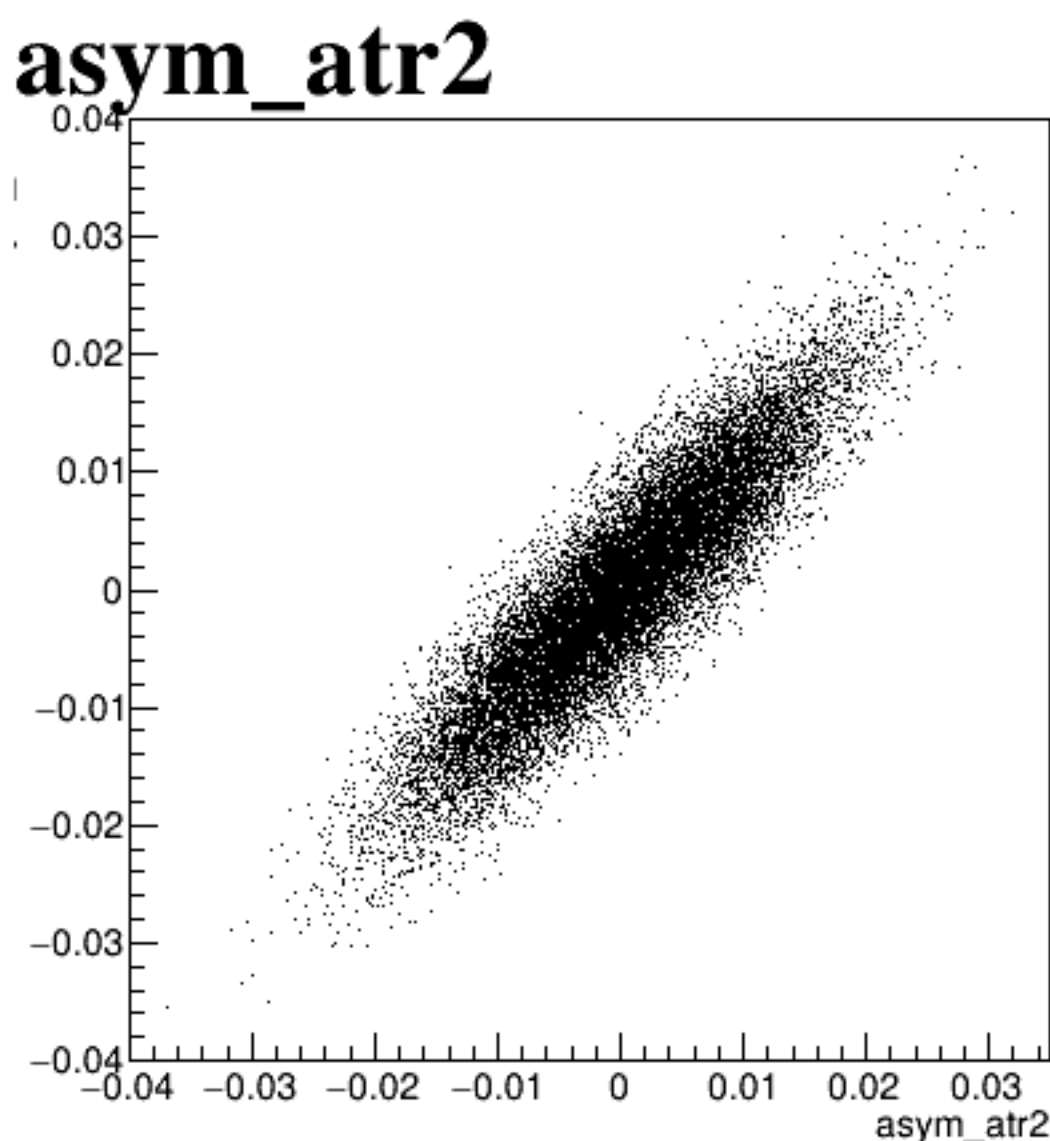
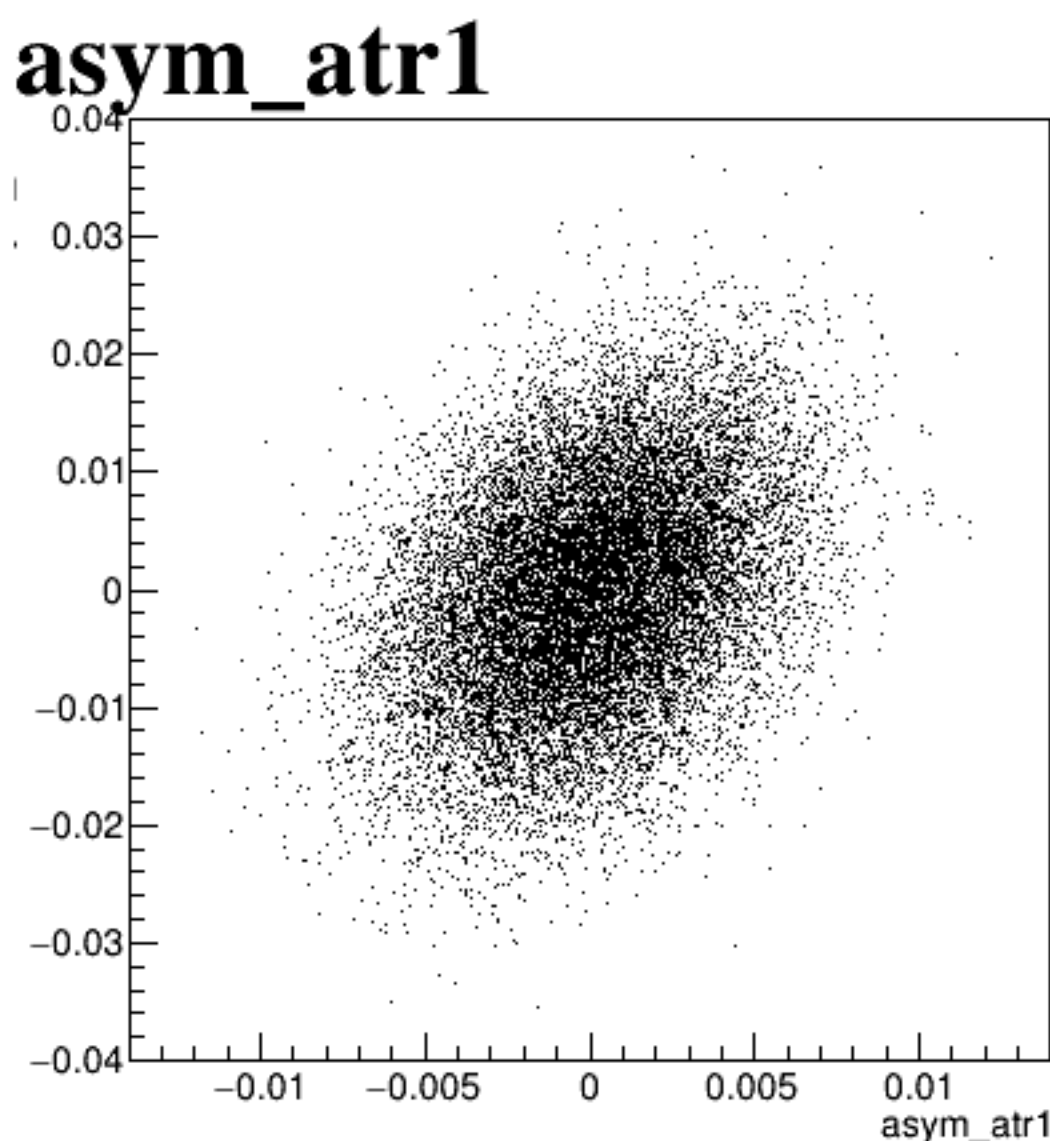
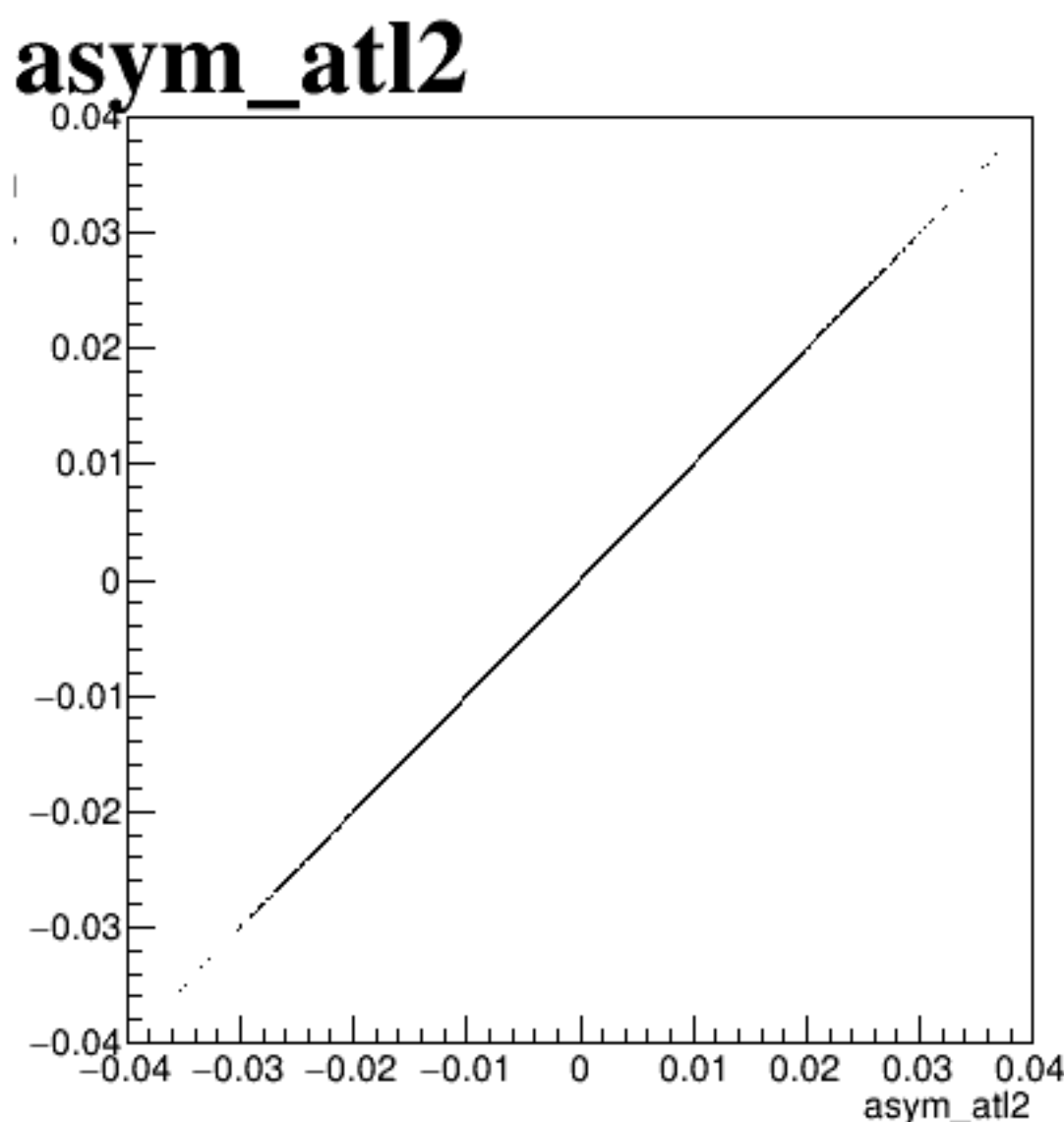
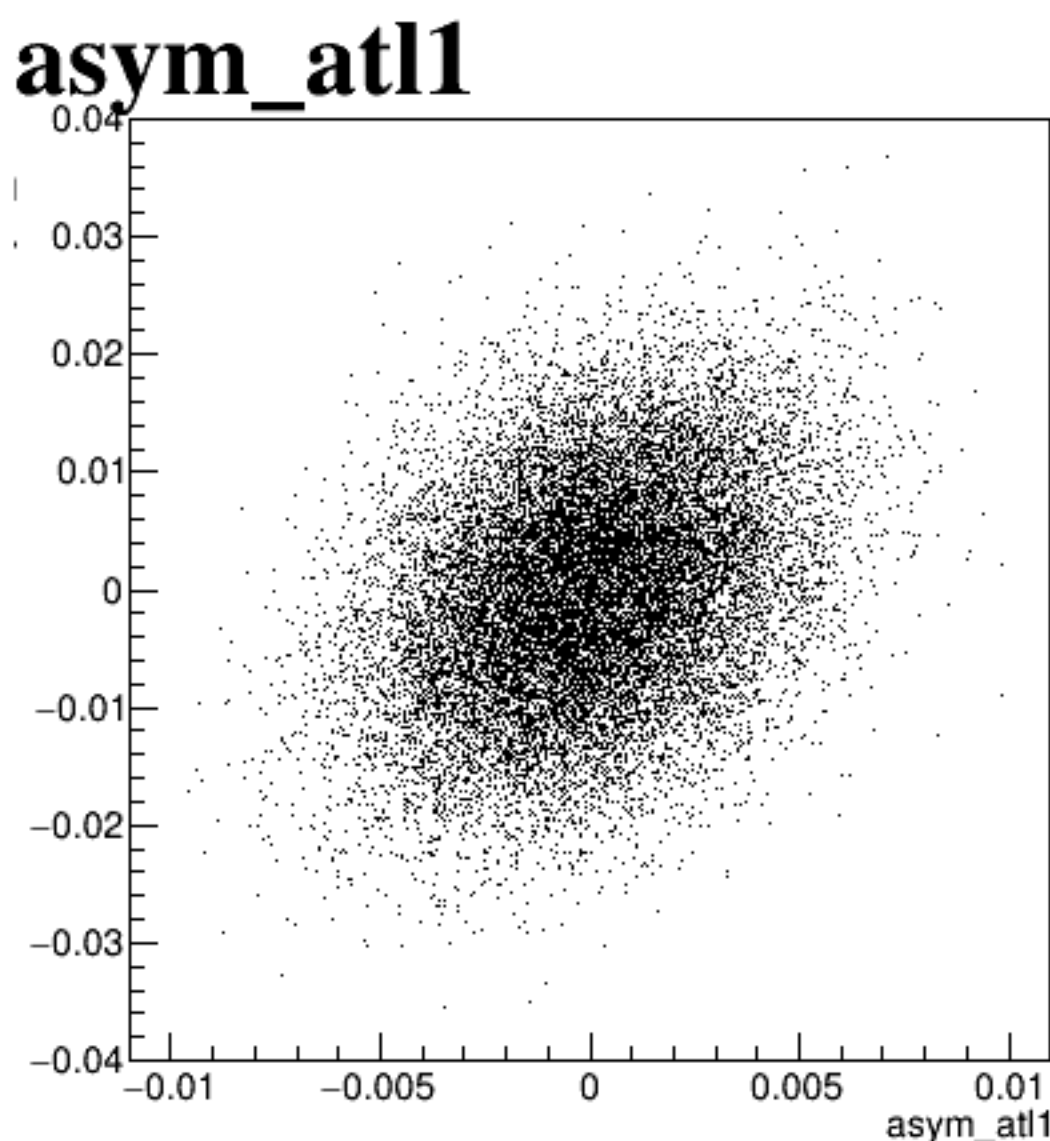
asym_atr2



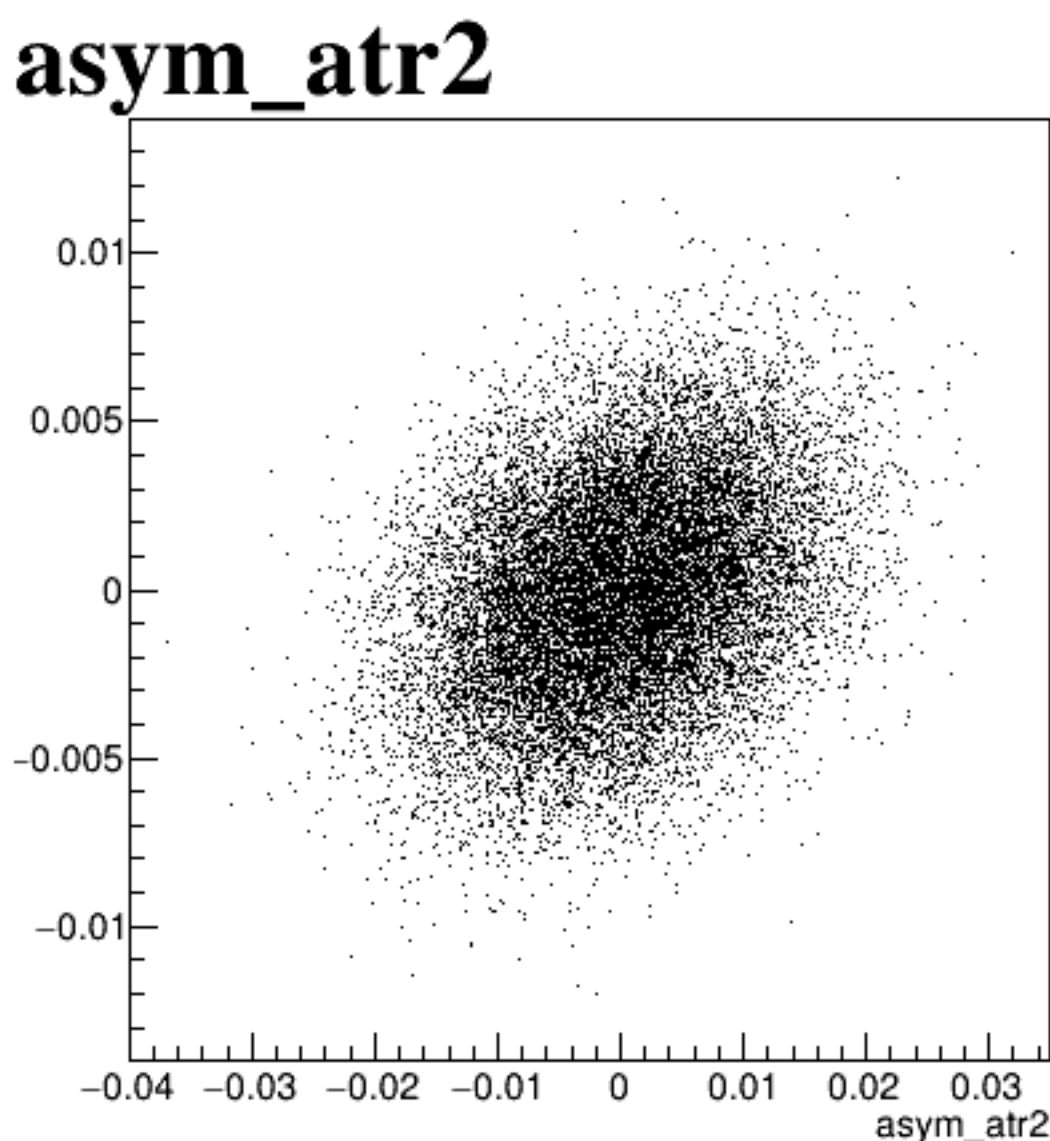
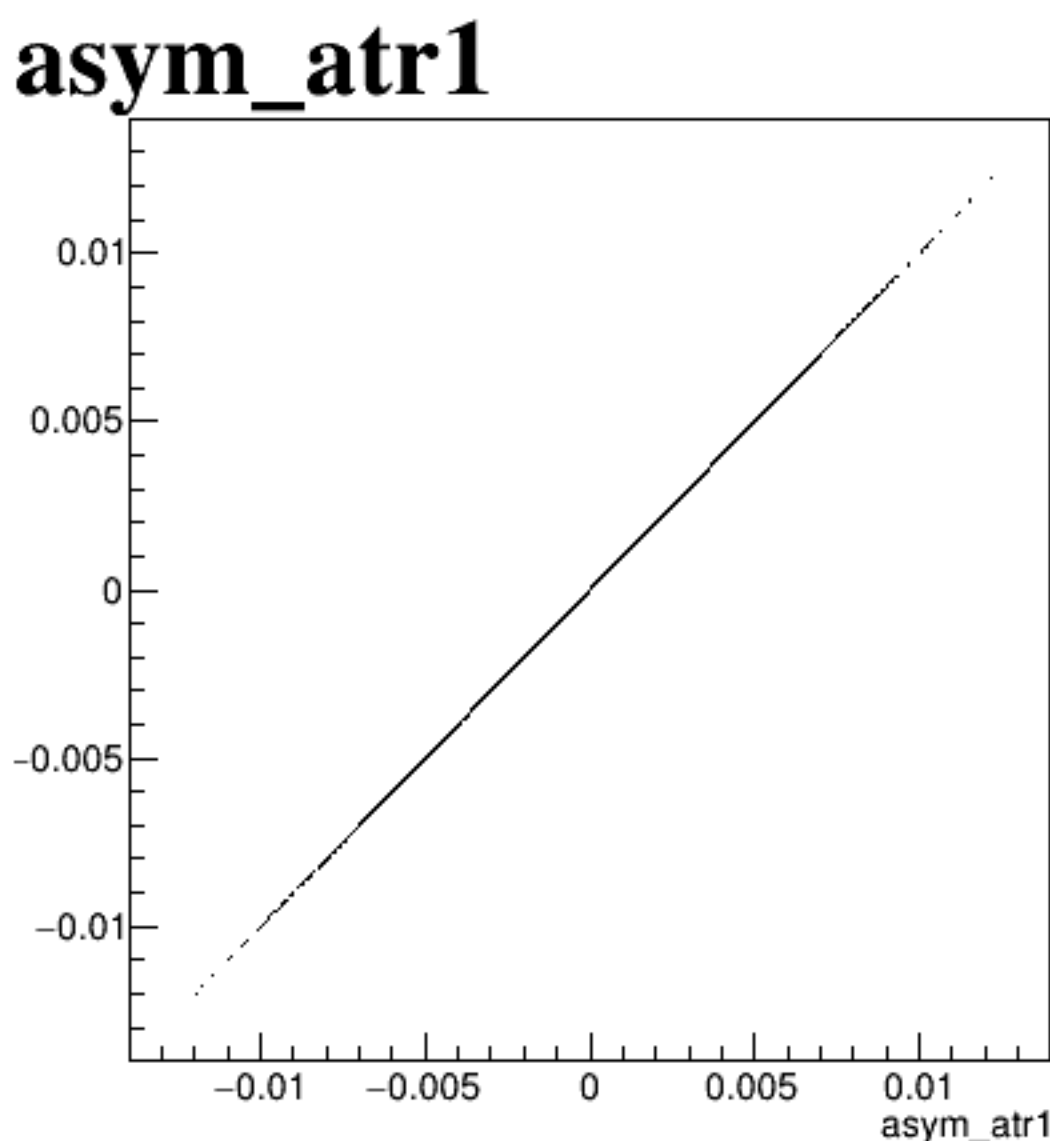
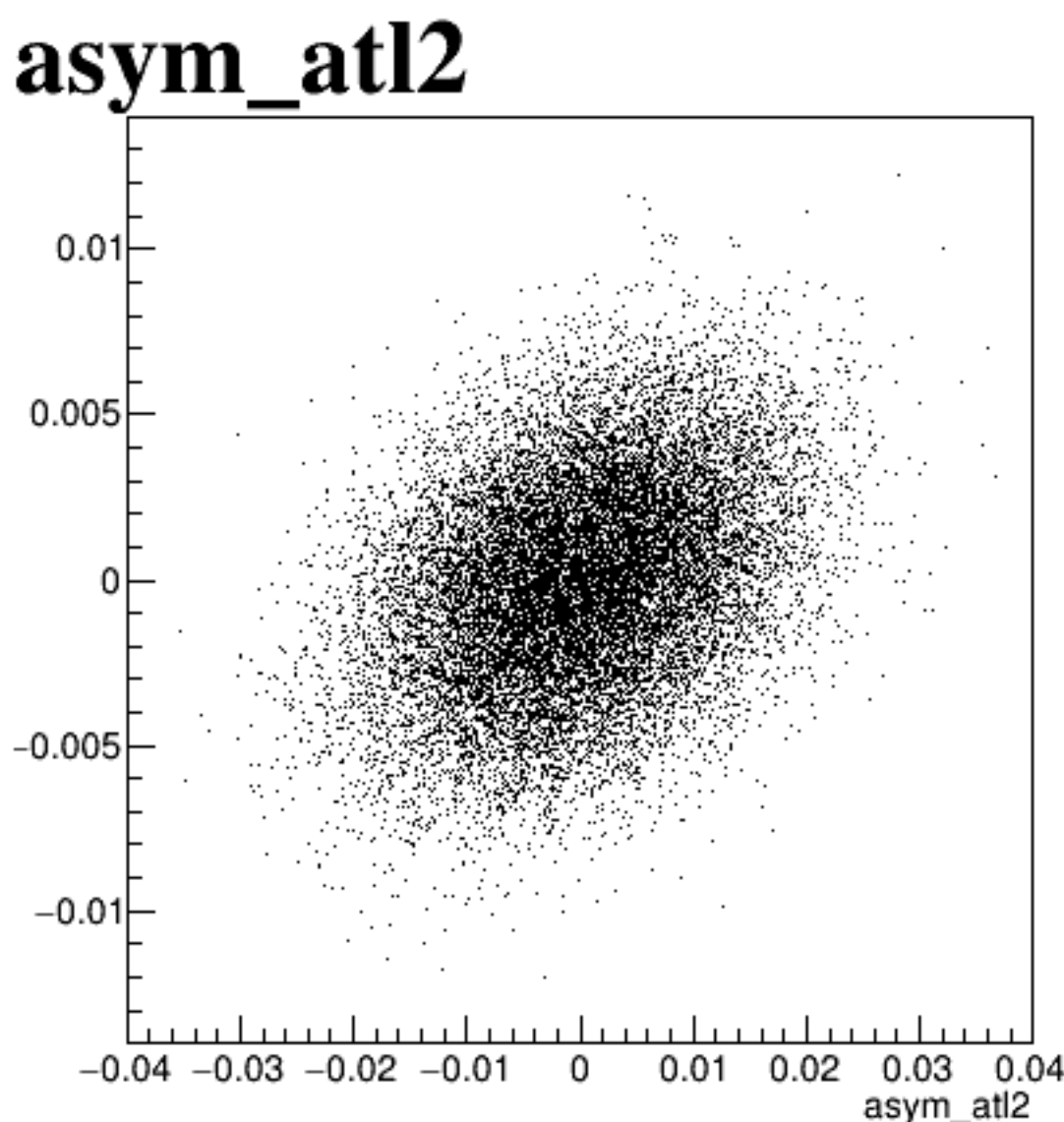
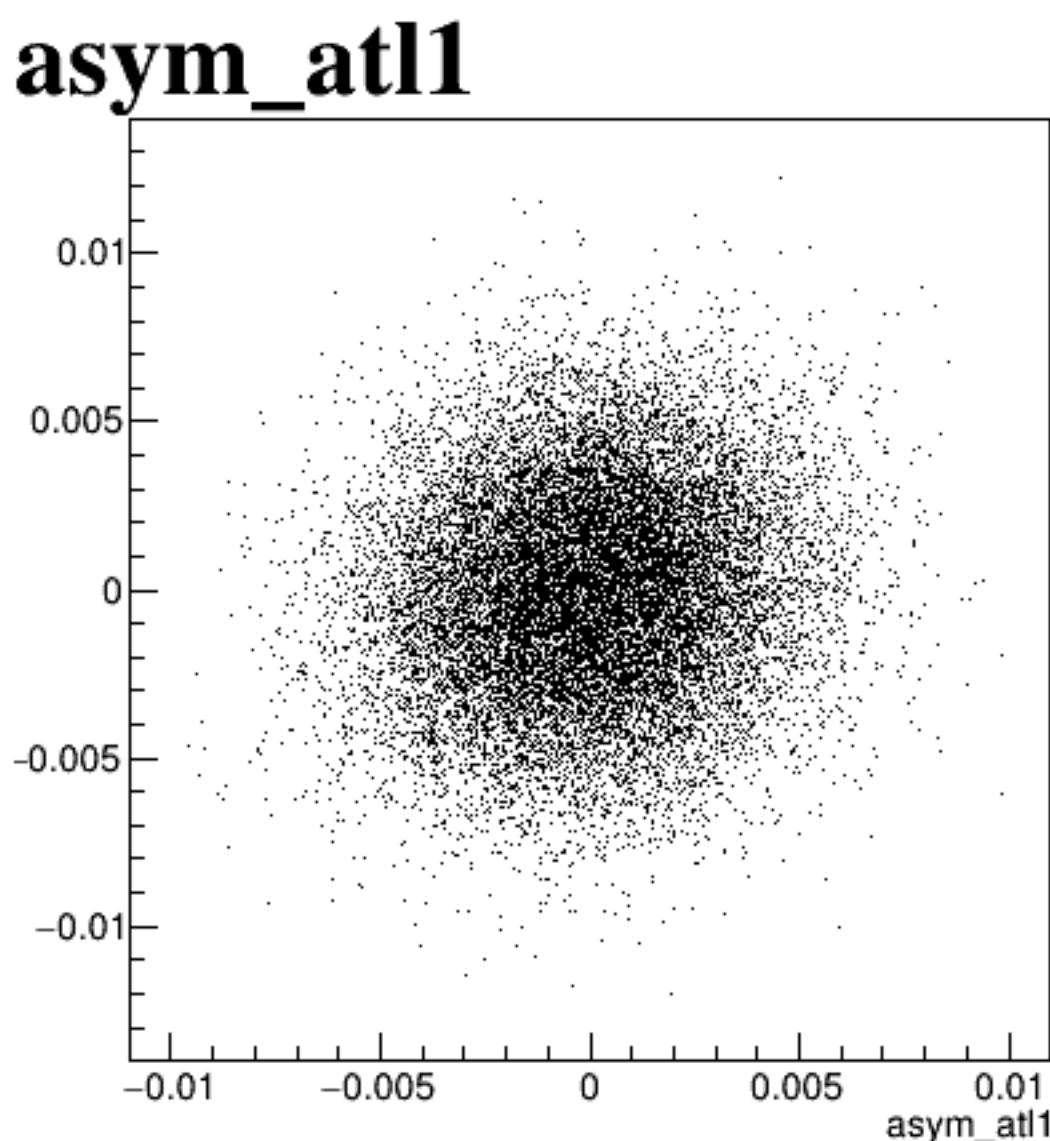
asym_atl1



asym_atl2



asym_atr1



asym_atr2

