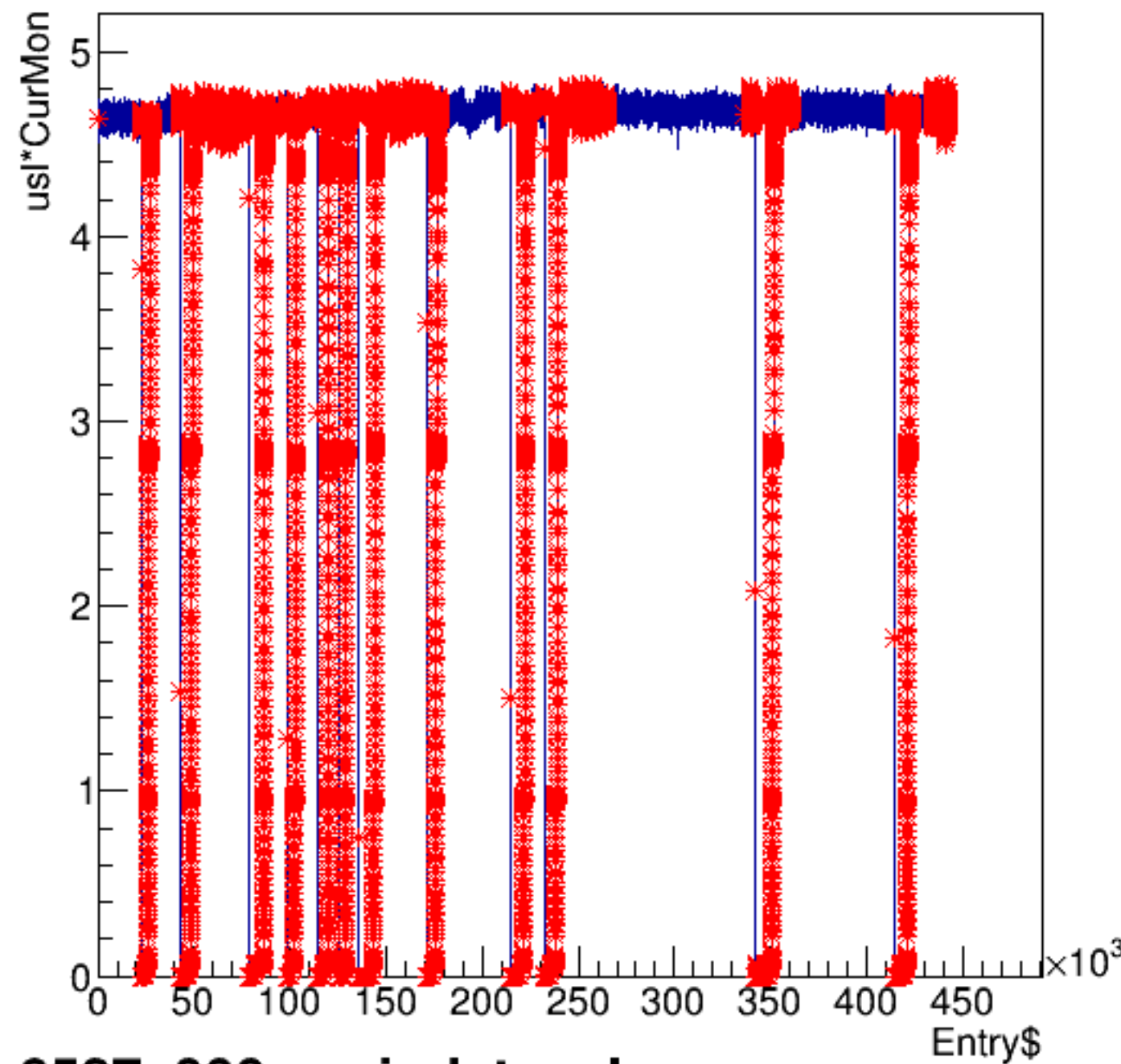
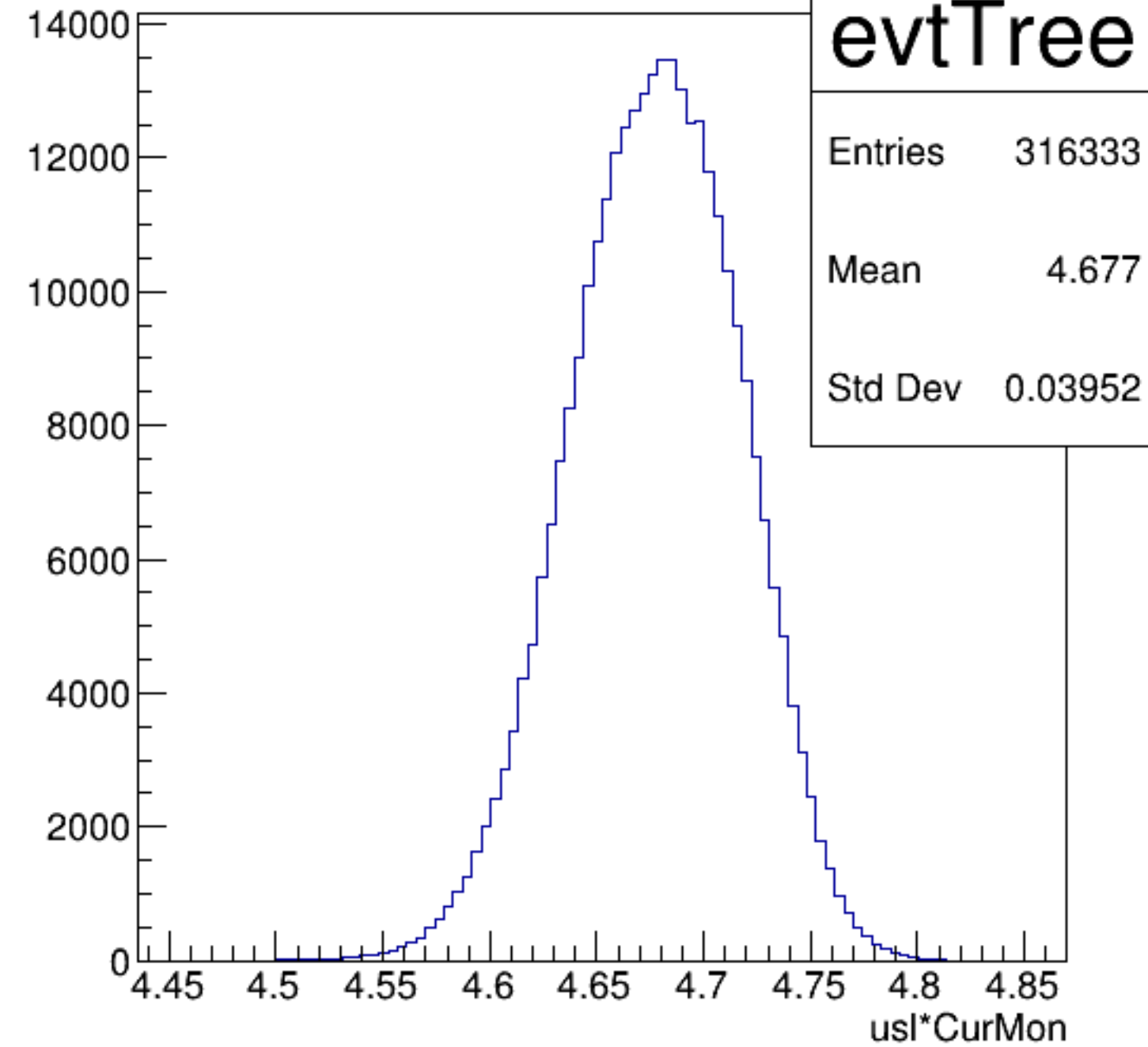


usl*CurMon:Entry\$

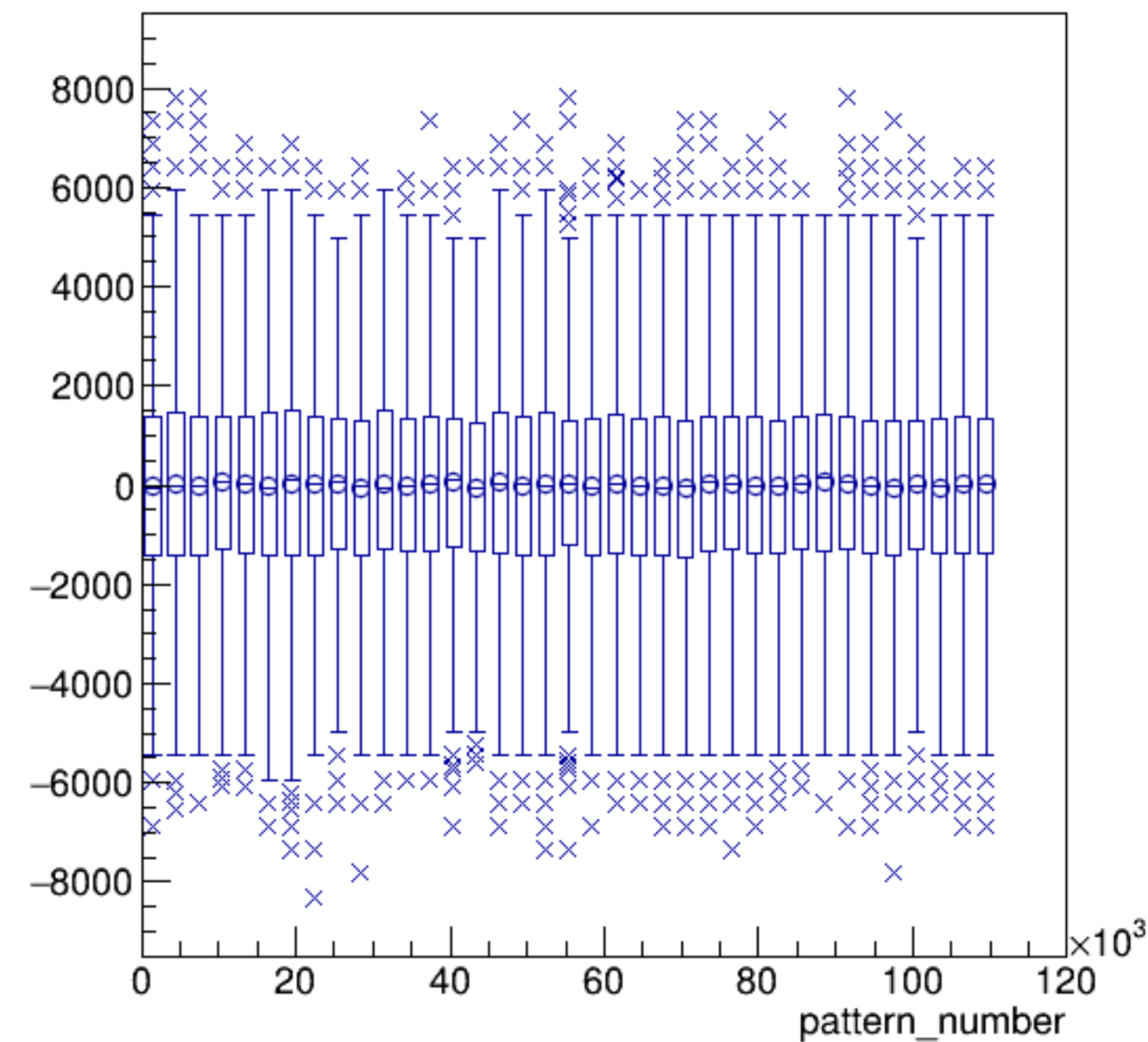


run6527_000_maindet_usl.png

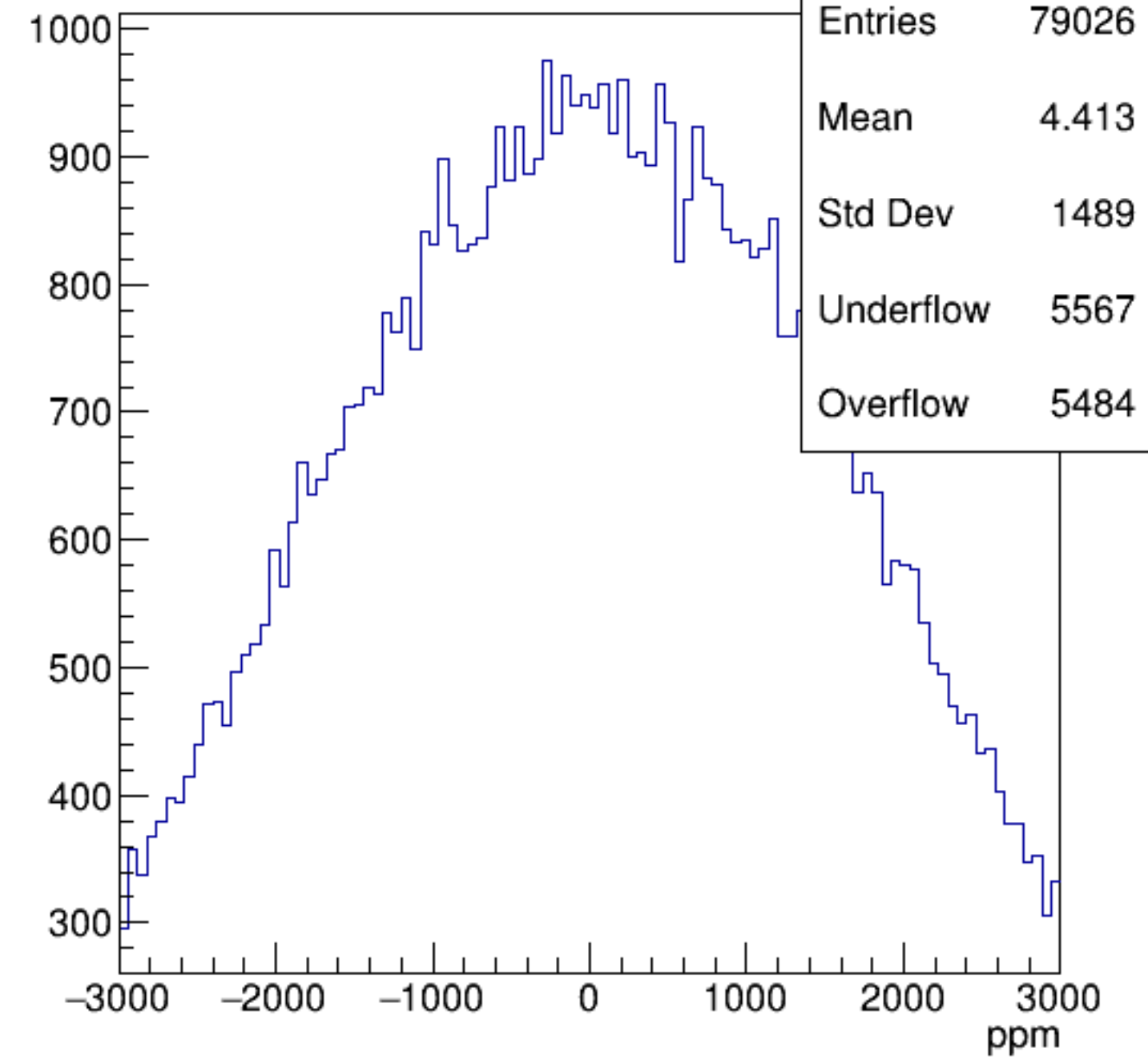
usl*CurMon {ErrorFlag==0}

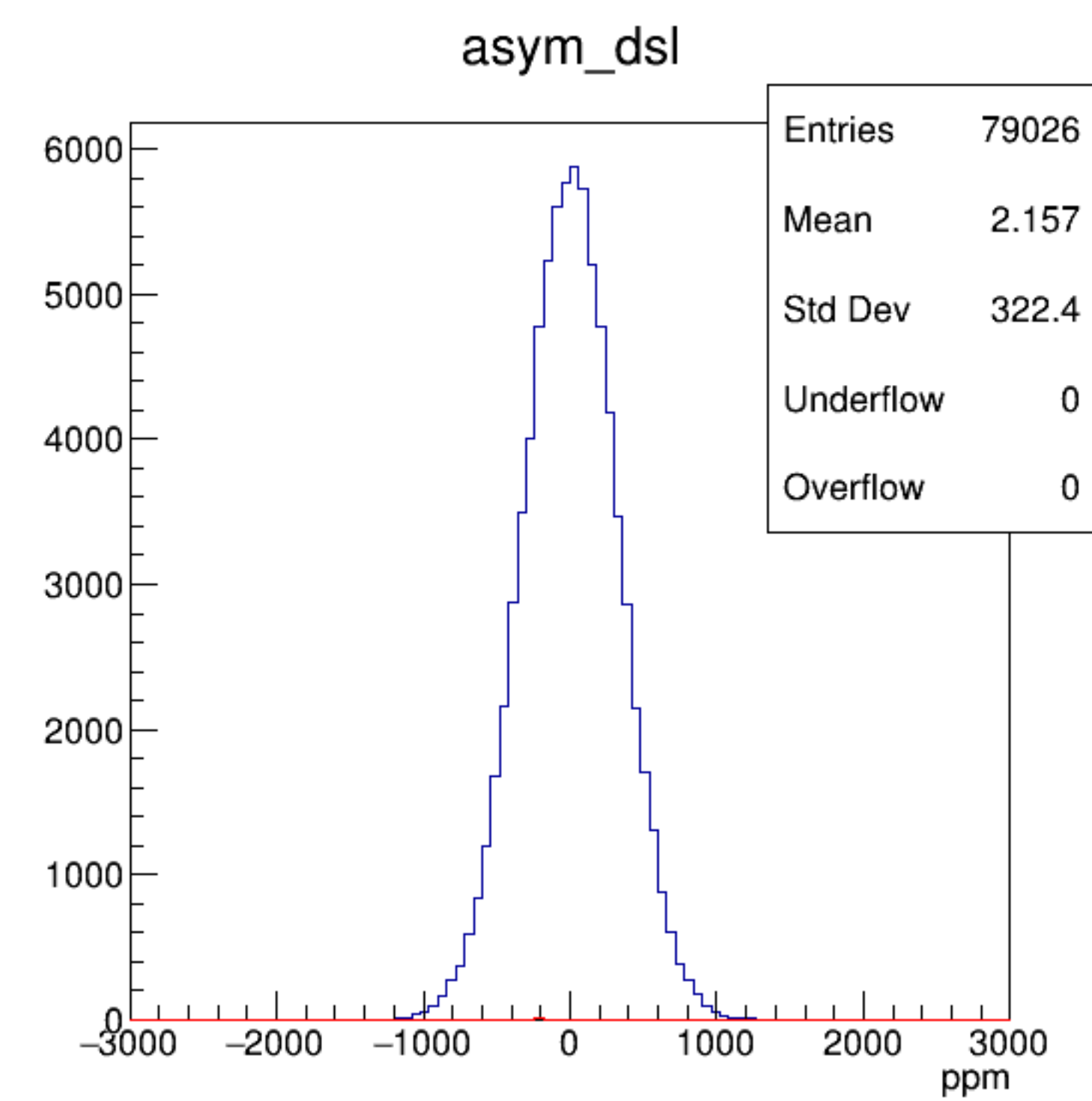
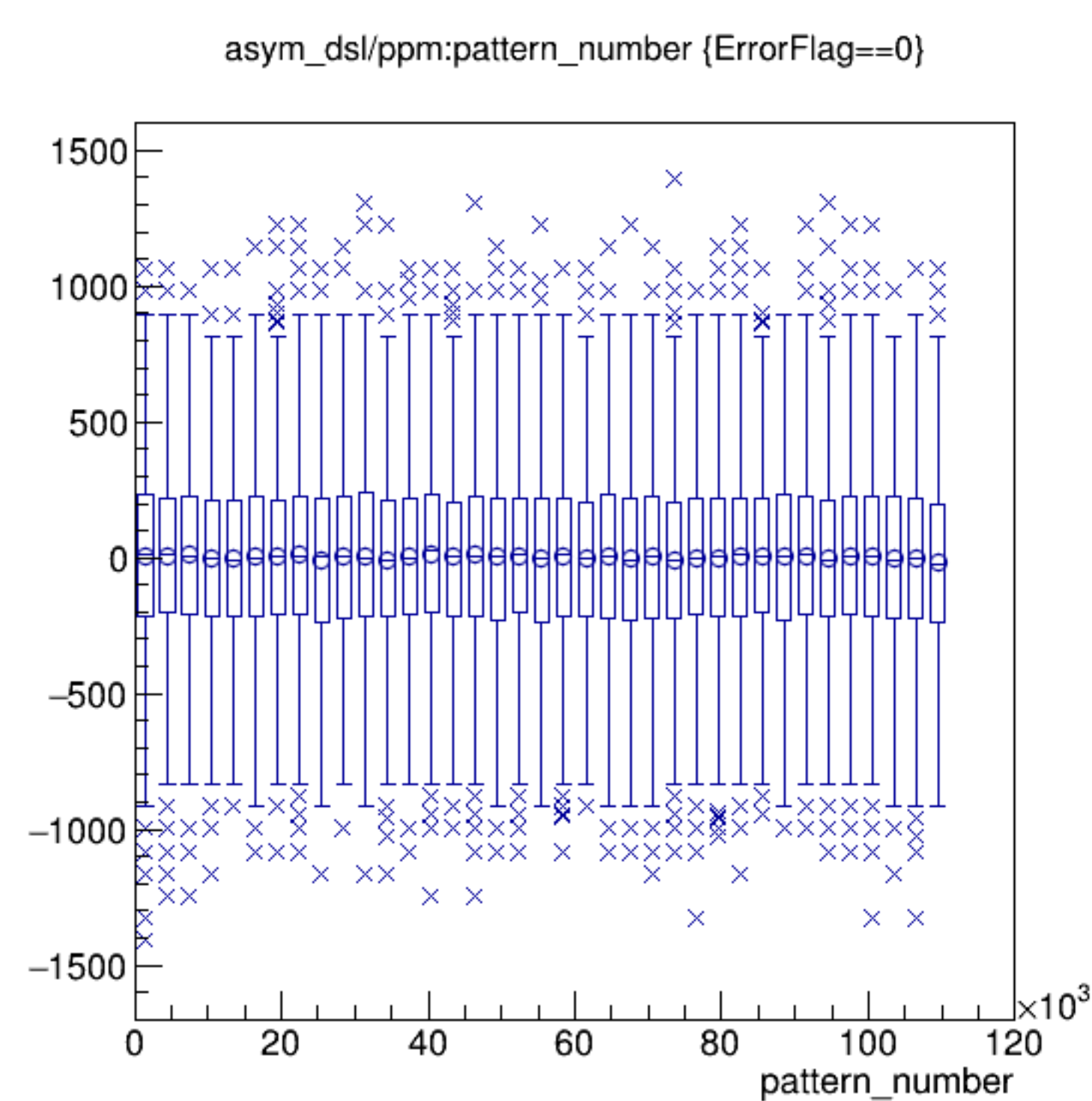
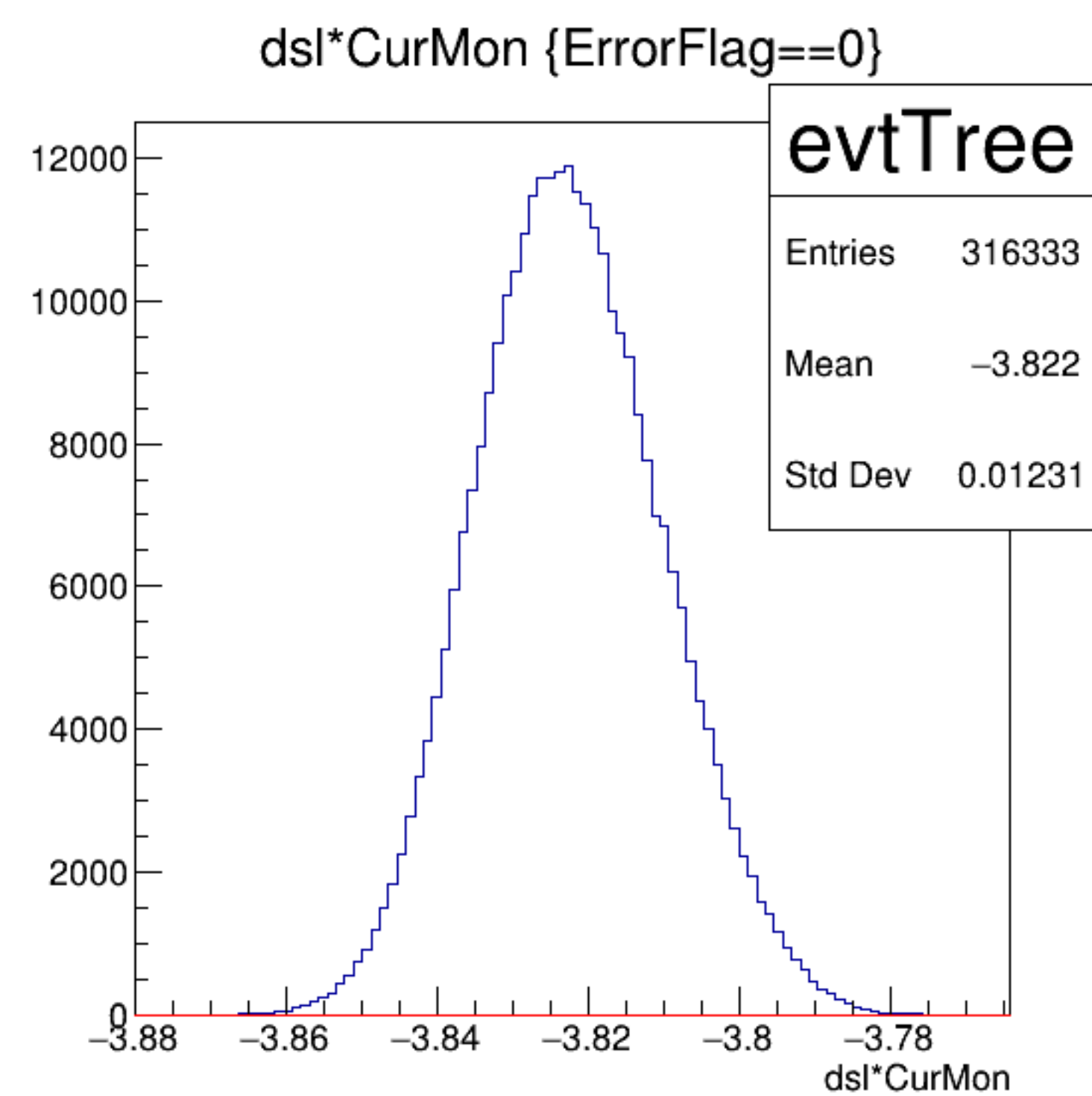
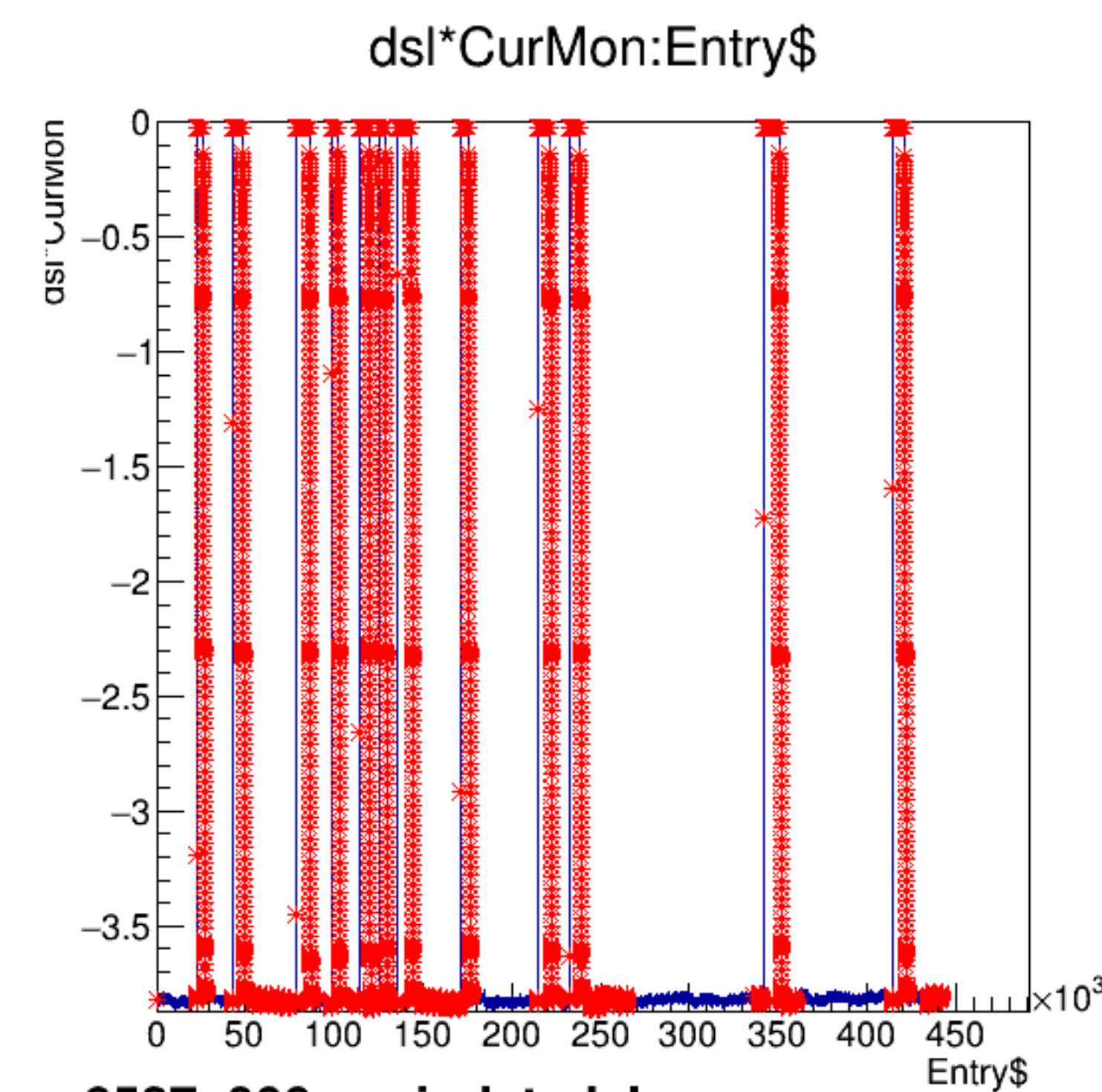


asym_usl/ppm:pattern_number {ErrorFlag==0}

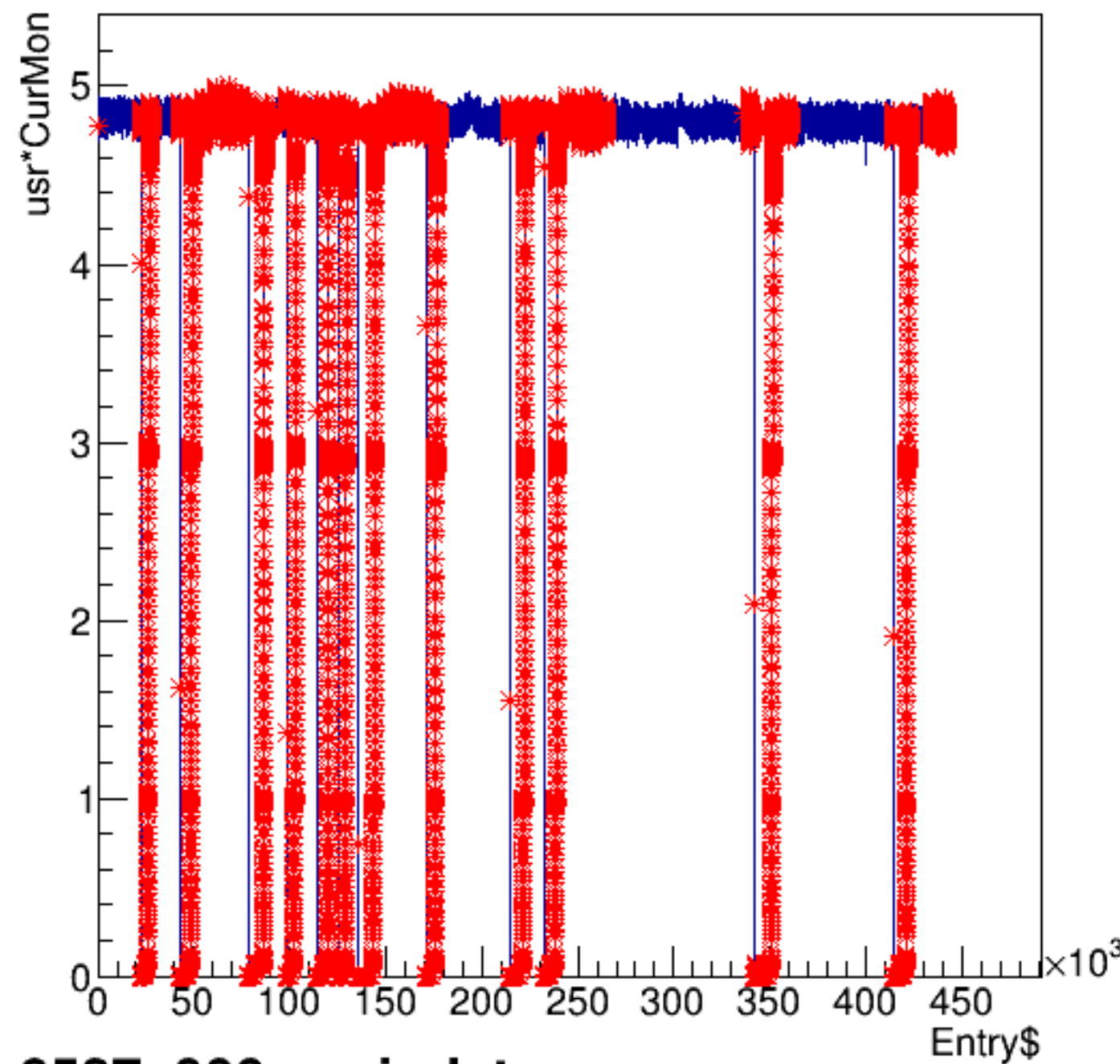


asym_usl



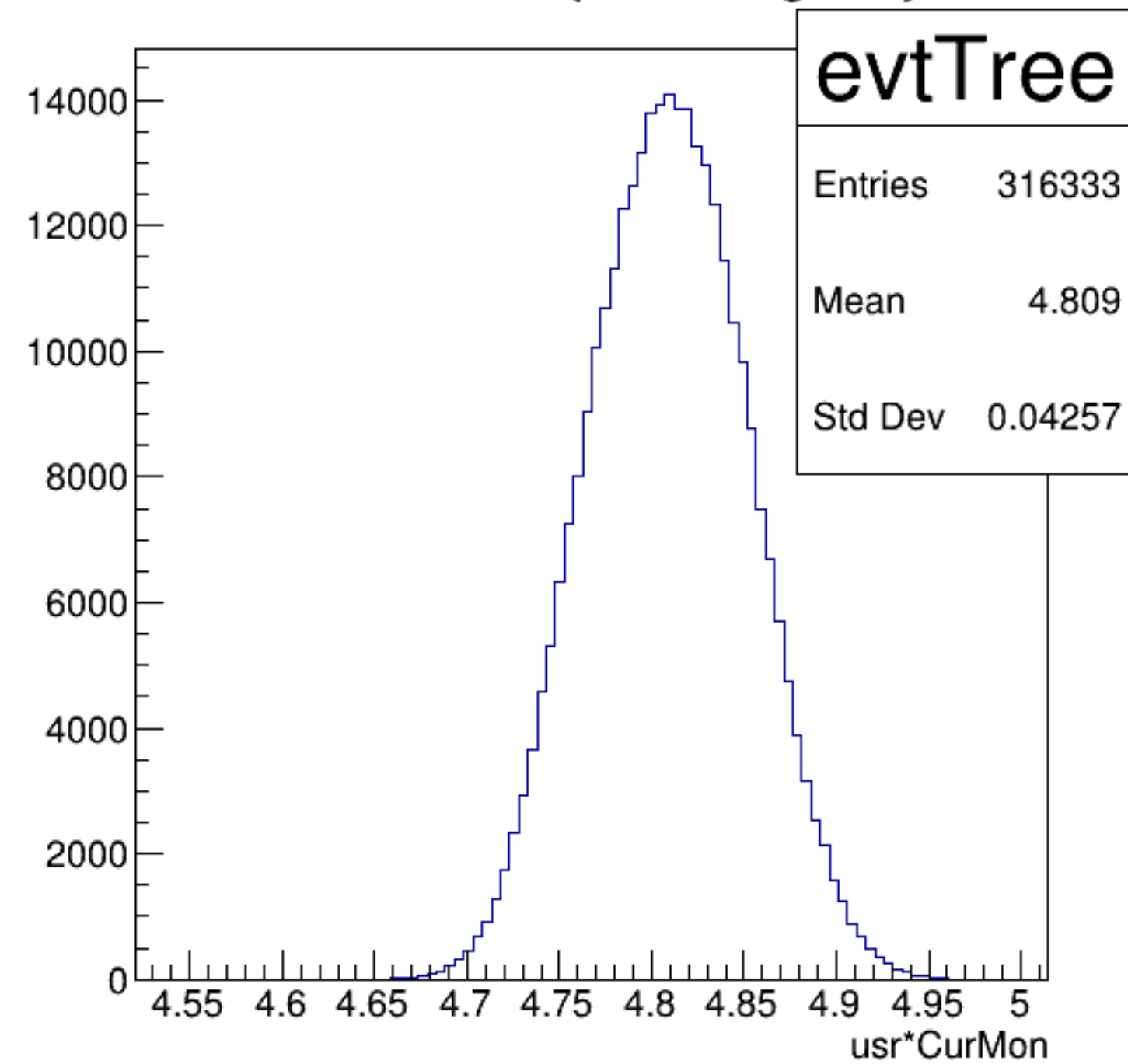


usr*CurMon:Entry\$



run6527_000_maindet_usr.png

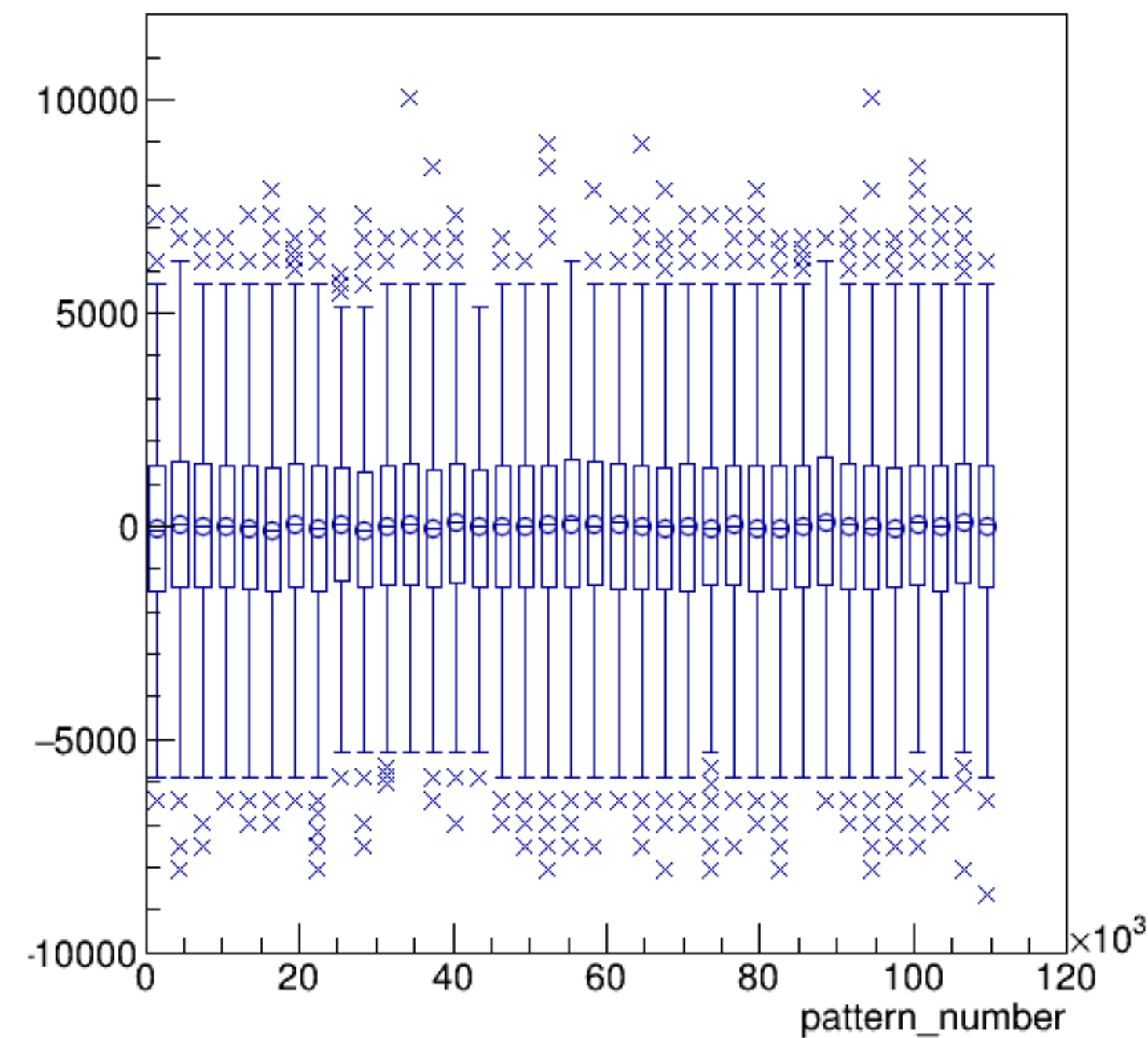
usr*CurMon {ErrorFlag==0}



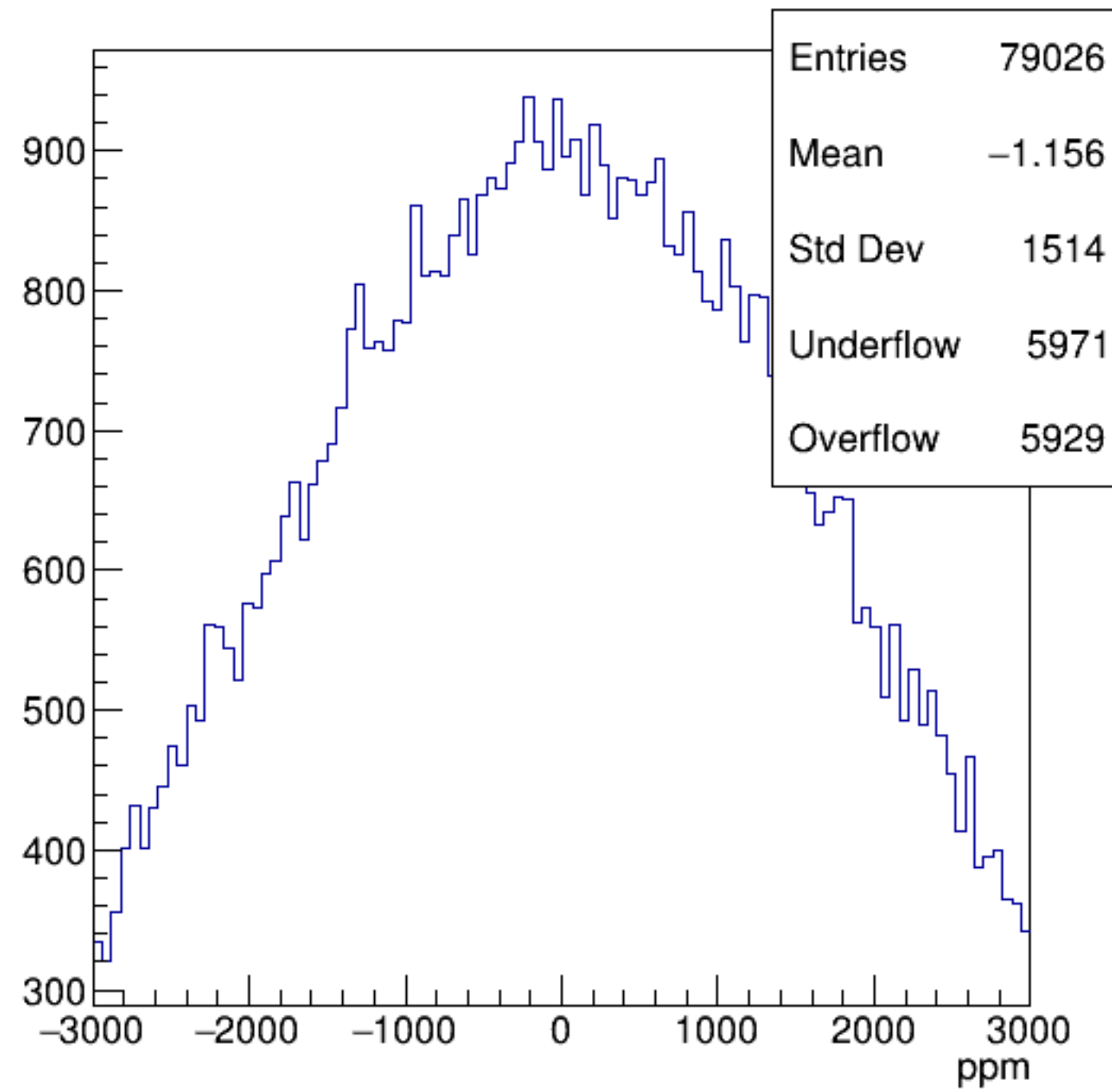
evtTree

Entries	316333
Mean	4.809
Std Dev	0.04257

asym_usr/ppm:pattern_number {ErrorFlag==0}

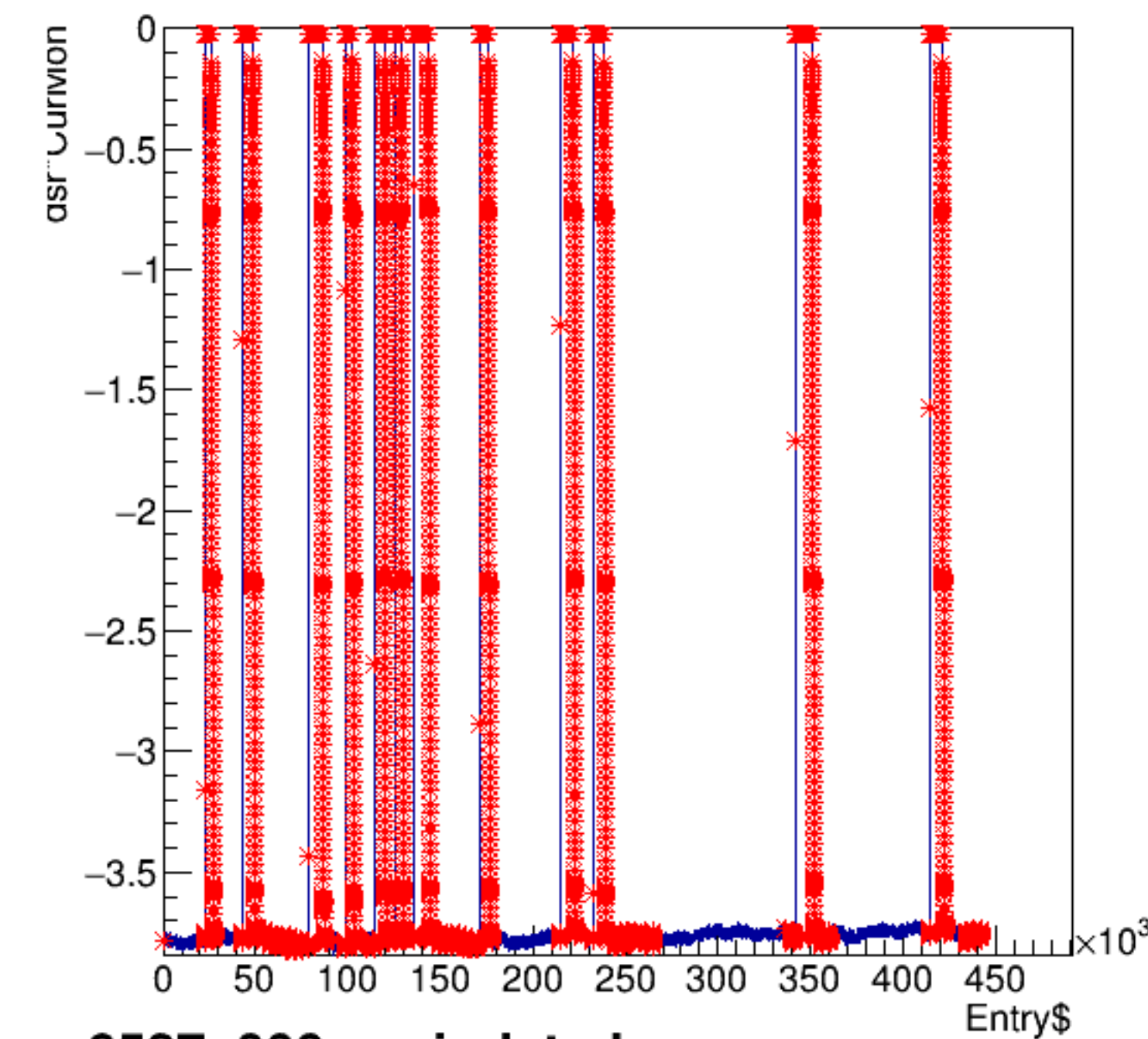


asym_usr



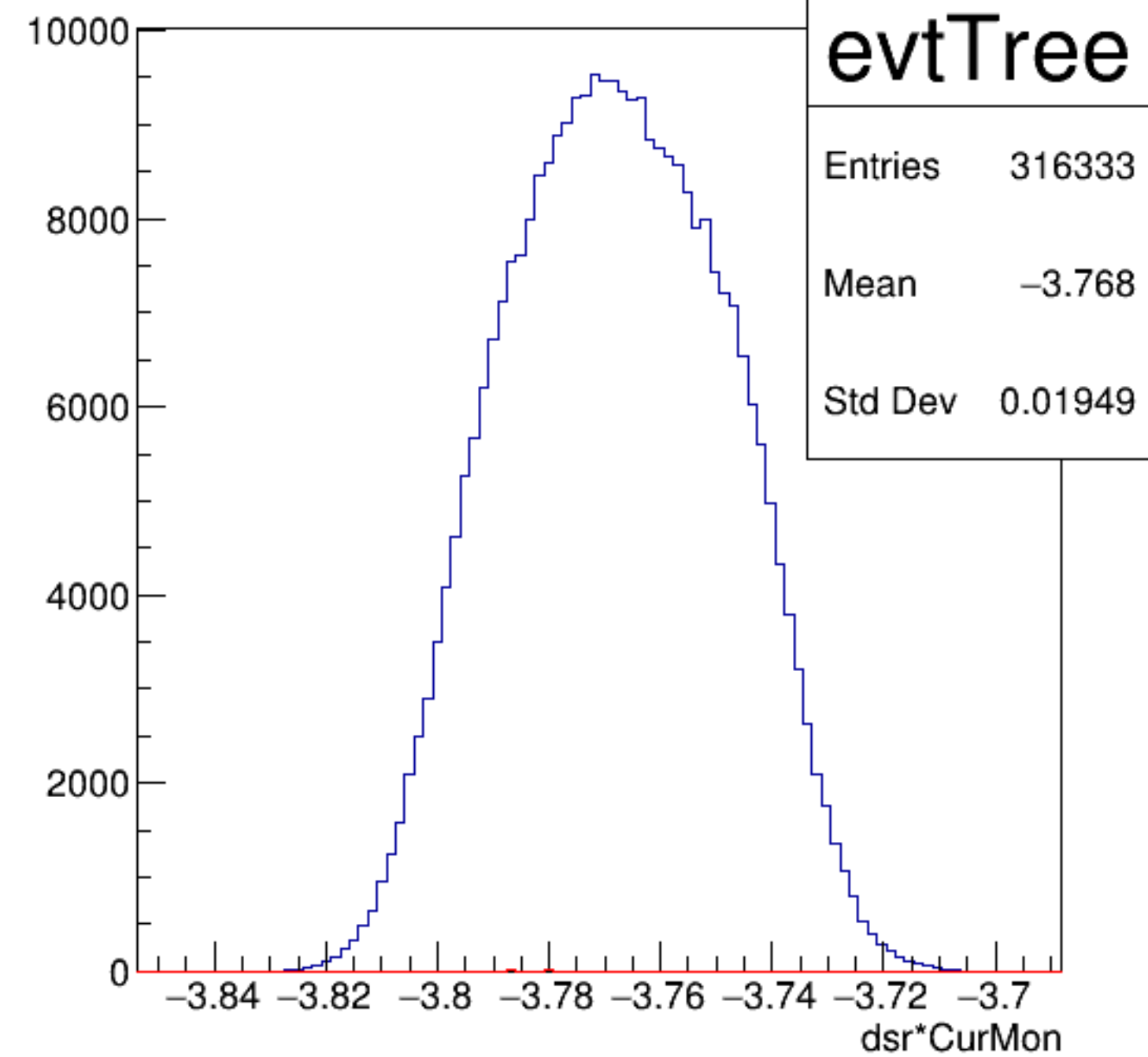
Entries	79026
Mean	-1.156
Std Dev	1514
Underflow	5971
Overflow	5929

dsr*CurMon:Entry\$

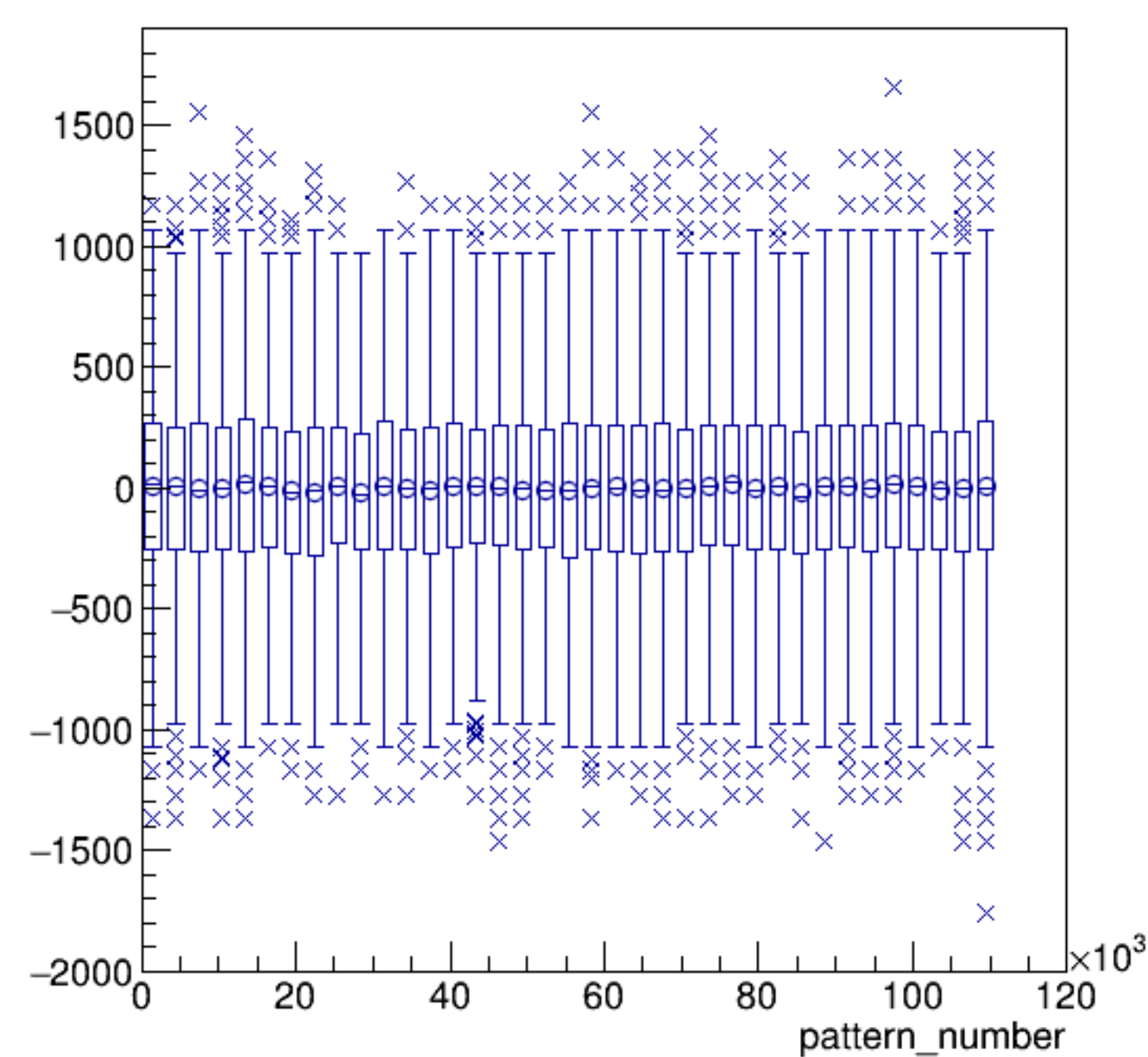


run6527_000_maindet_dsr.png

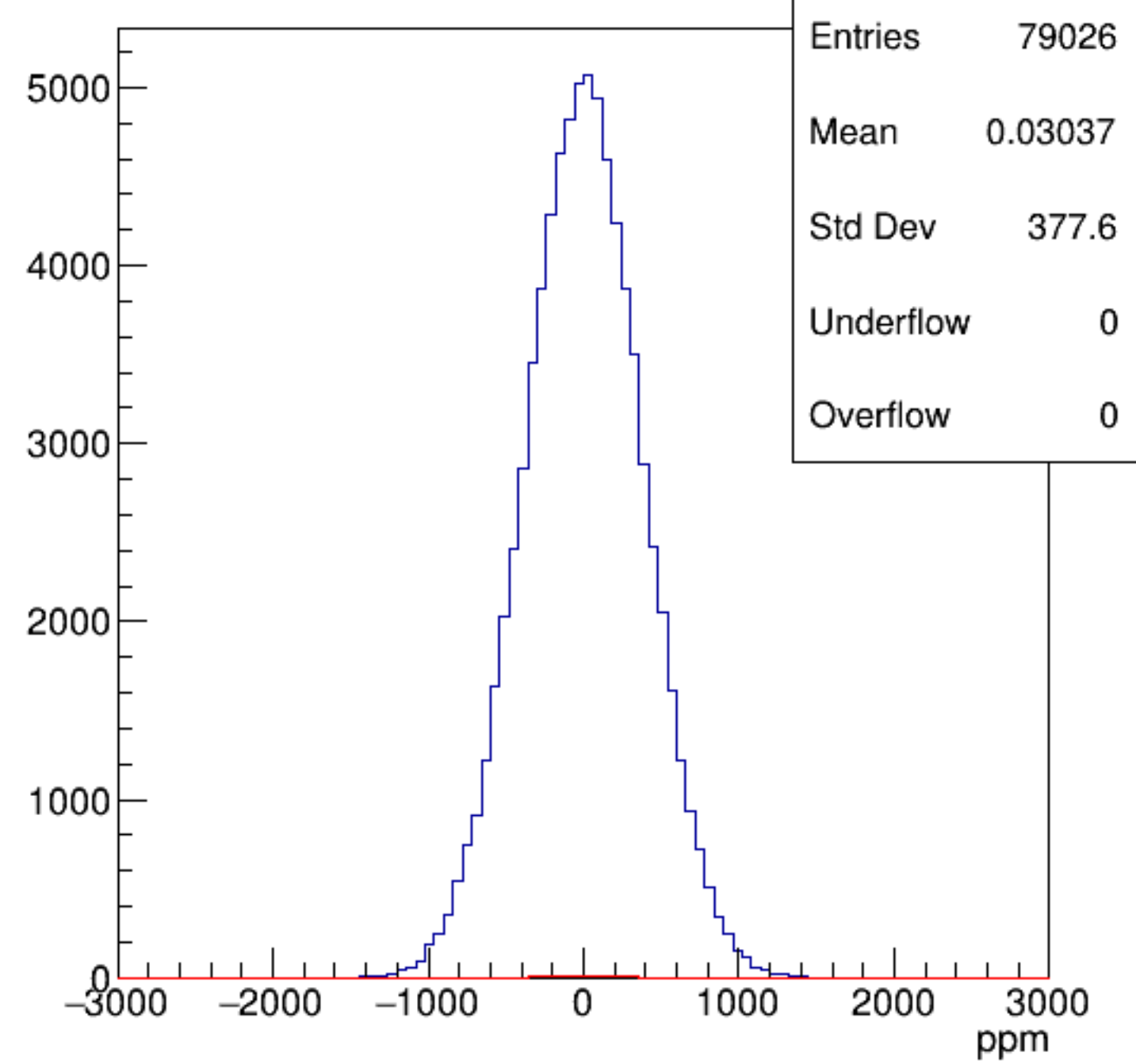
dsr*CurMon {ErrorFlag==0}



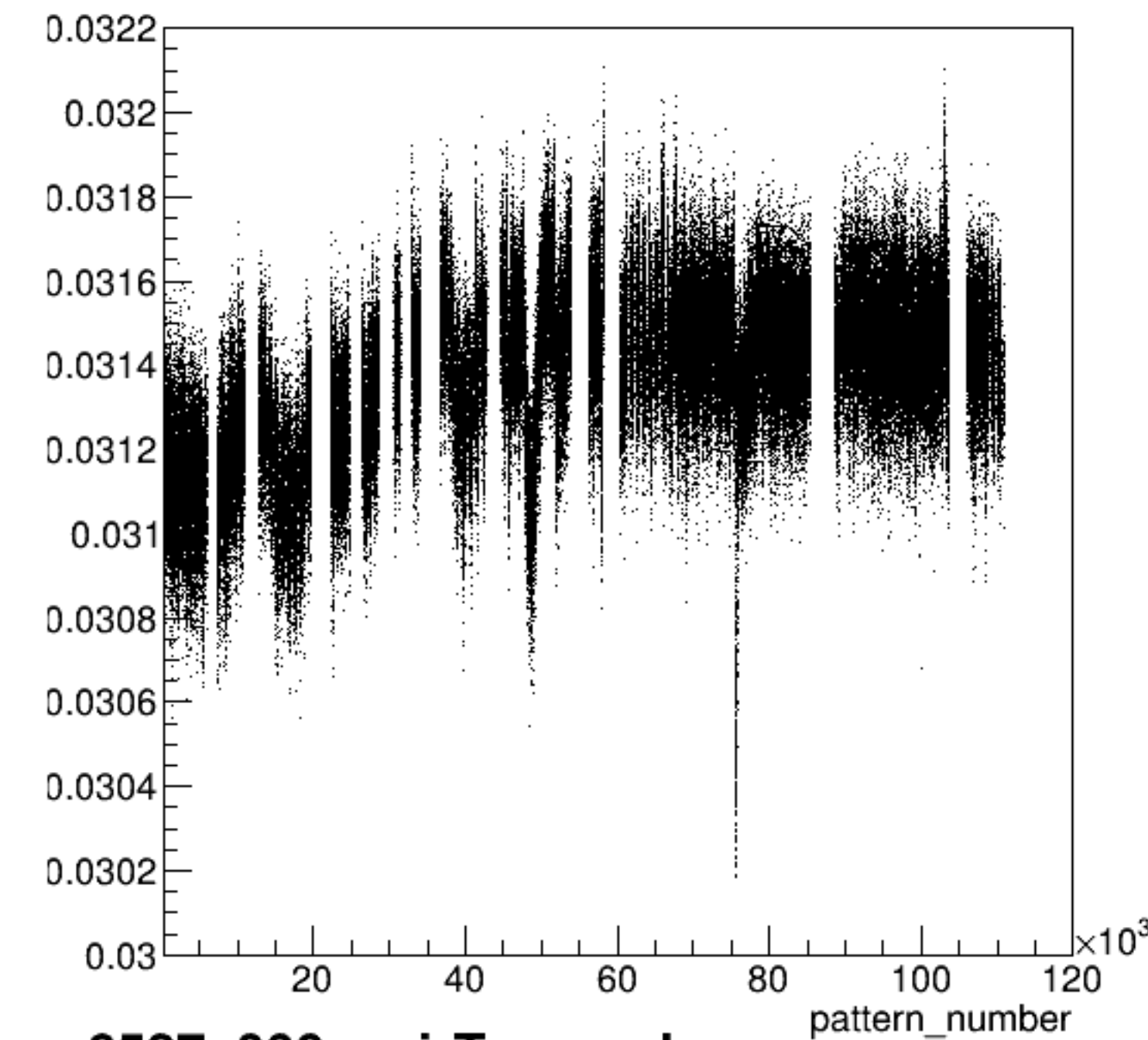
asym_dsr/ppm:pattern_number {ErrorFlag==0}



asym_dsr

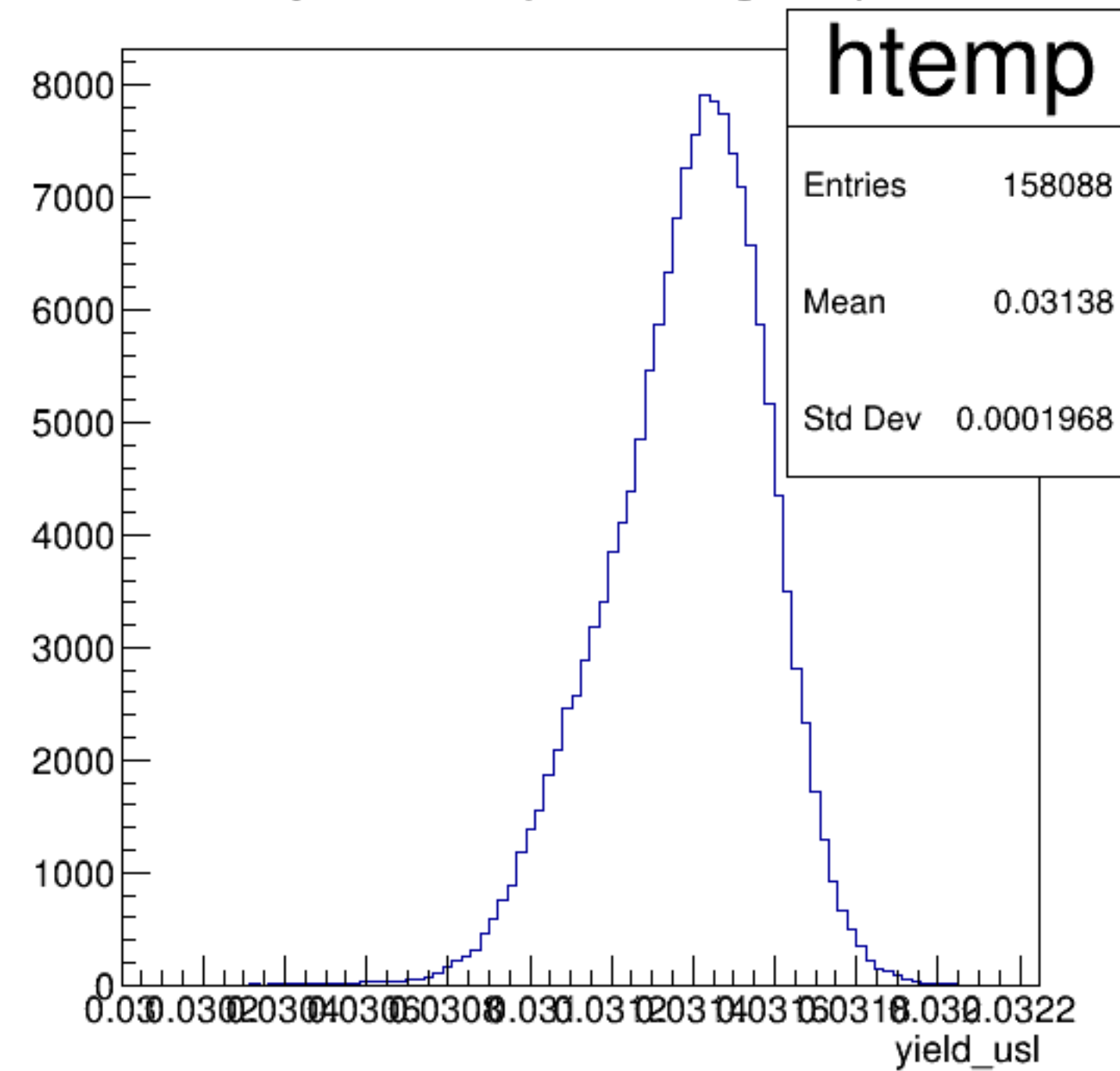


yield_usl:pattern_number {ErrorFlag==0}

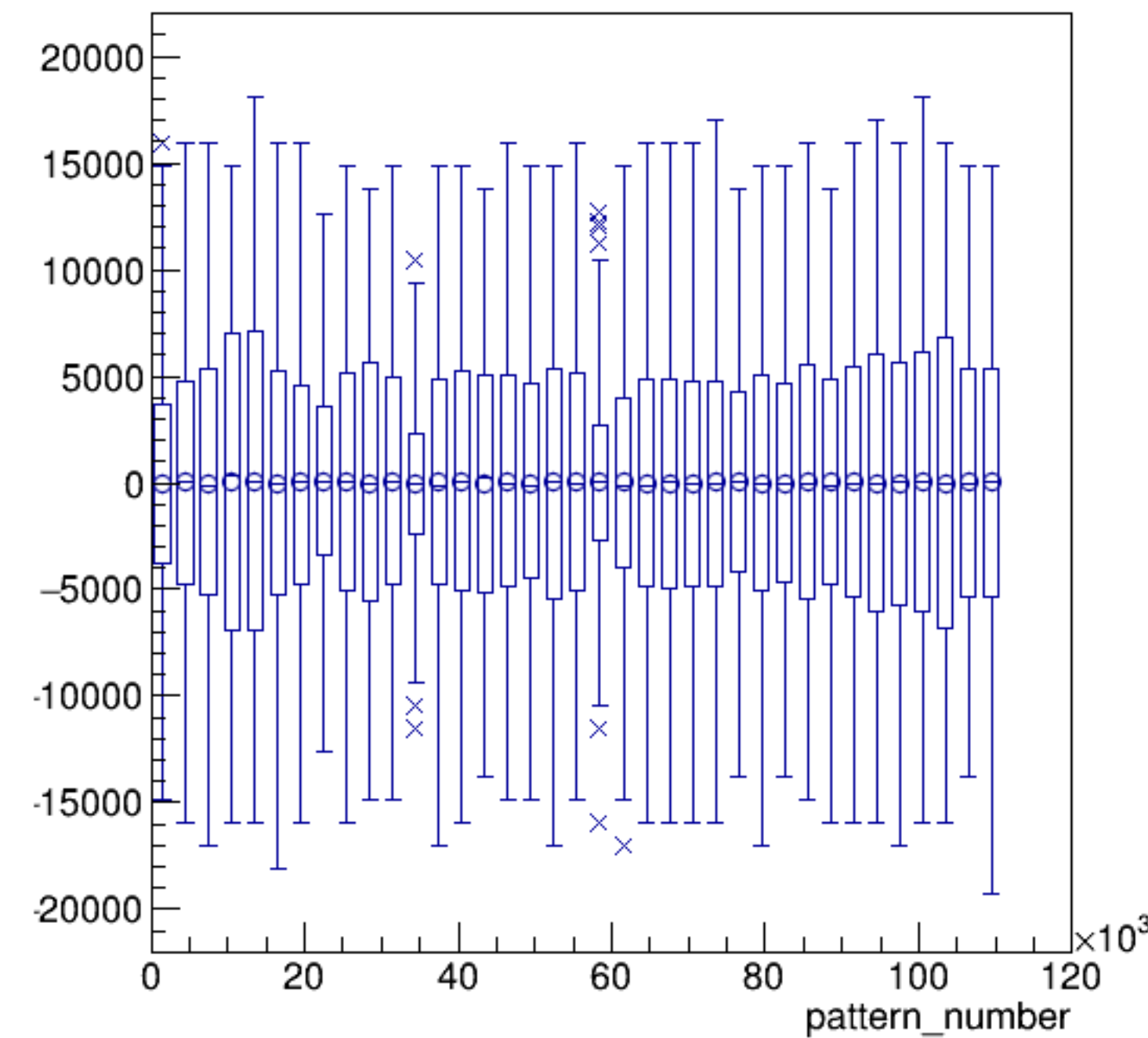


run6527_000_pairTree_usl.png

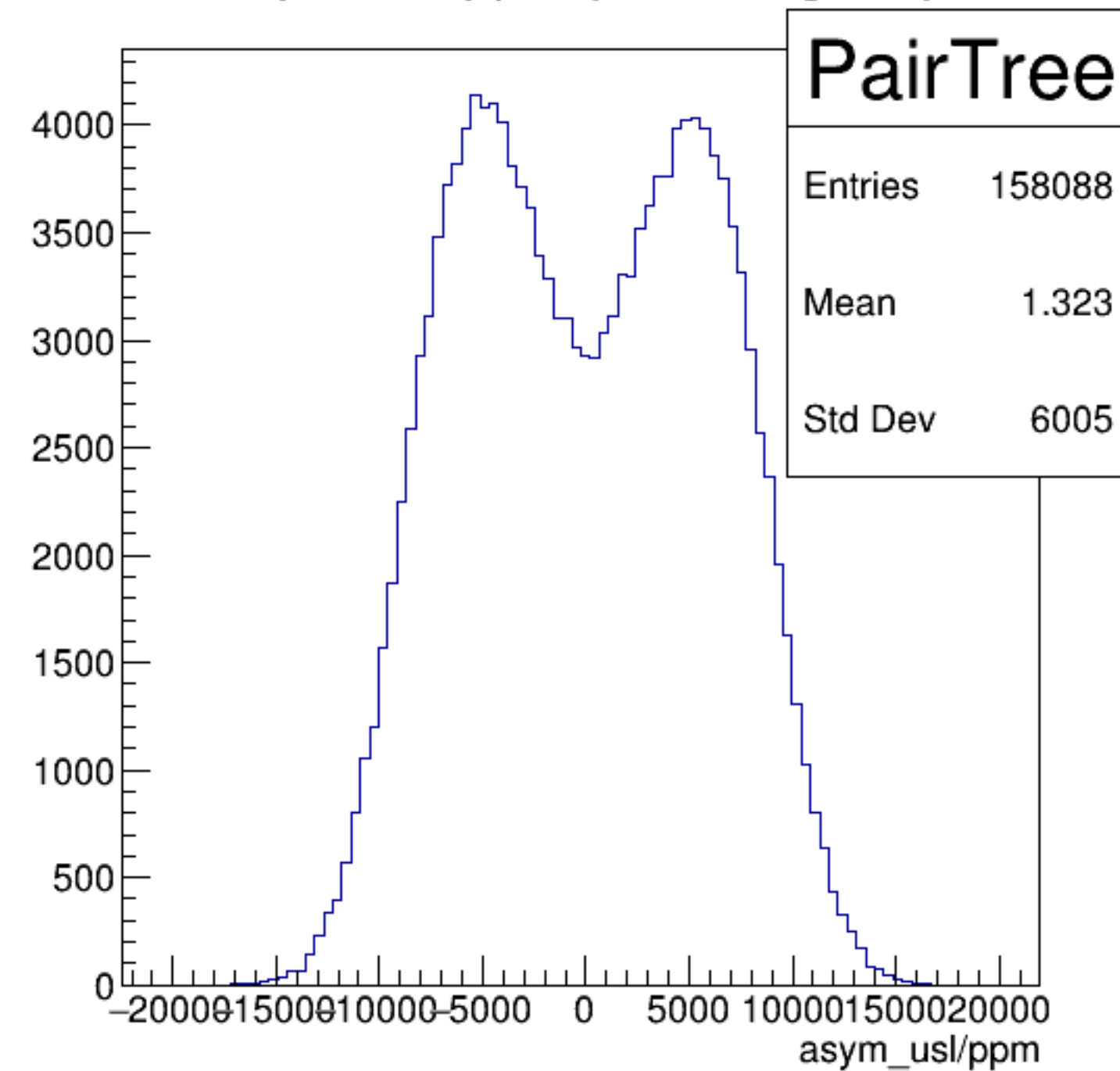
yield_usl {ErrorFlag==0}

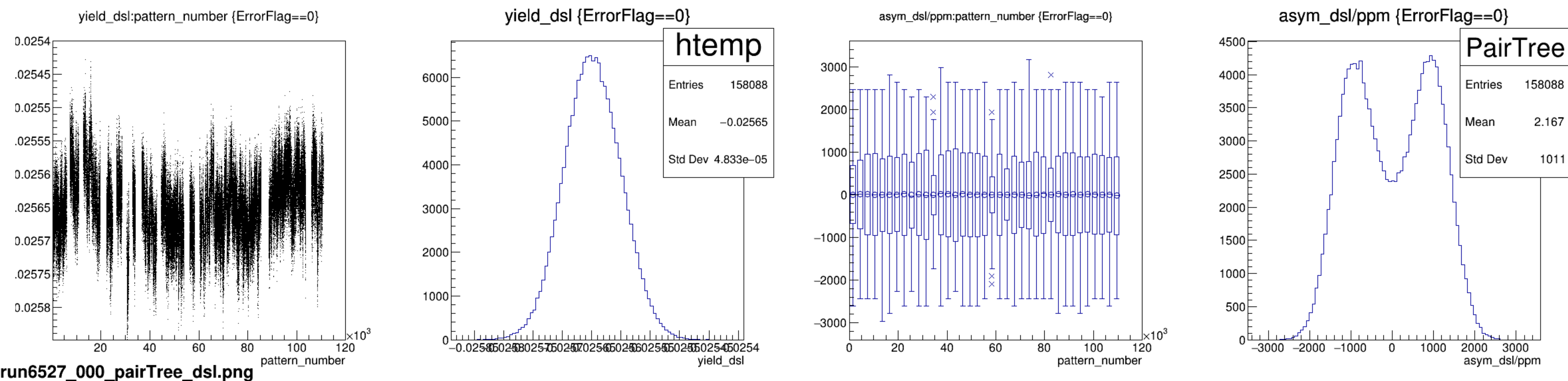


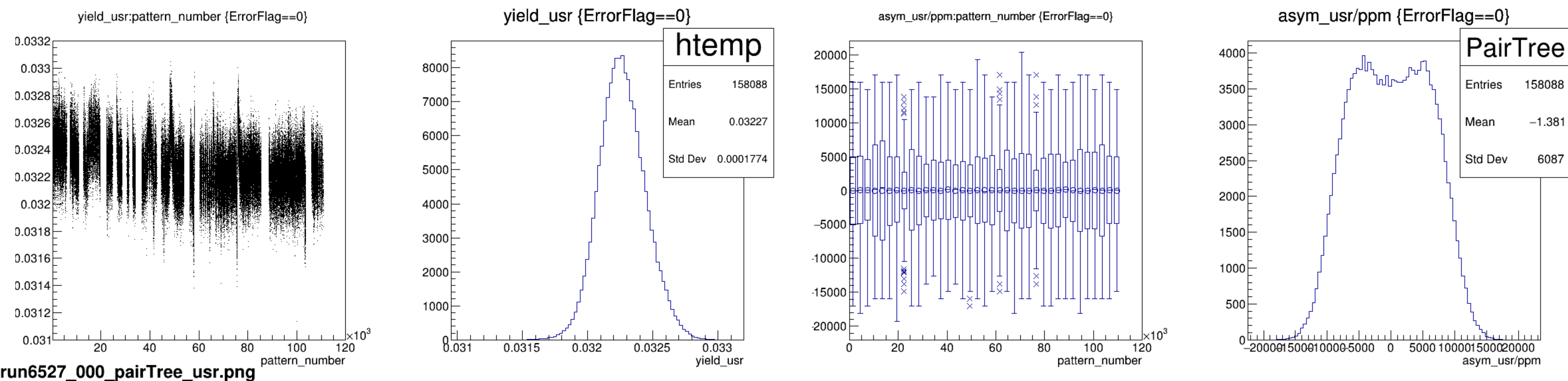
asym_usl/ppm:pattern_number {ErrorFlag==0}

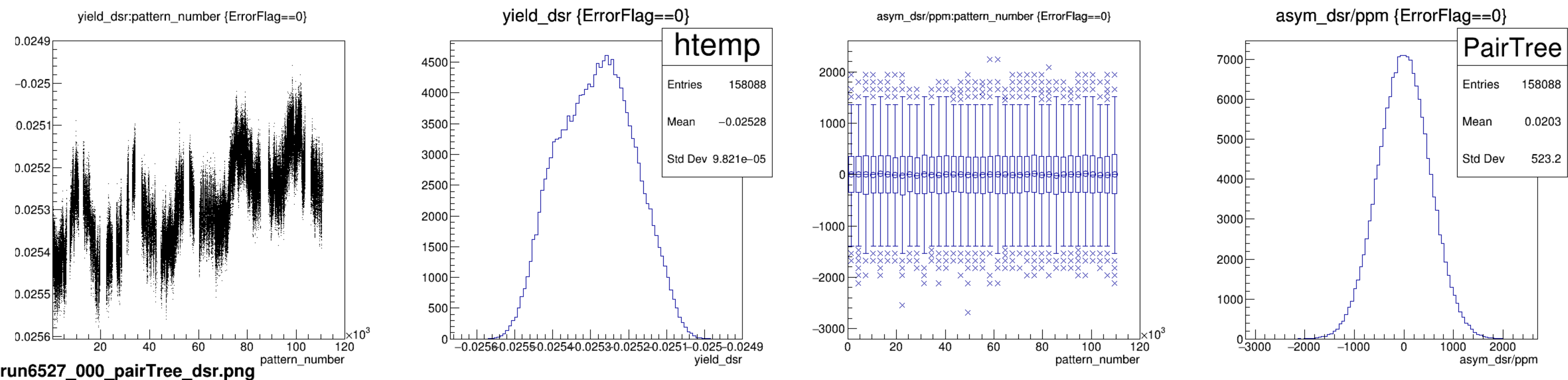


asym_usl/ppm {ErrorFlag==0}

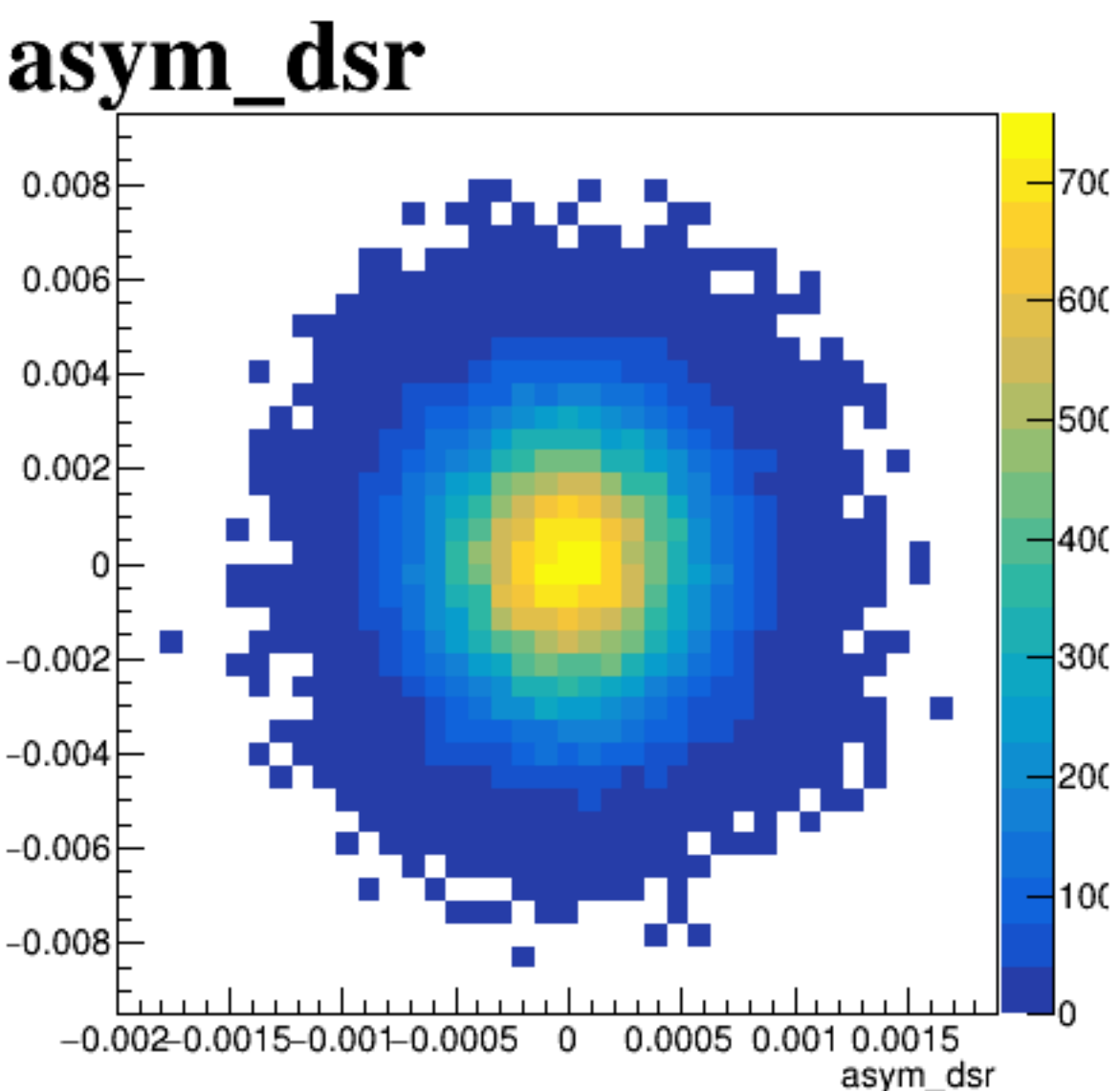
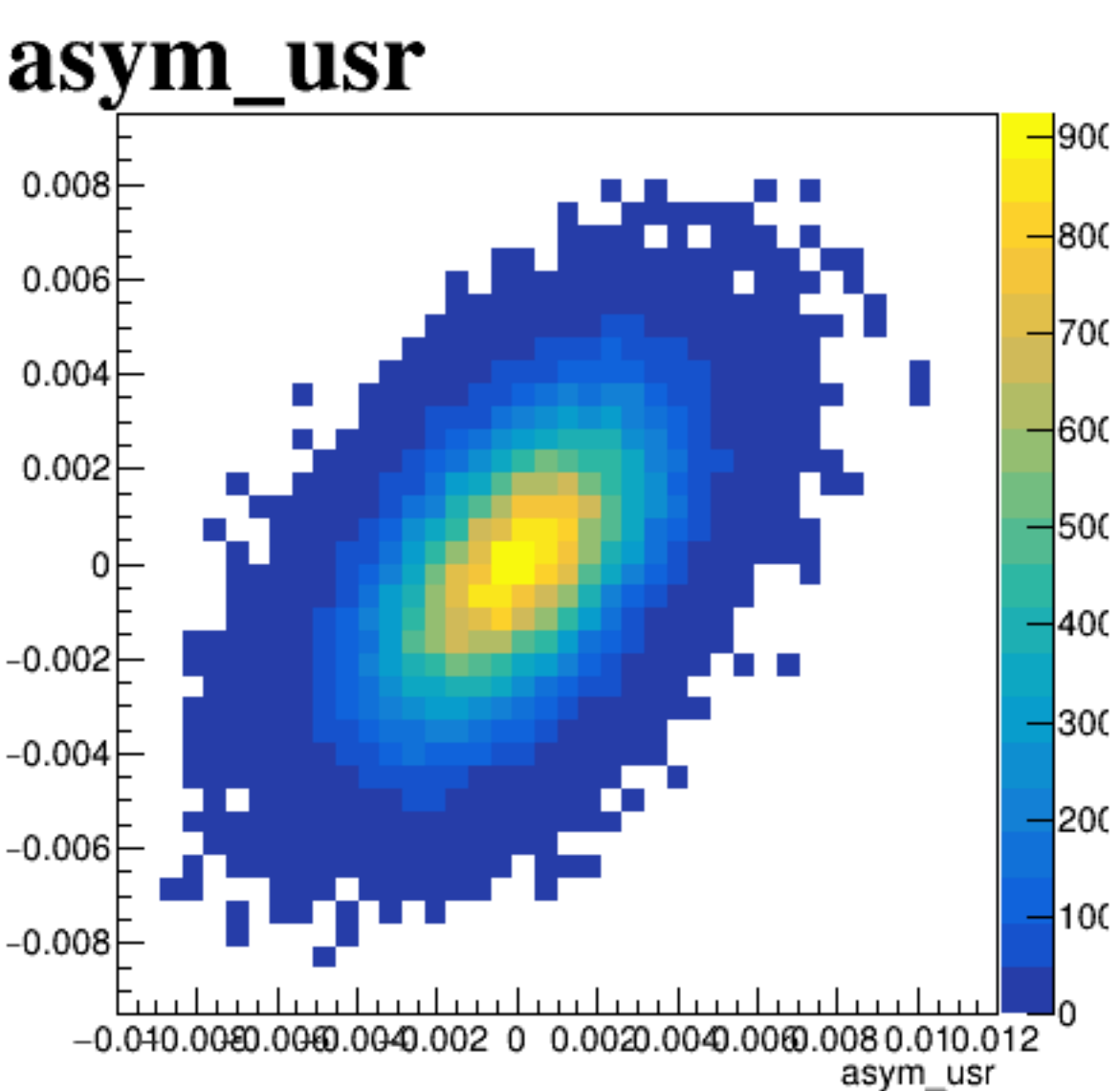
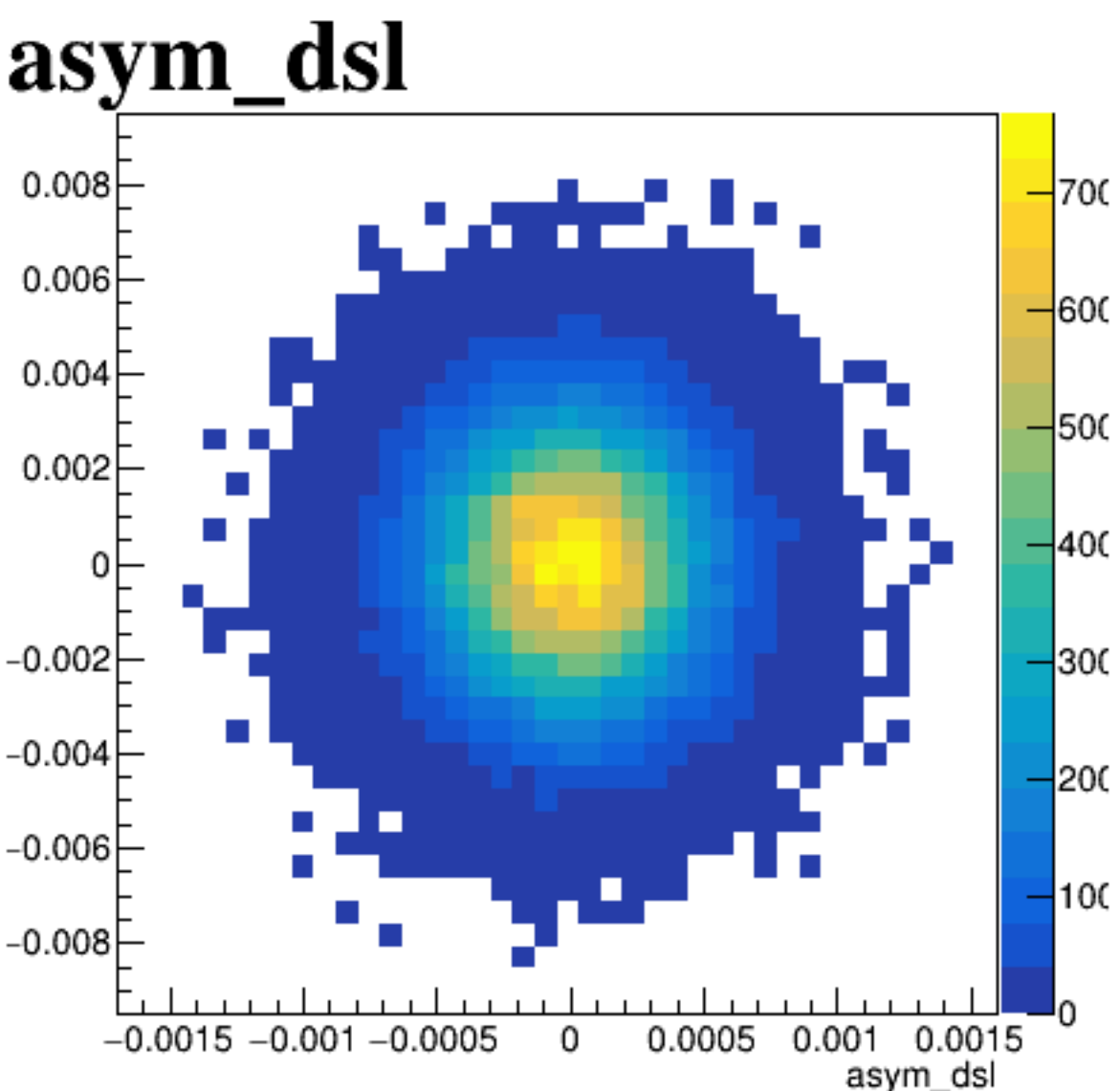
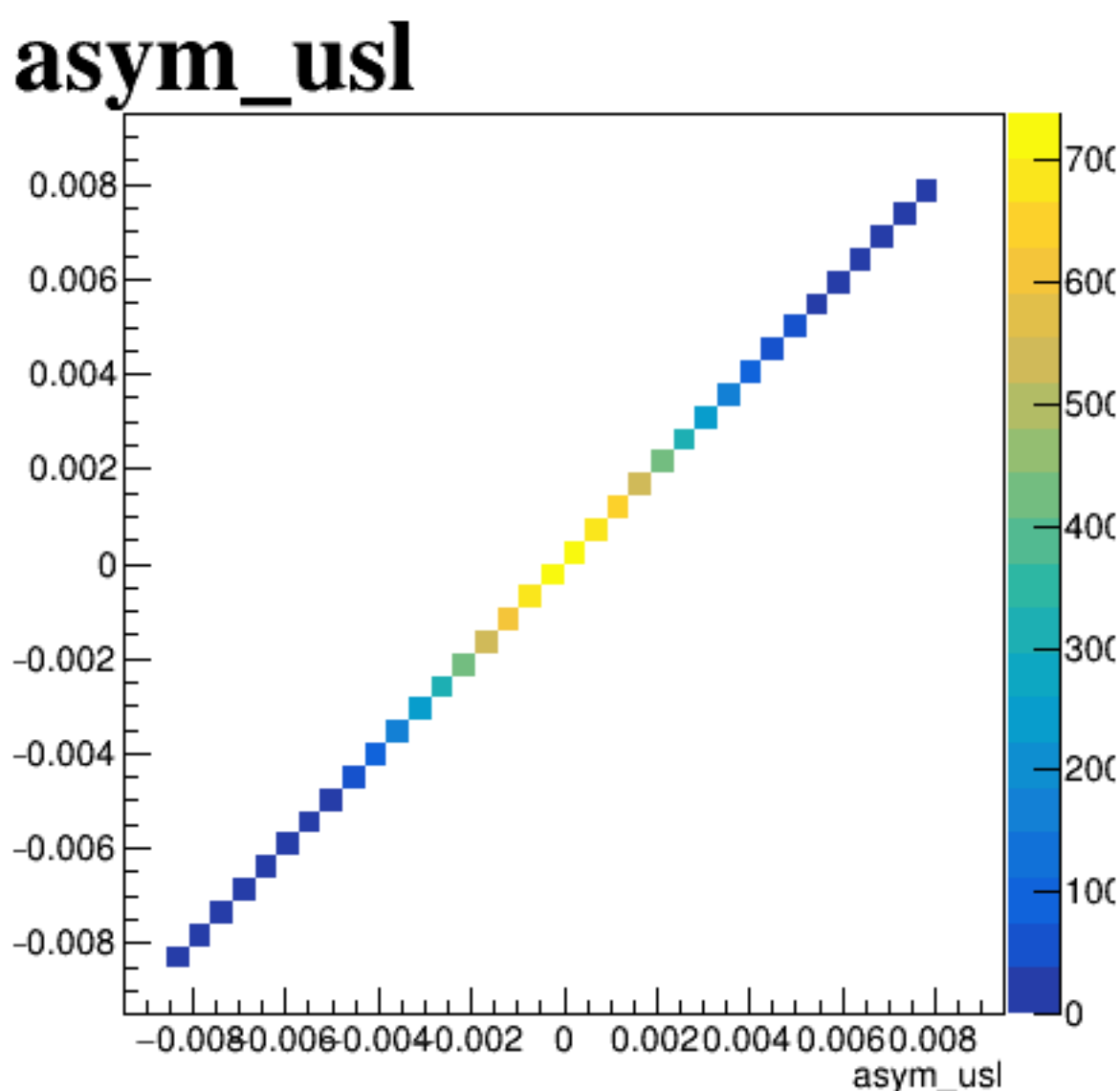




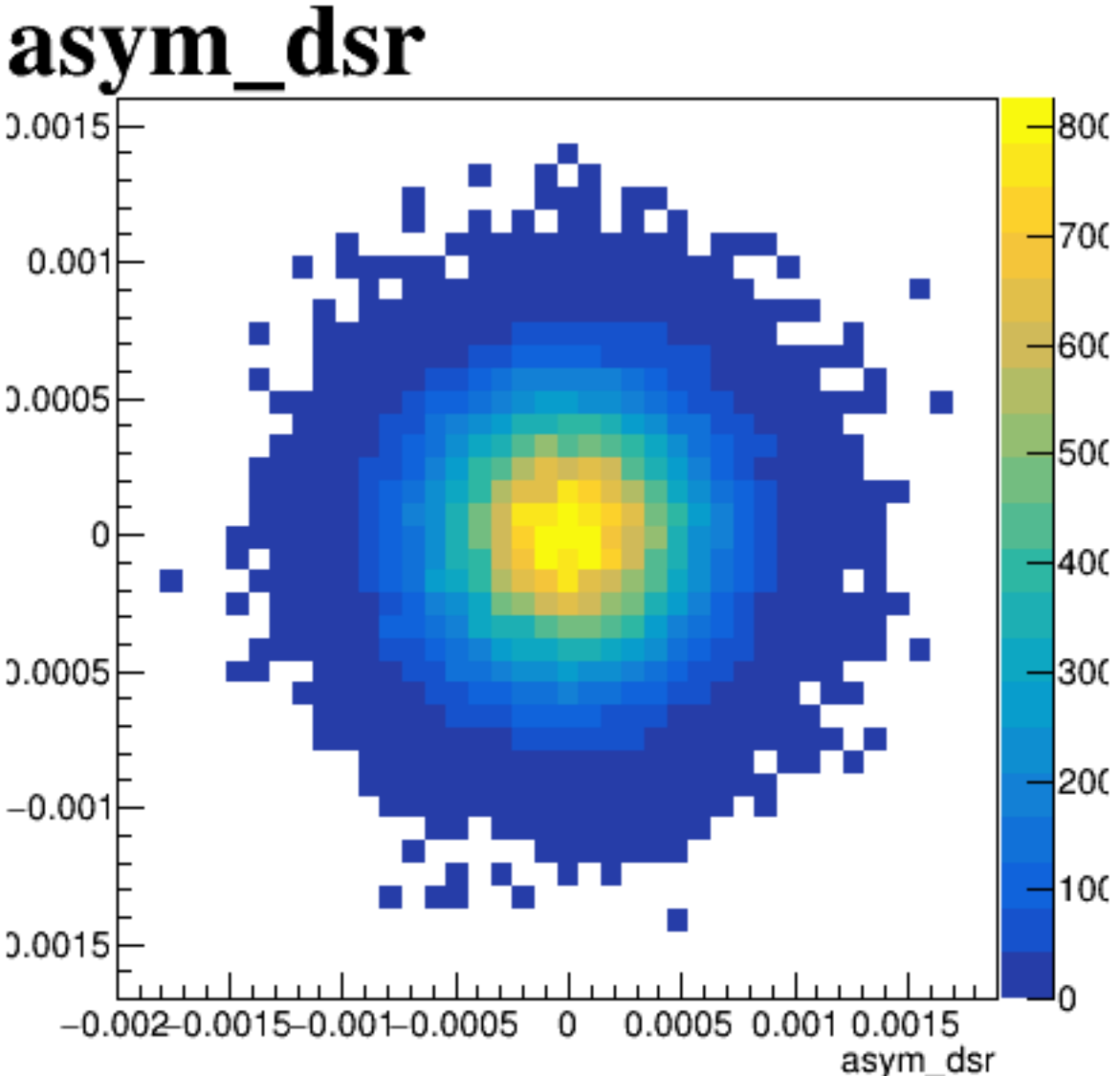
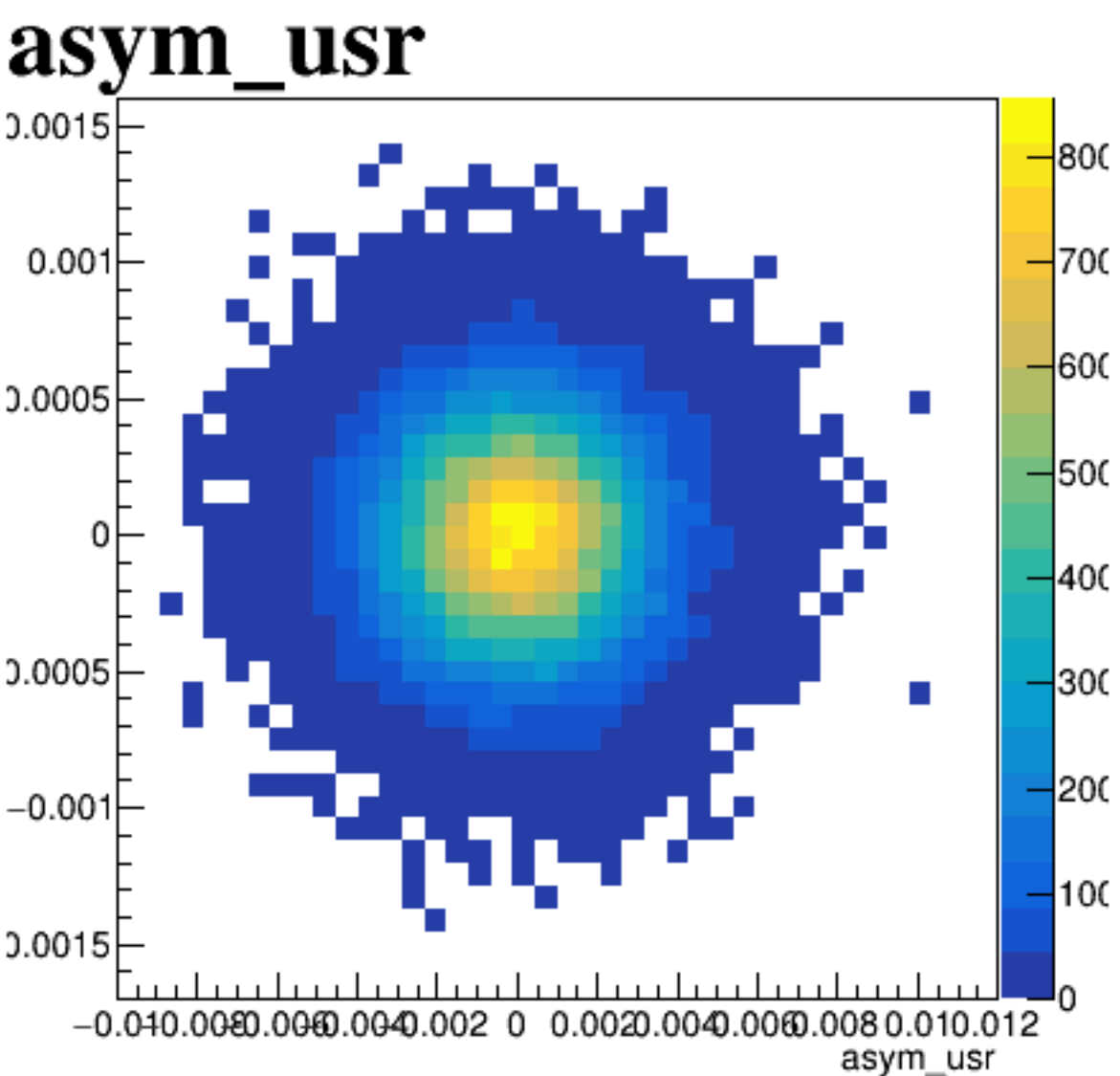
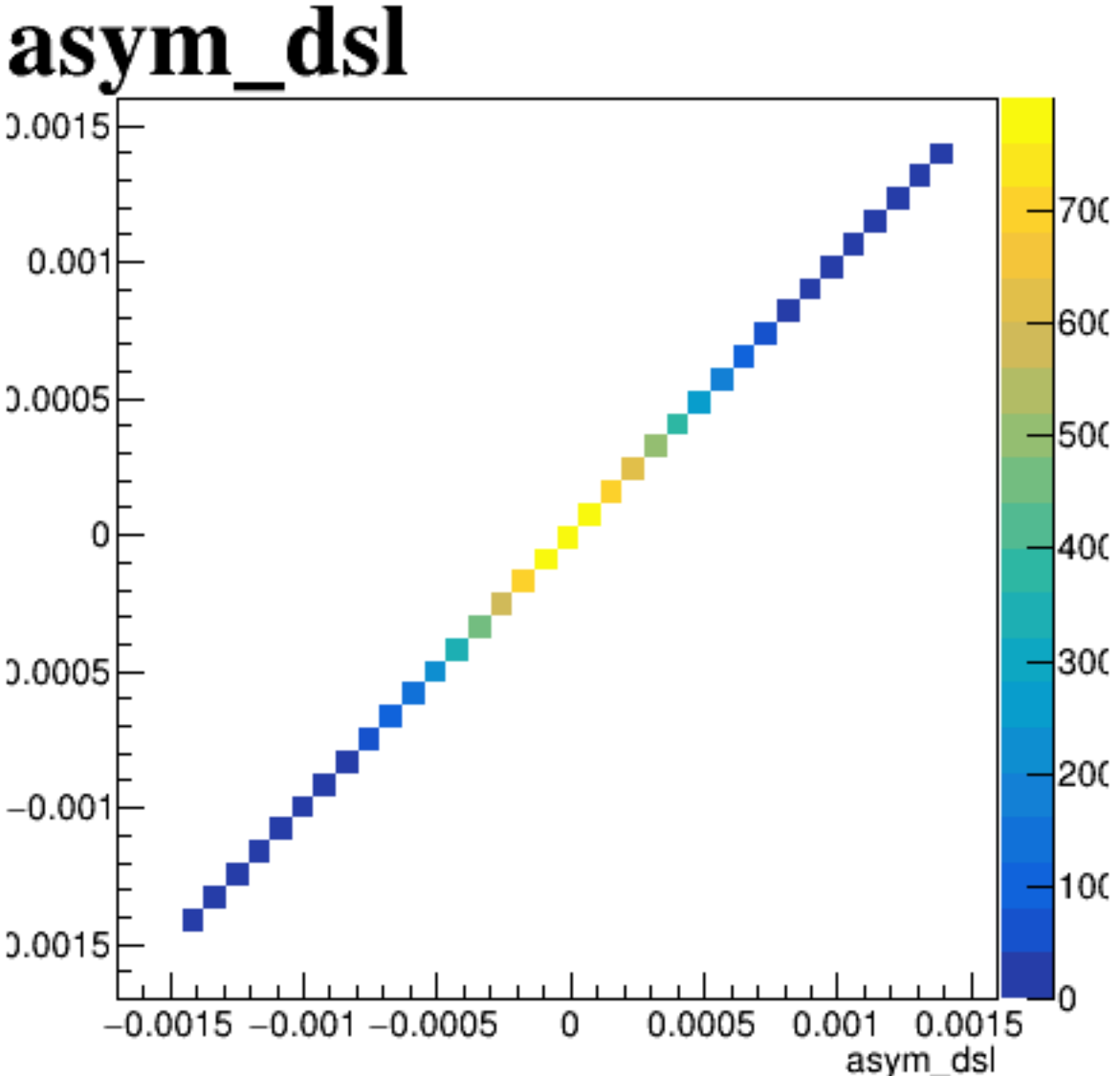
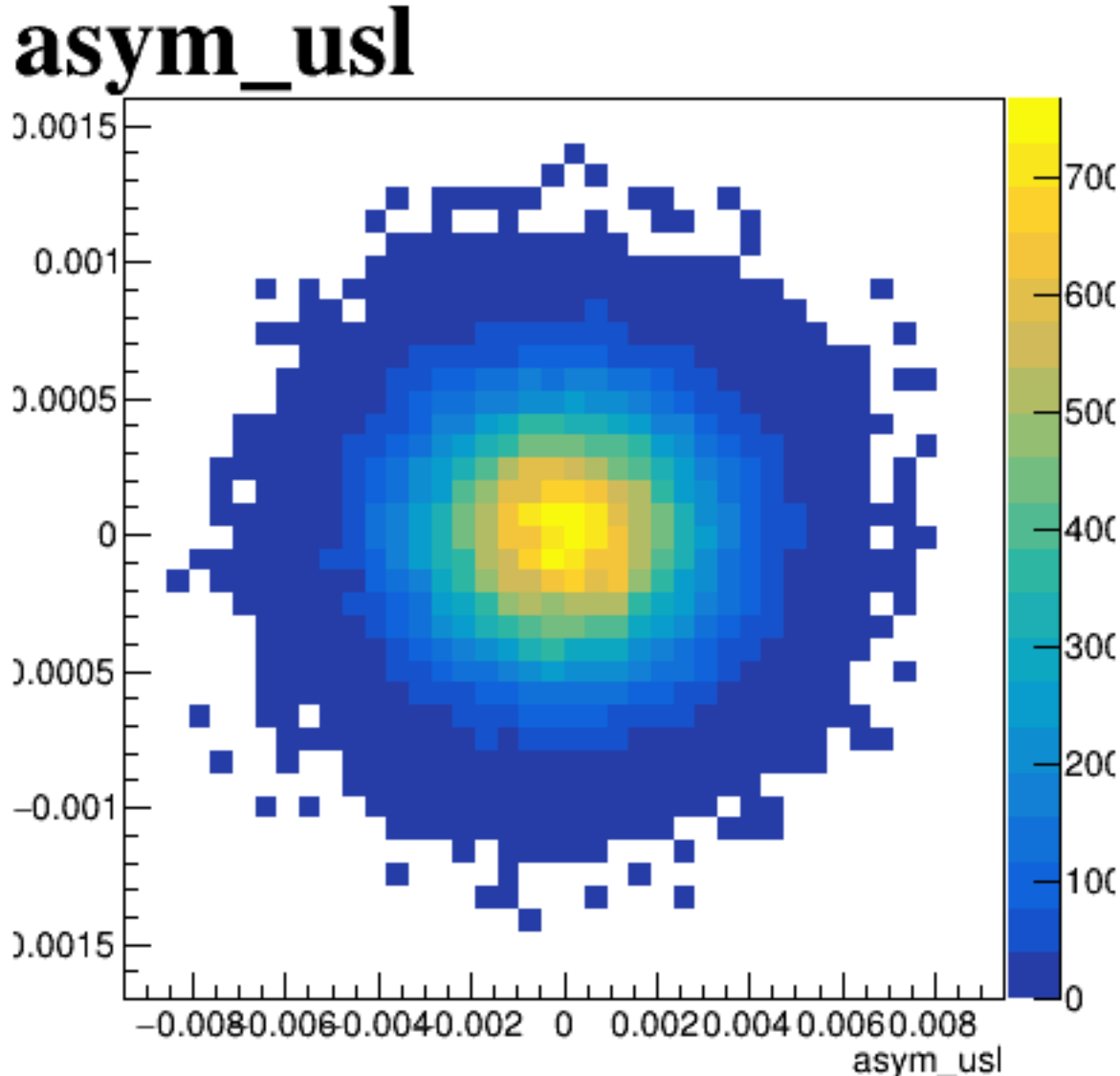




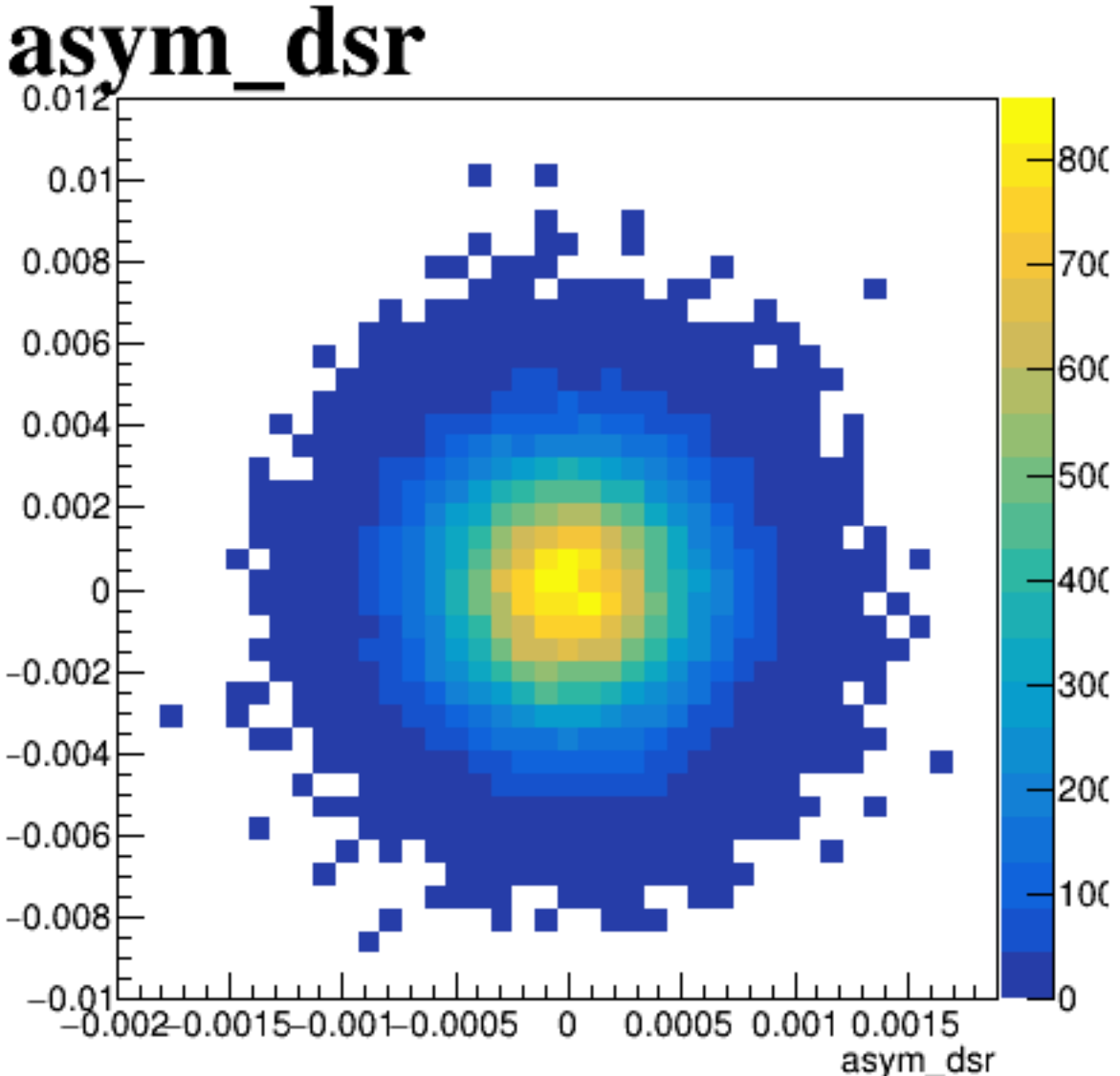
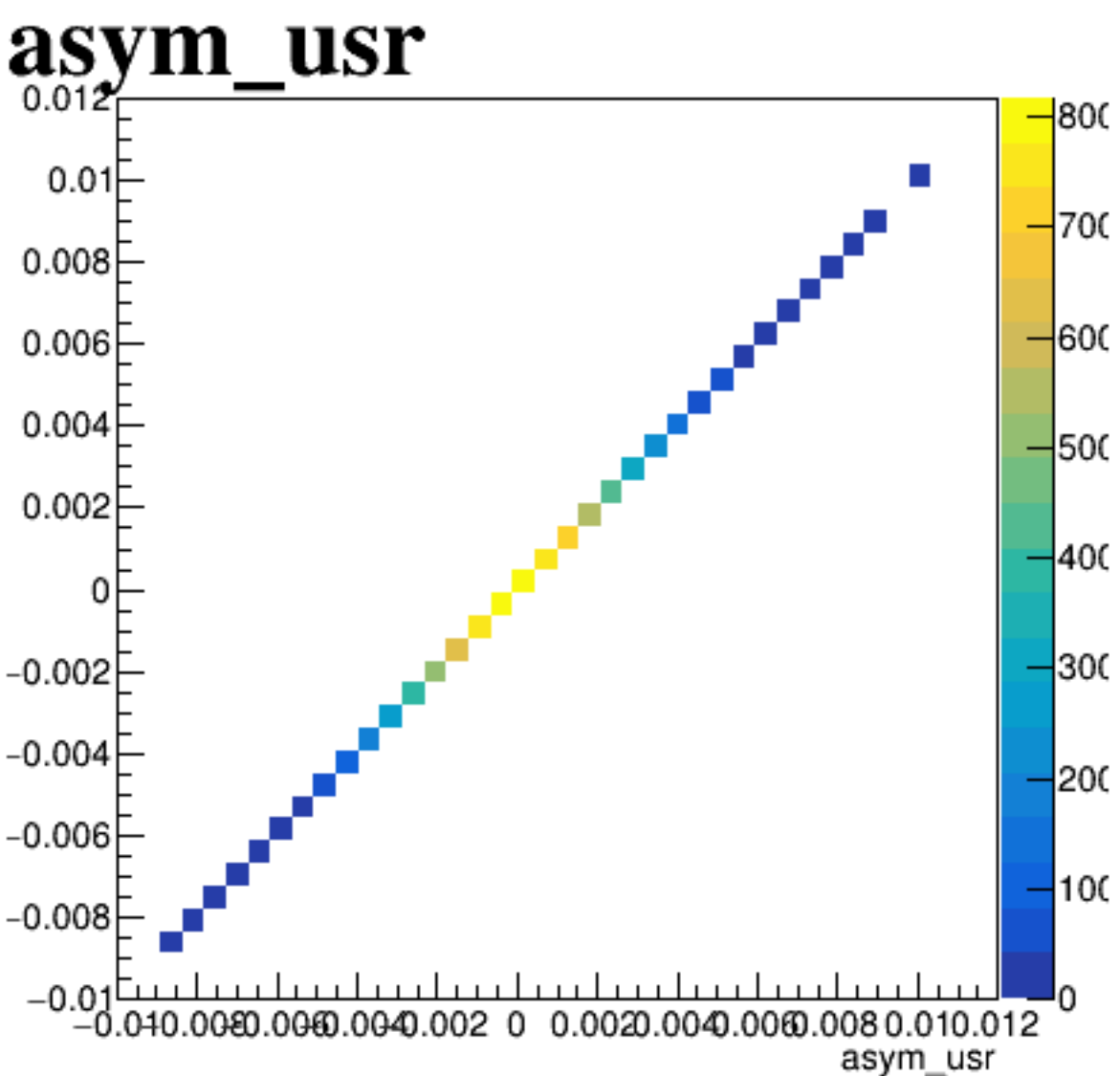
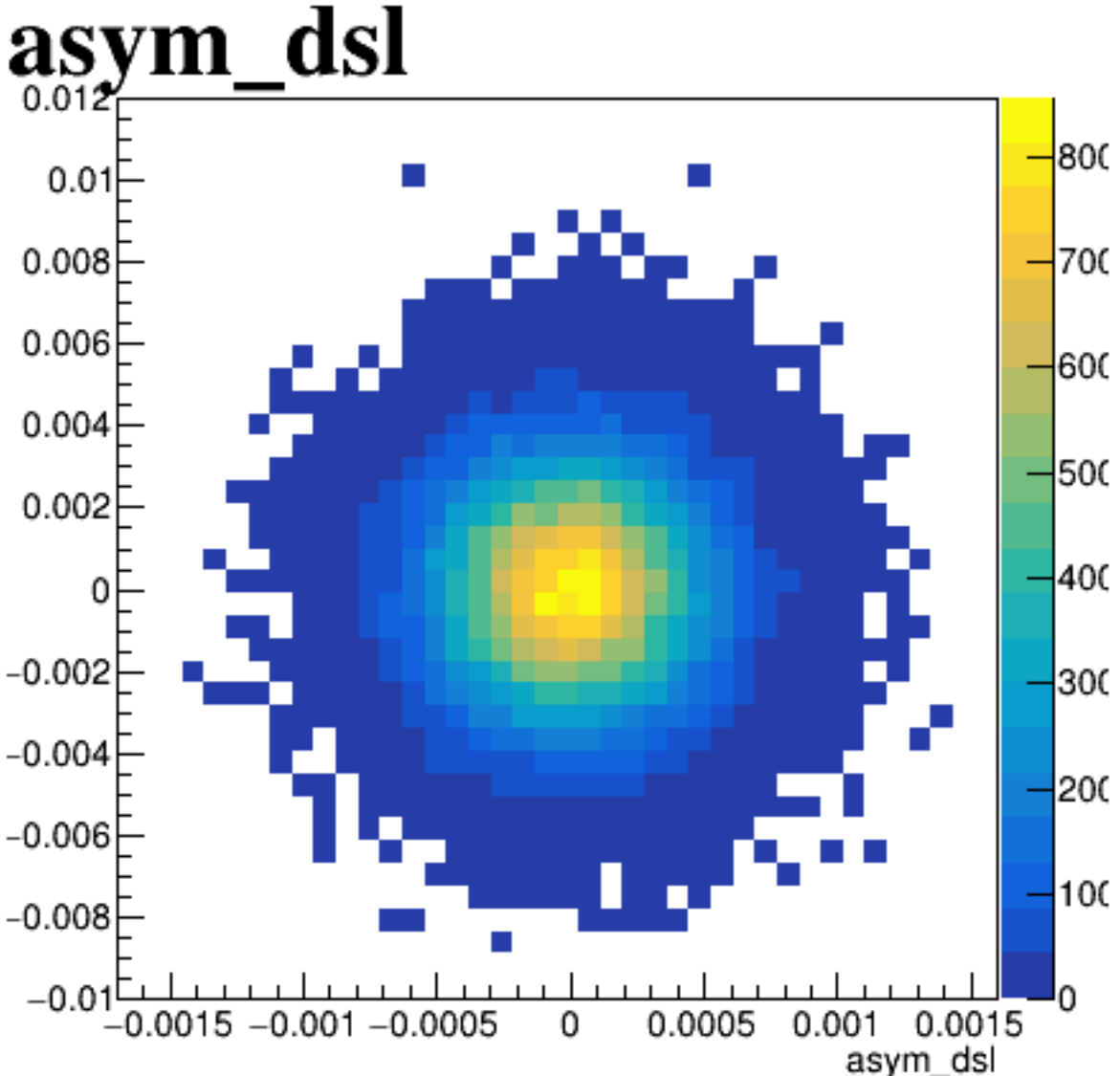
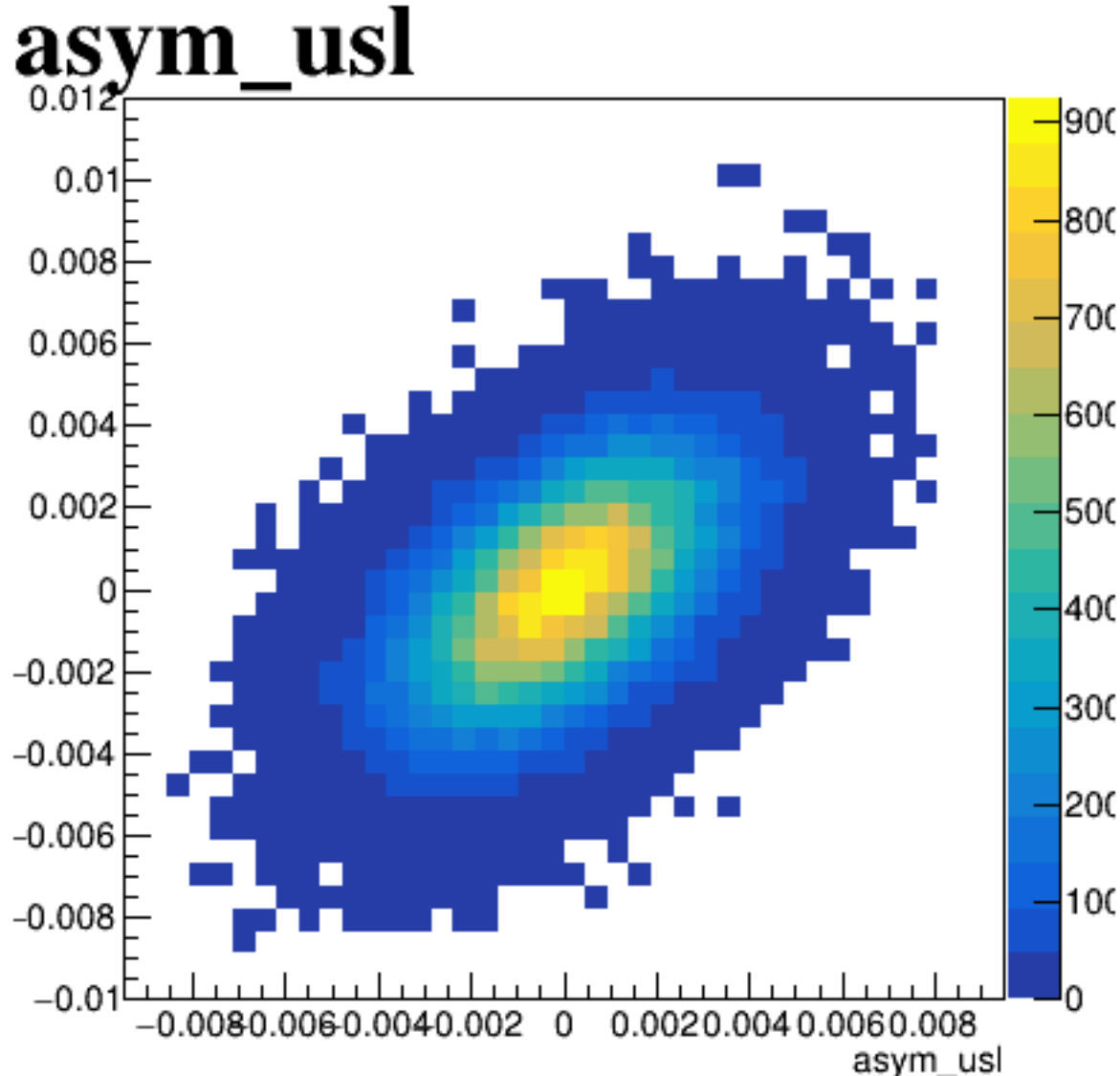
asym_usl



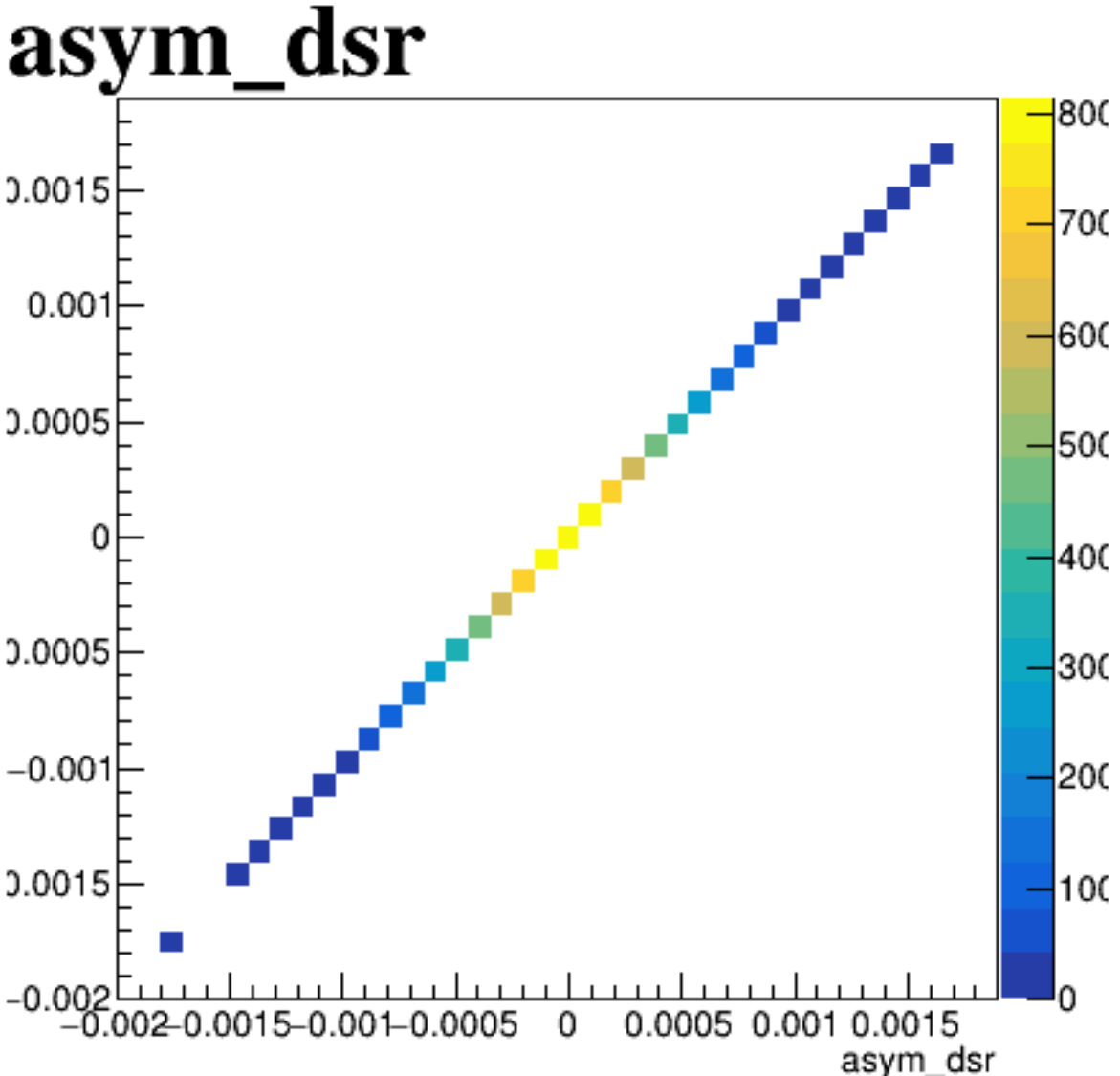
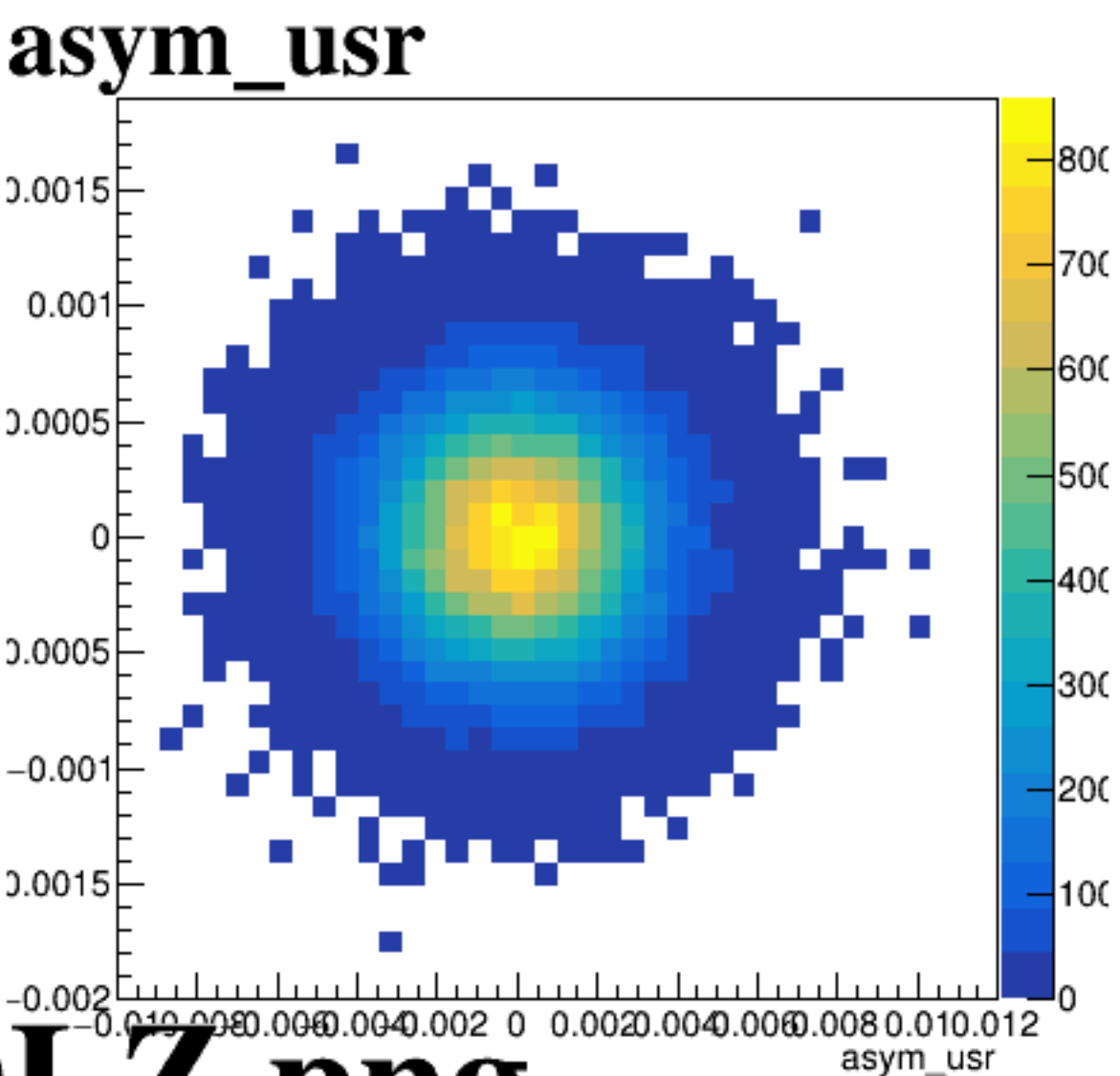
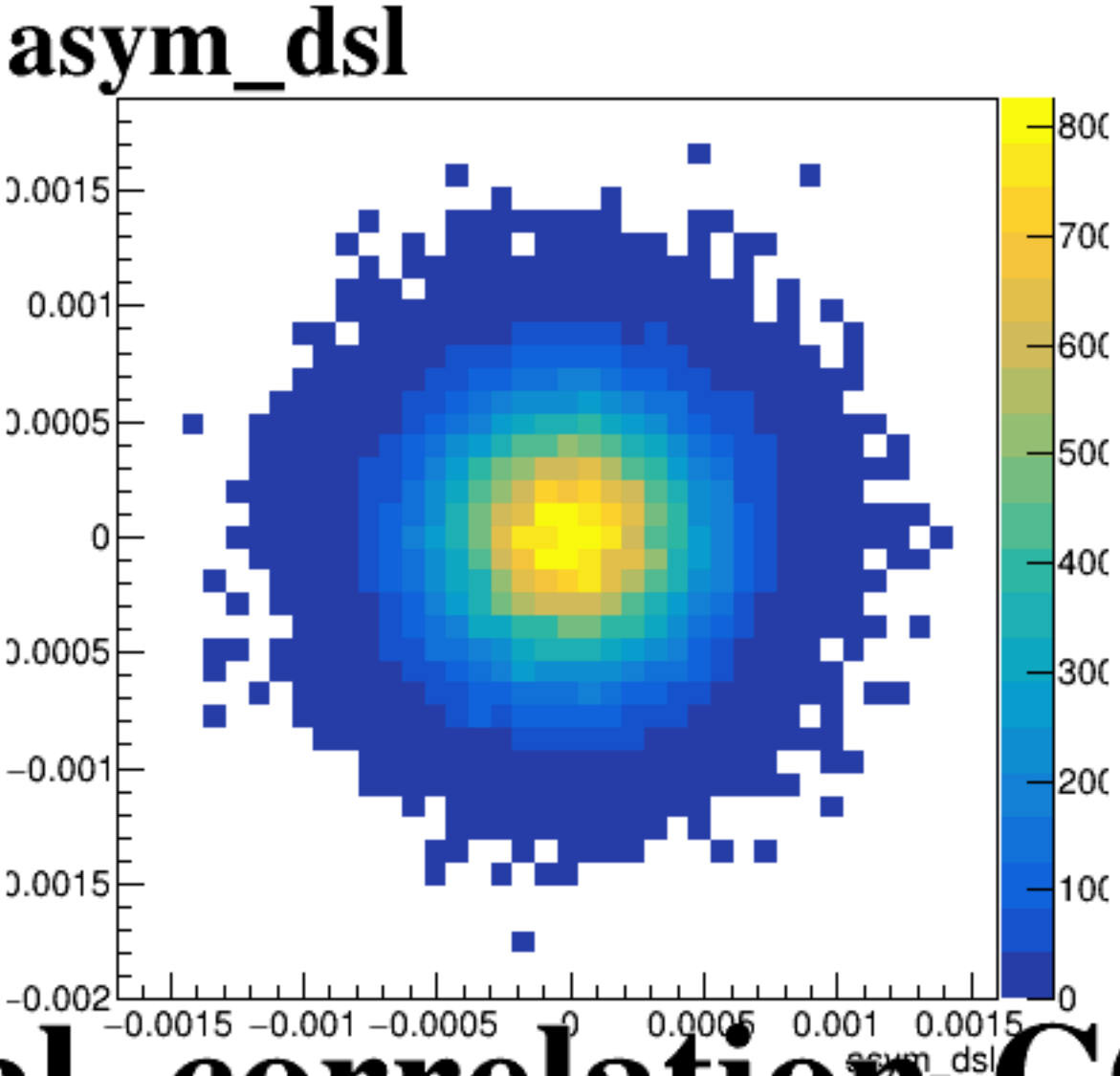
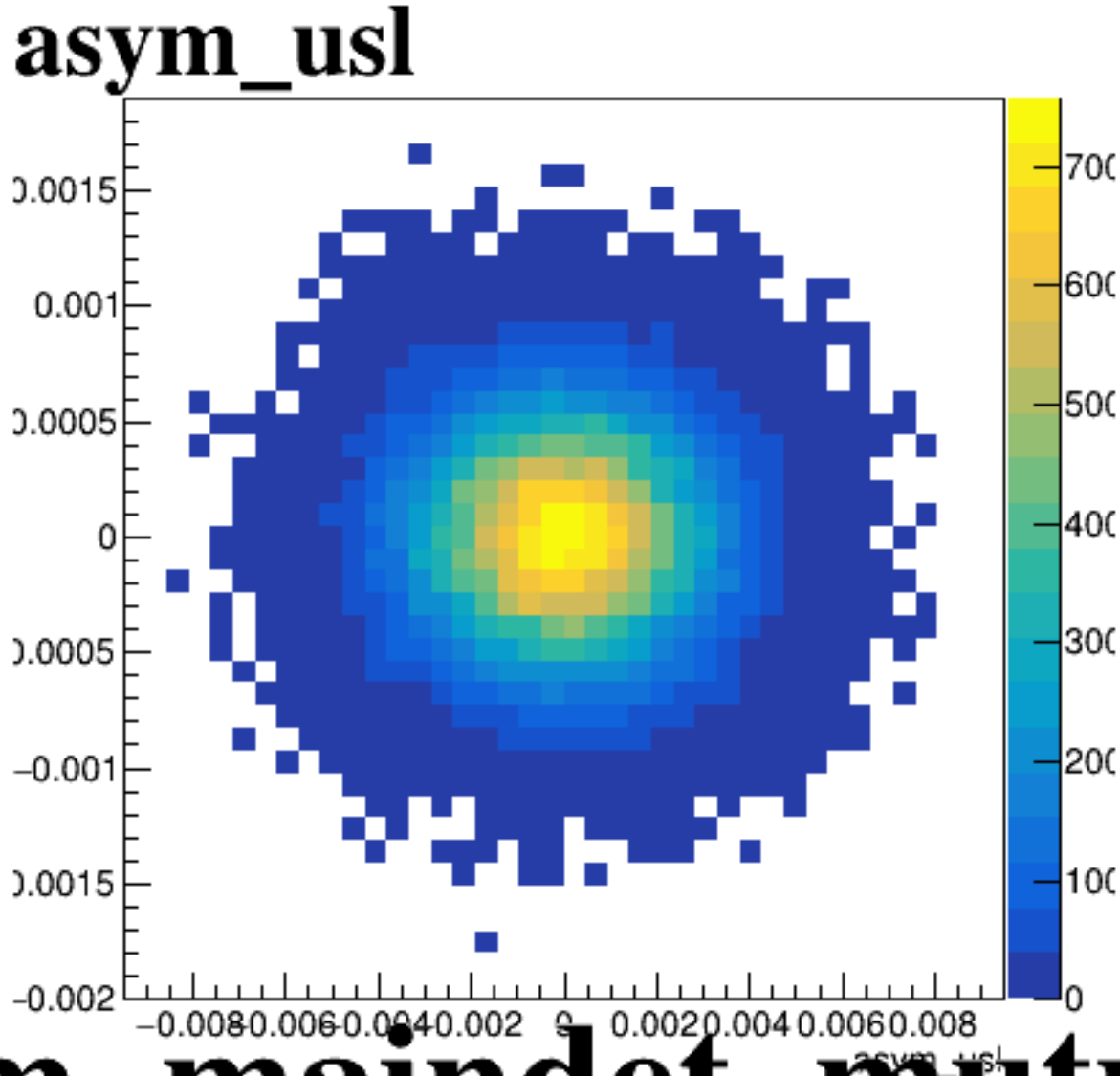
asym_dsl



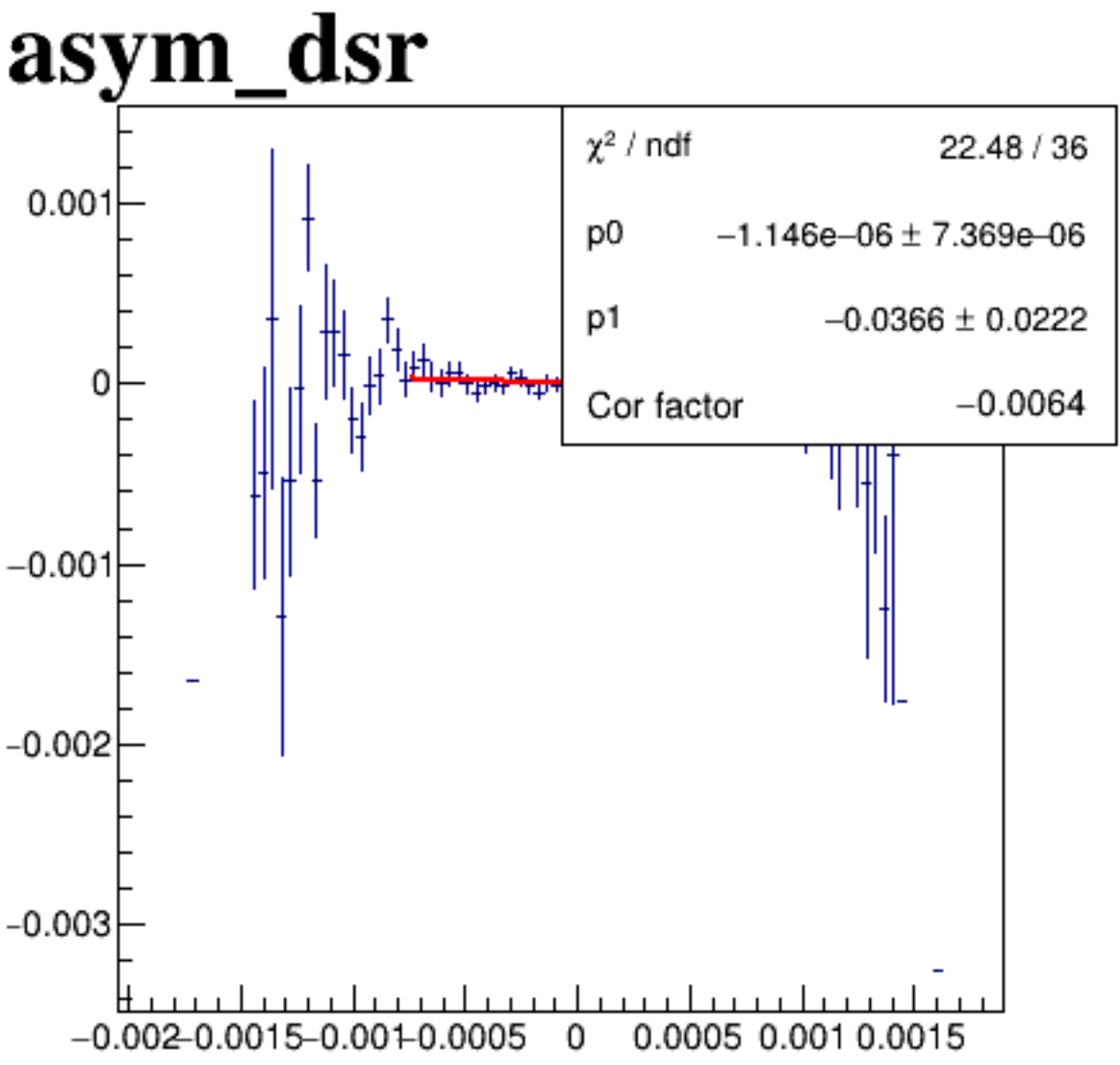
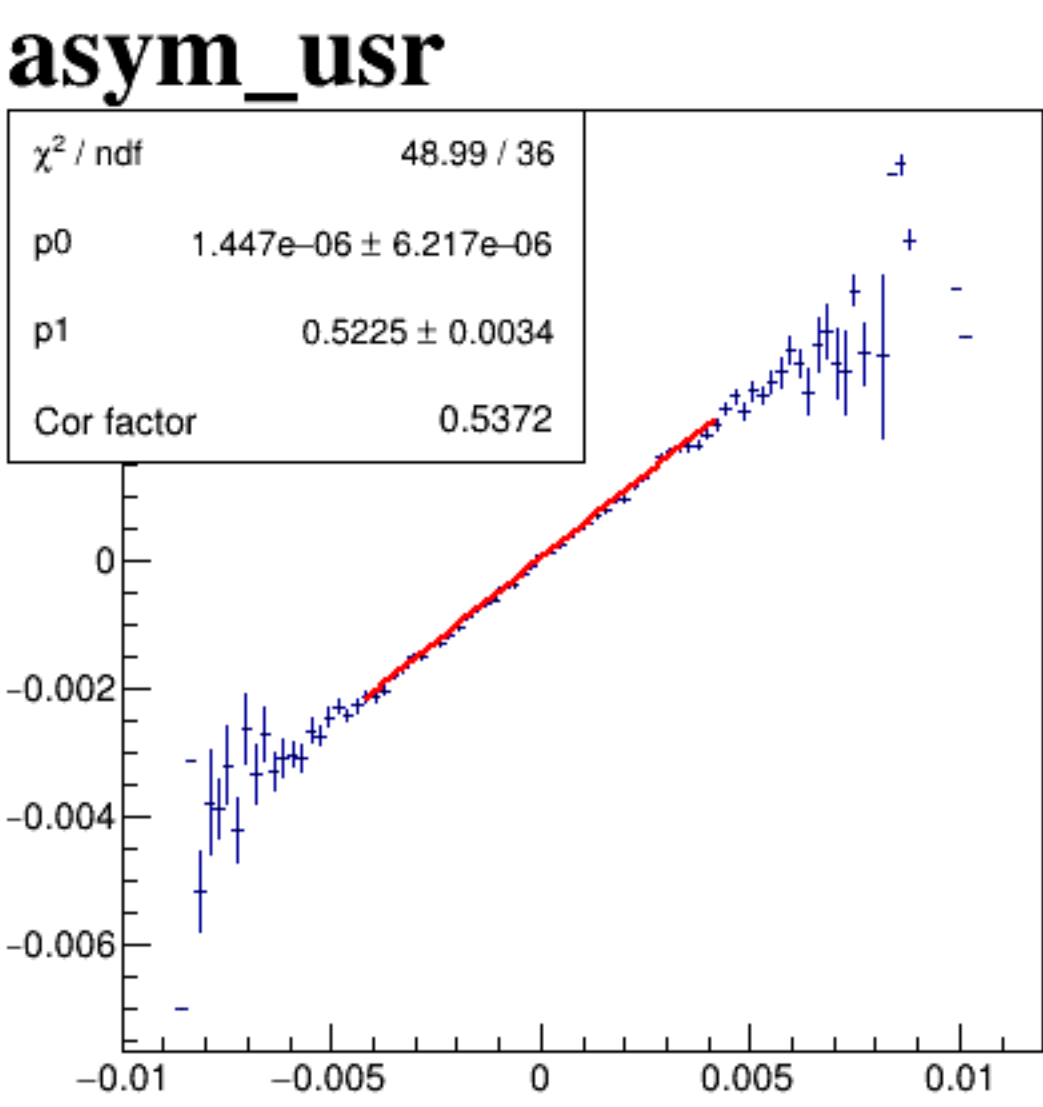
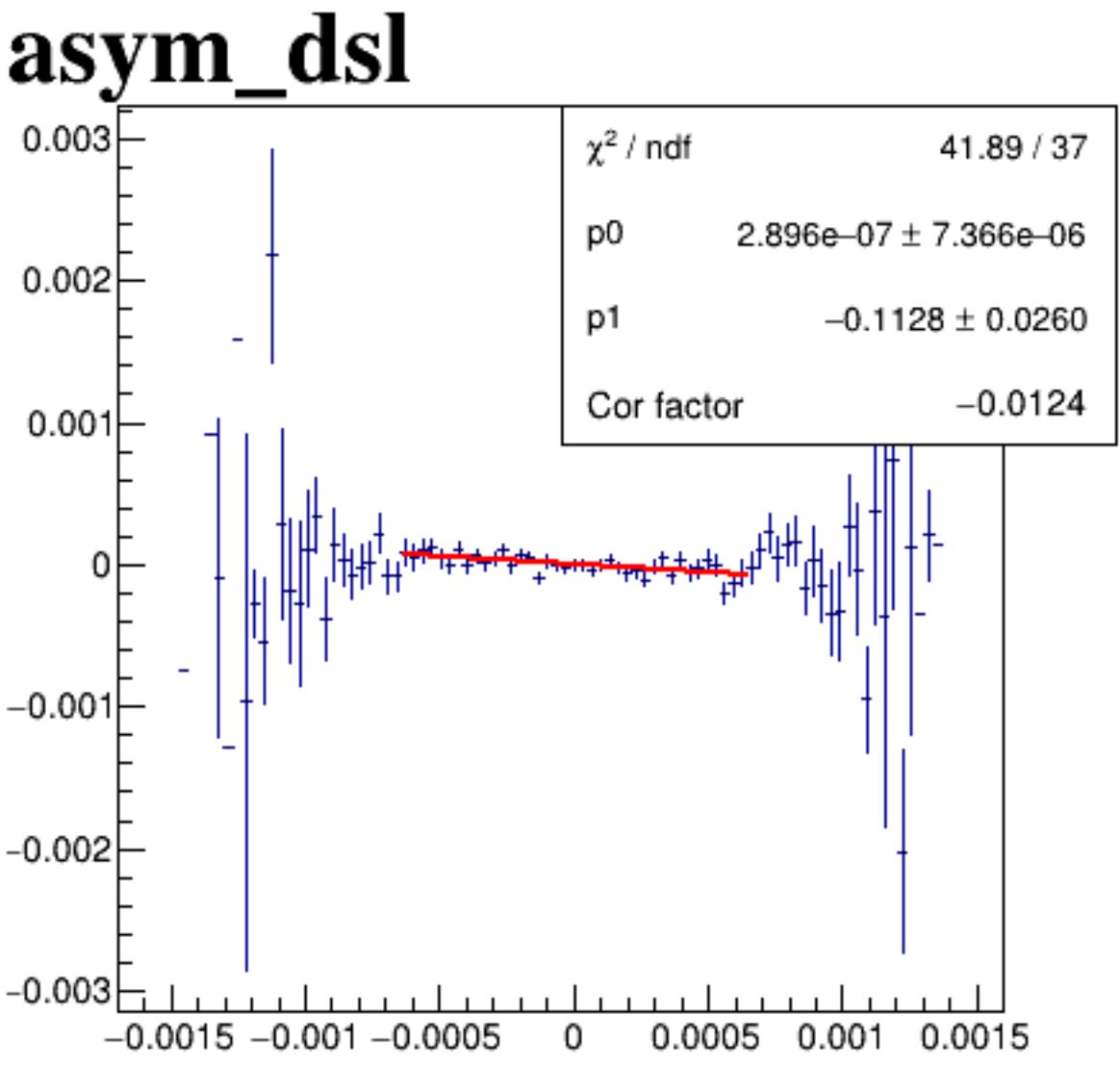
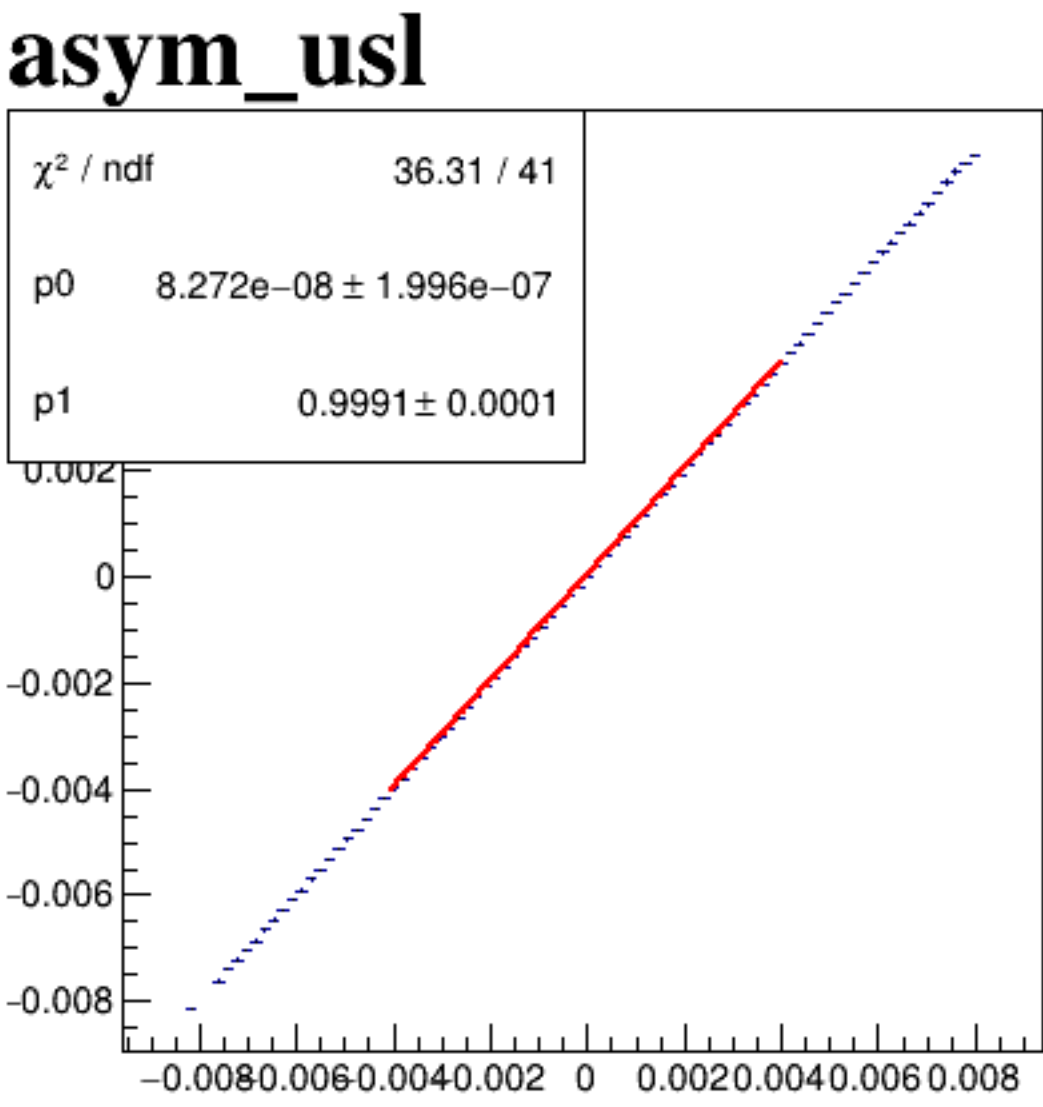
asym_usr



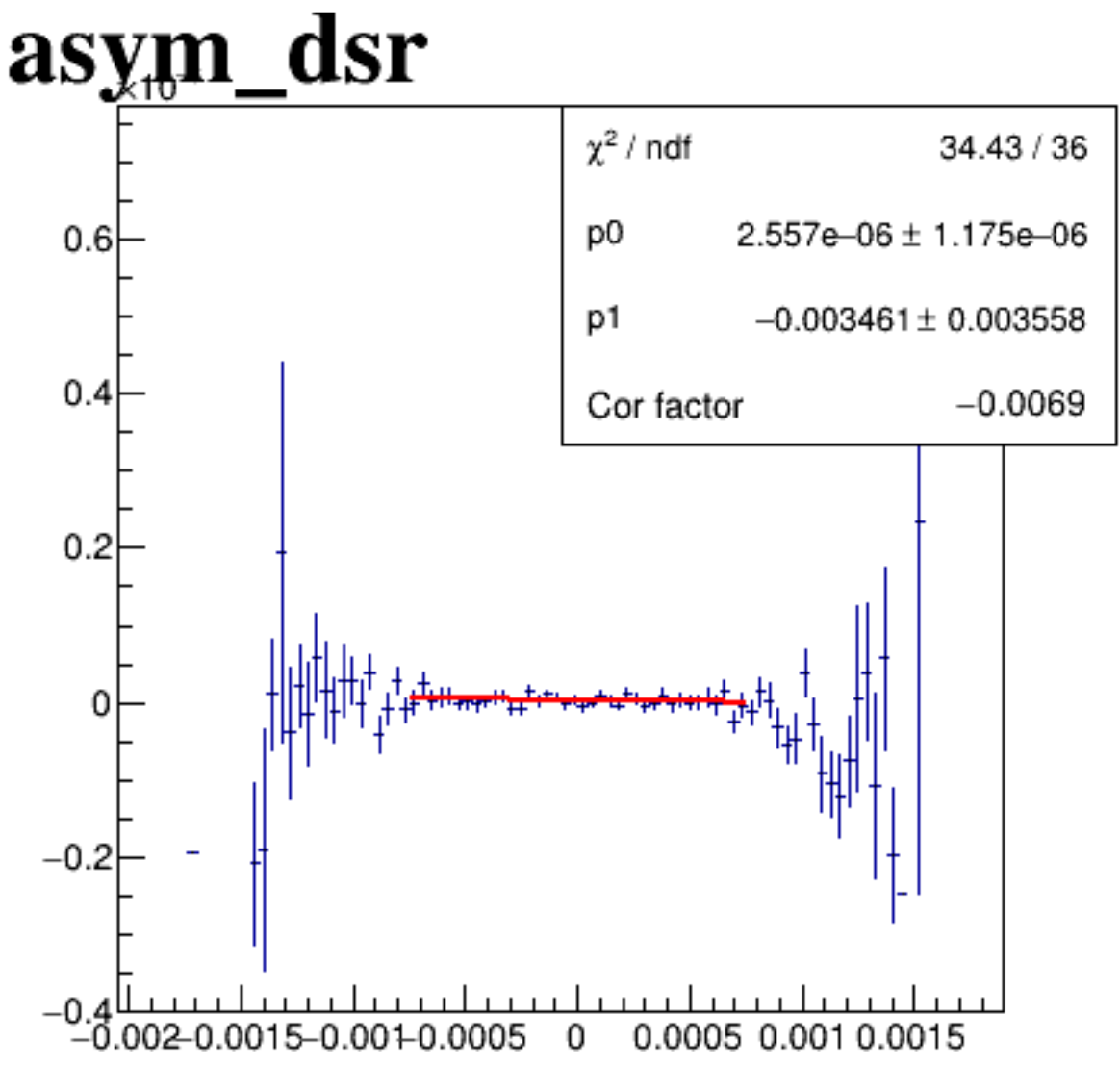
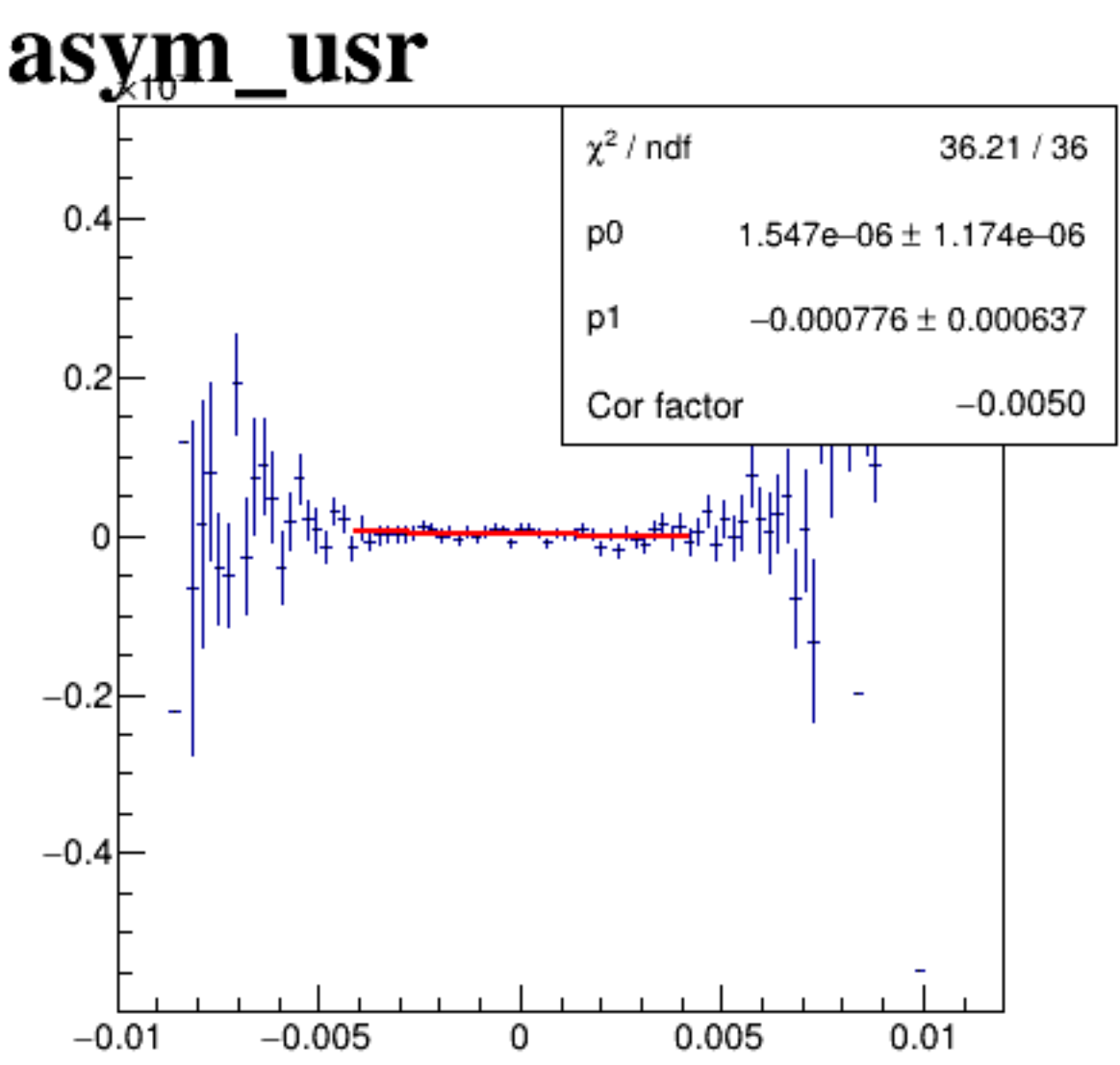
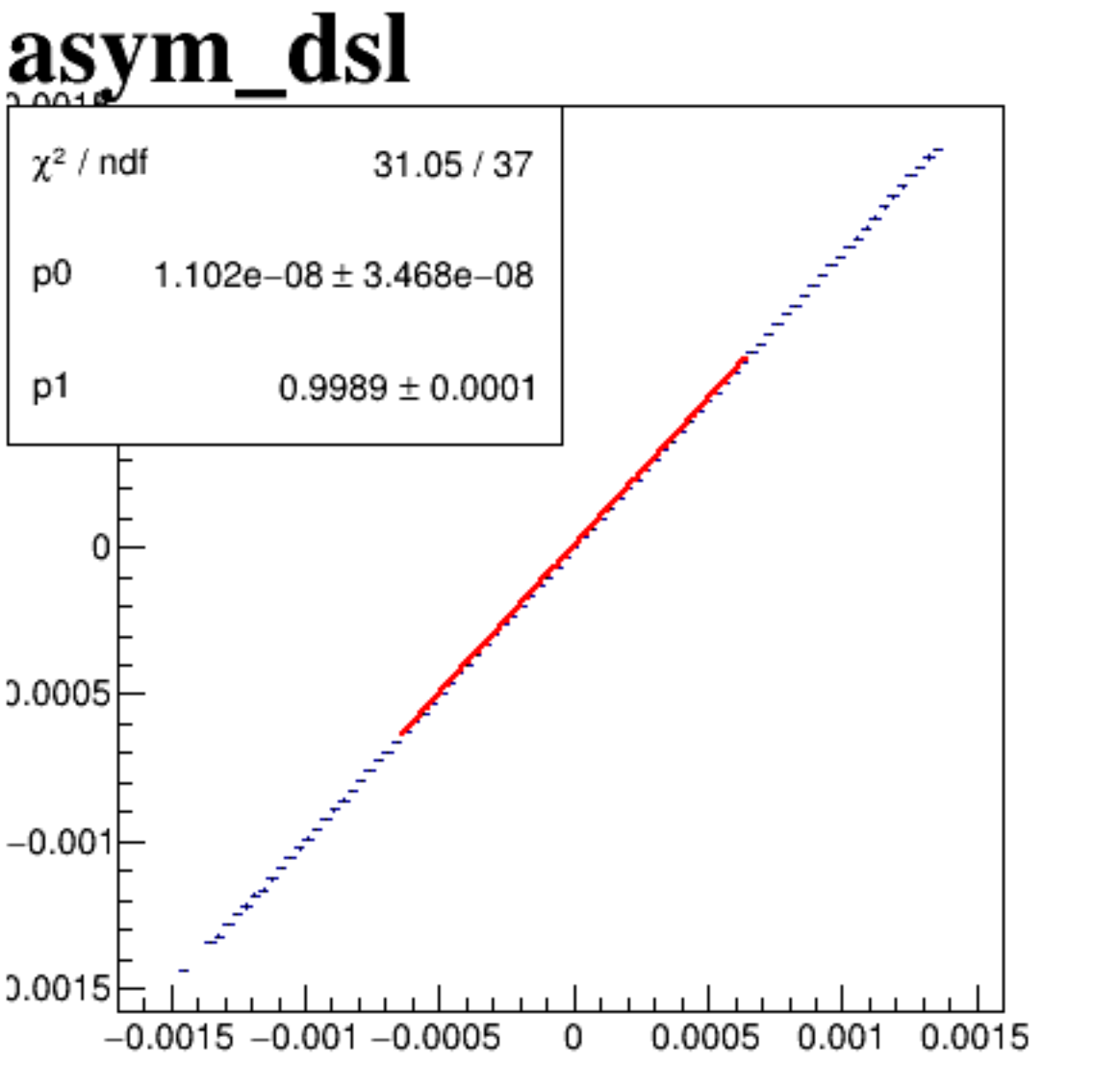
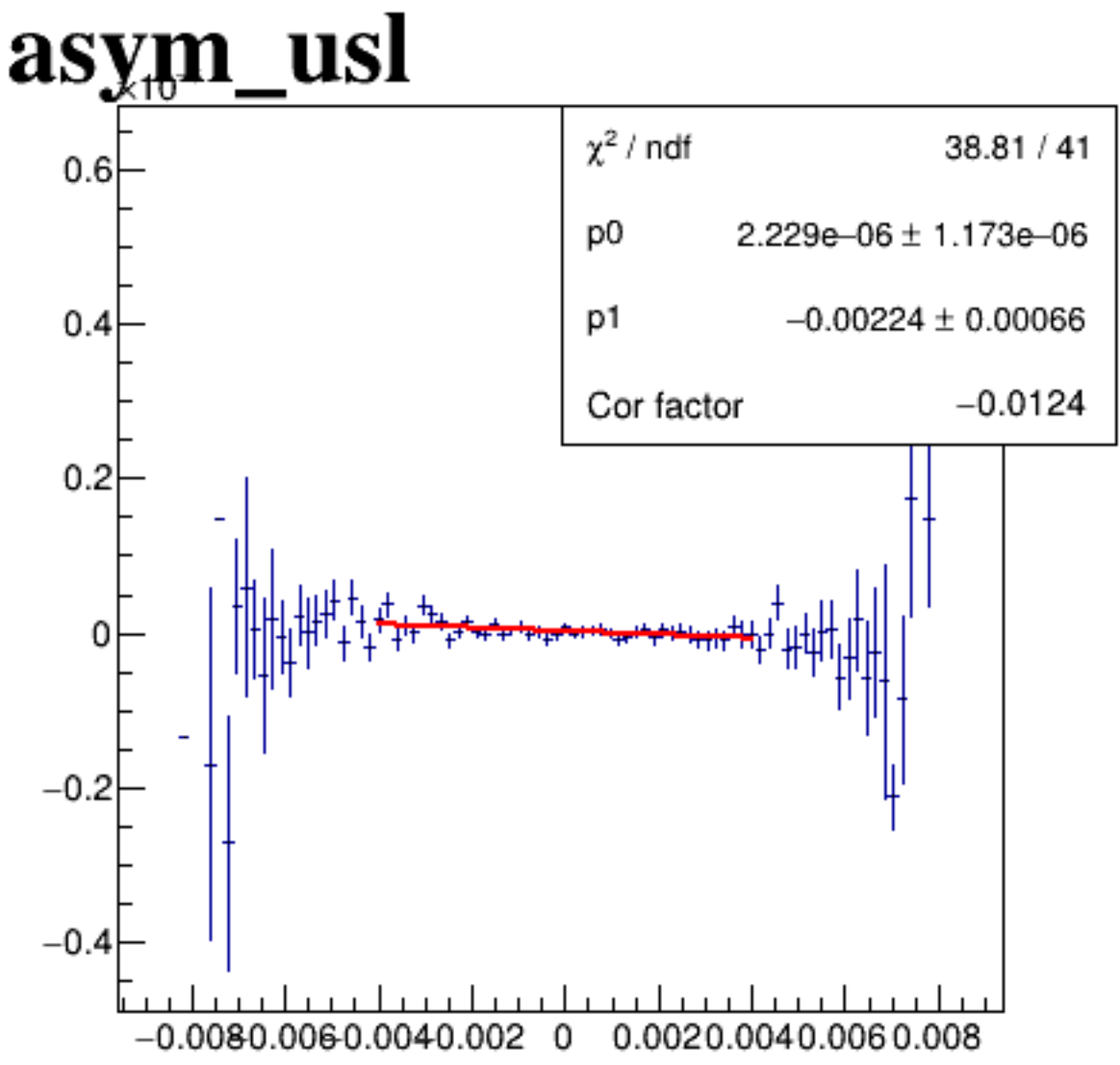
asym_dsr



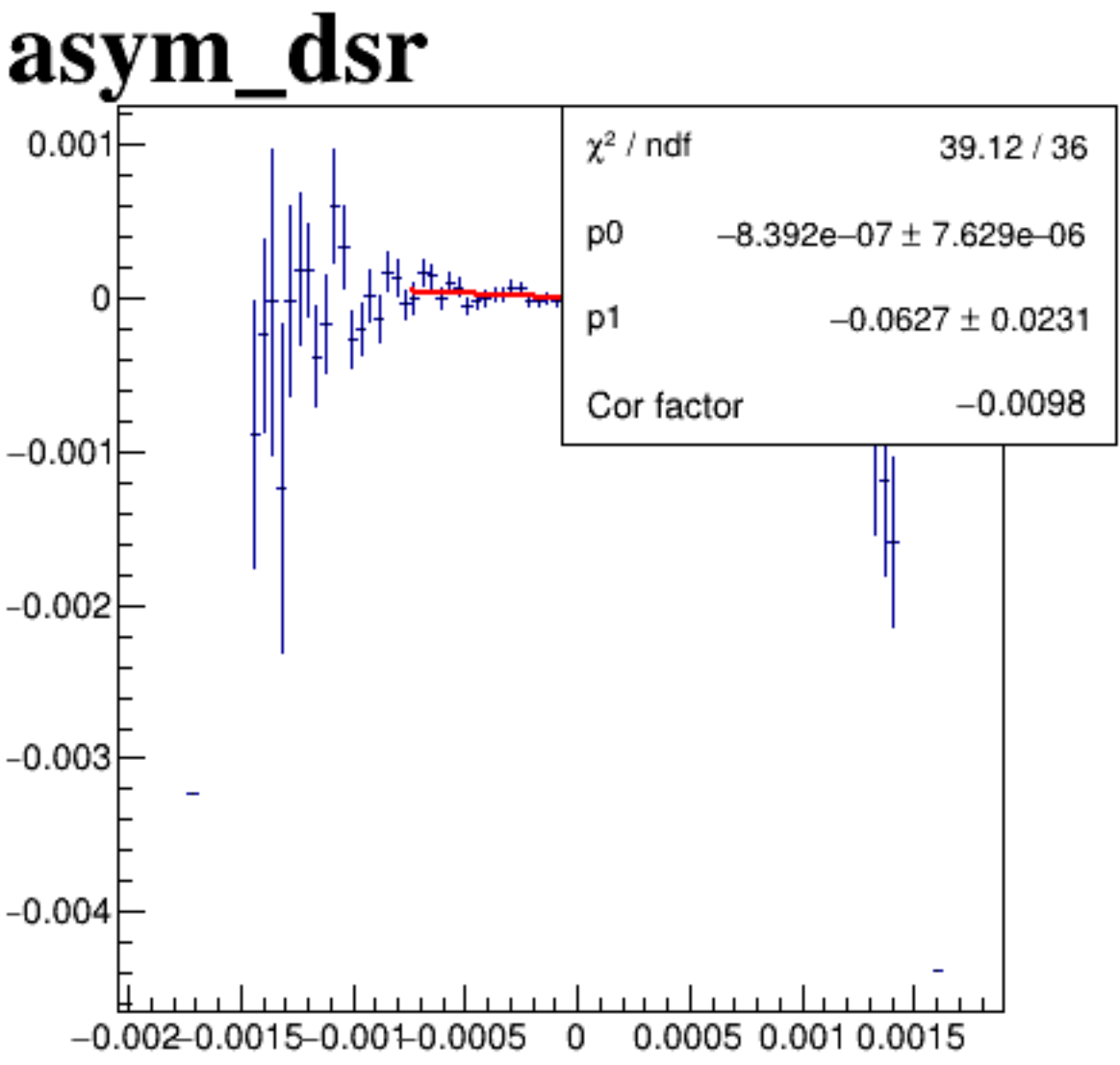
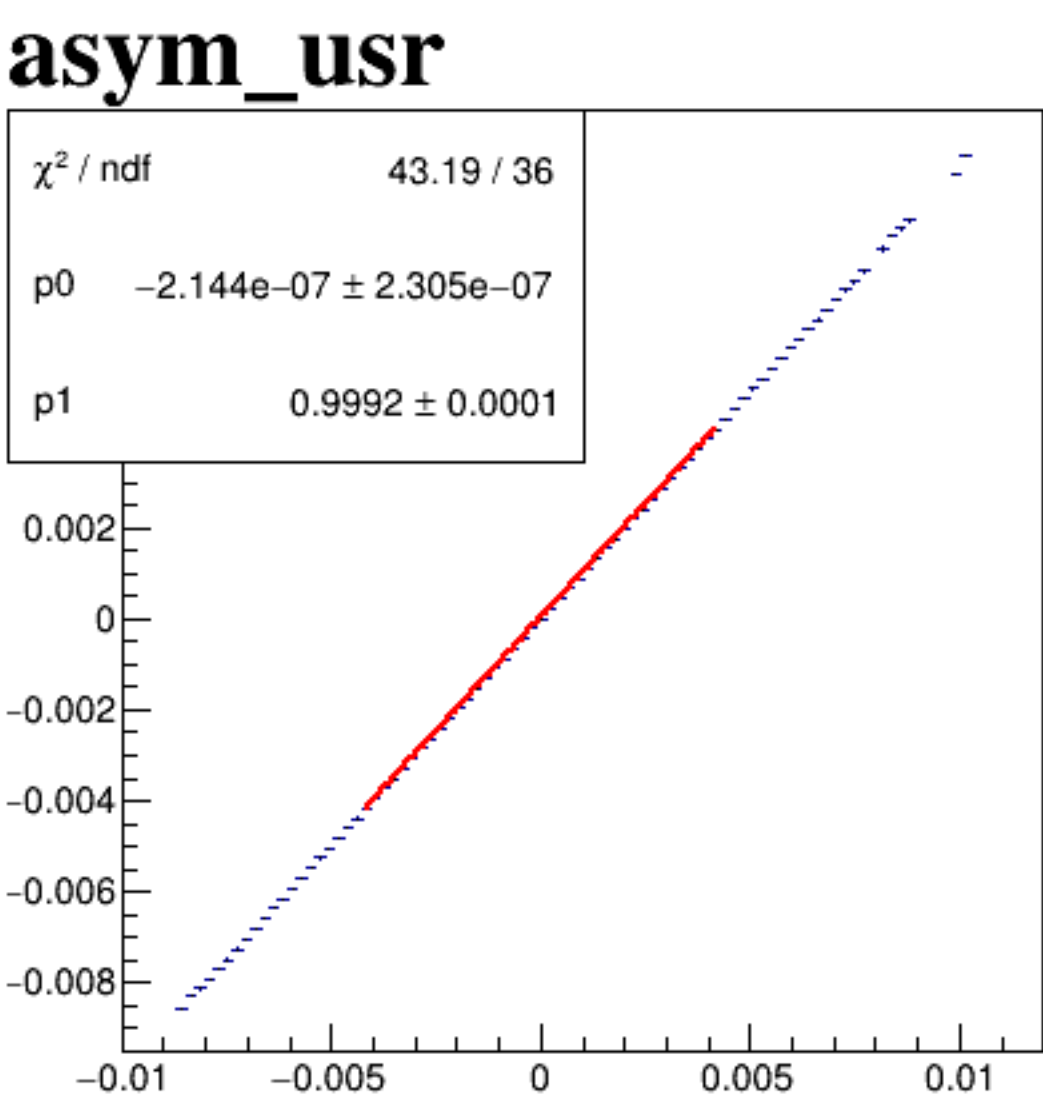
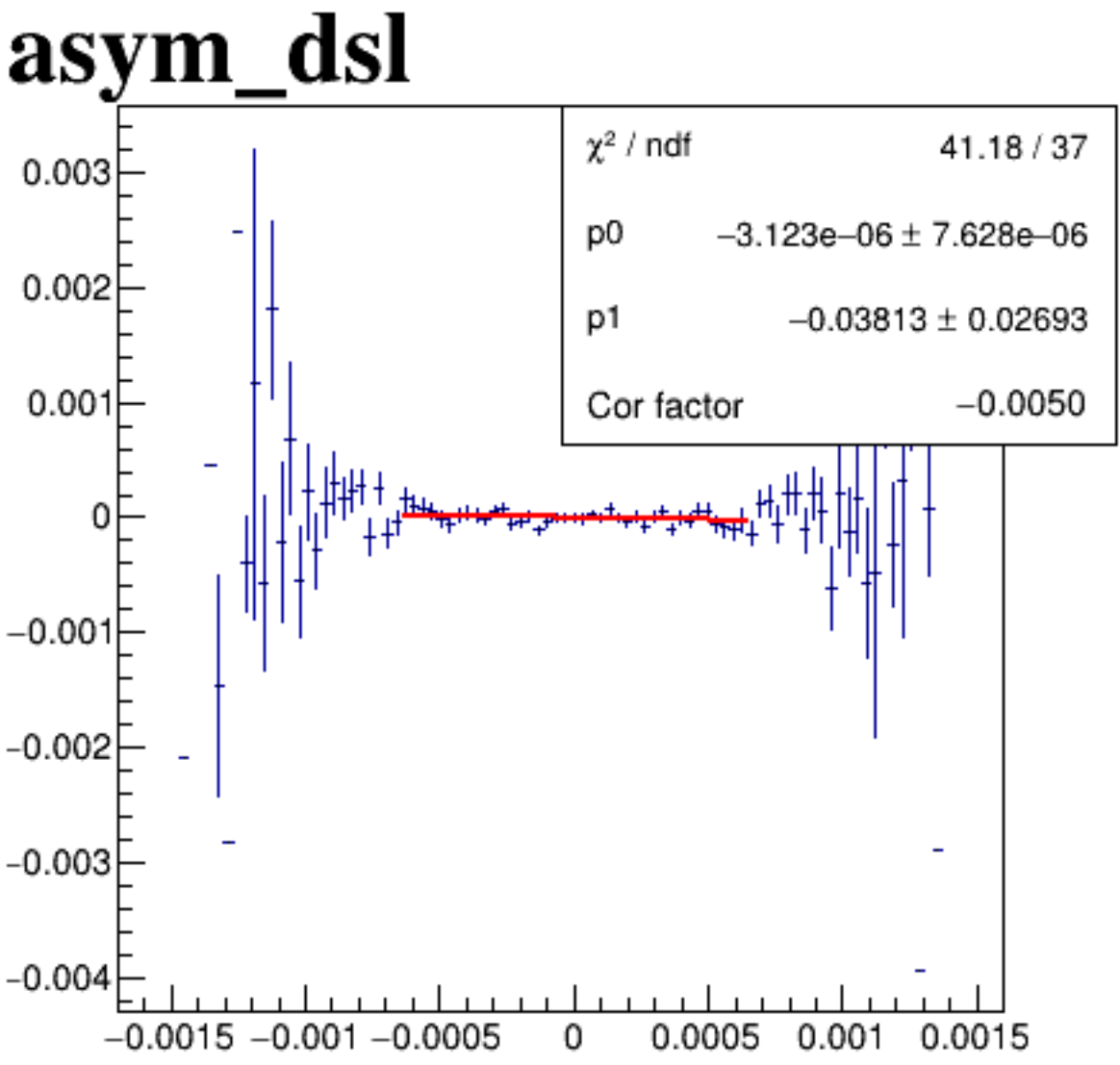
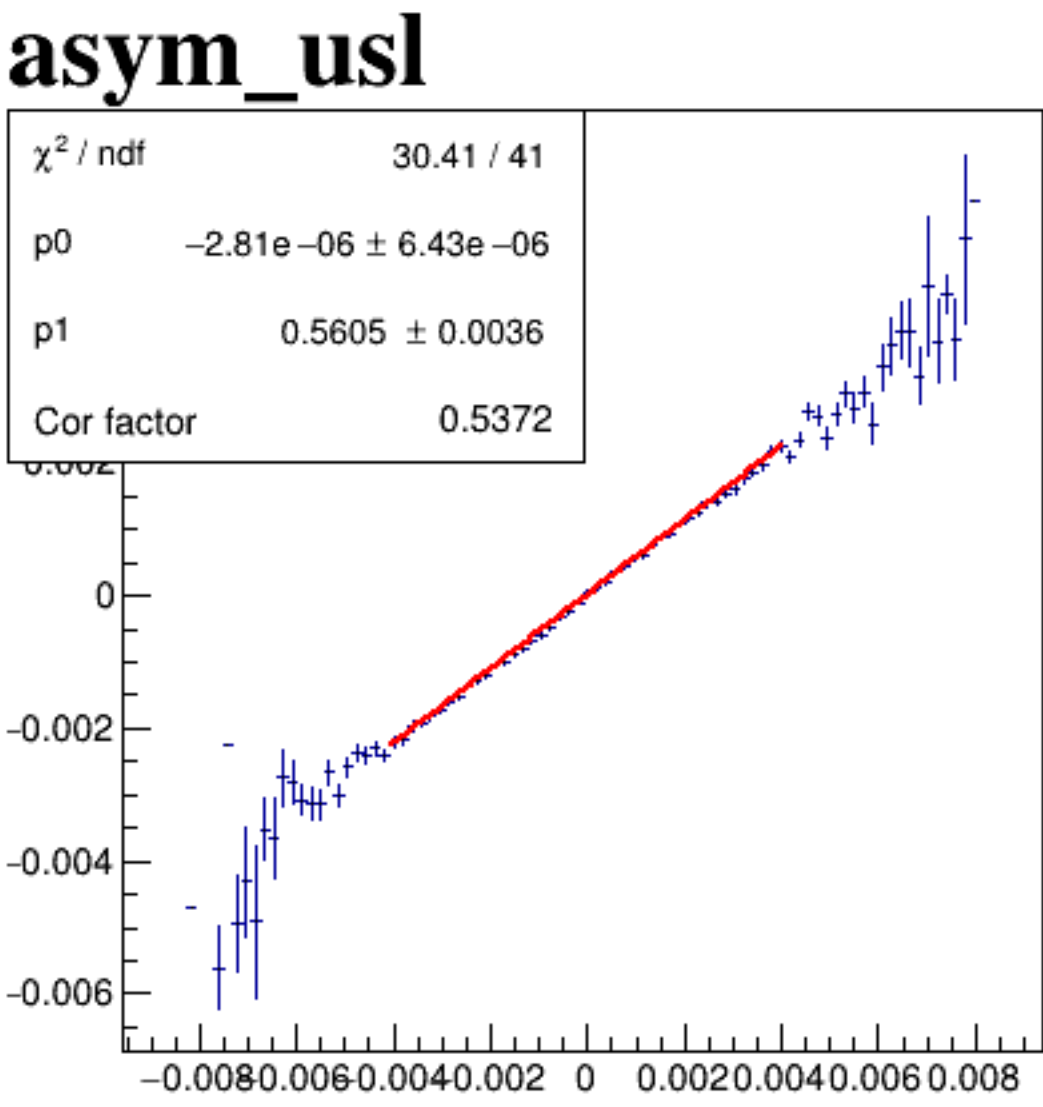
asym_usl



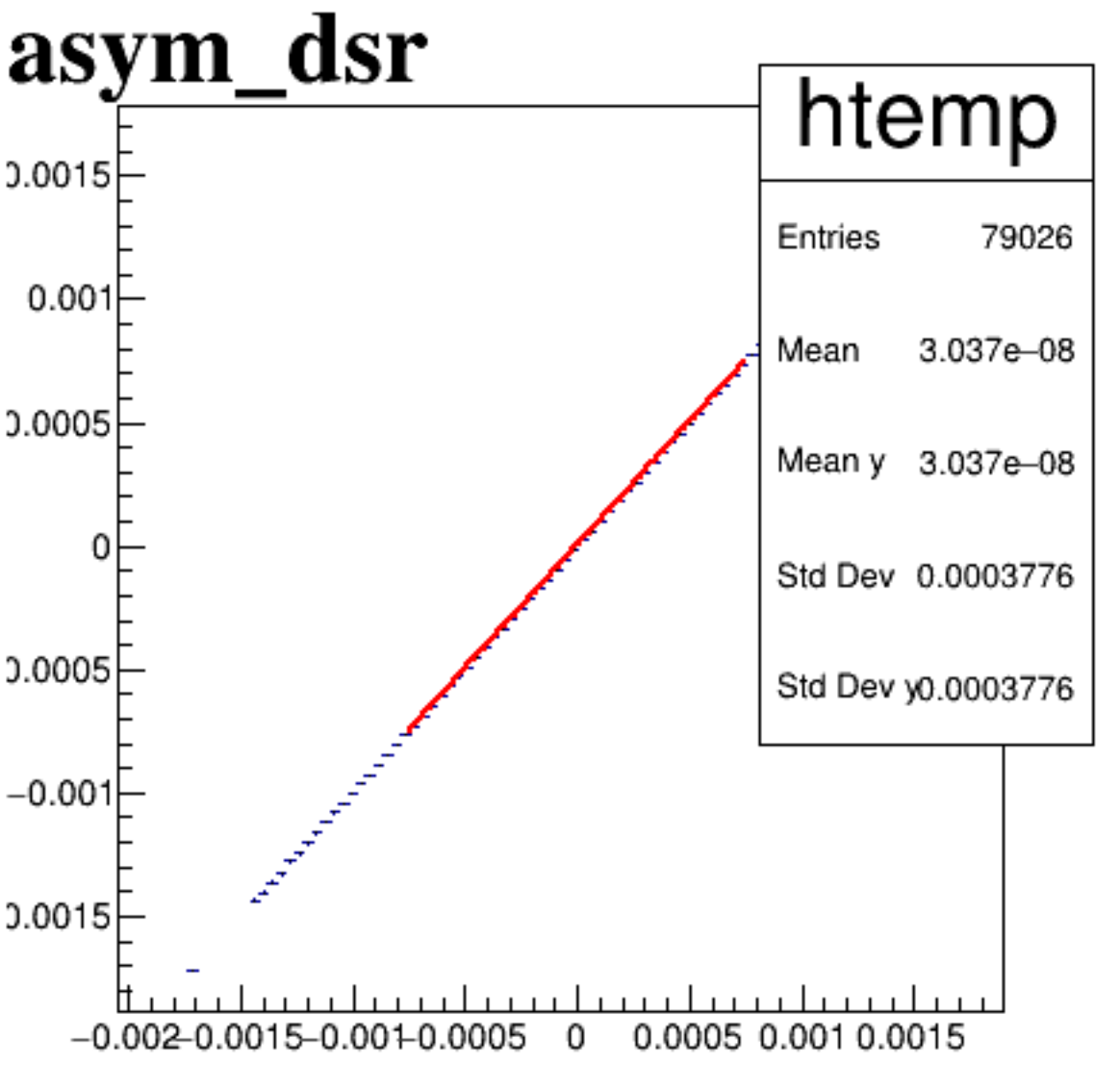
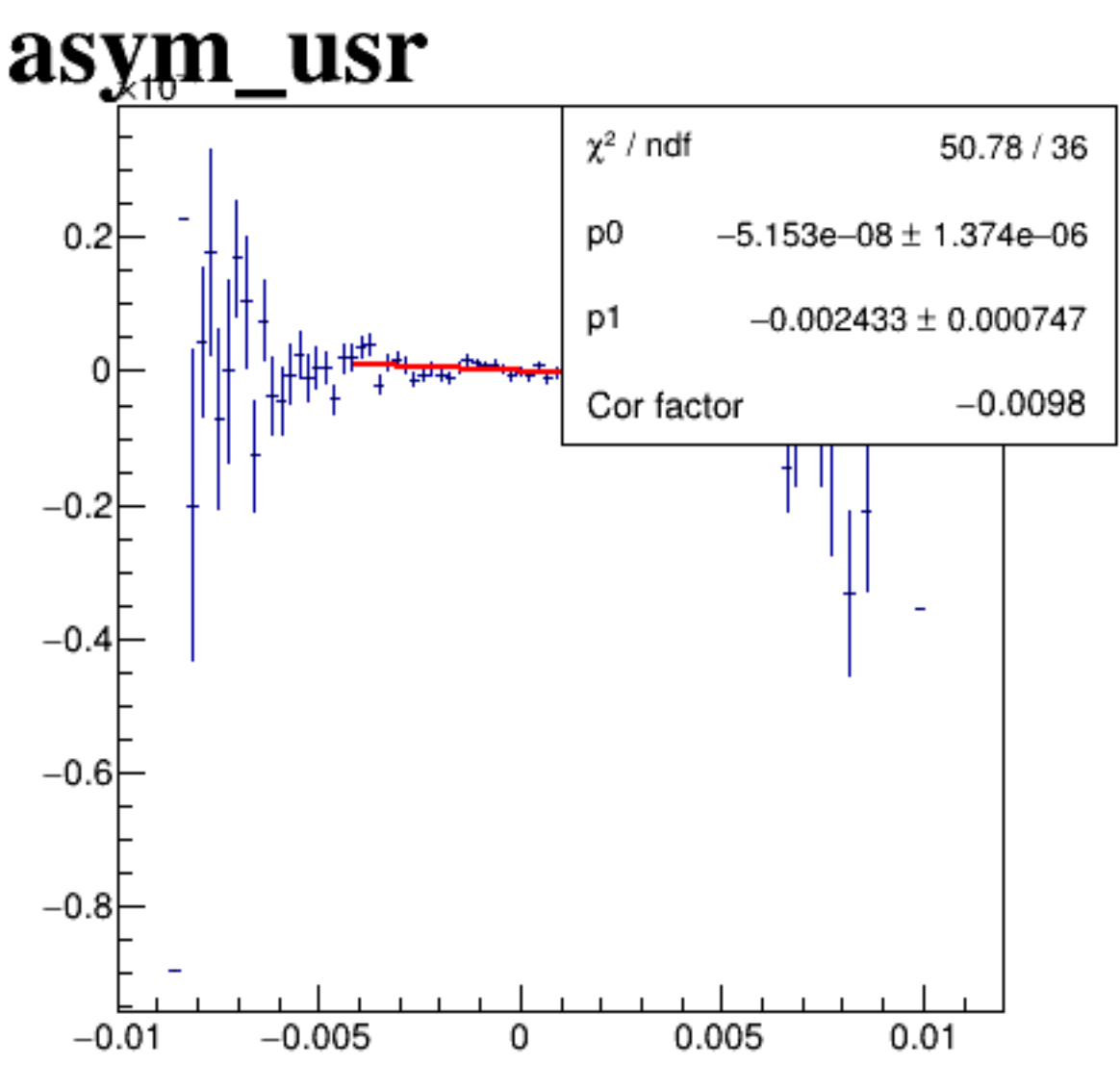
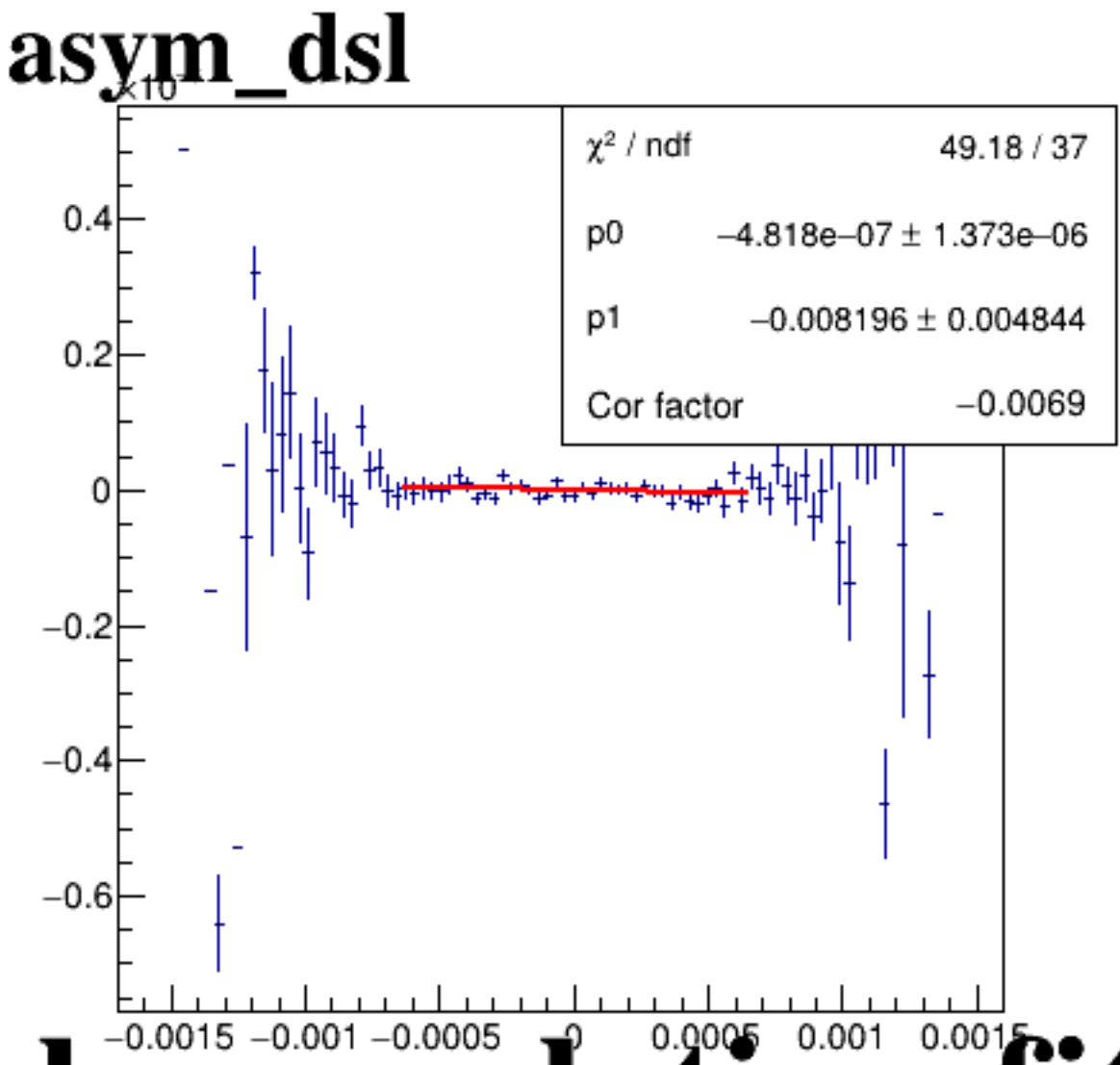
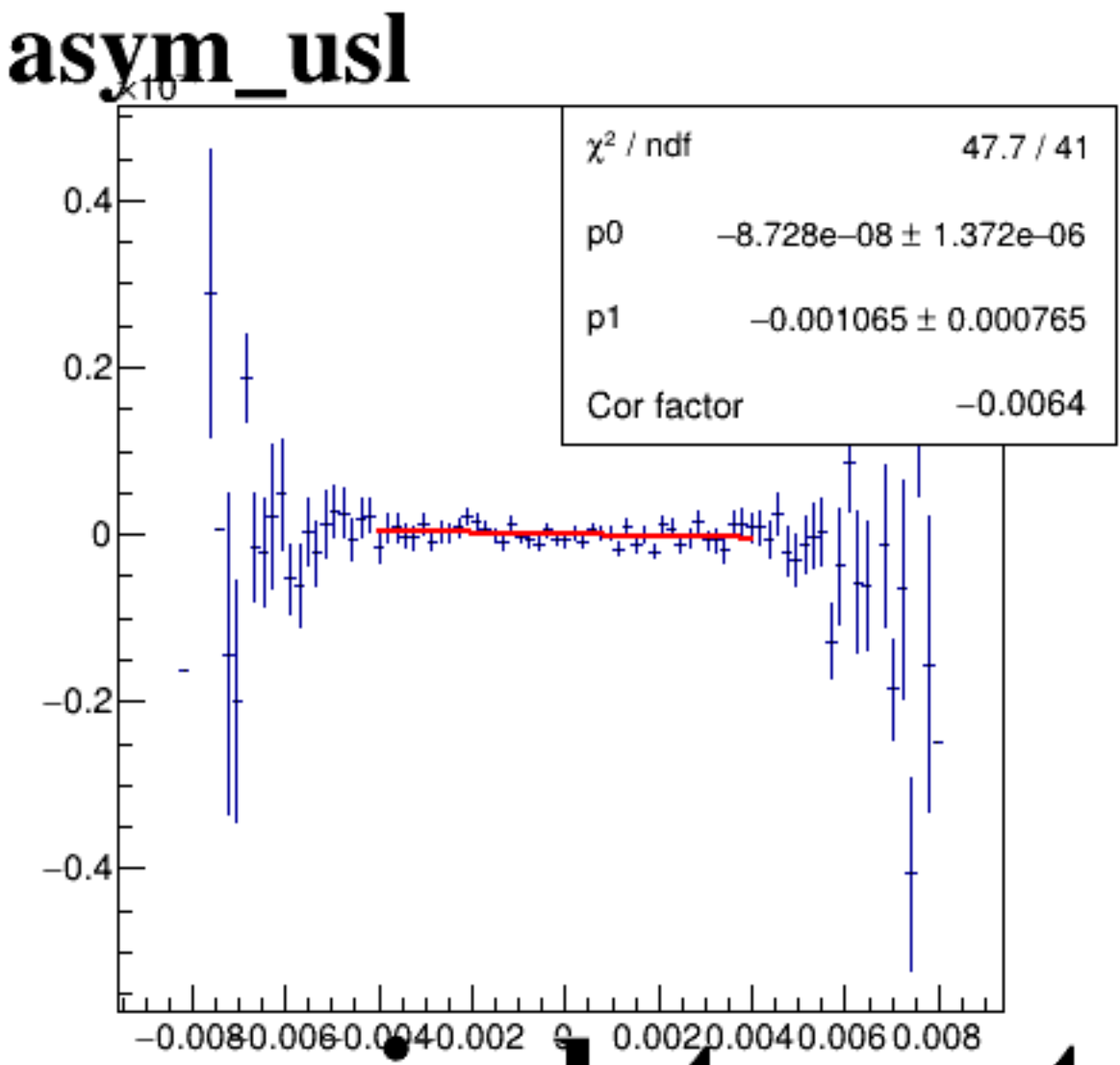
asym_dsl



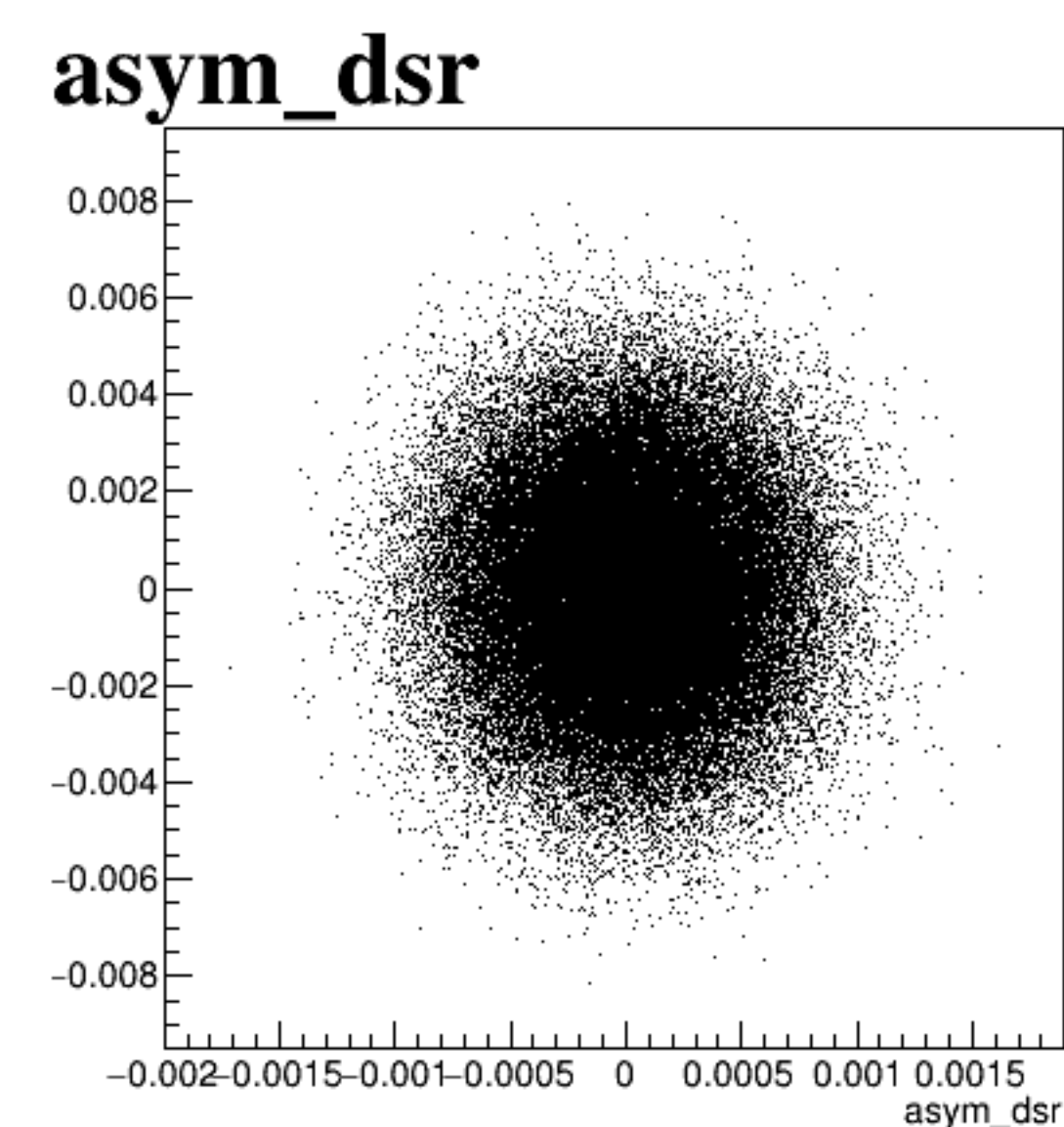
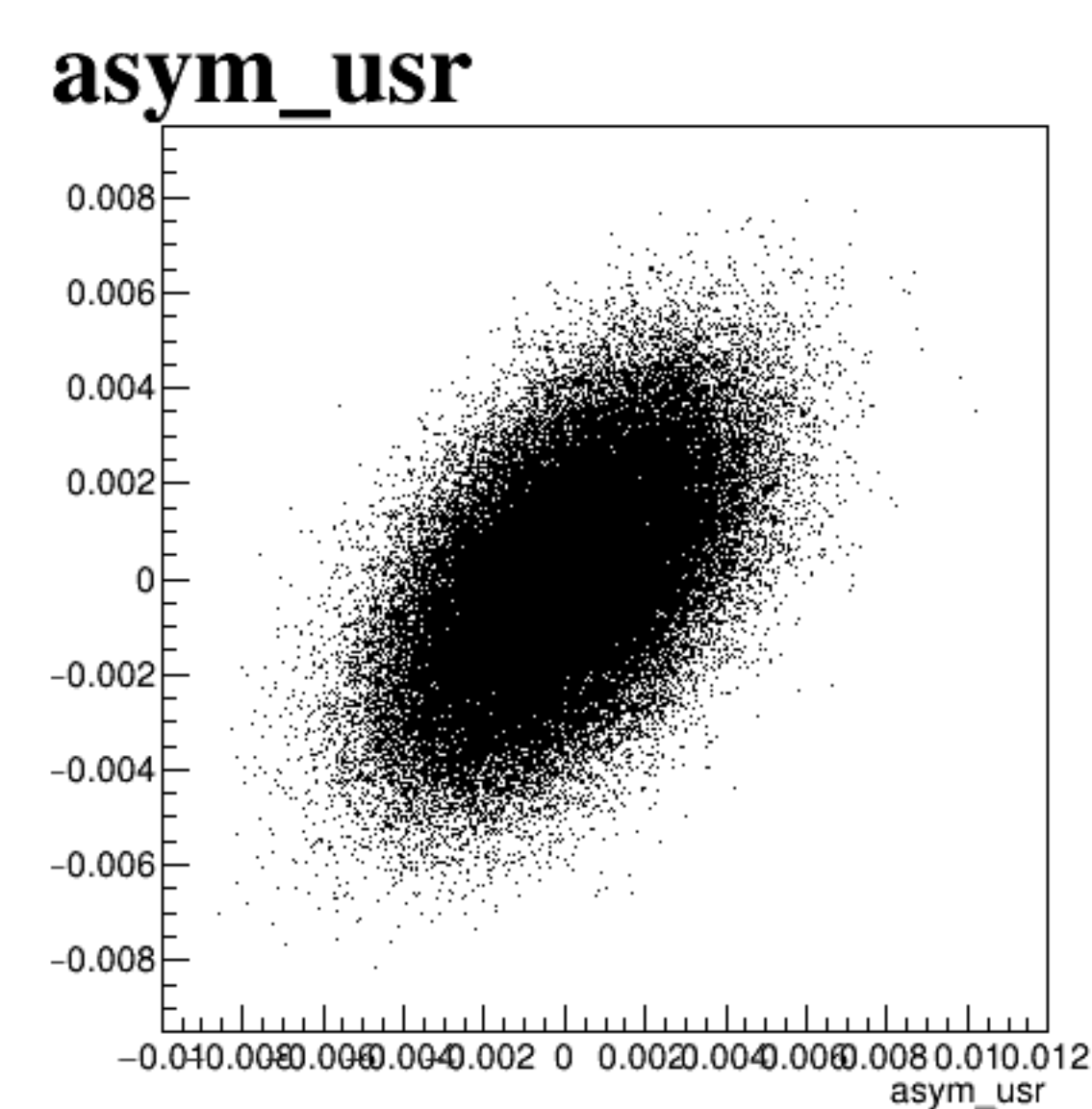
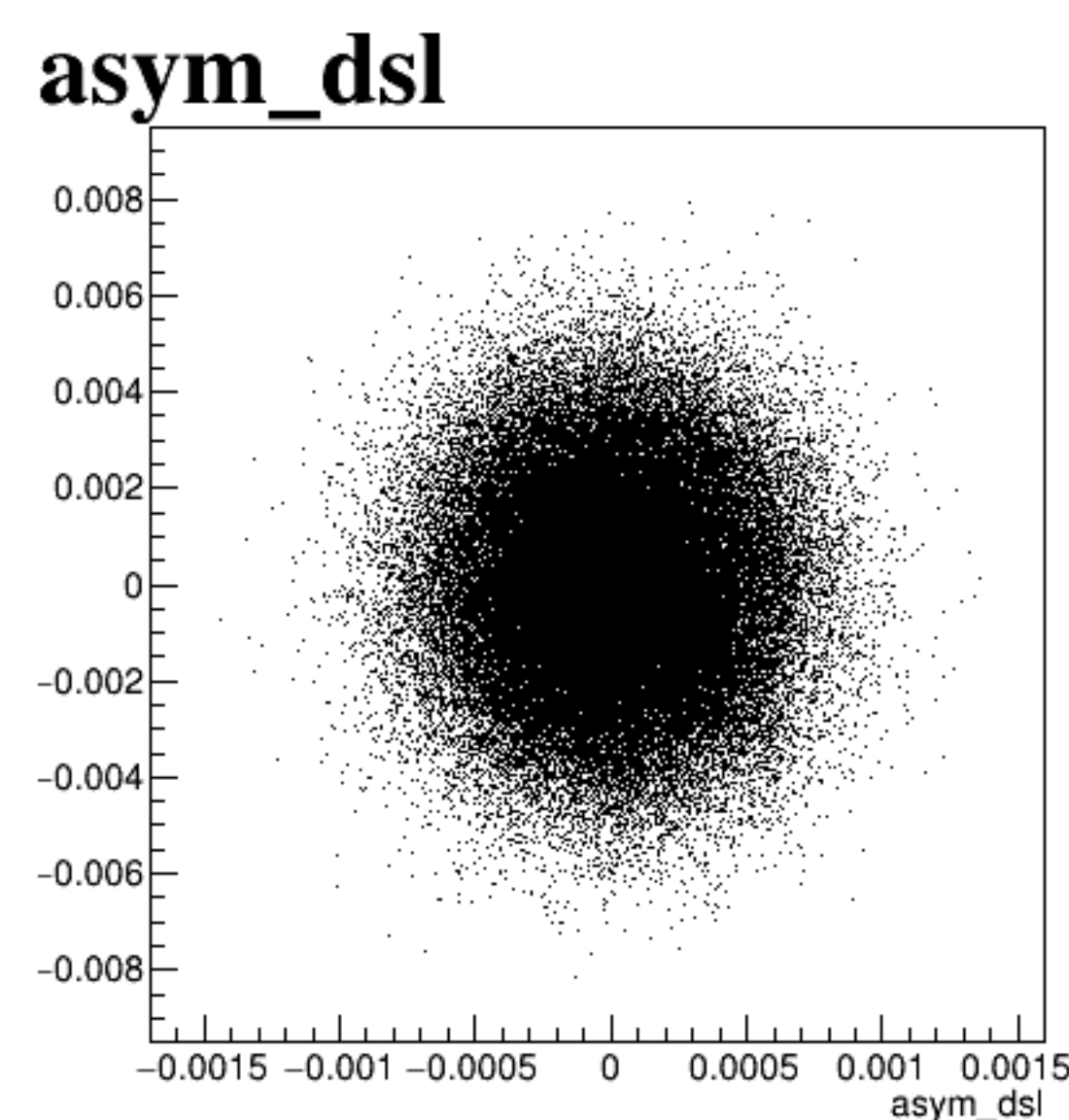
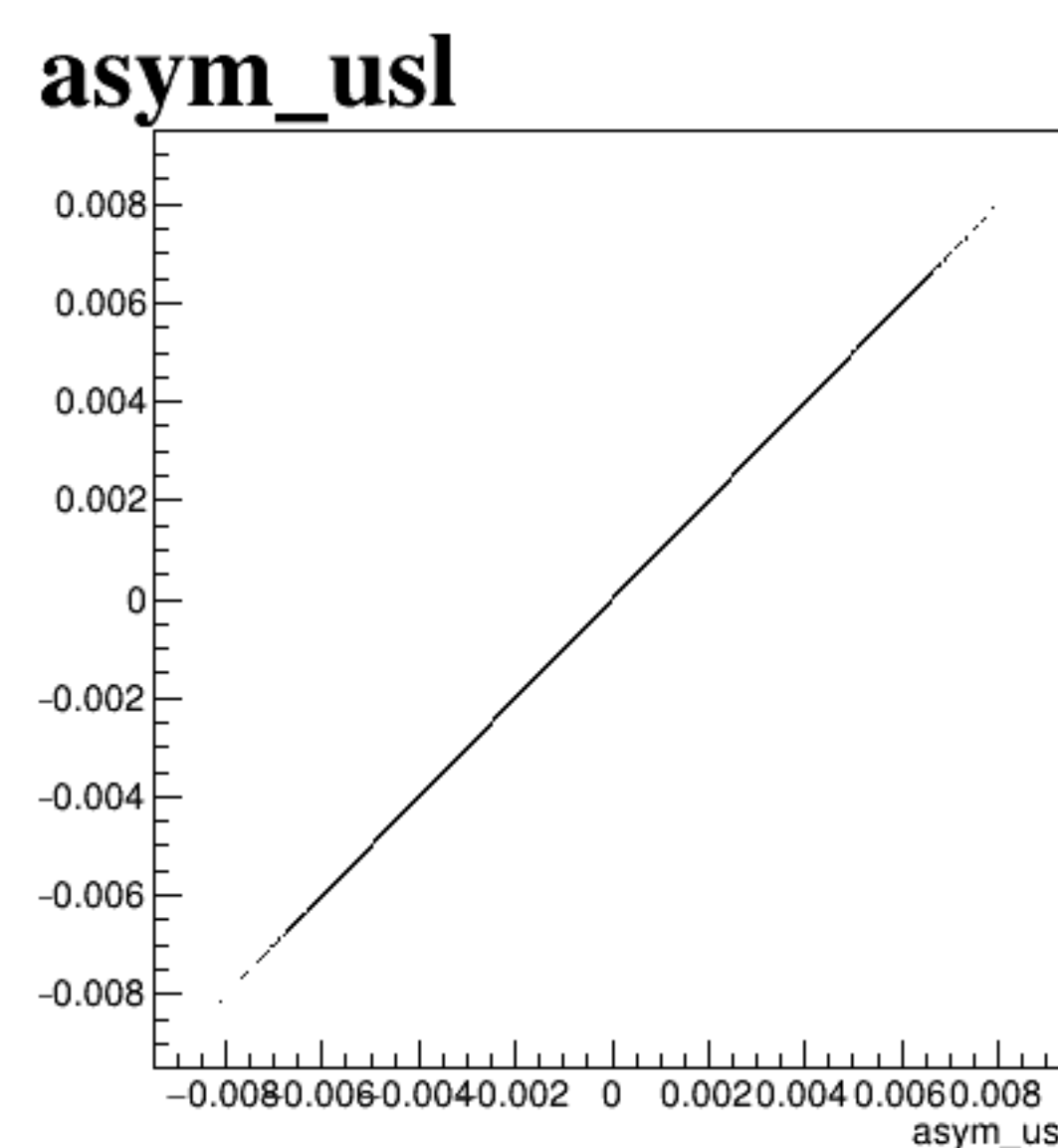
asym_usr



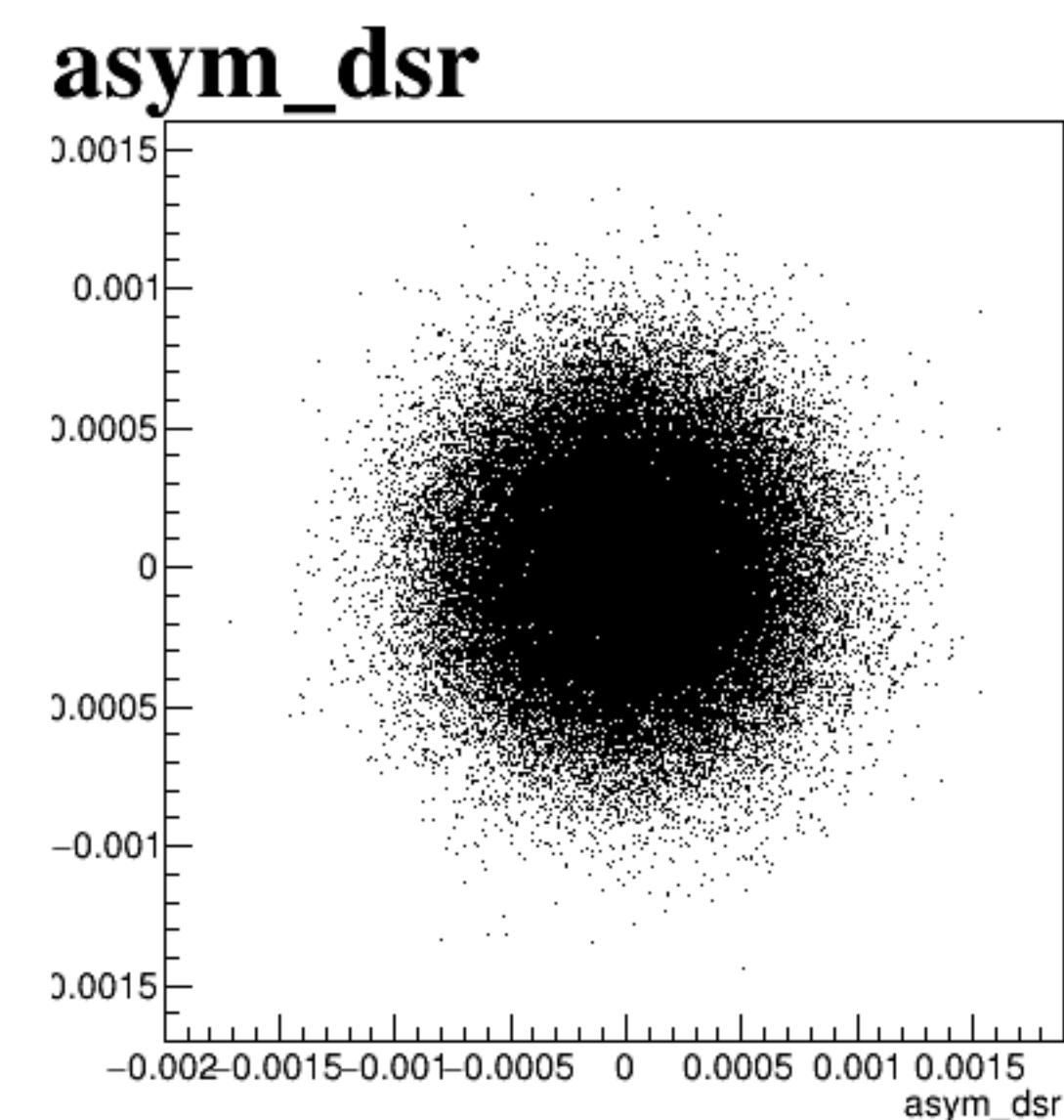
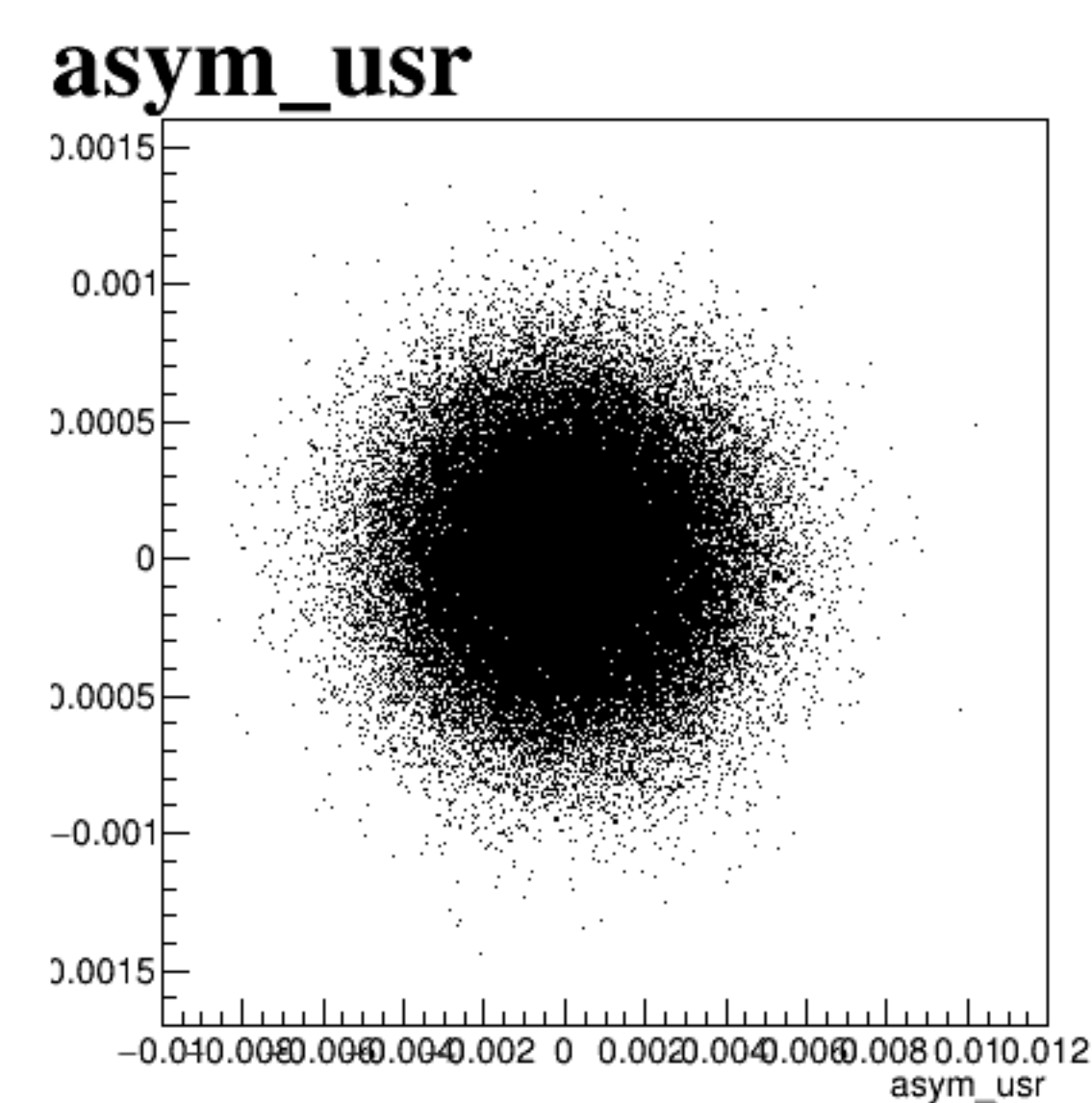
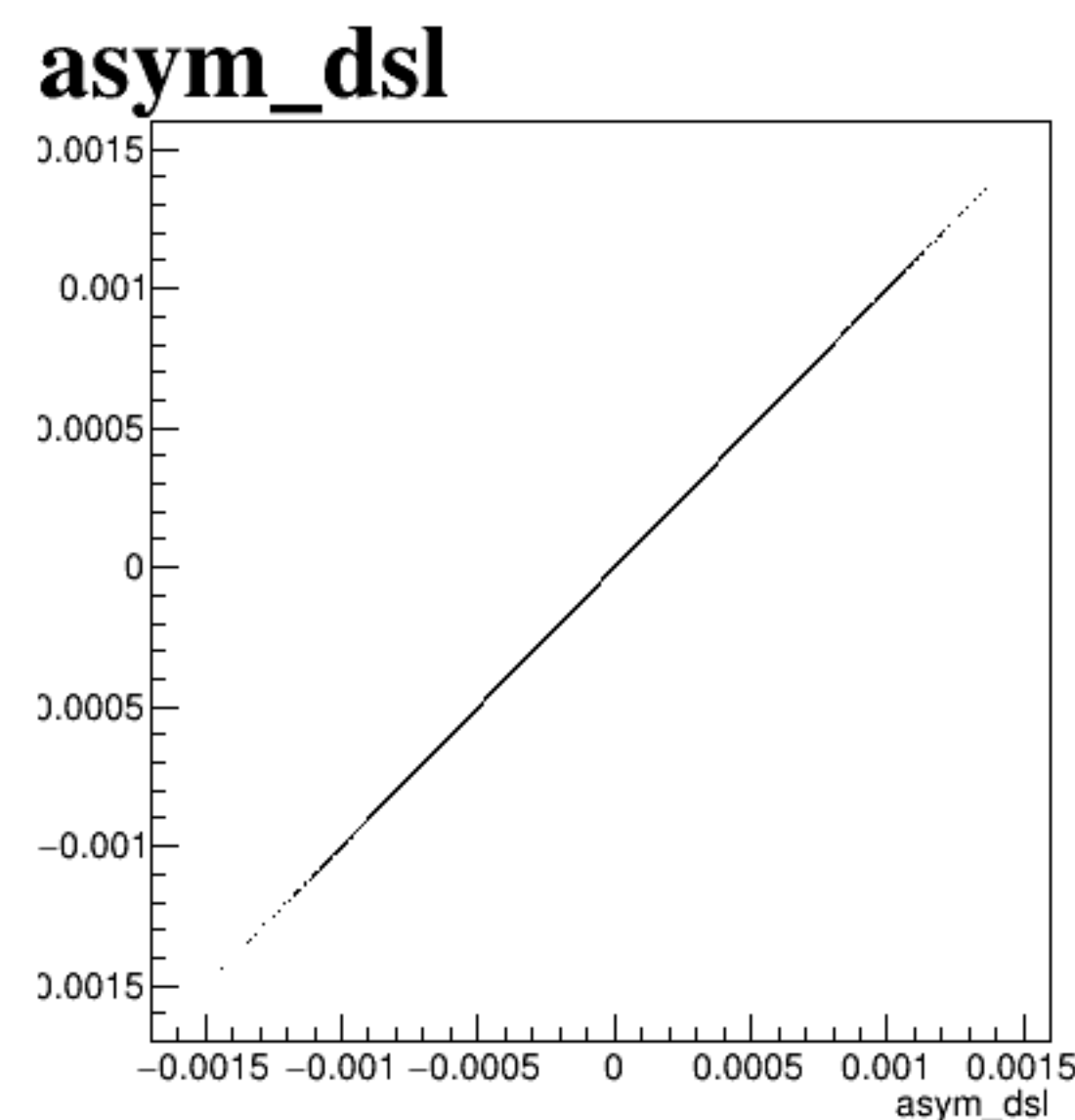
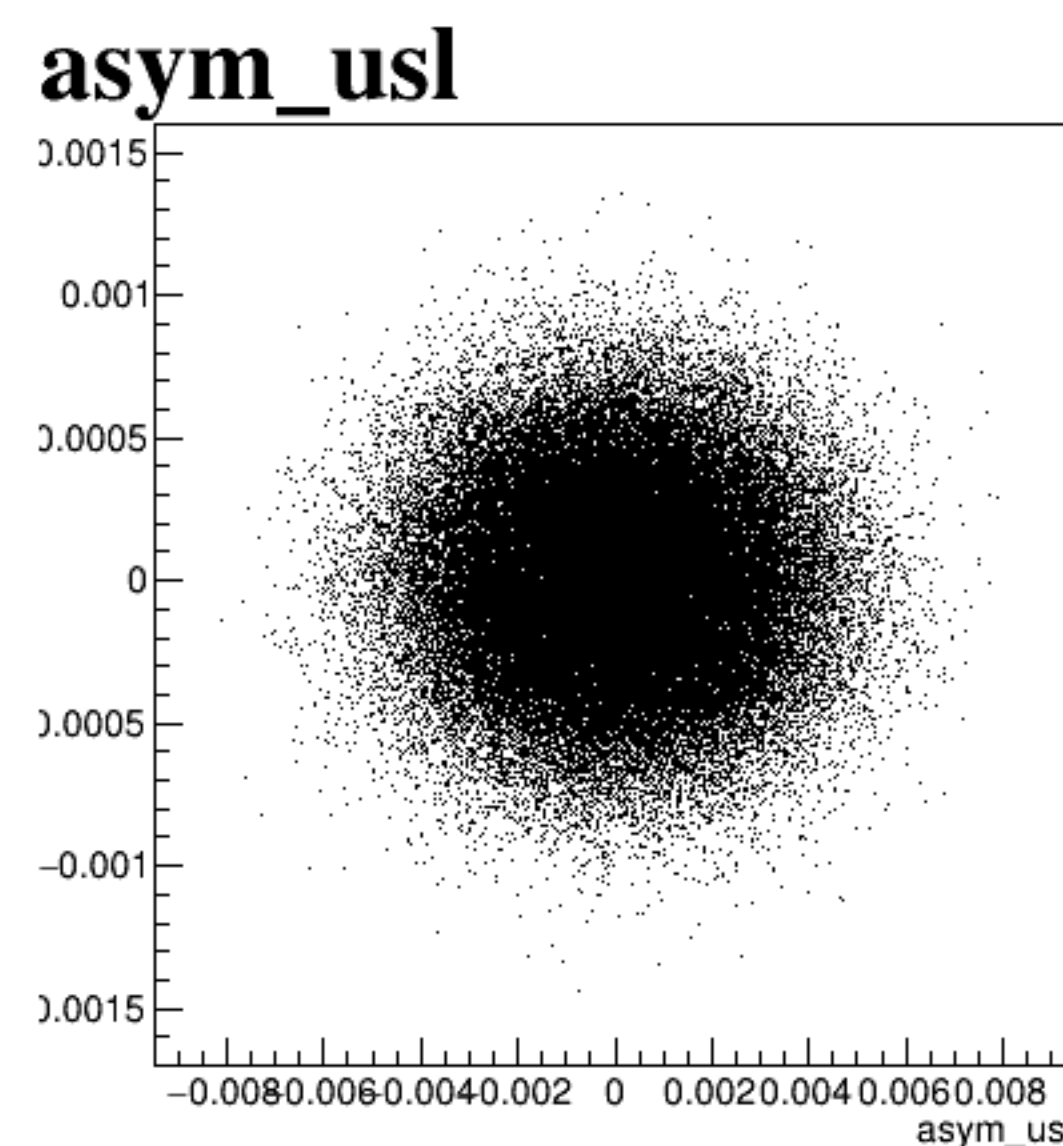
asym_dsr



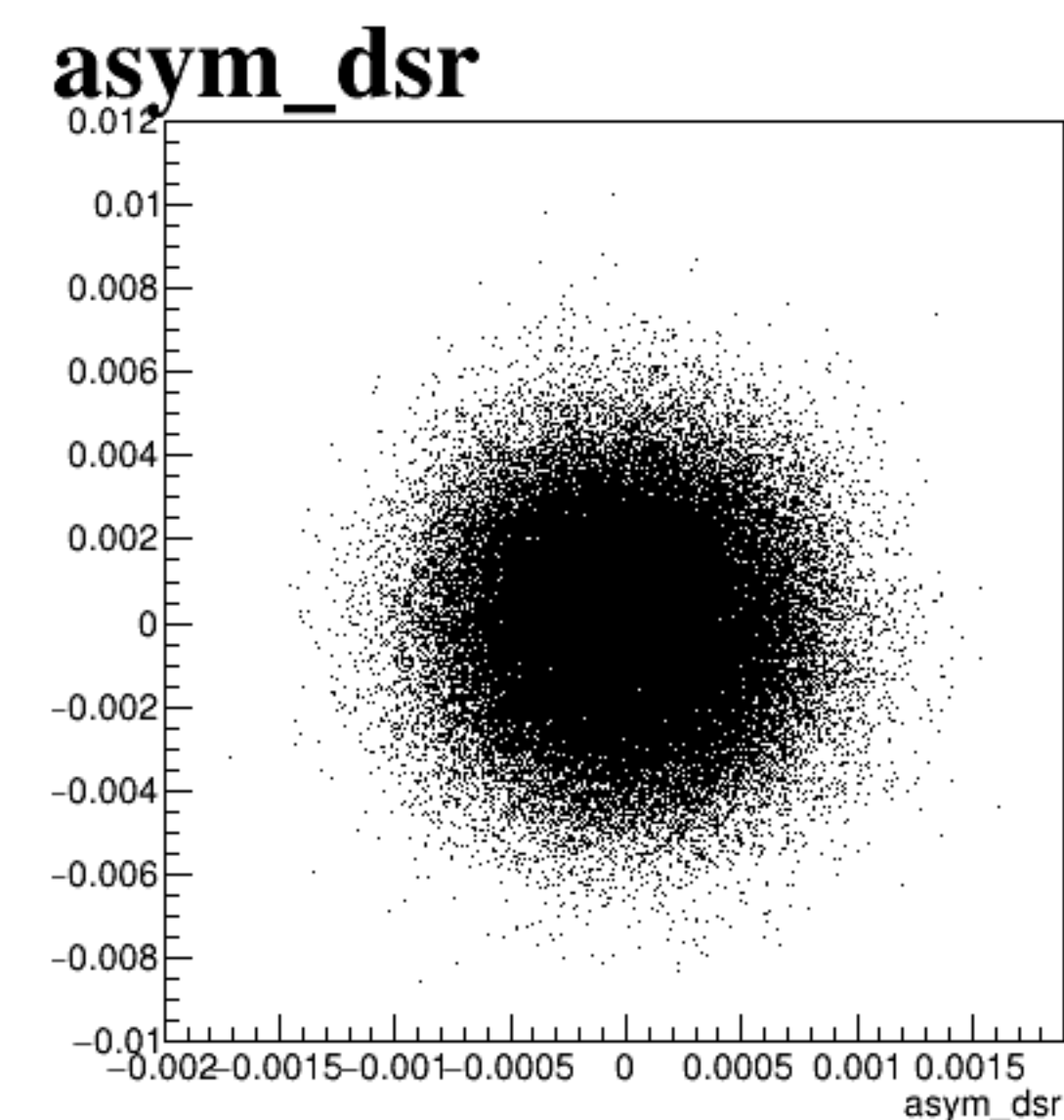
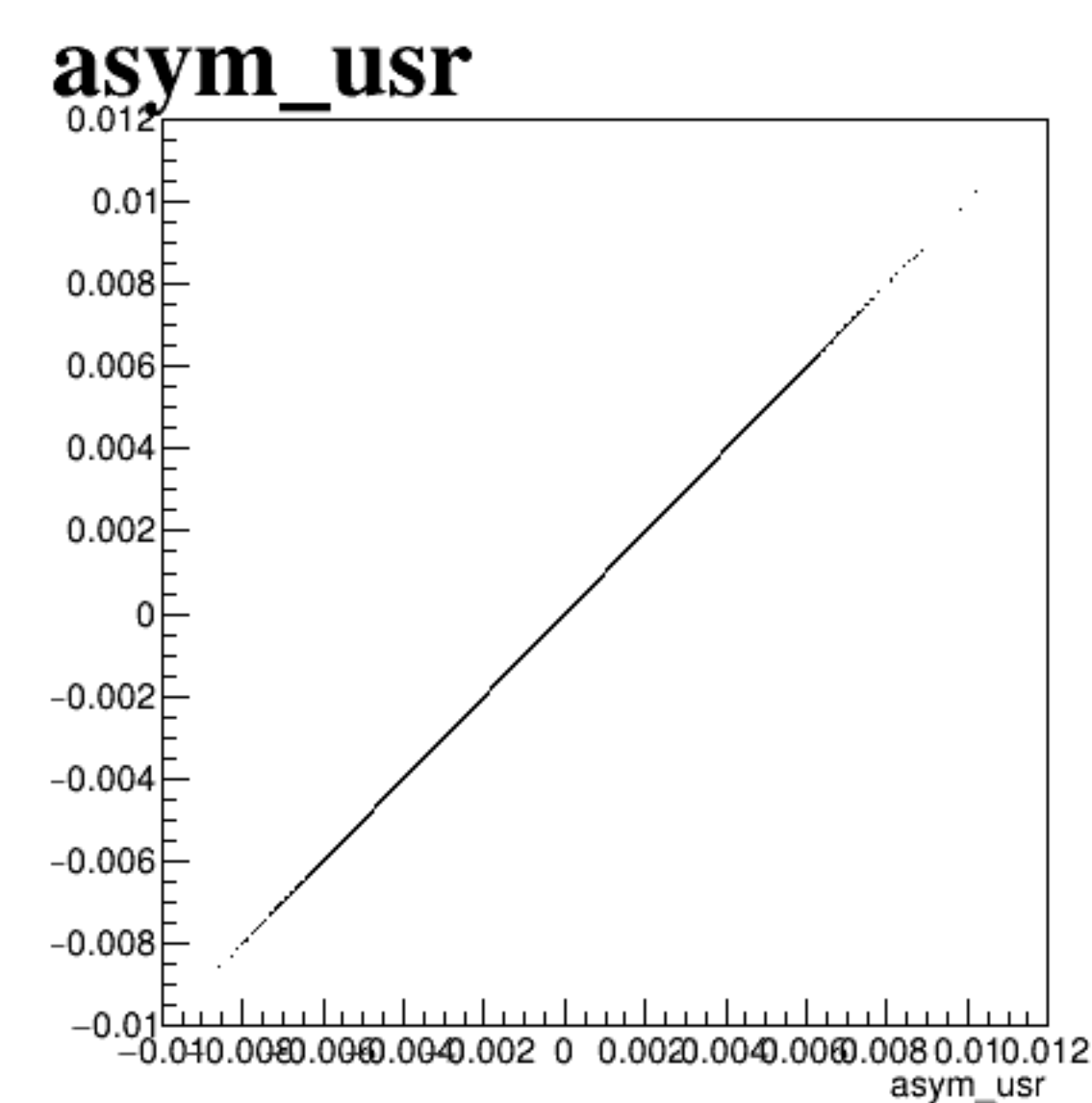
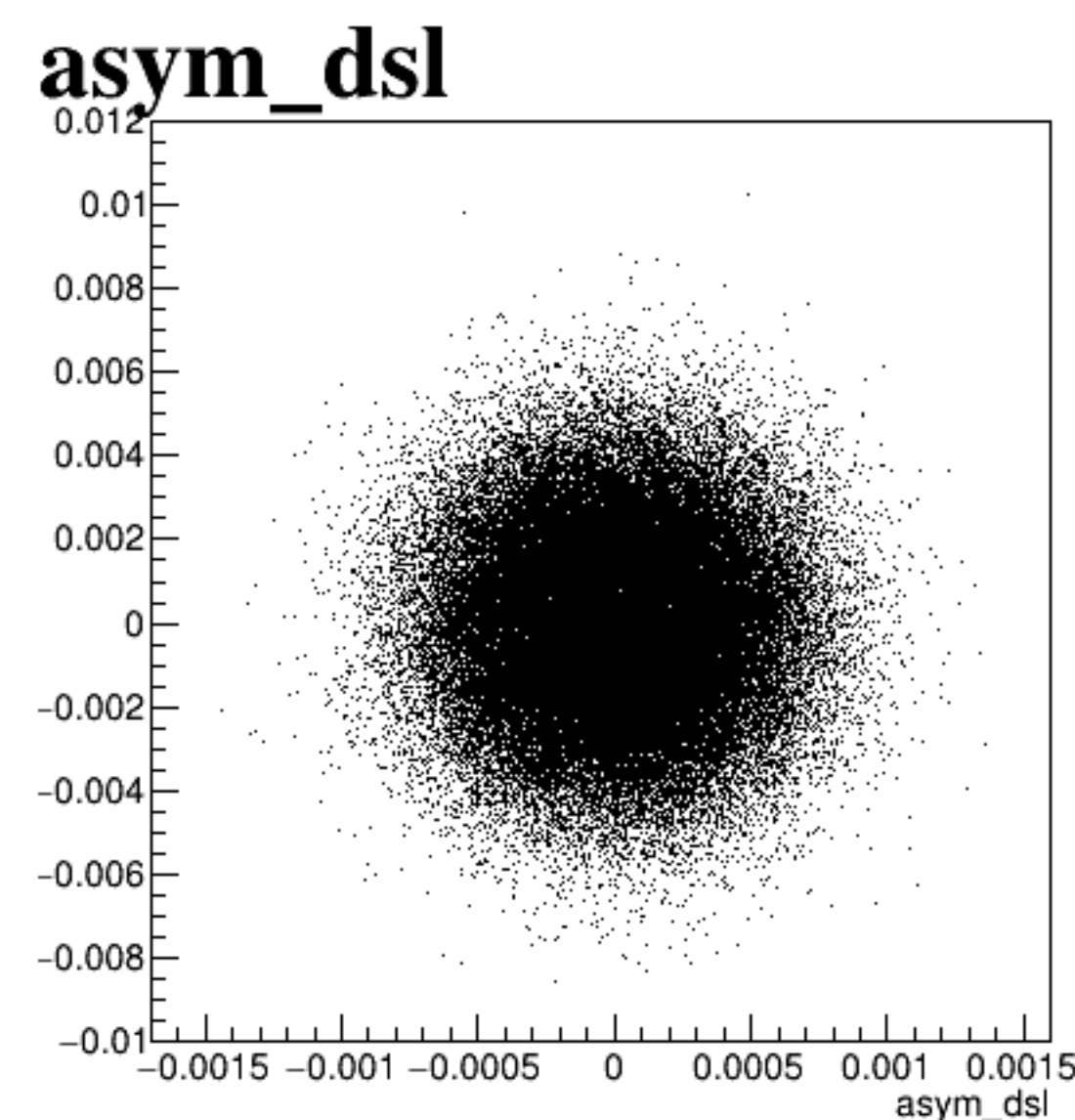
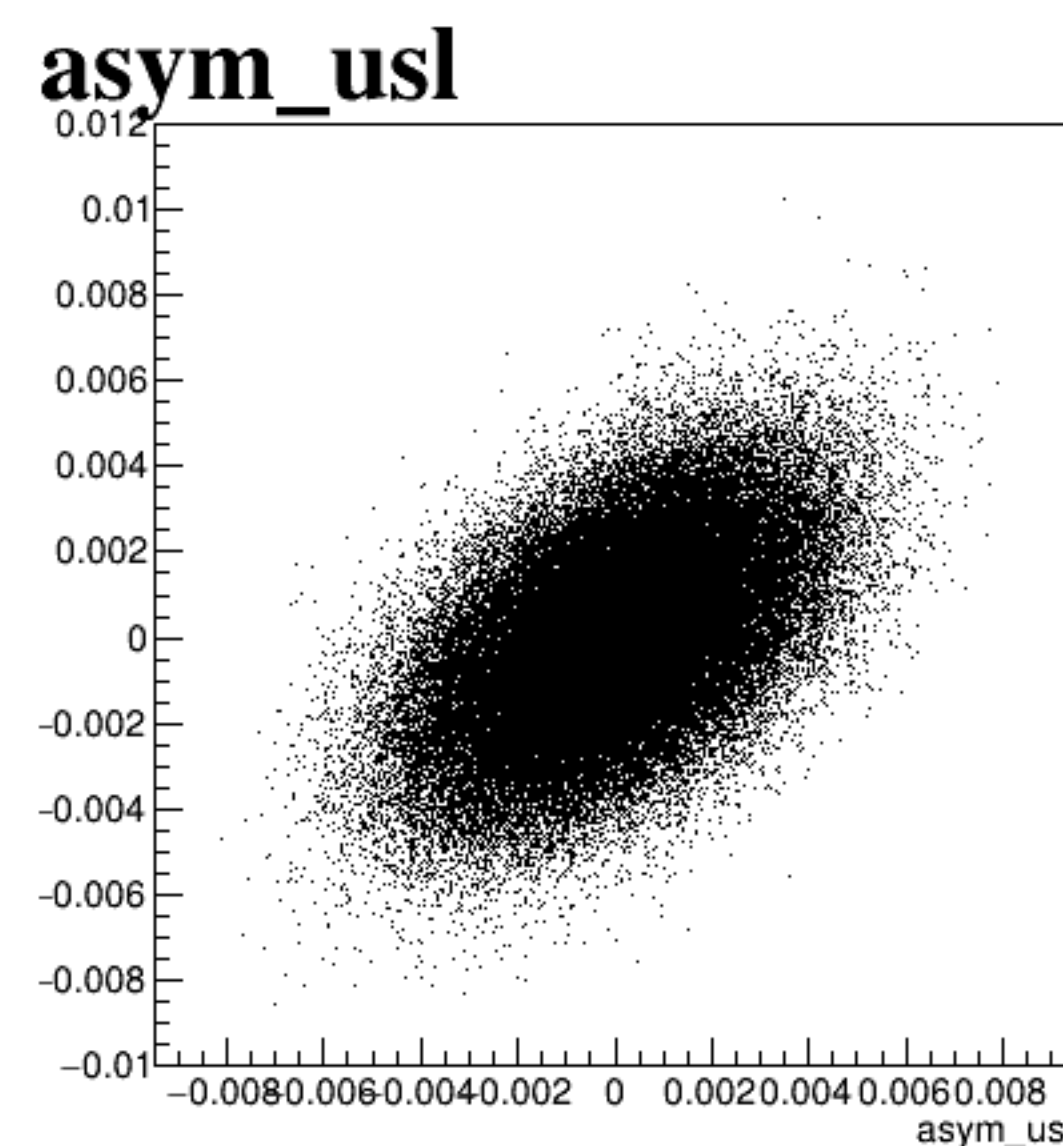
asym_usl



asym_dsl



asym_usr

**asym_dsr**