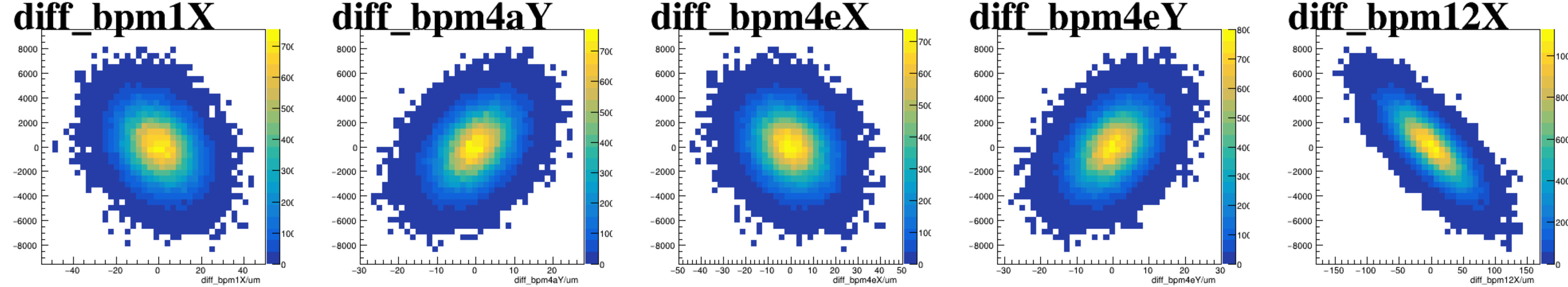
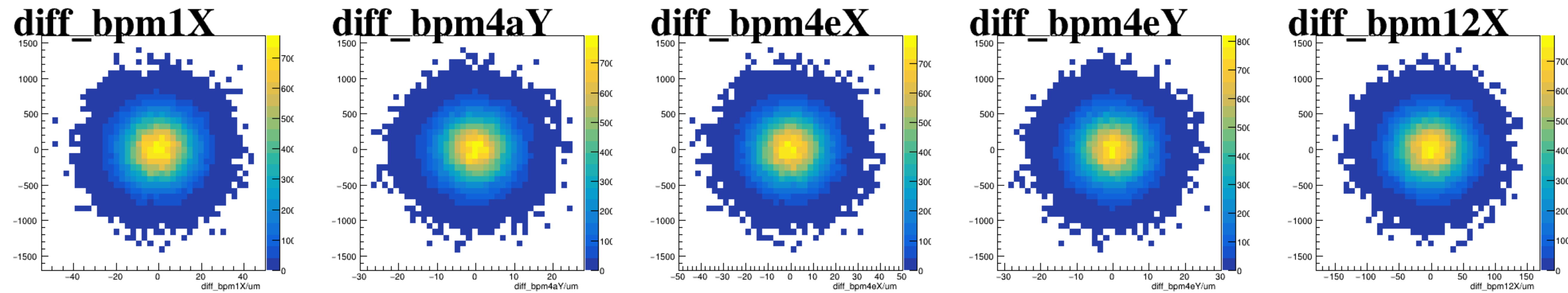


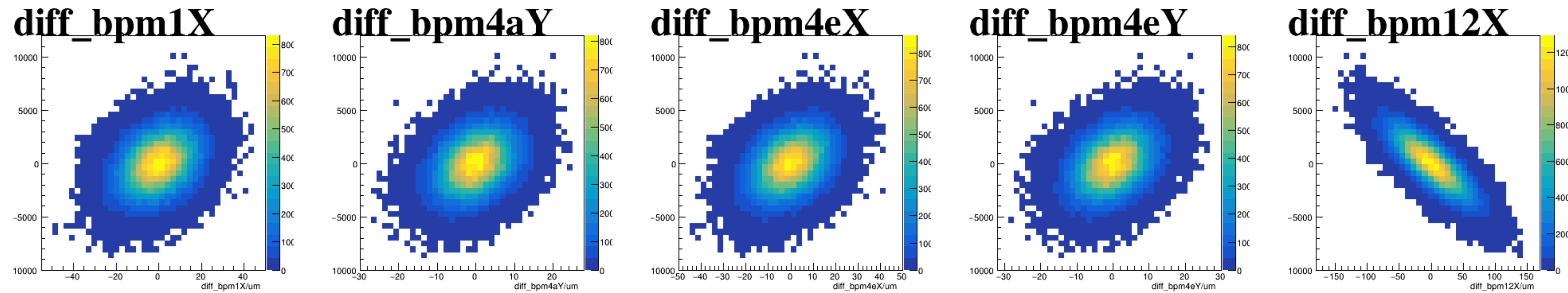
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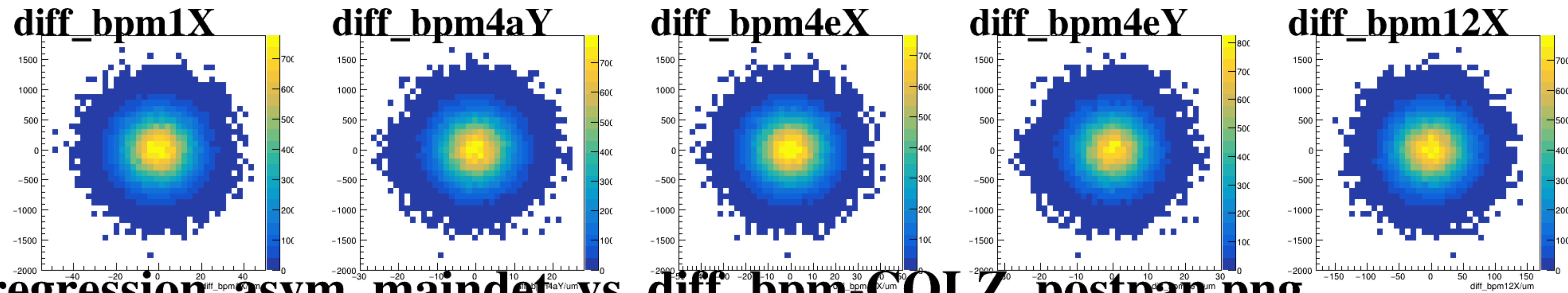
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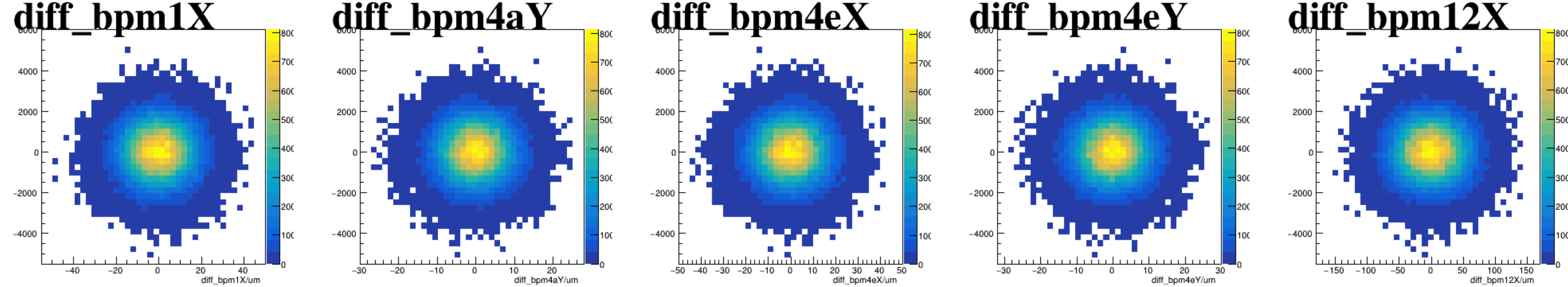
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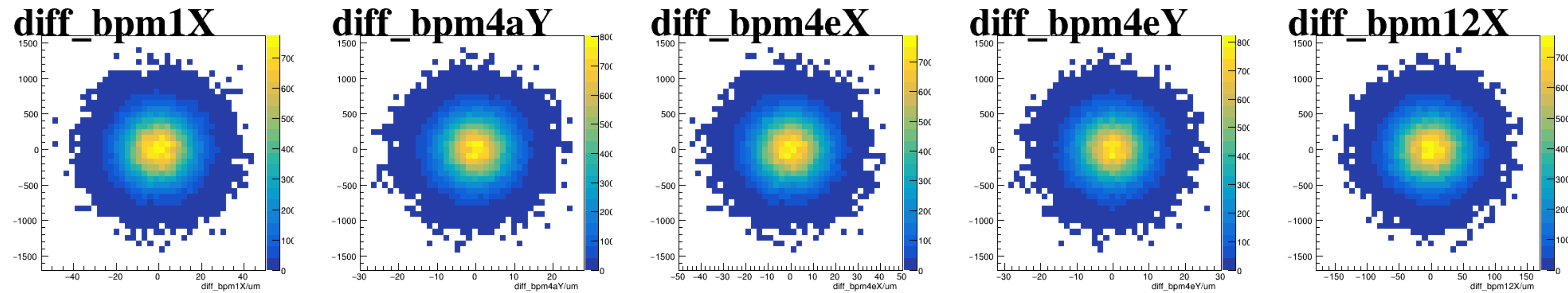
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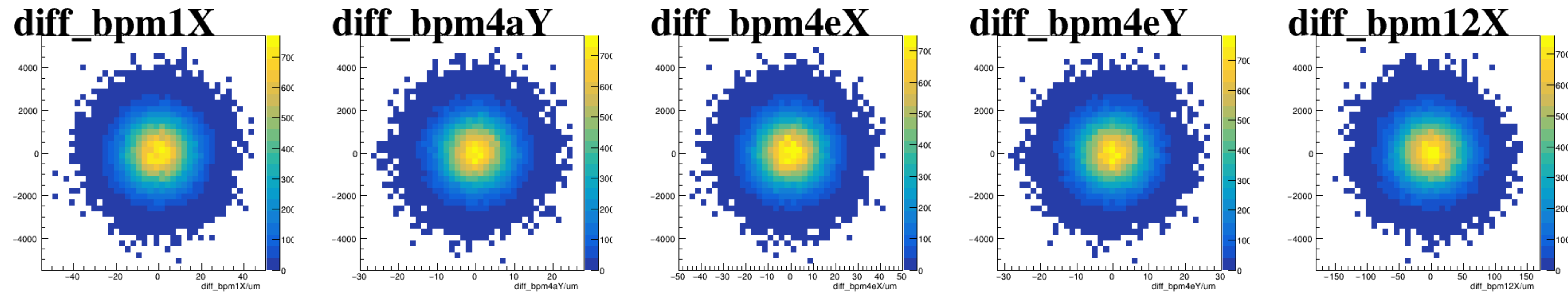
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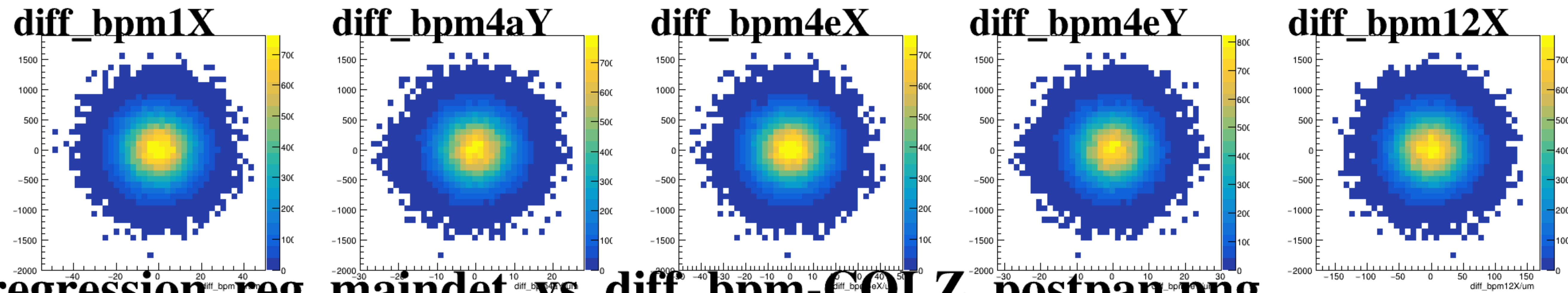
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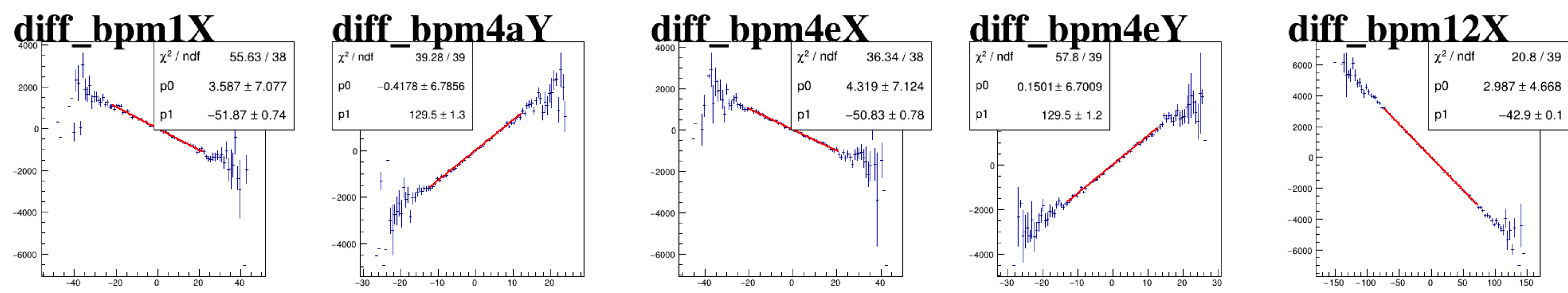
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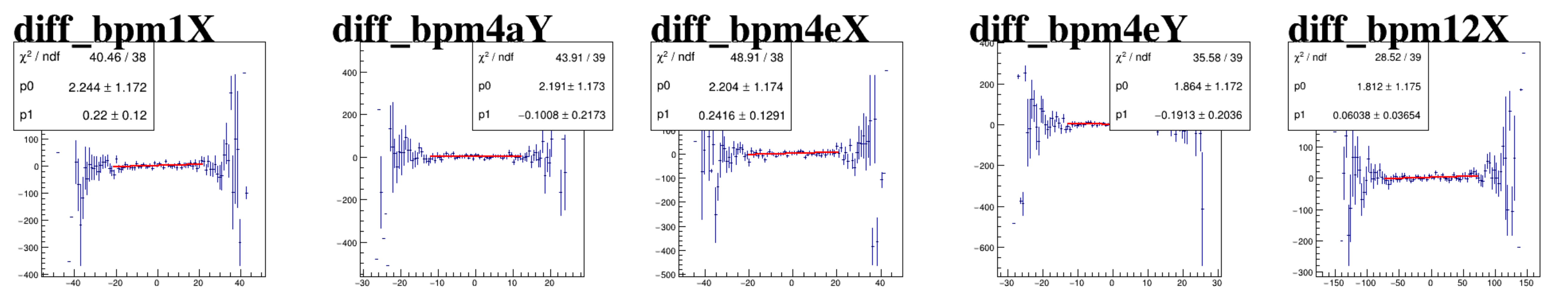
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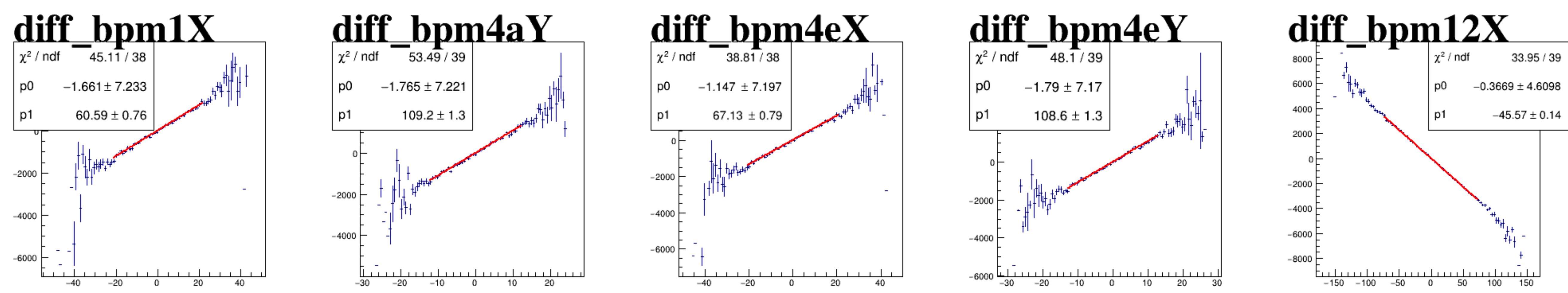
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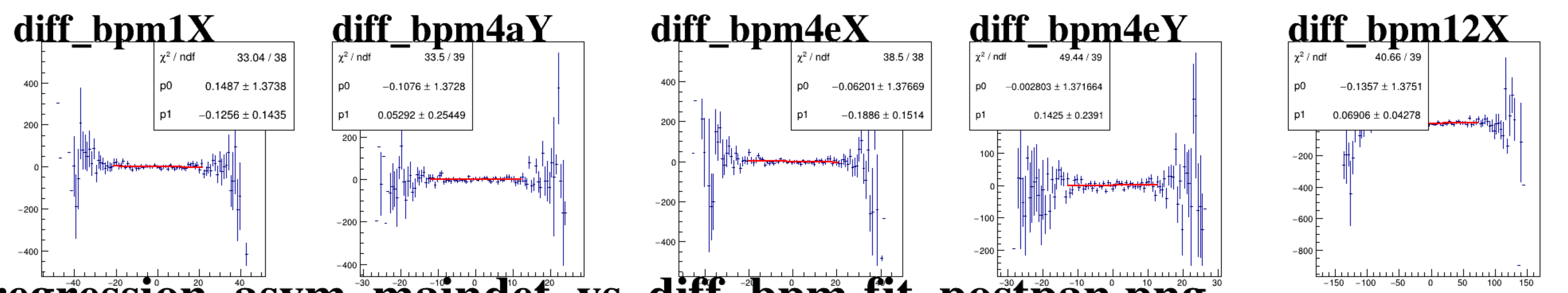
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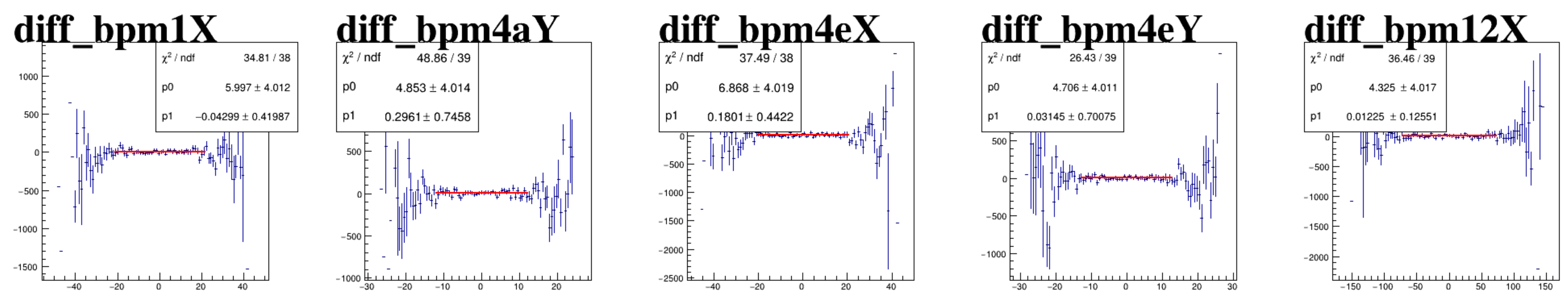
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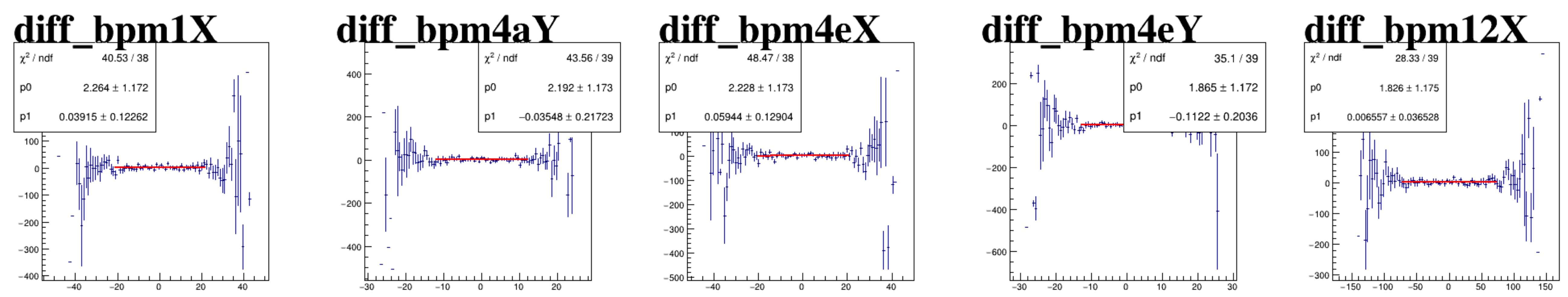
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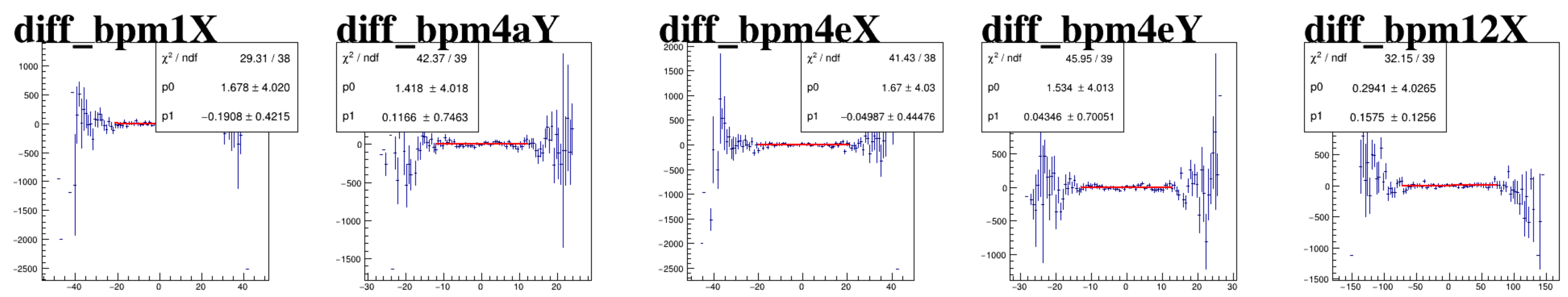
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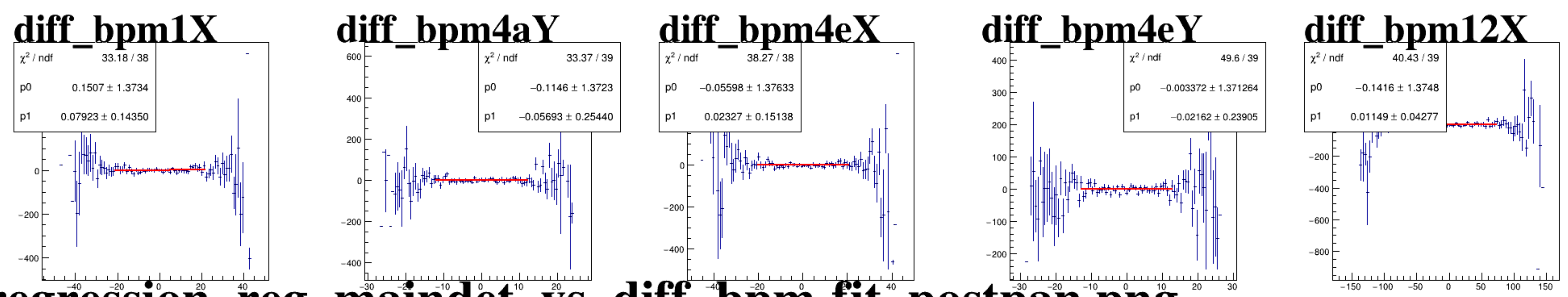
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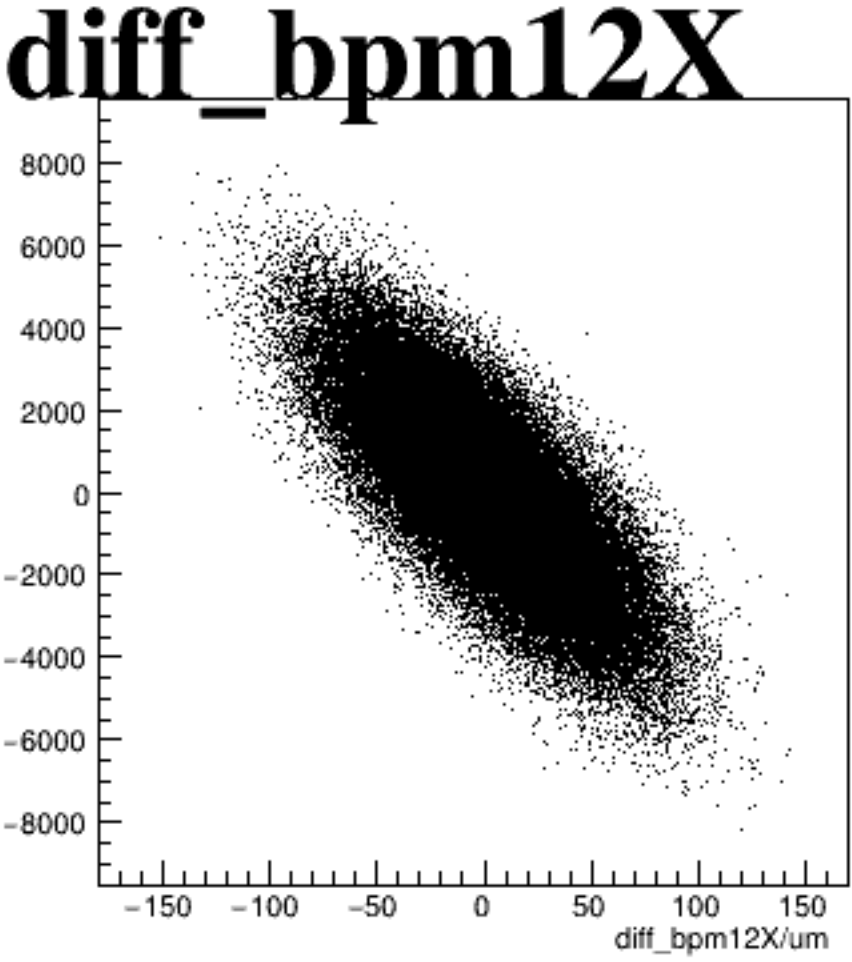
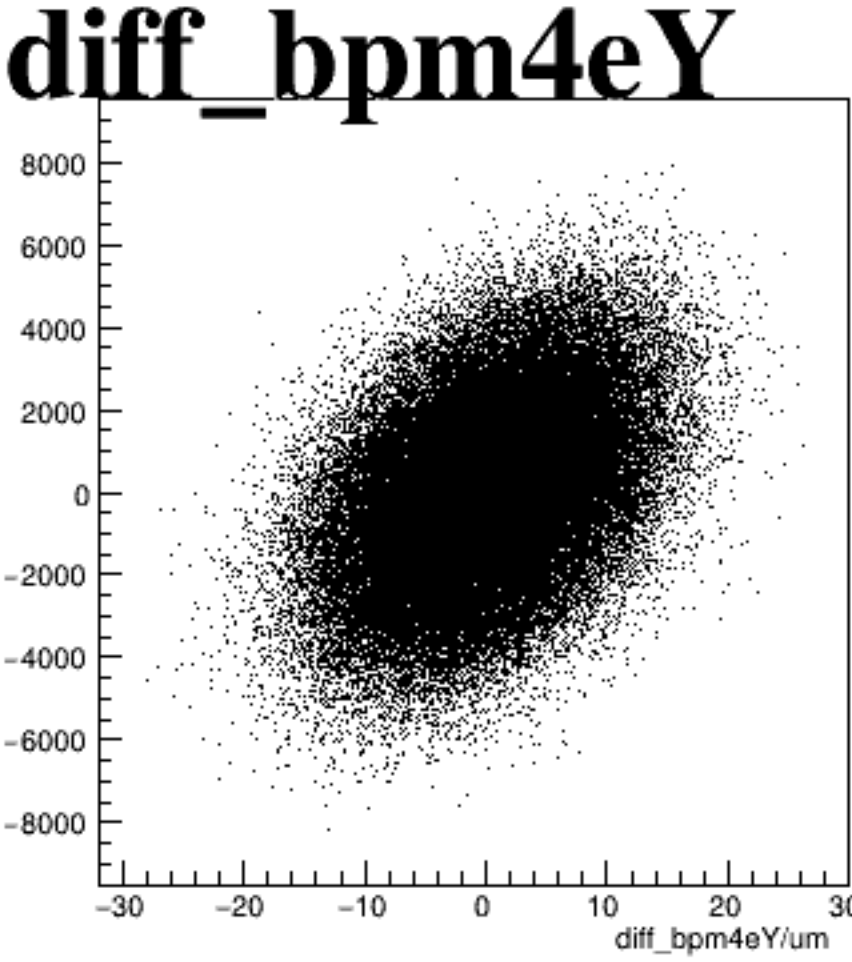
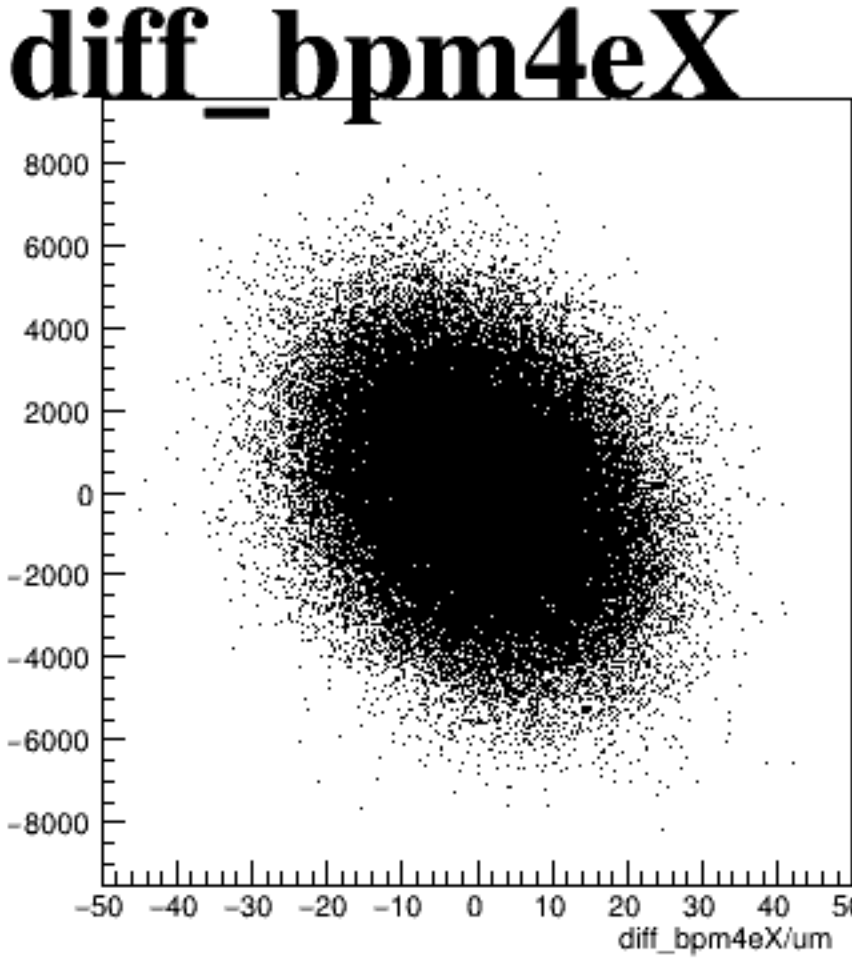
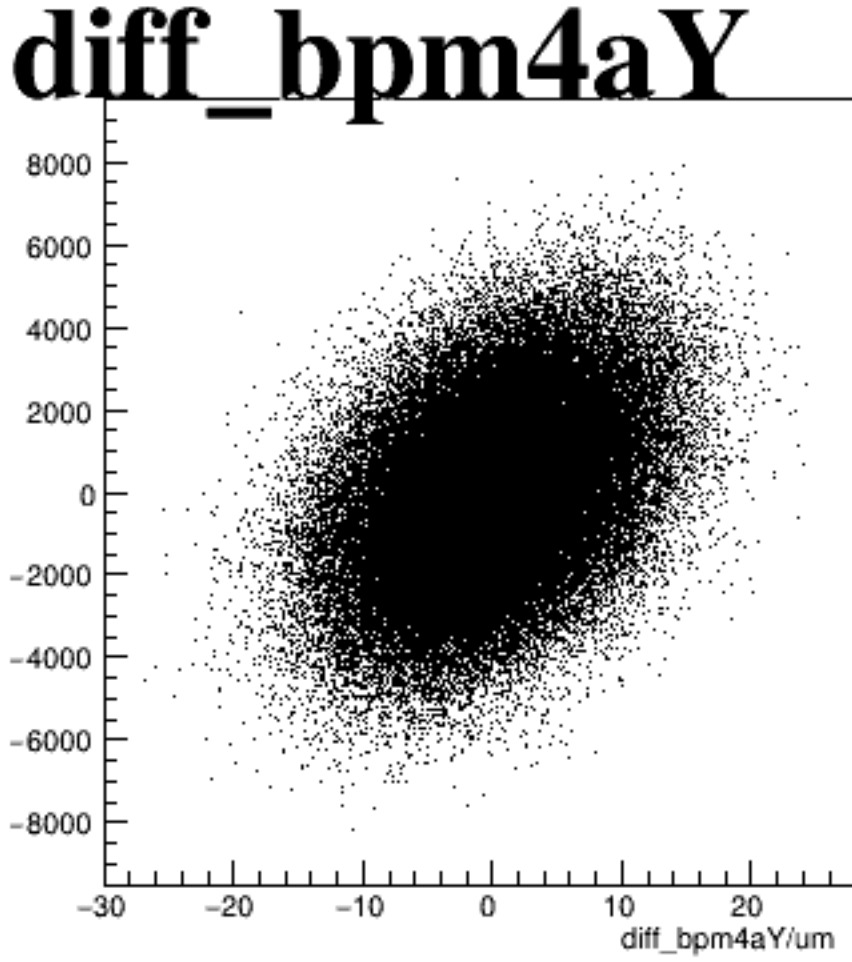
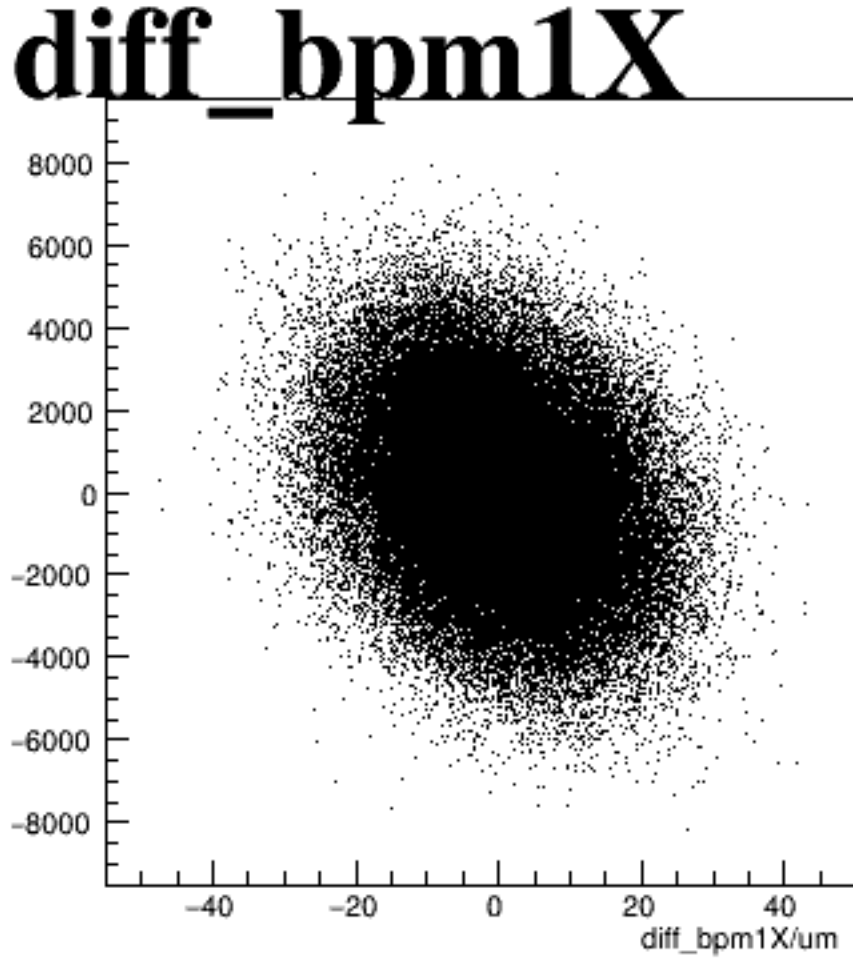
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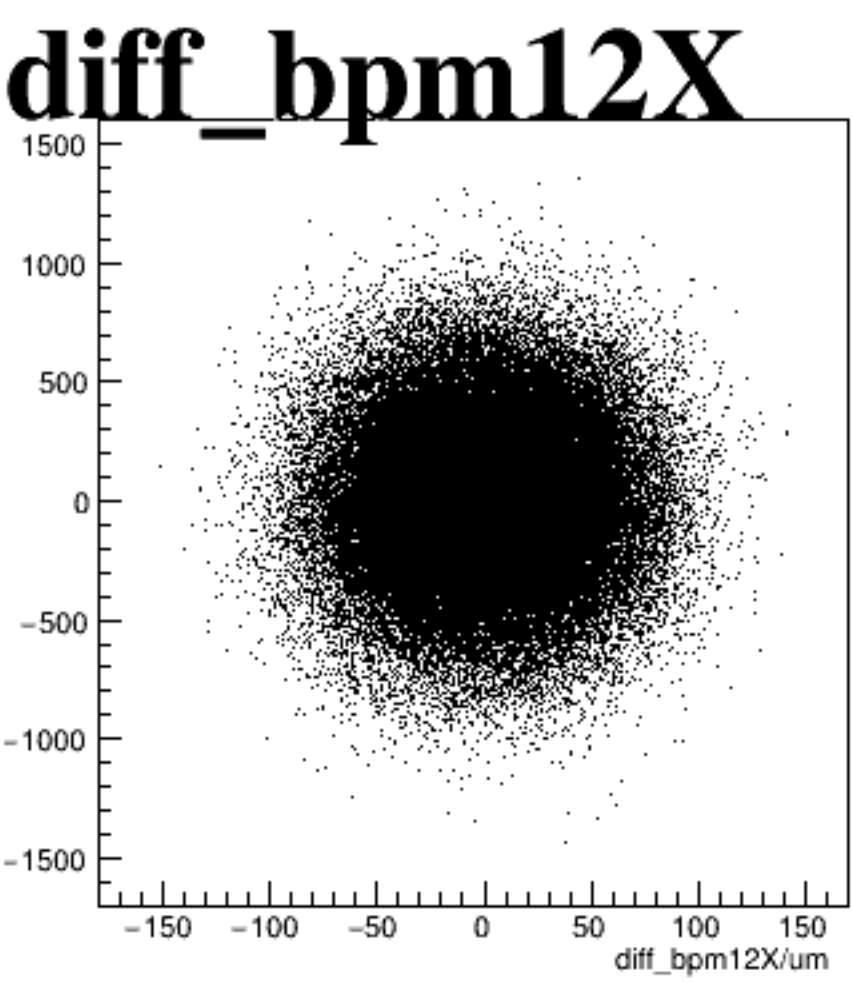
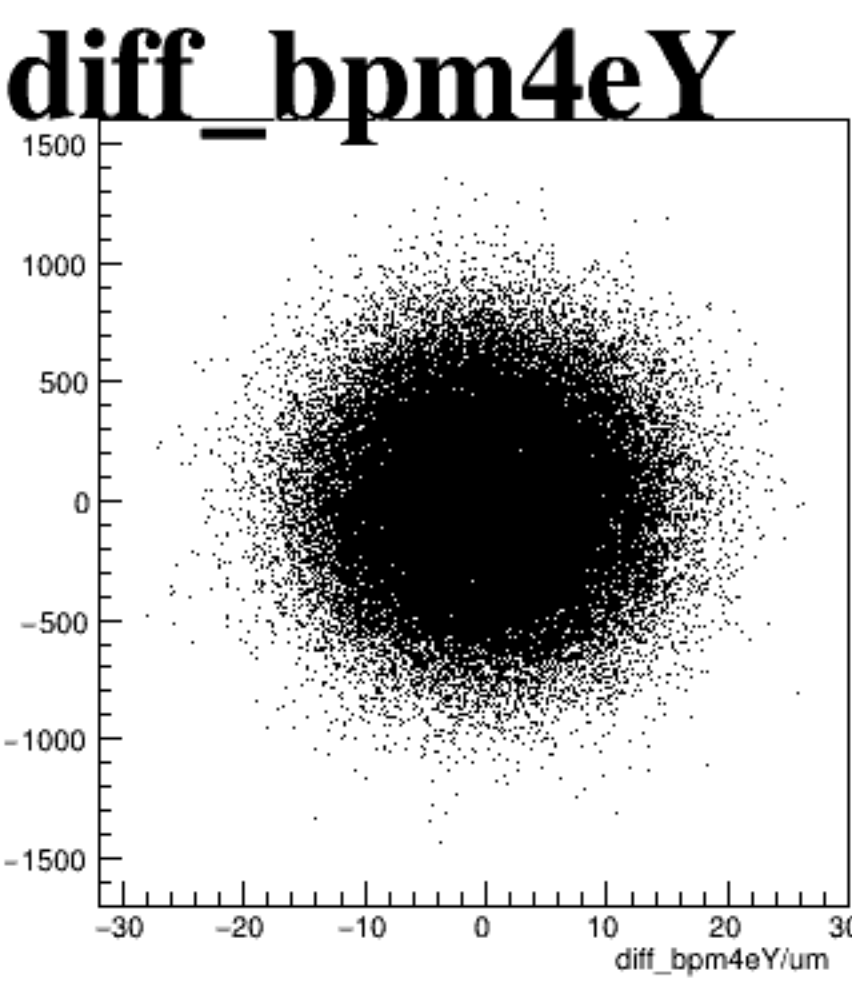
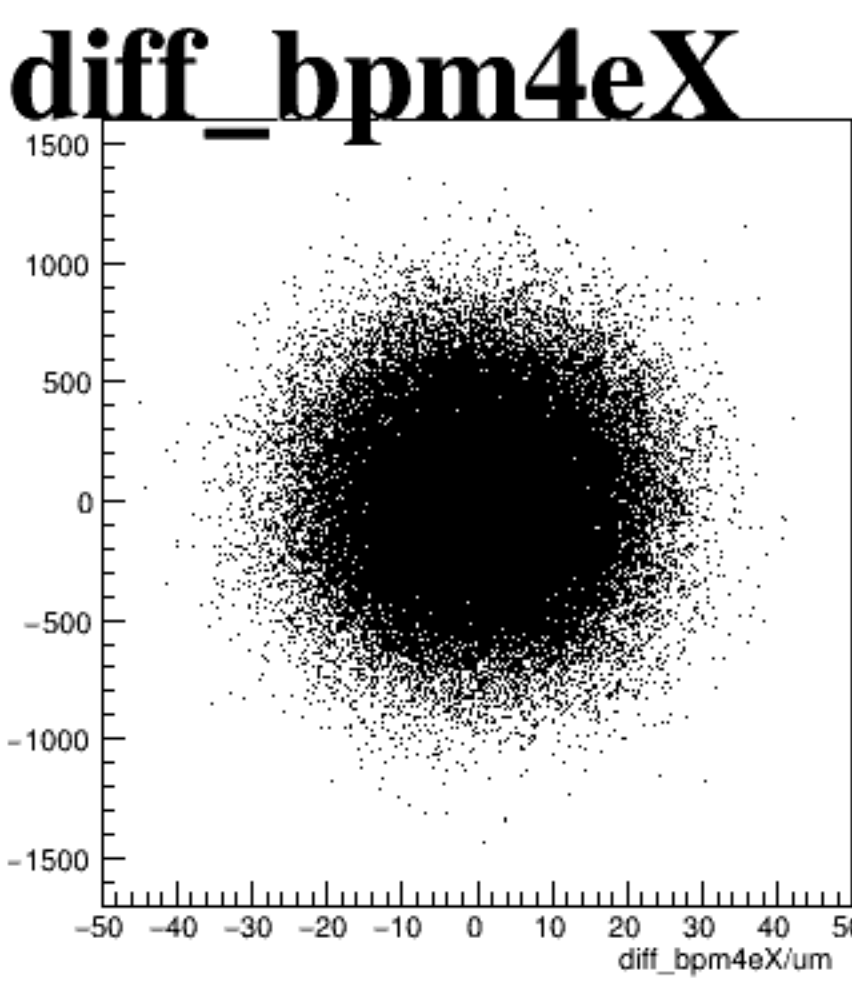
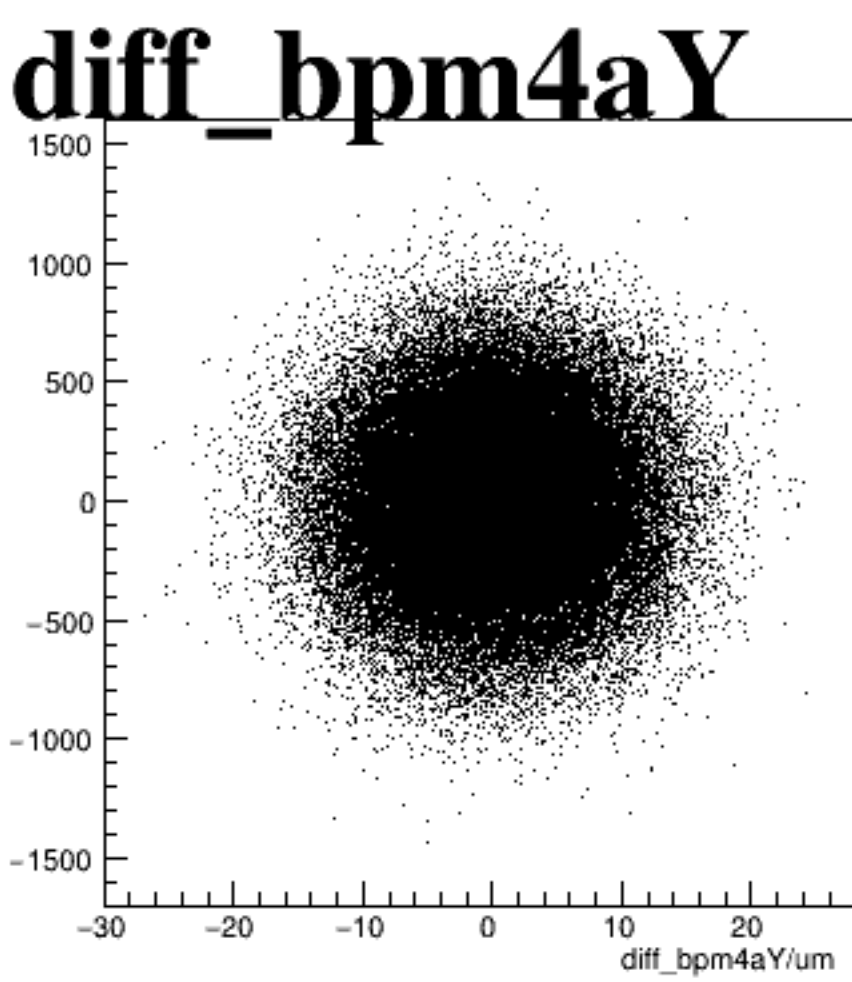
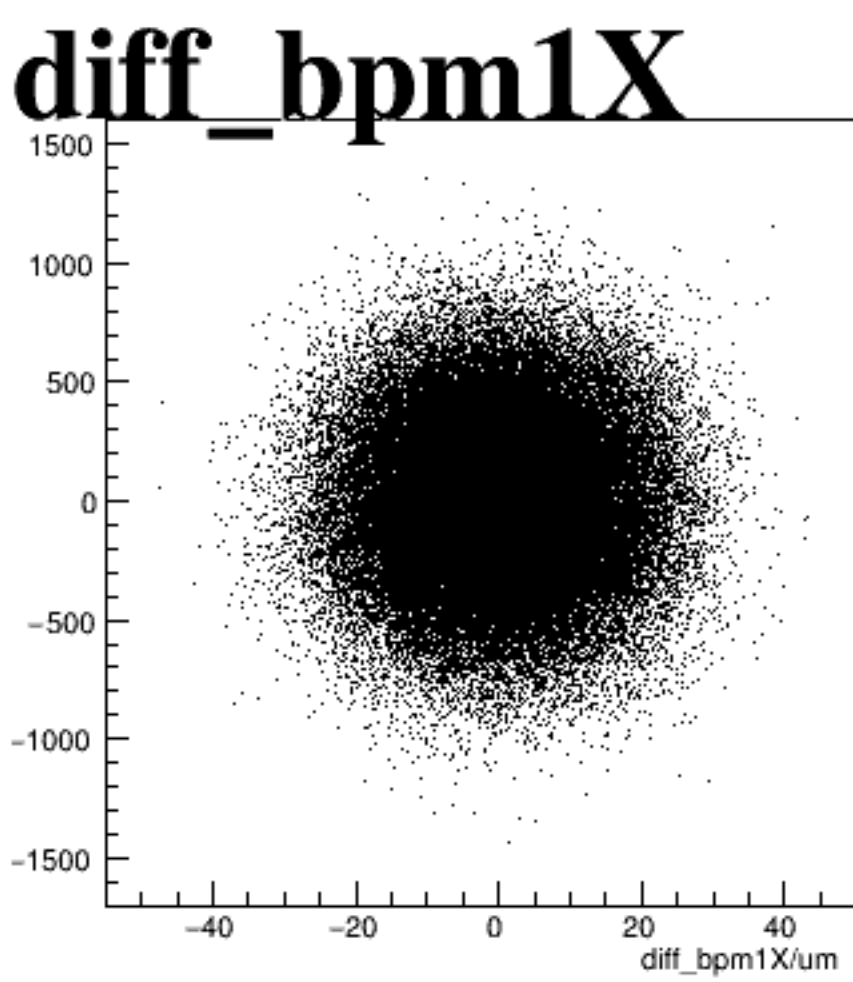
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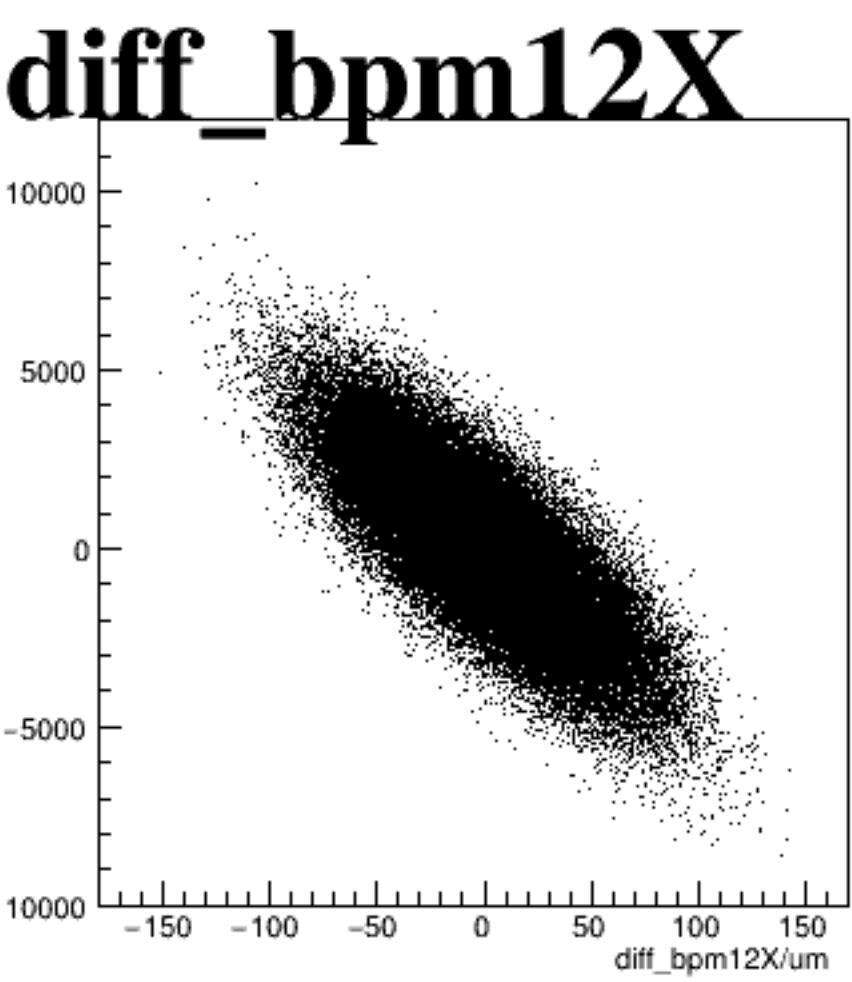
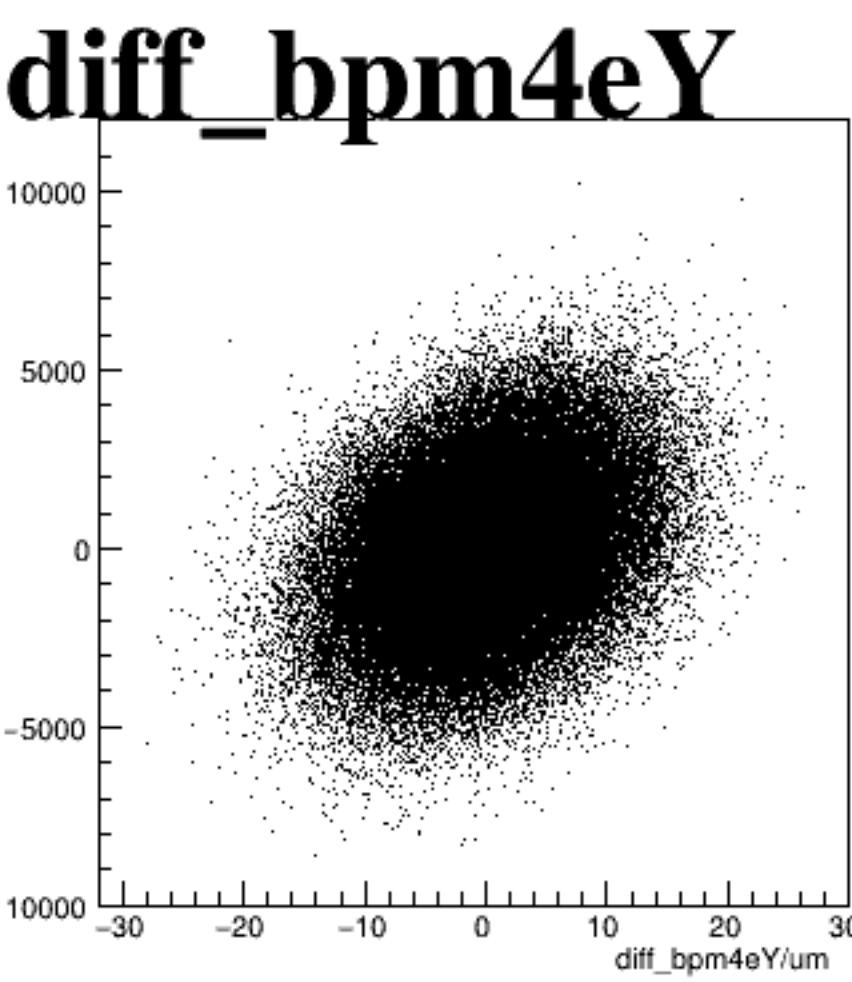
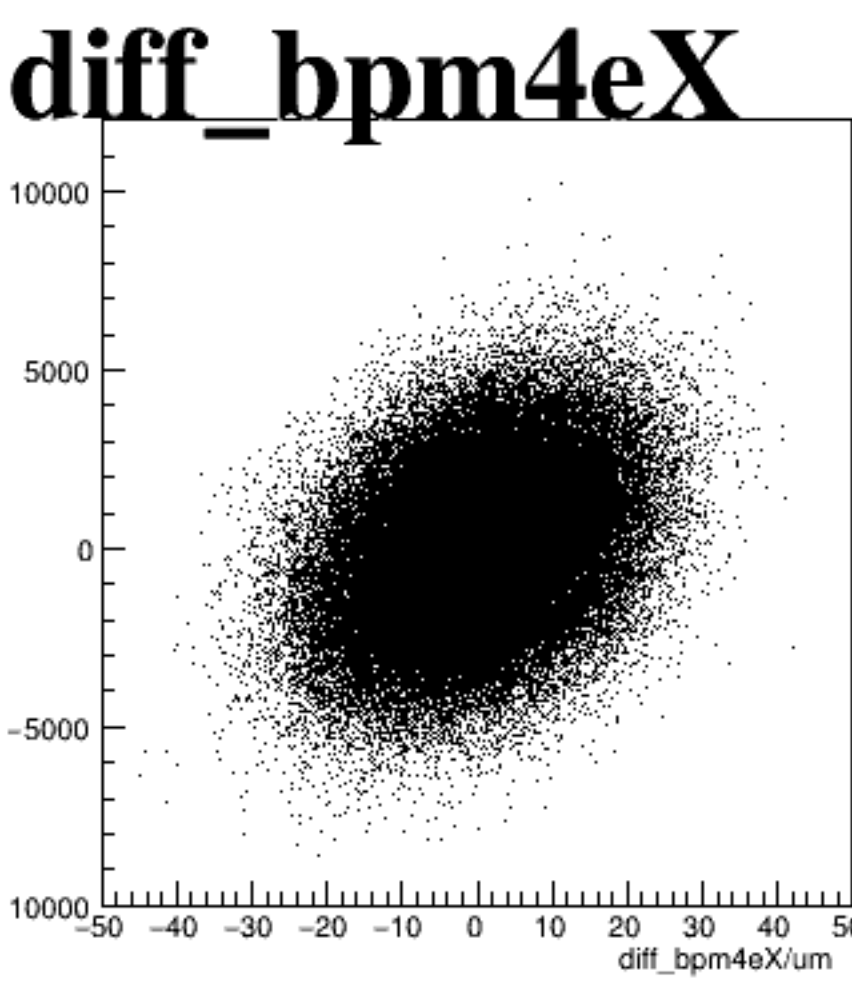
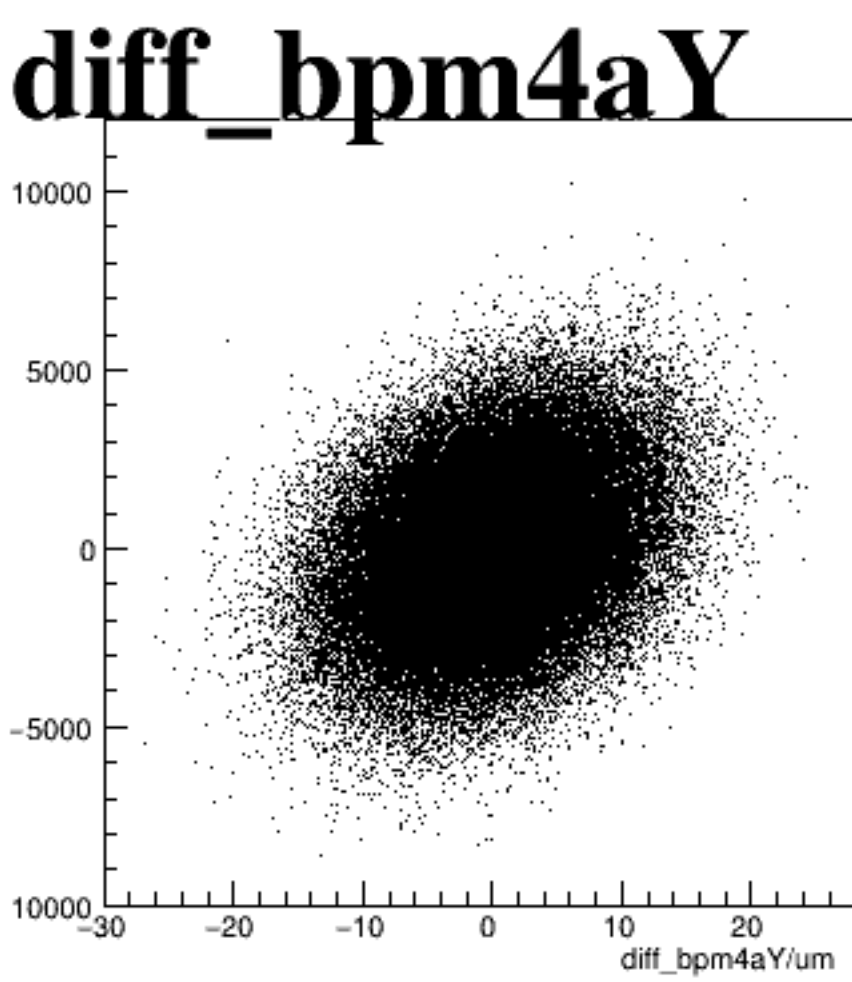
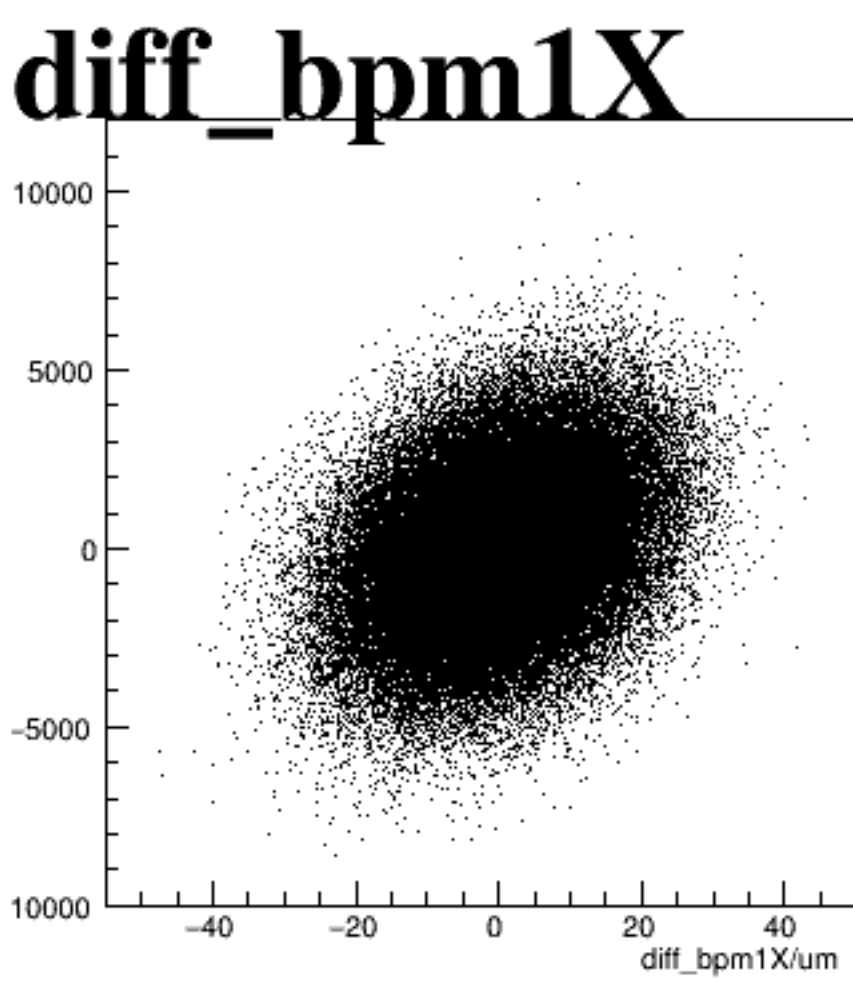
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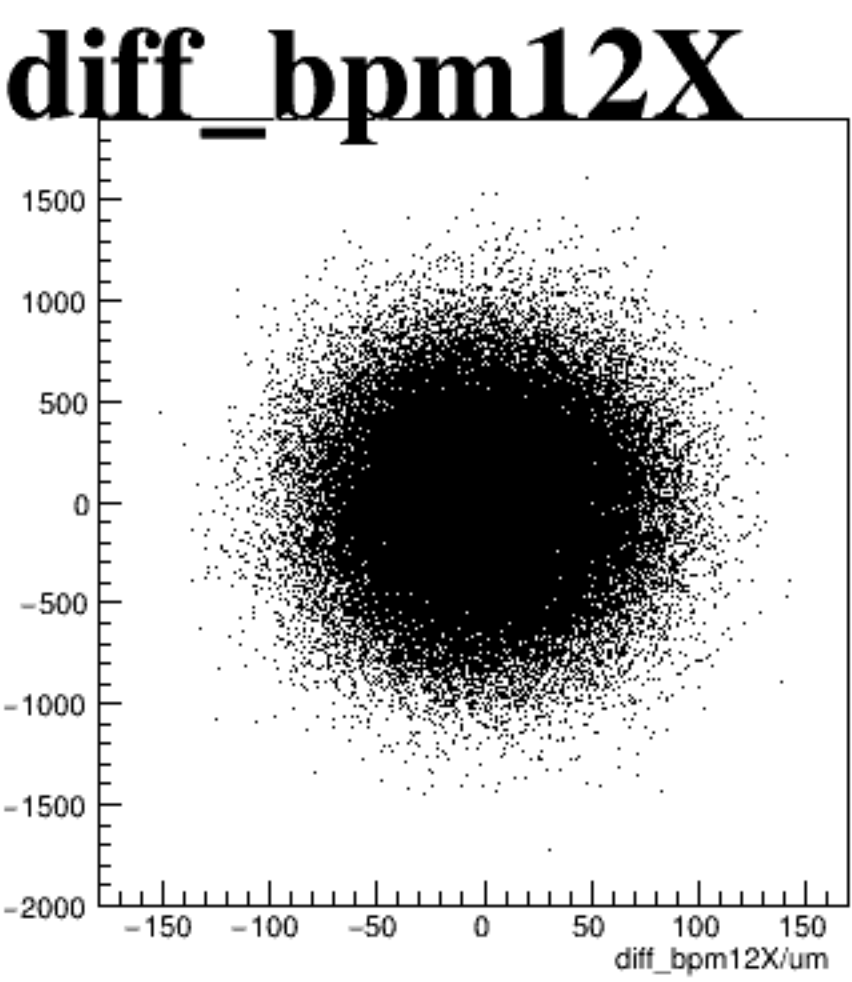
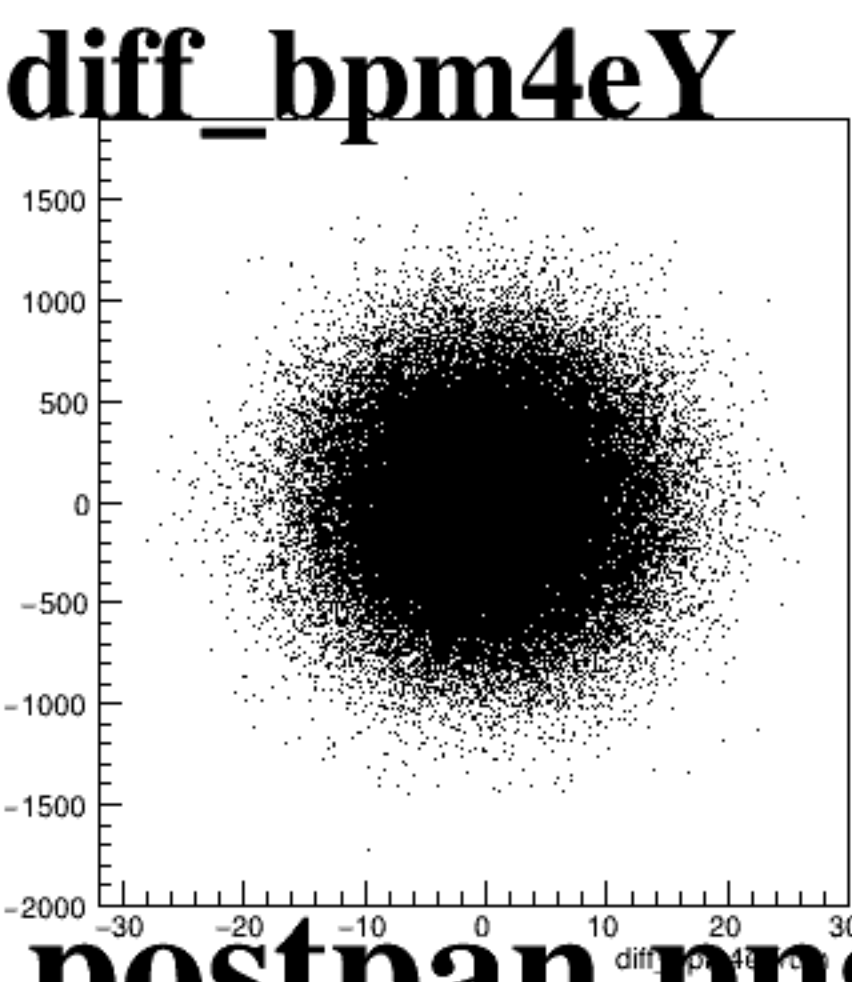
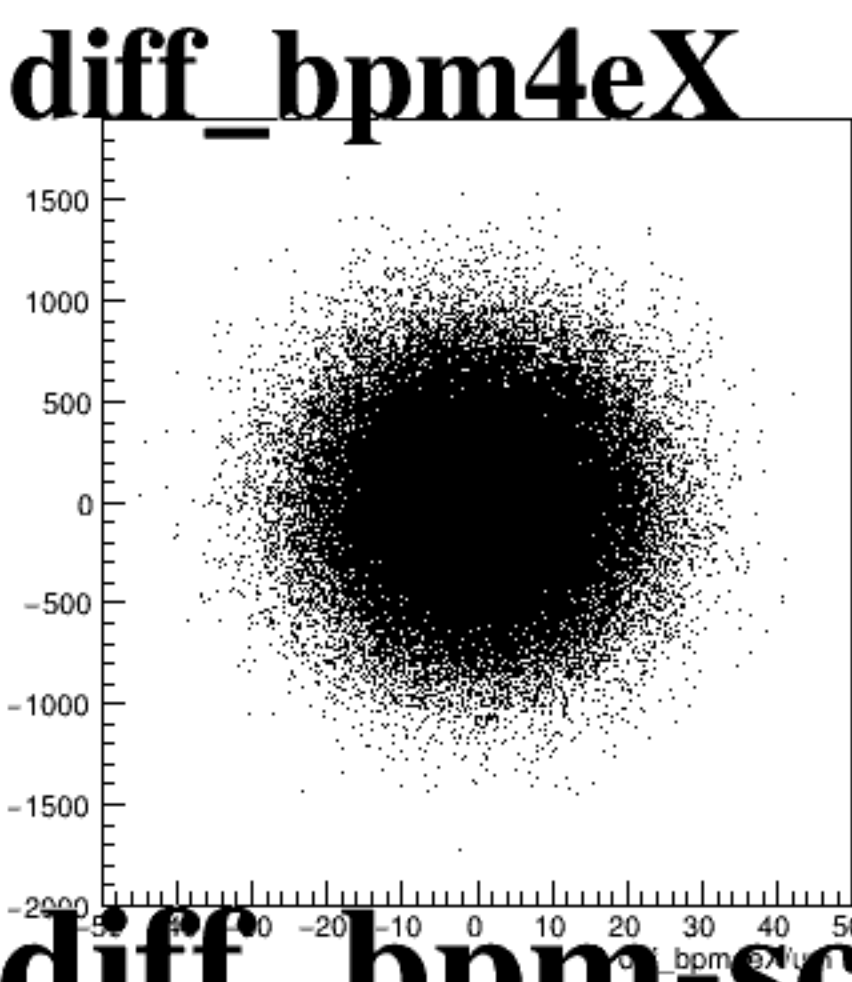
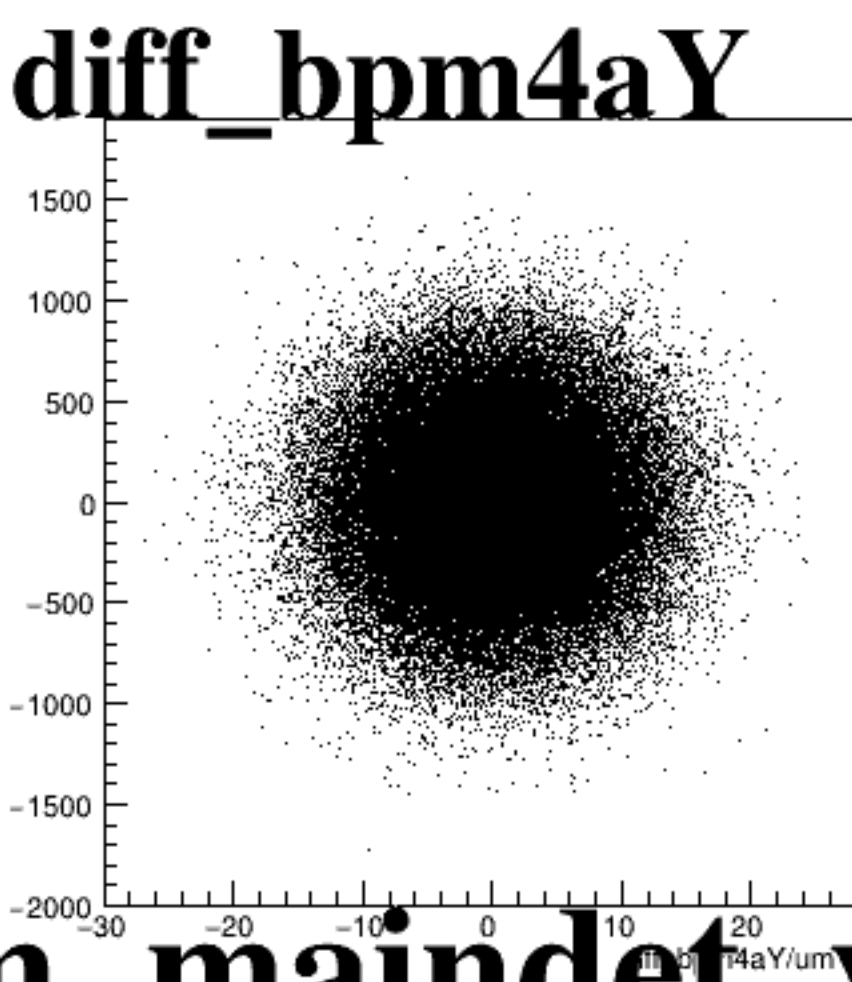
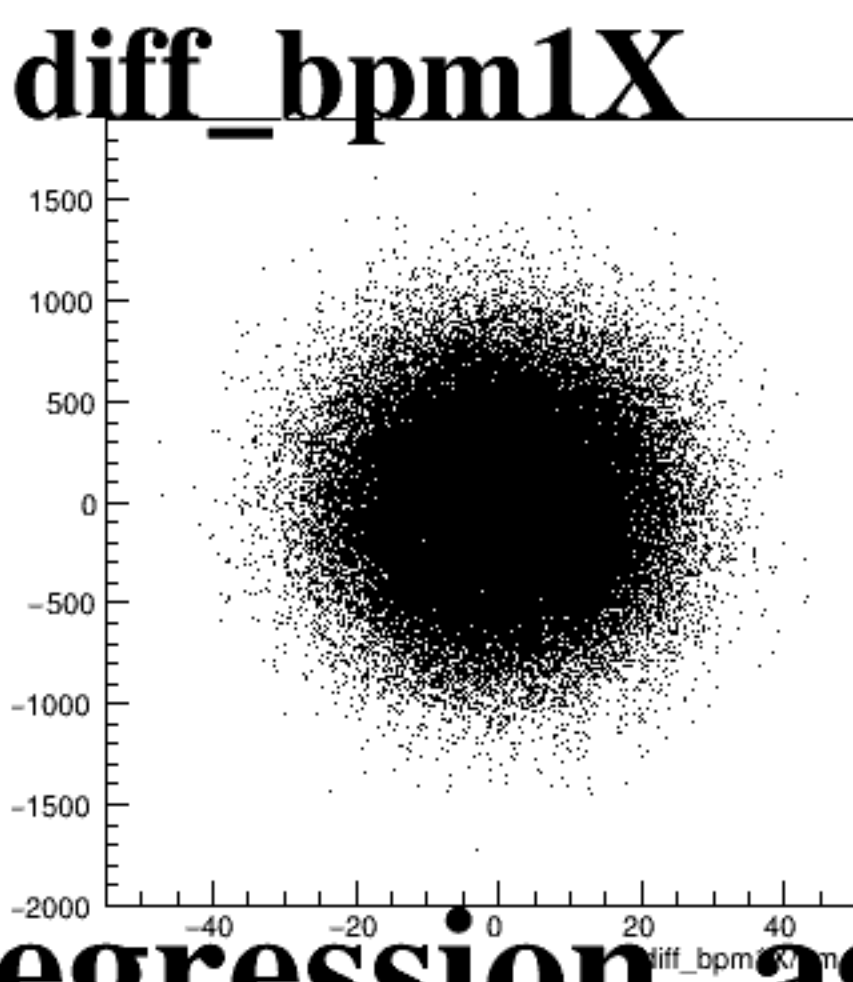
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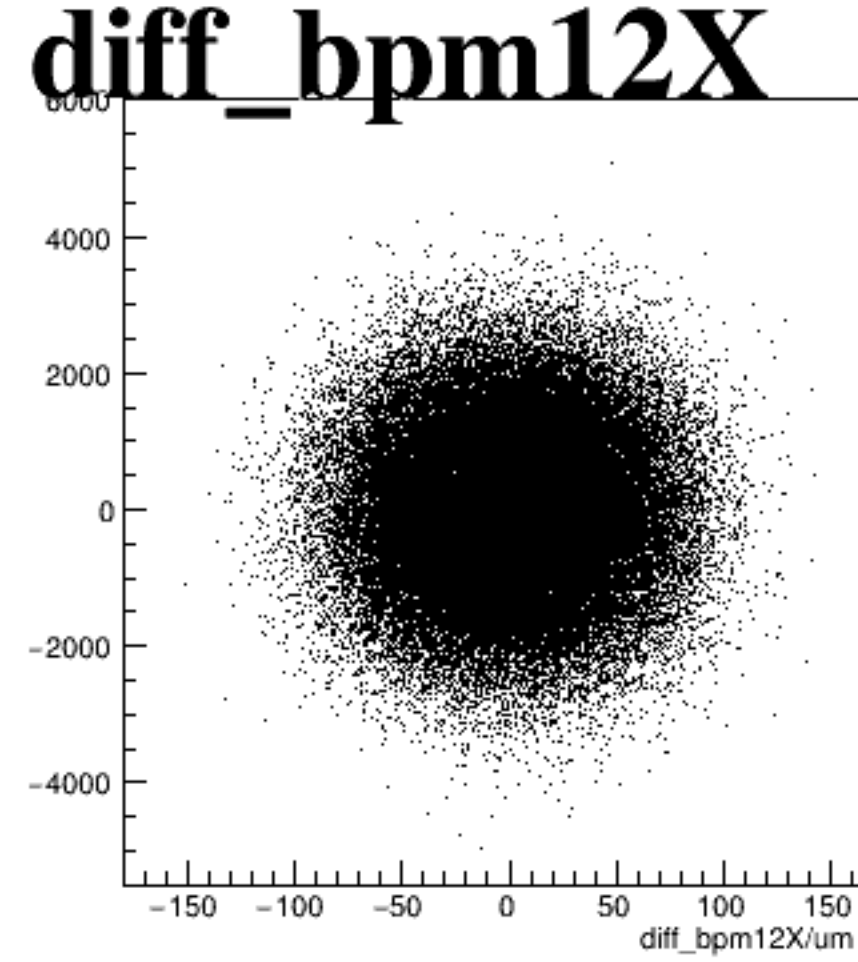
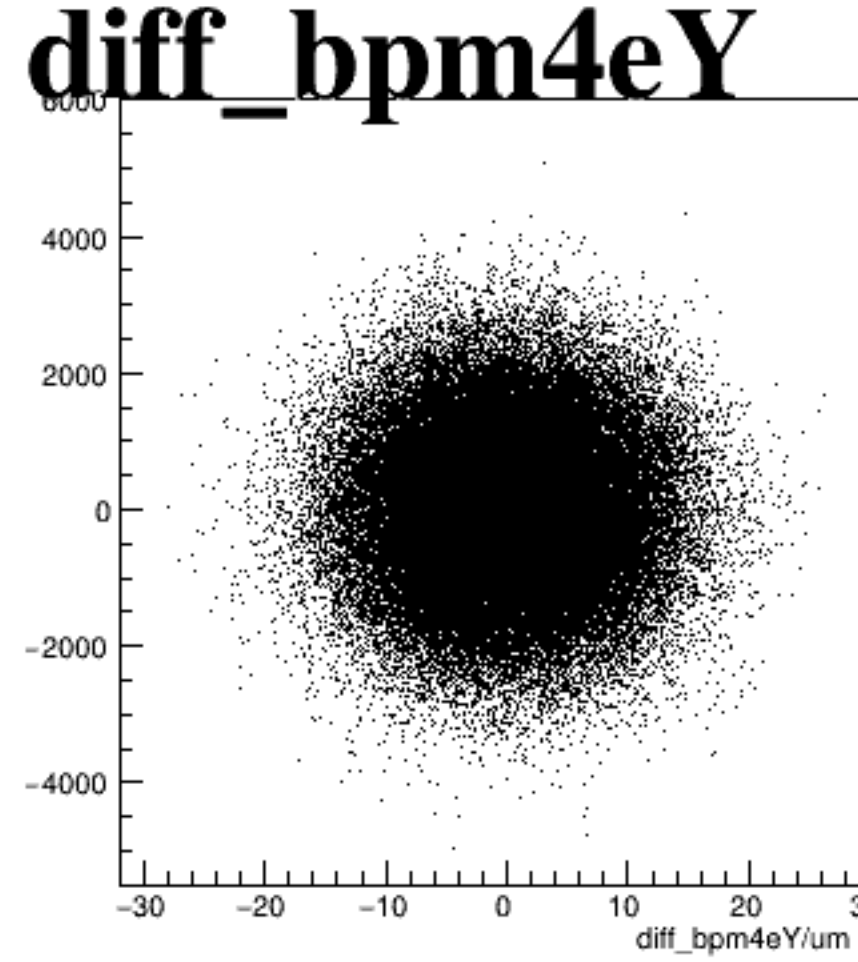
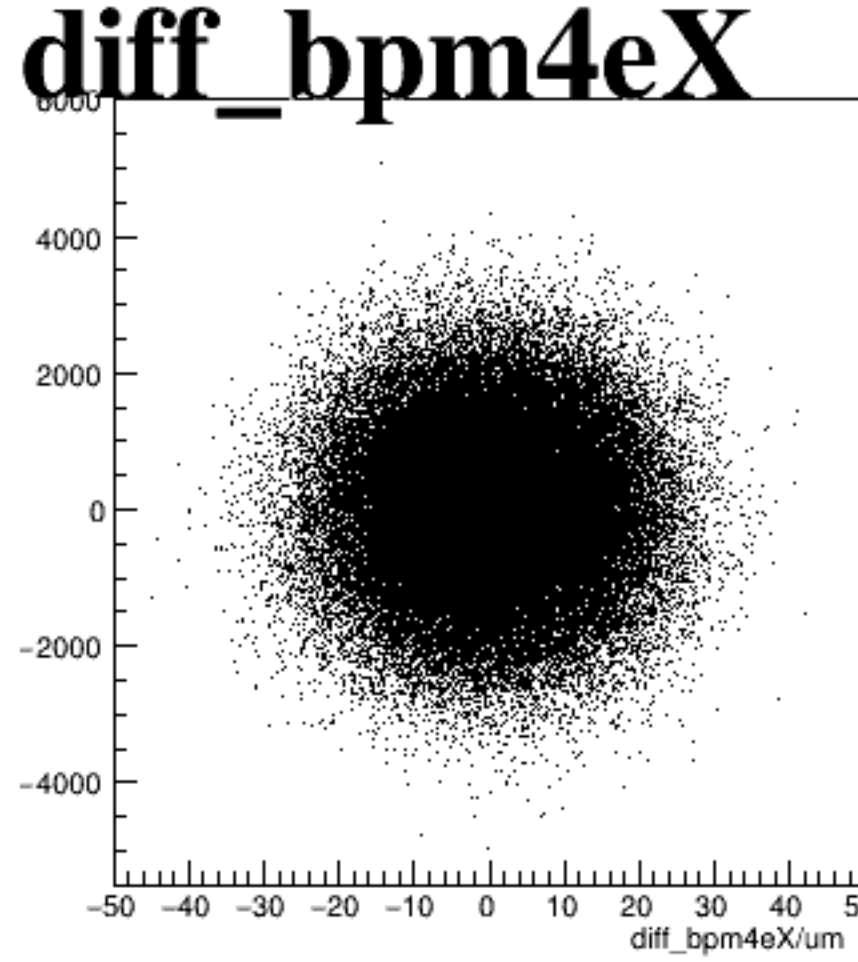
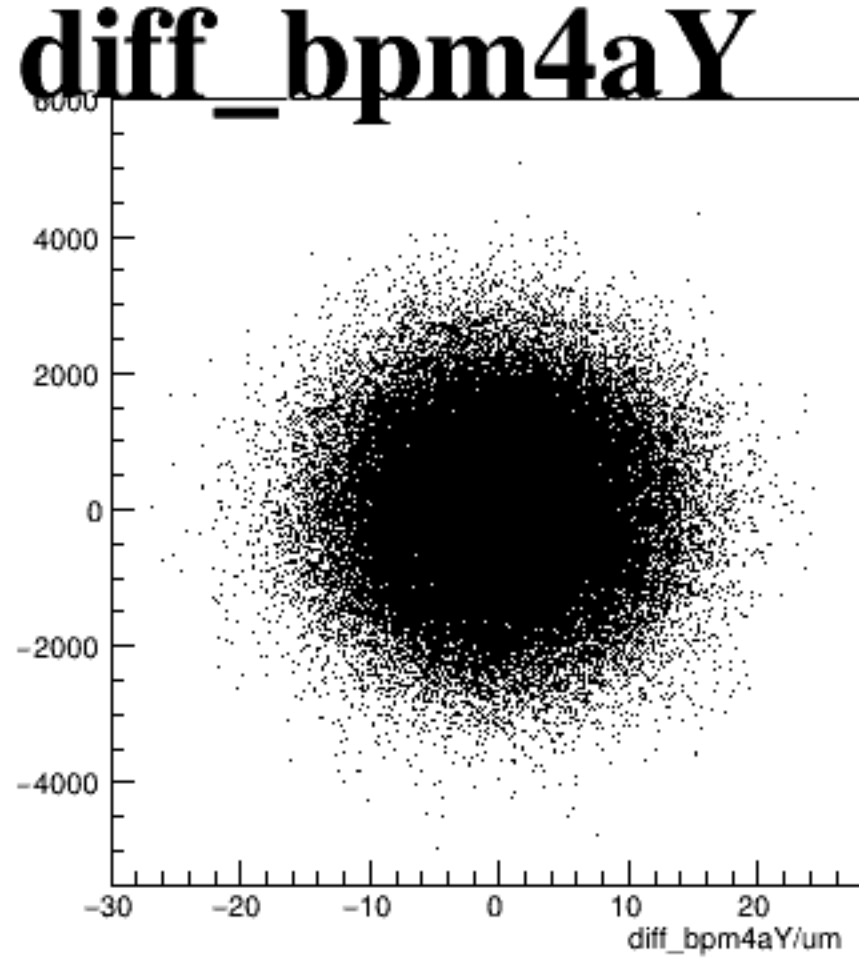
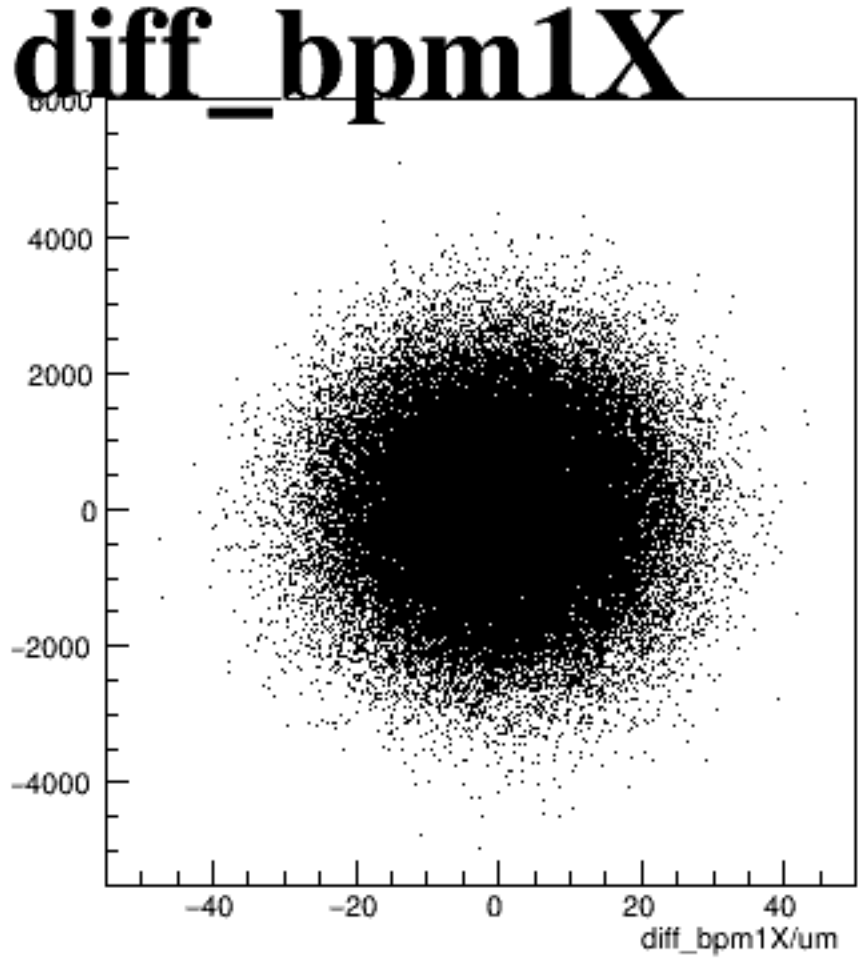
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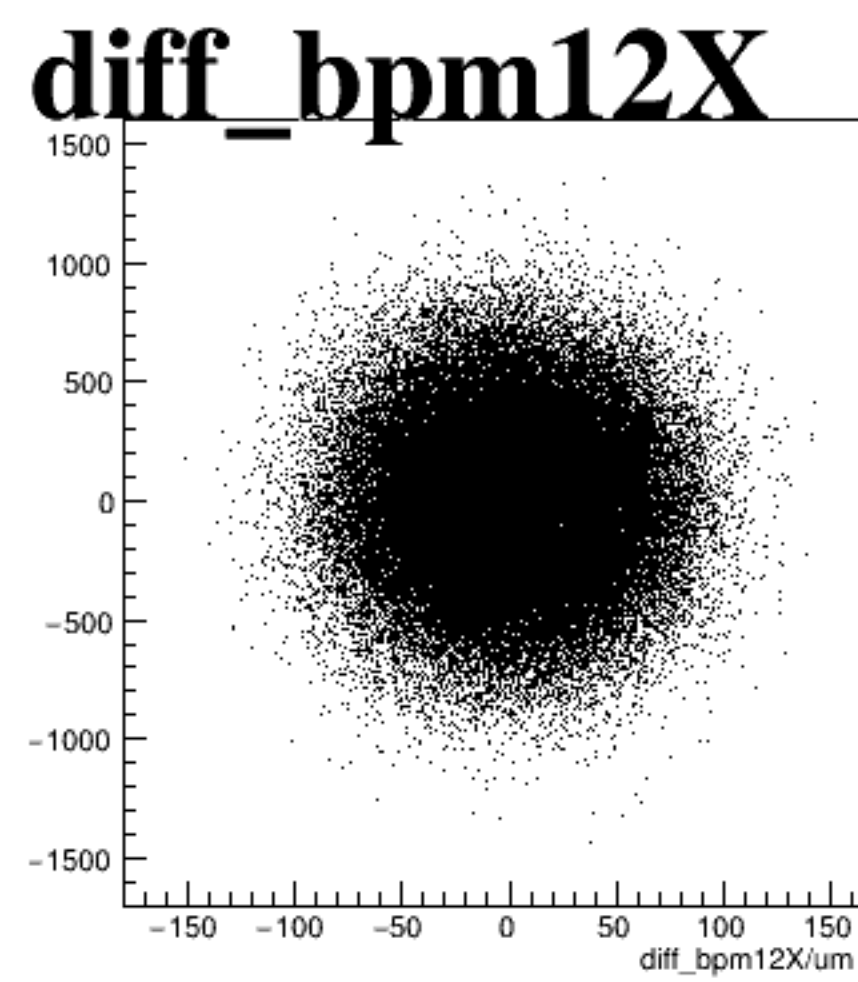
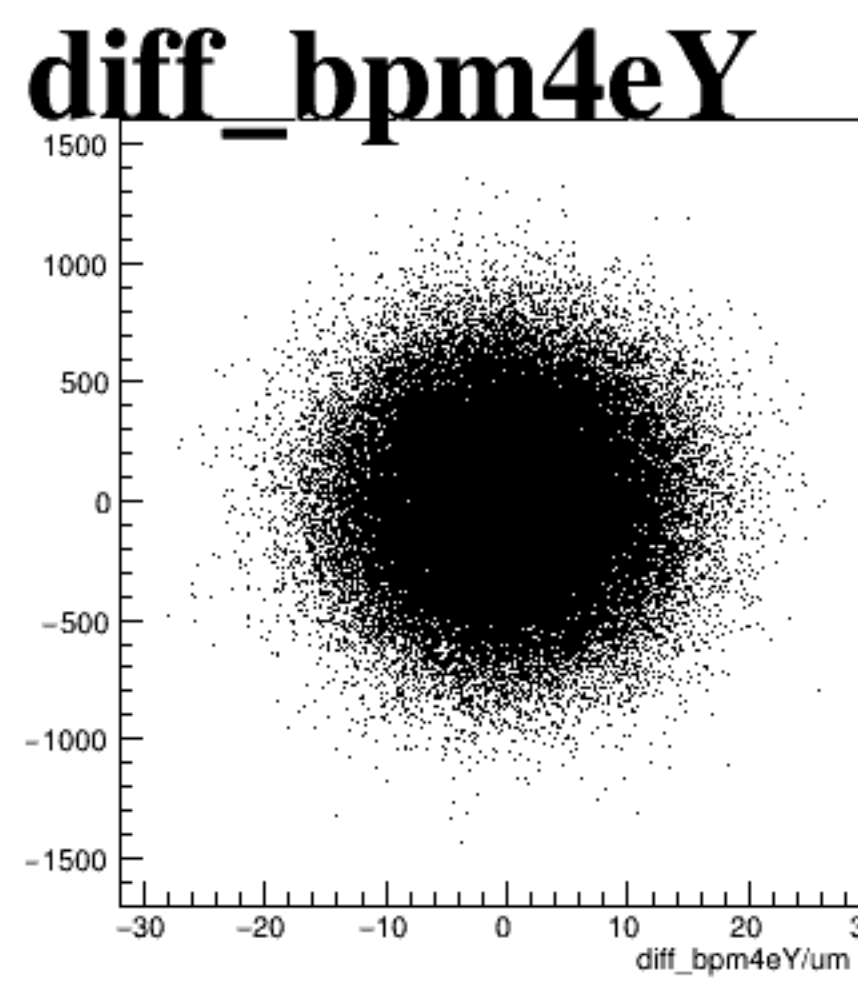
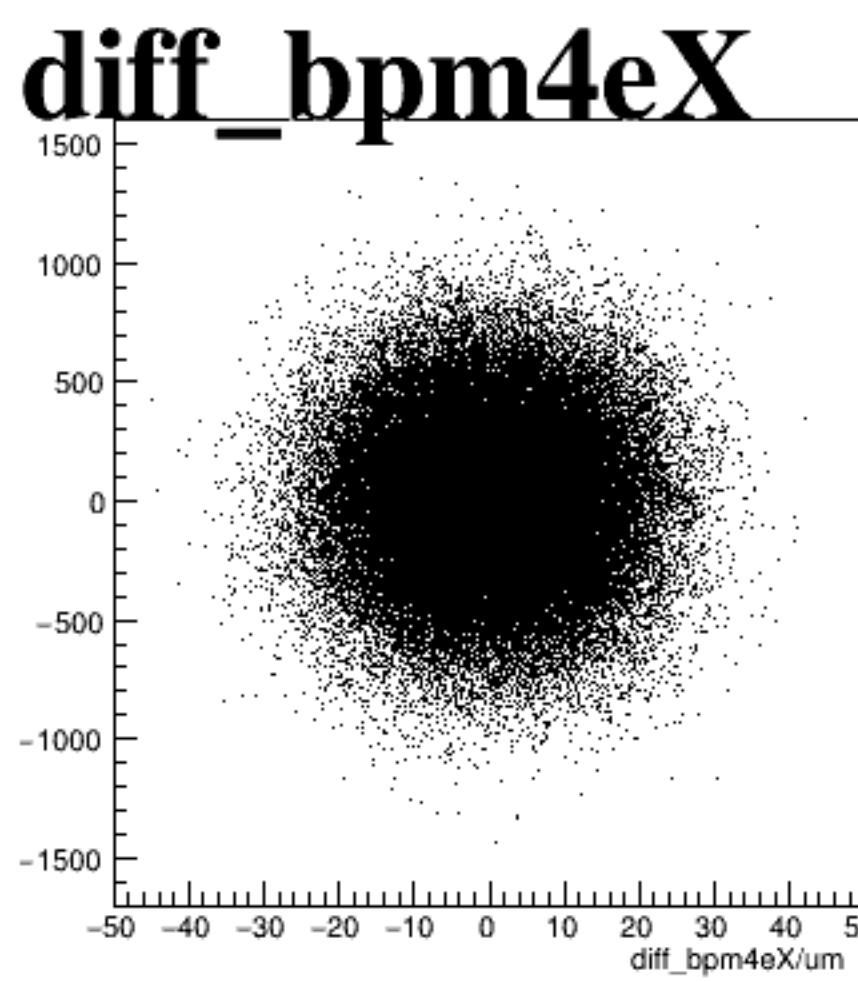
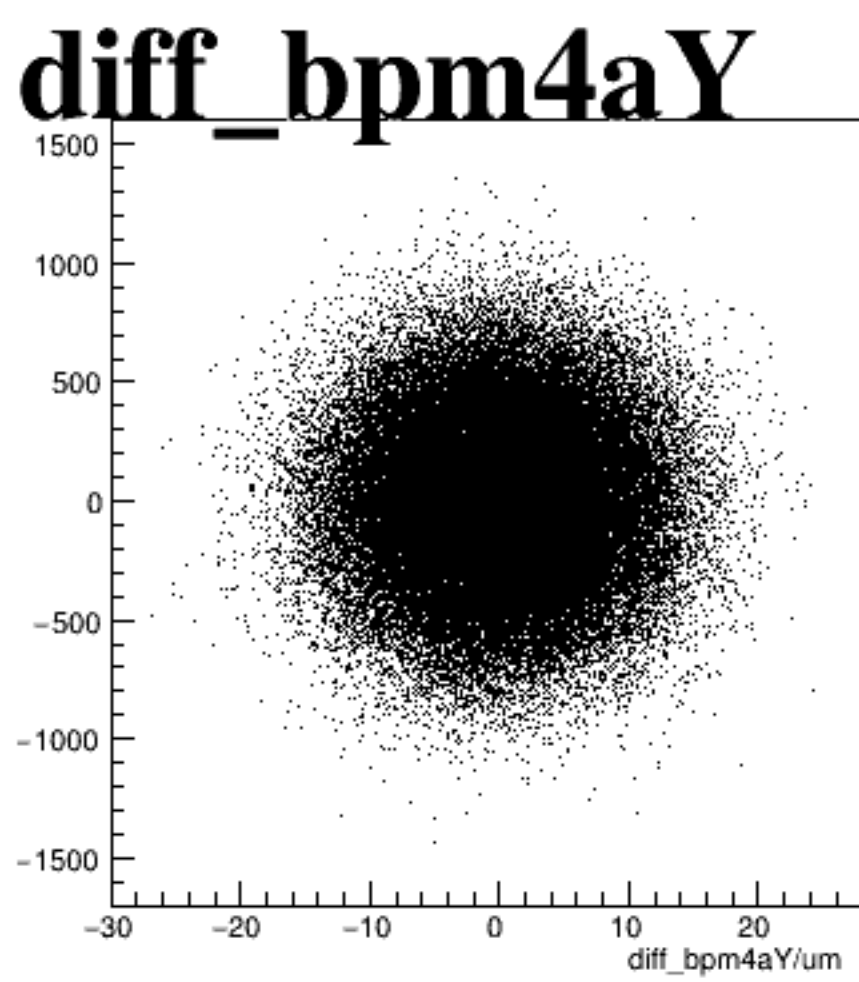
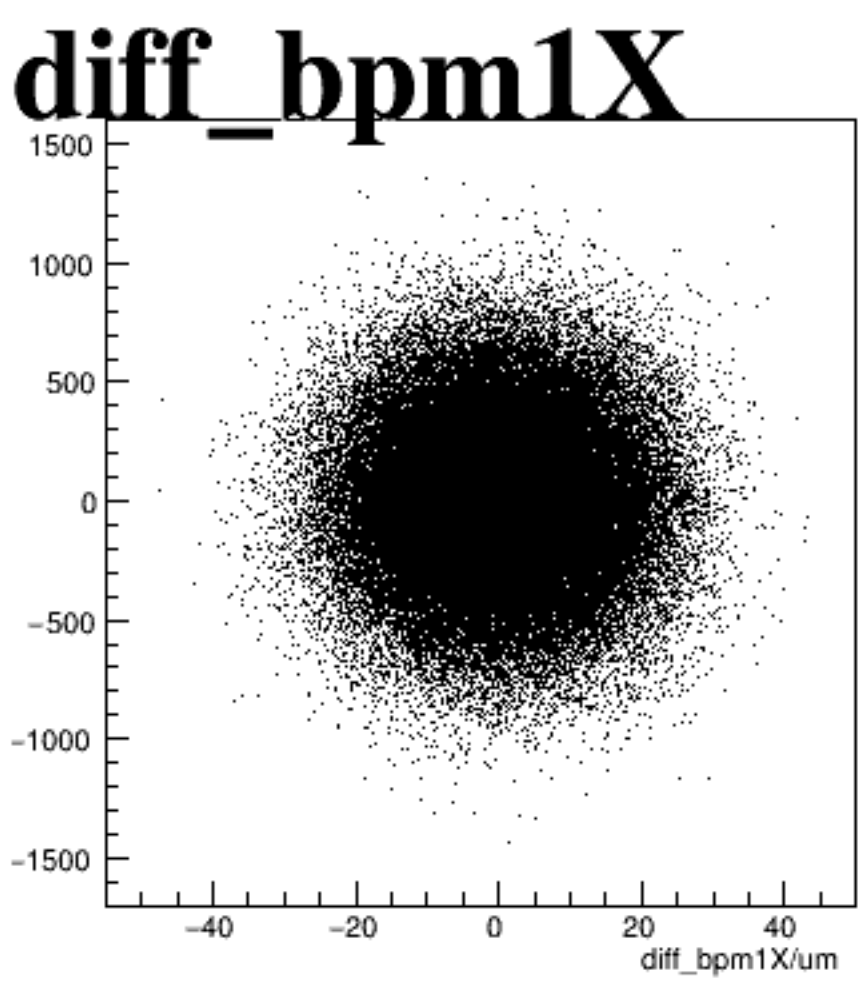
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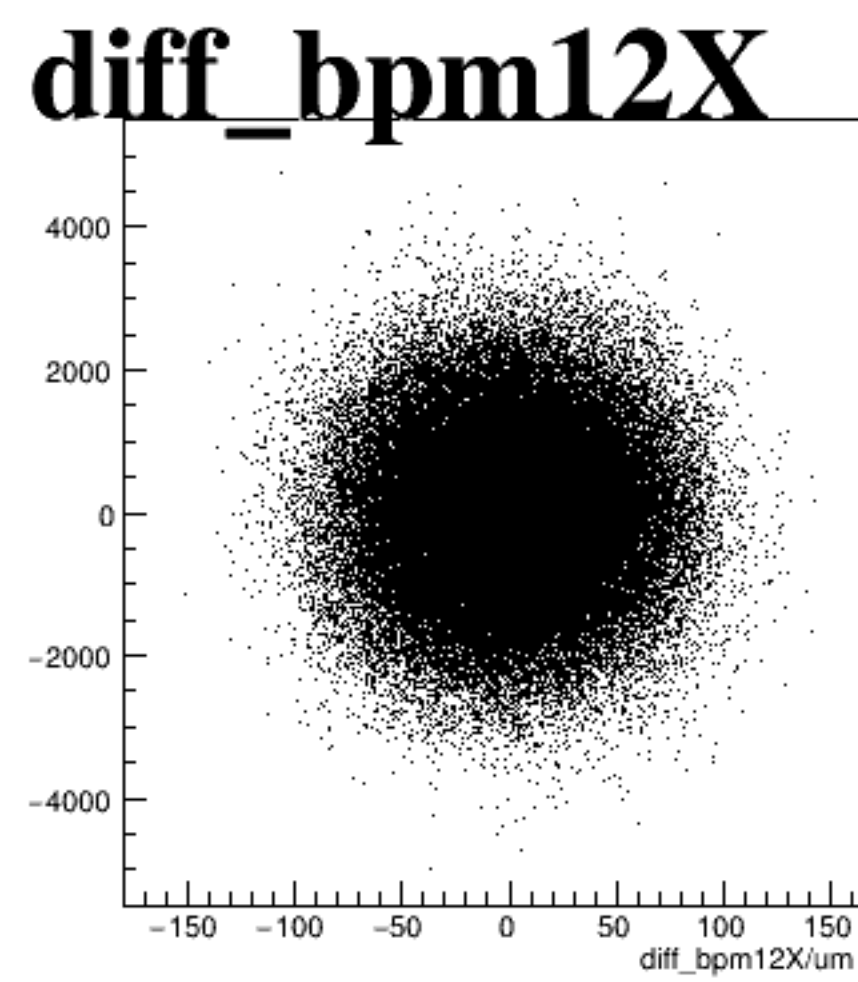
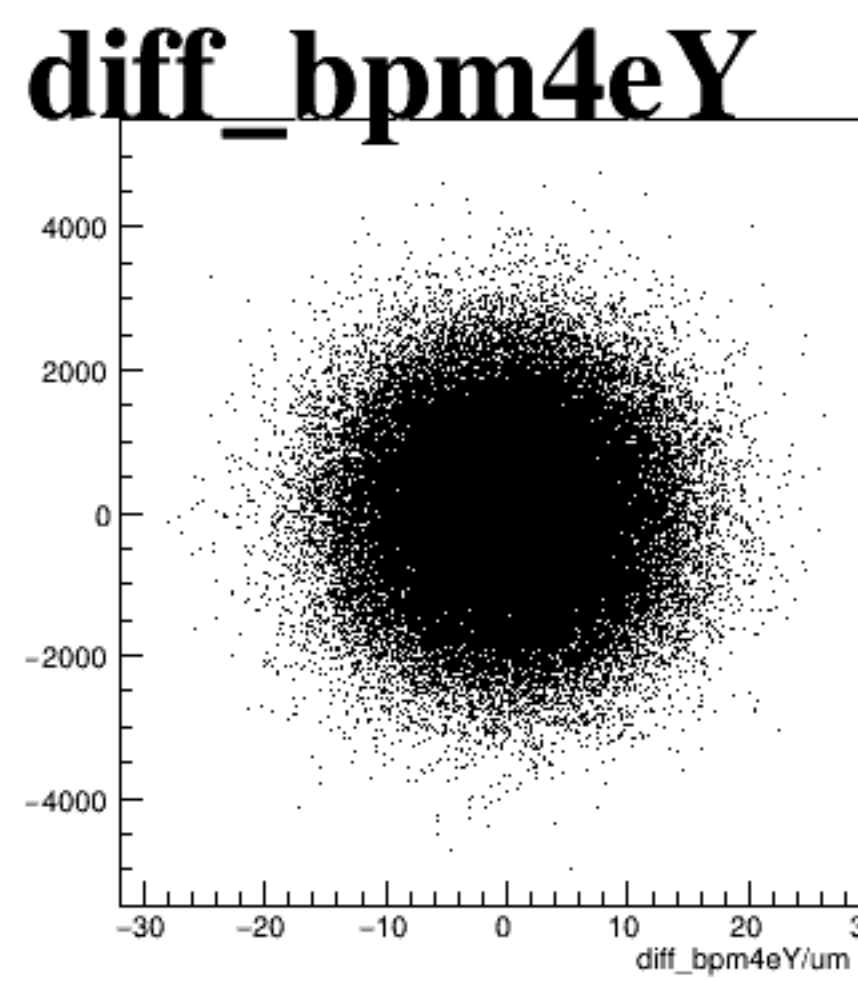
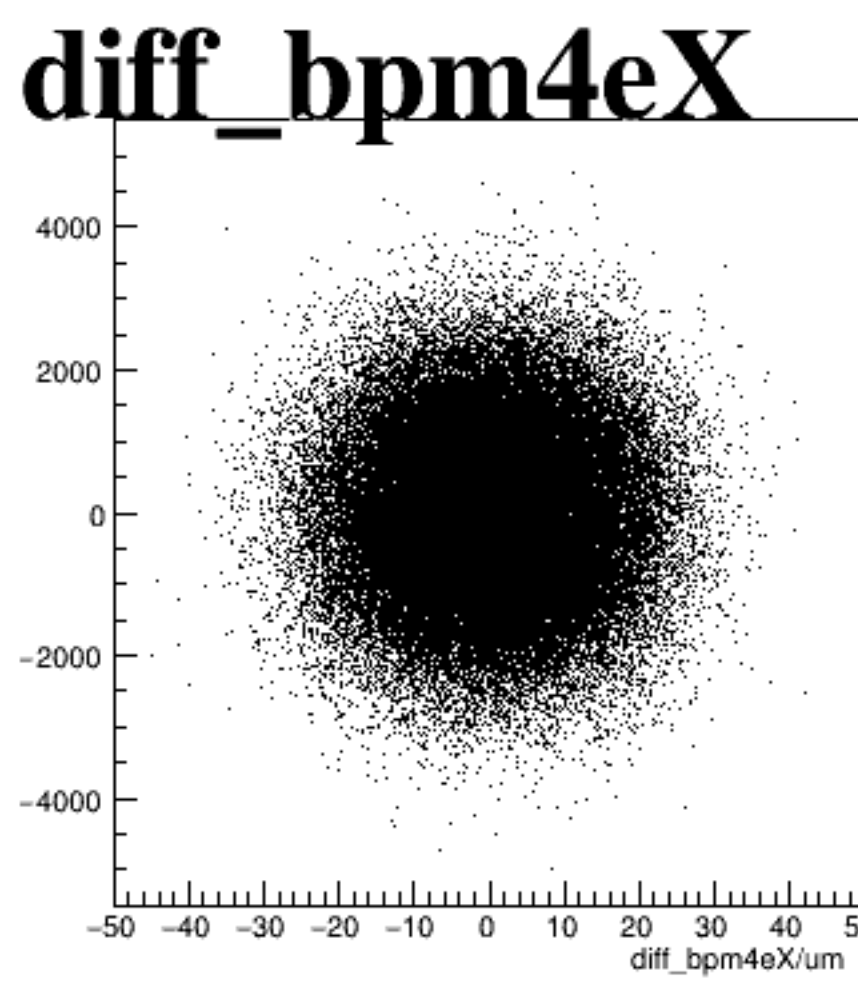
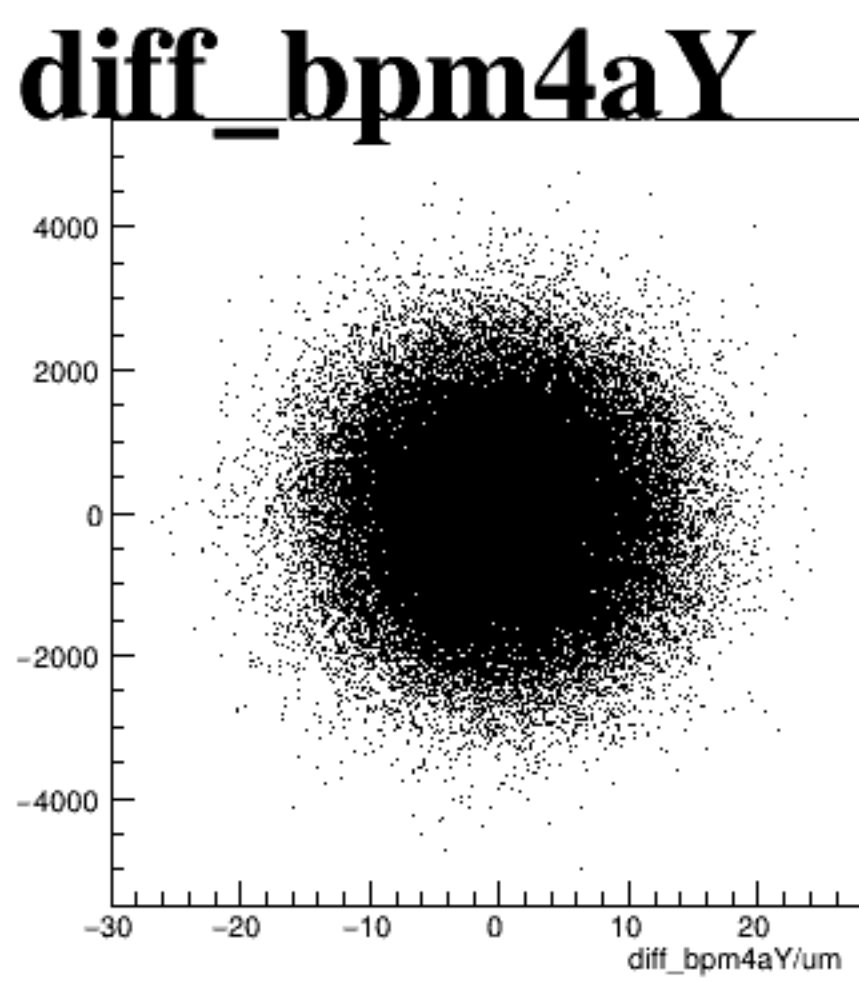
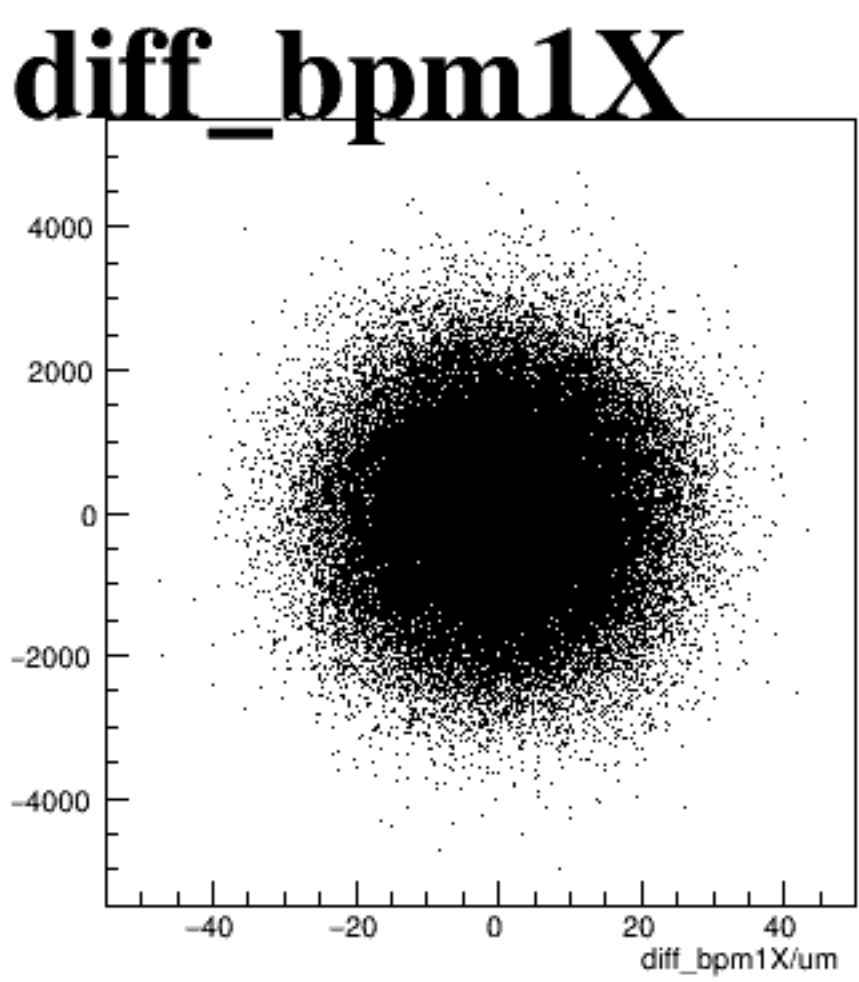
reg_asym_usl



reg_asym_dsl



reg_asym_usr



reg_asym_dsr

