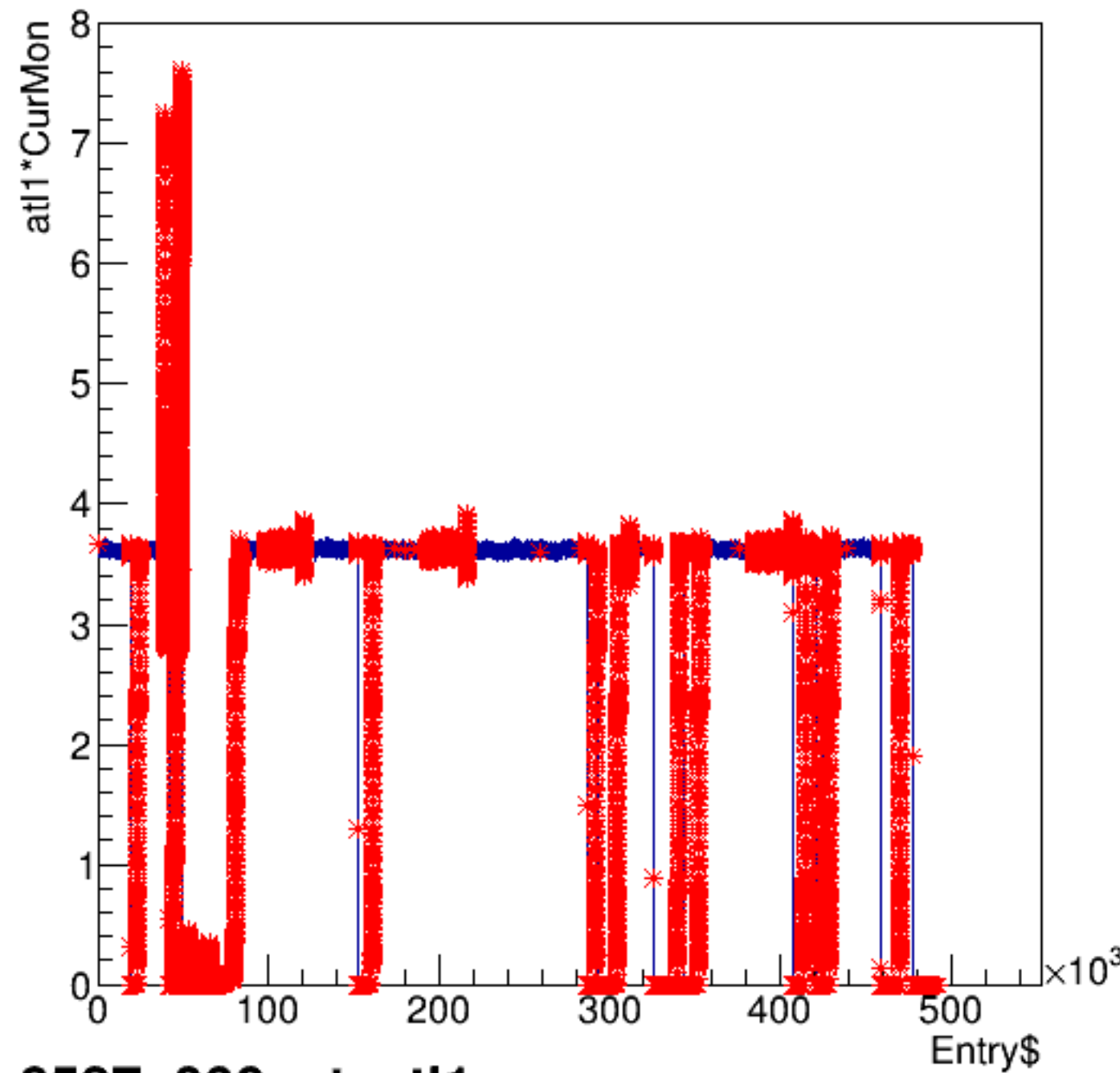
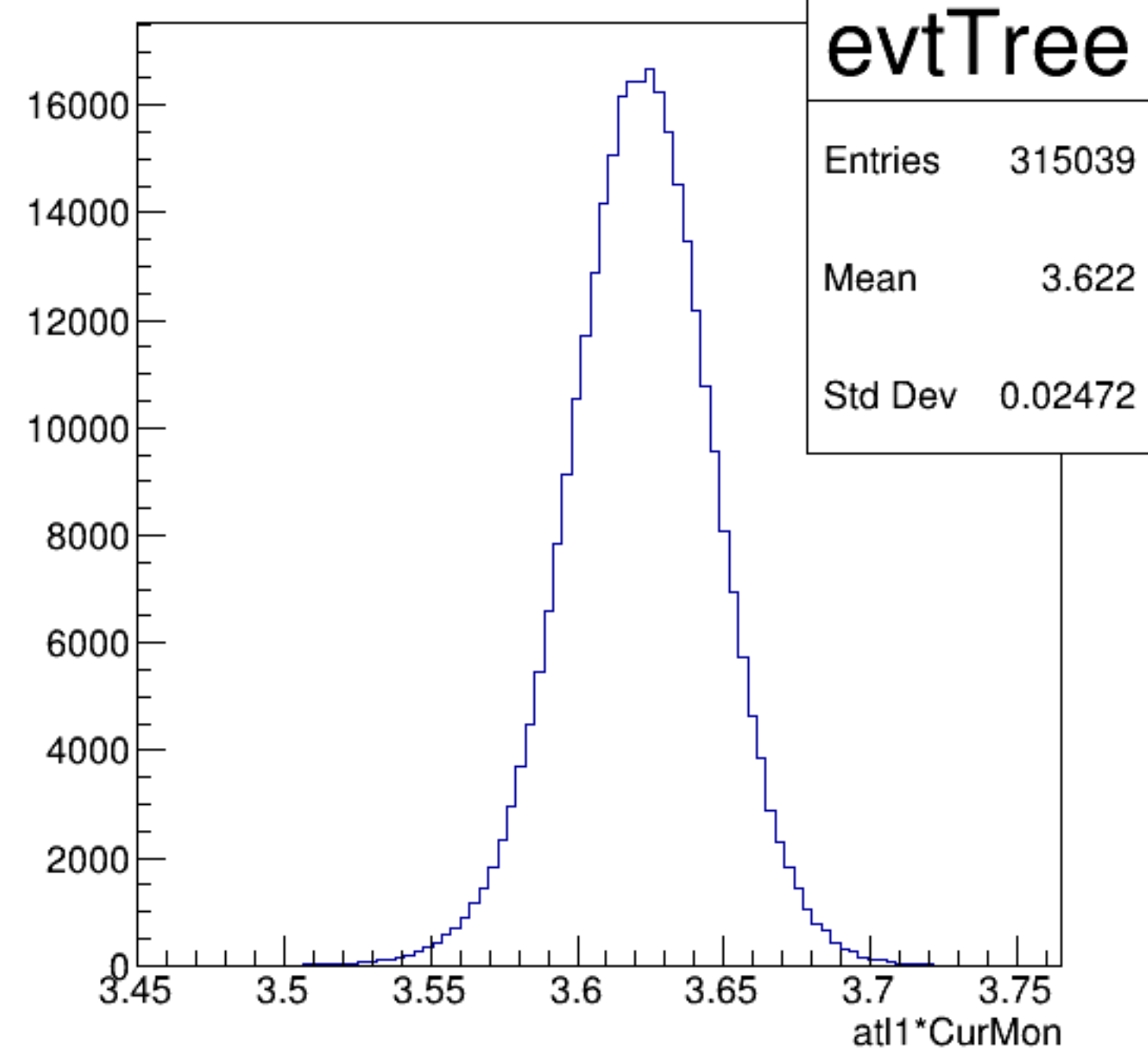


atl1*CurMon:Entry\$



run6587_000_at_atl1.png

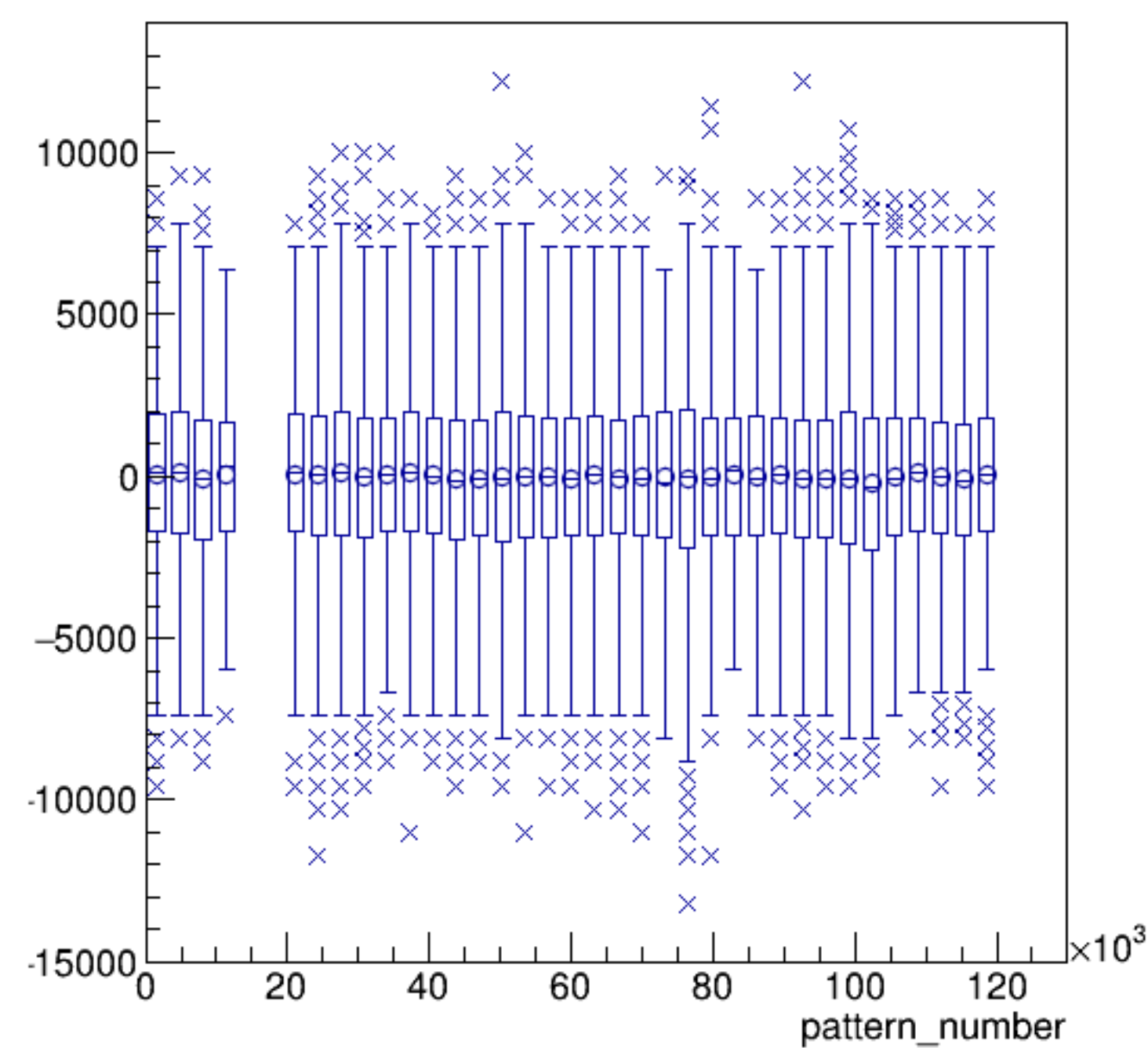
atl1*CurMon {ErrorFlag==0}



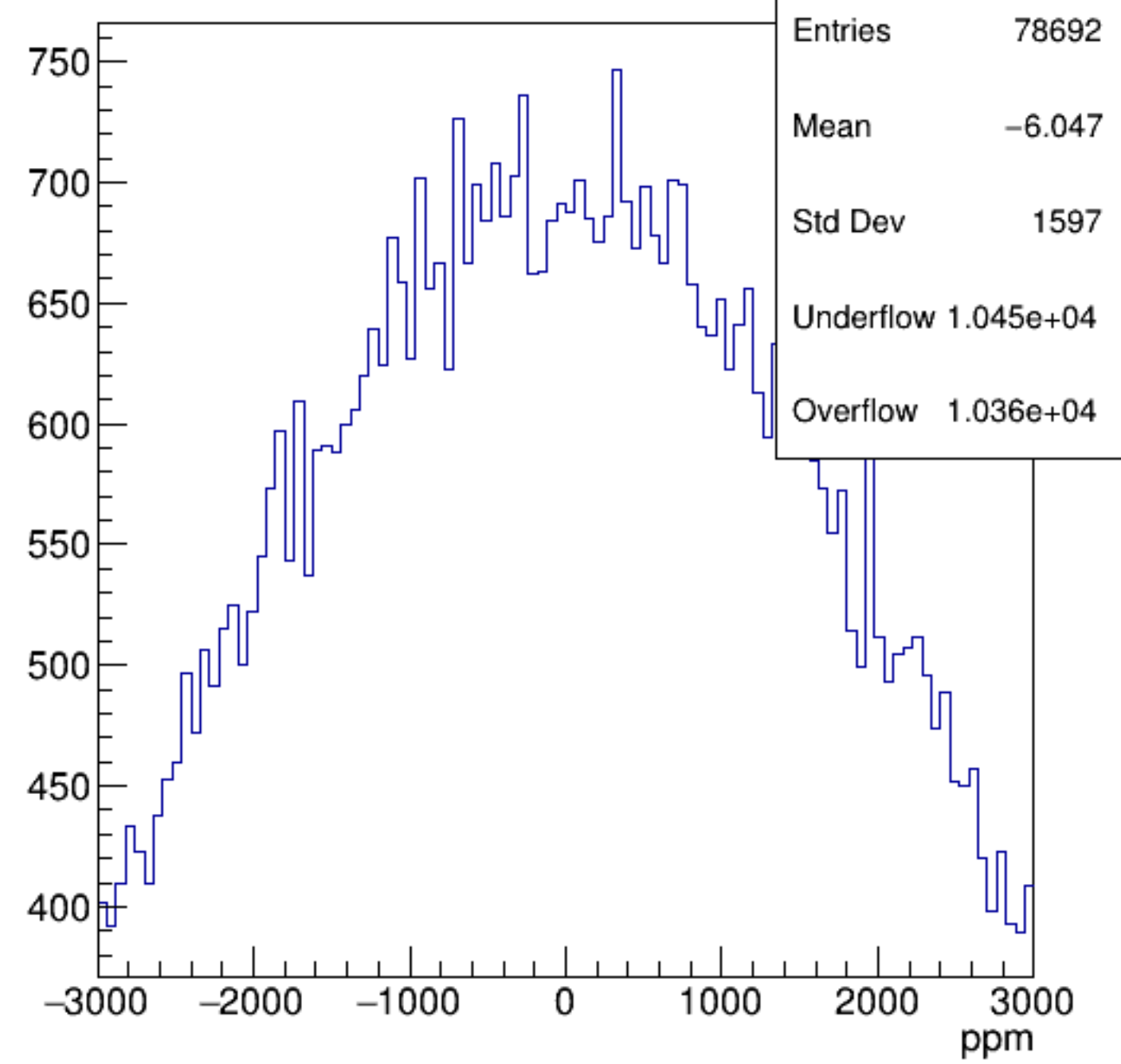
evtTree

Entries	315039
Mean	3.622
Std Dev	0.02472

asym_atl1/ppm:pattern_number {ErrorFlag==0}

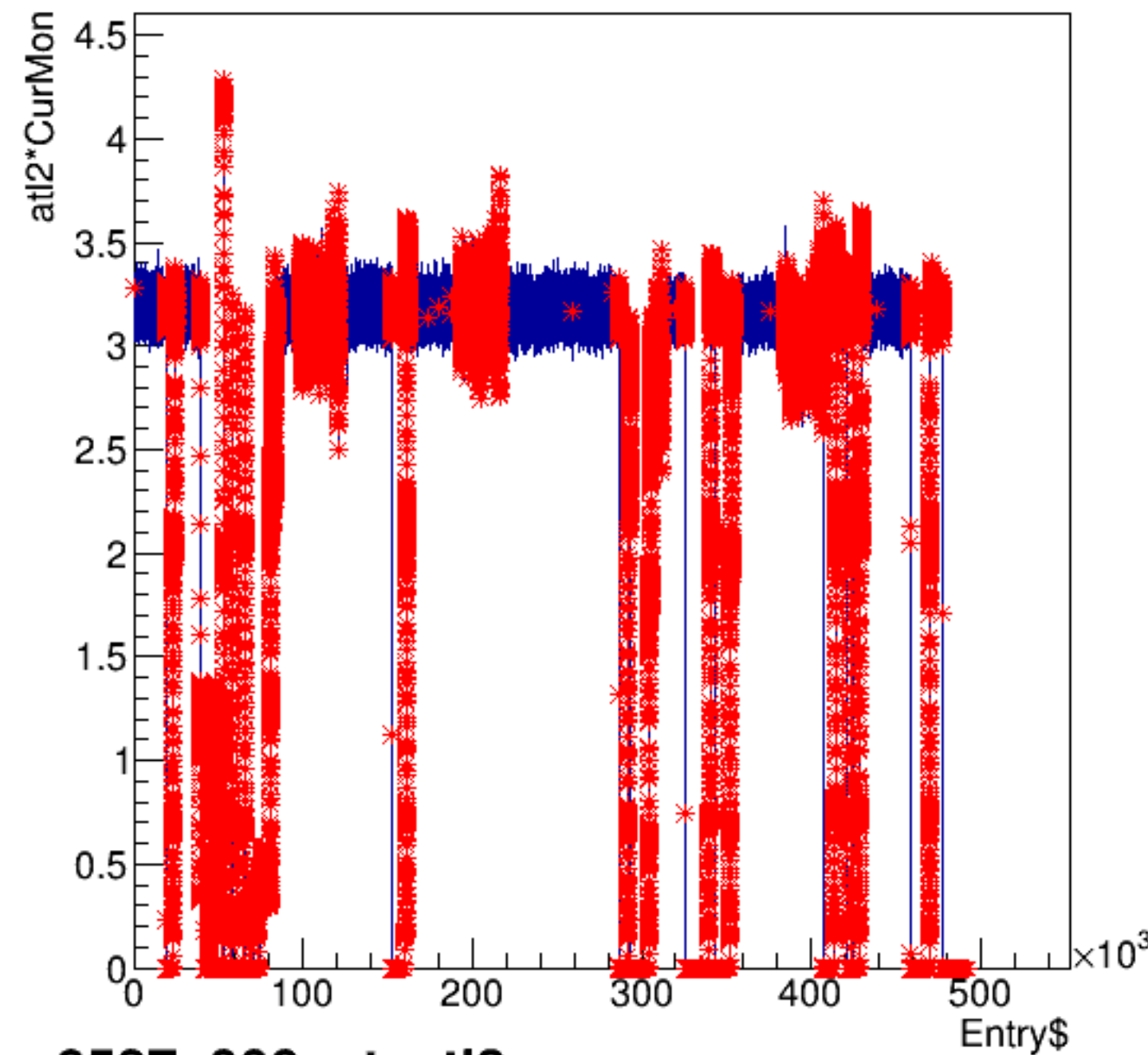


asym_atl1



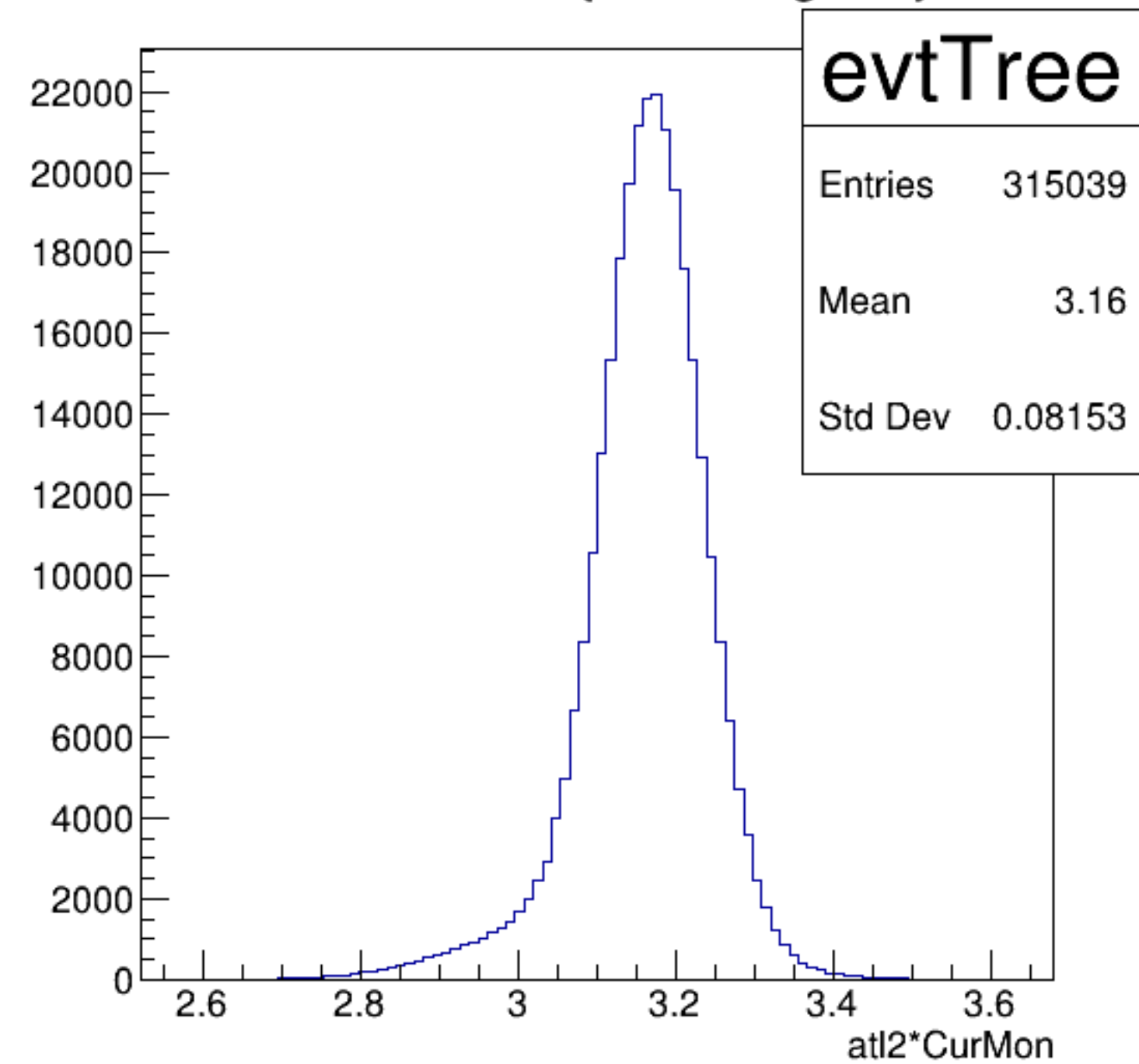
Entries	78692
Mean	-6.047
Std Dev	1597
Underflow	1.045e+04
Overflow	1.036e+04

atl2*CurMon:Entry\$

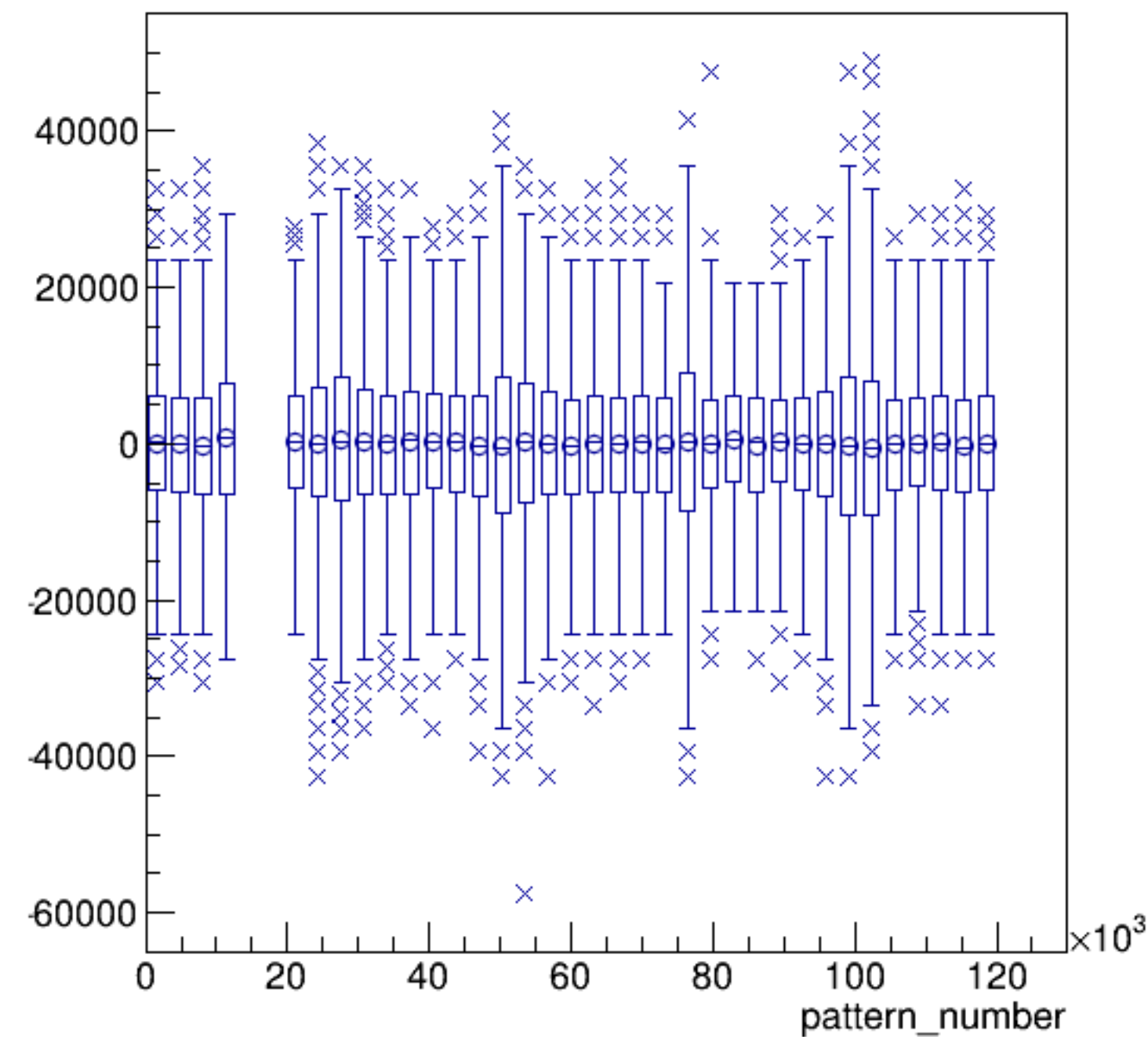


run6587_000_at_atl2.png

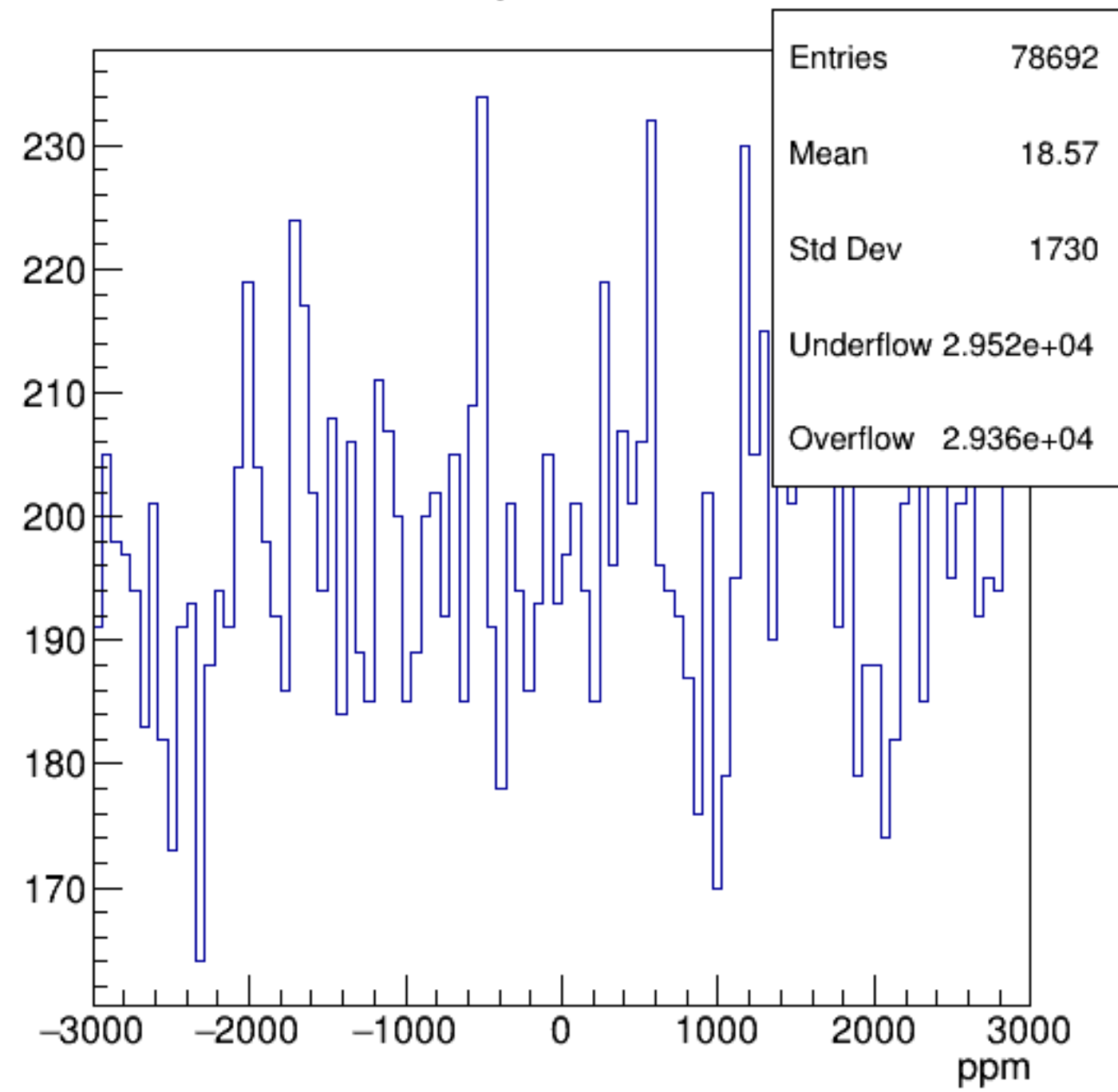
atl2*CurMon {ErrorFlag==0}



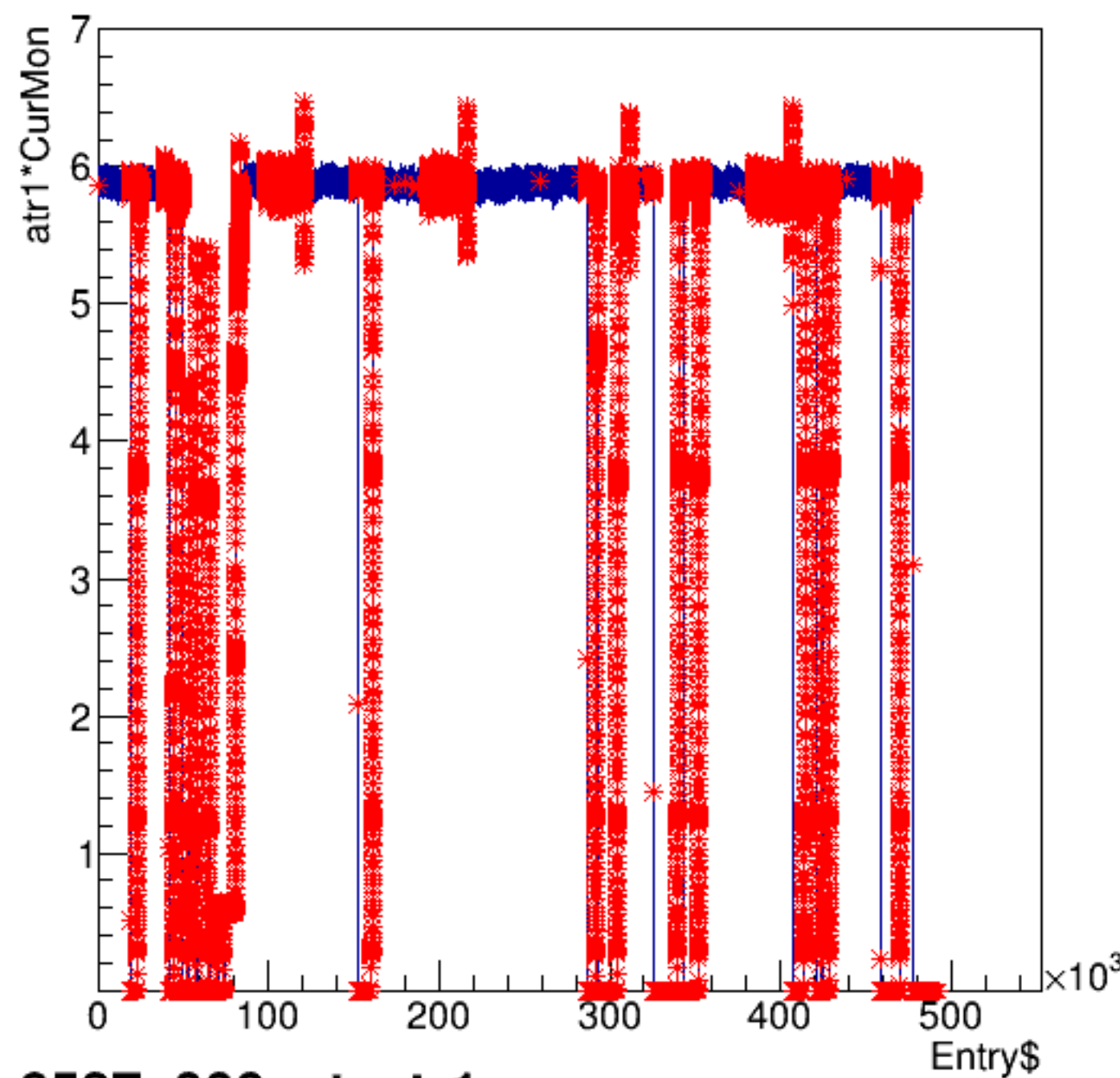
asym_atl2/ppm:pattern_number {ErrorFlag==0}



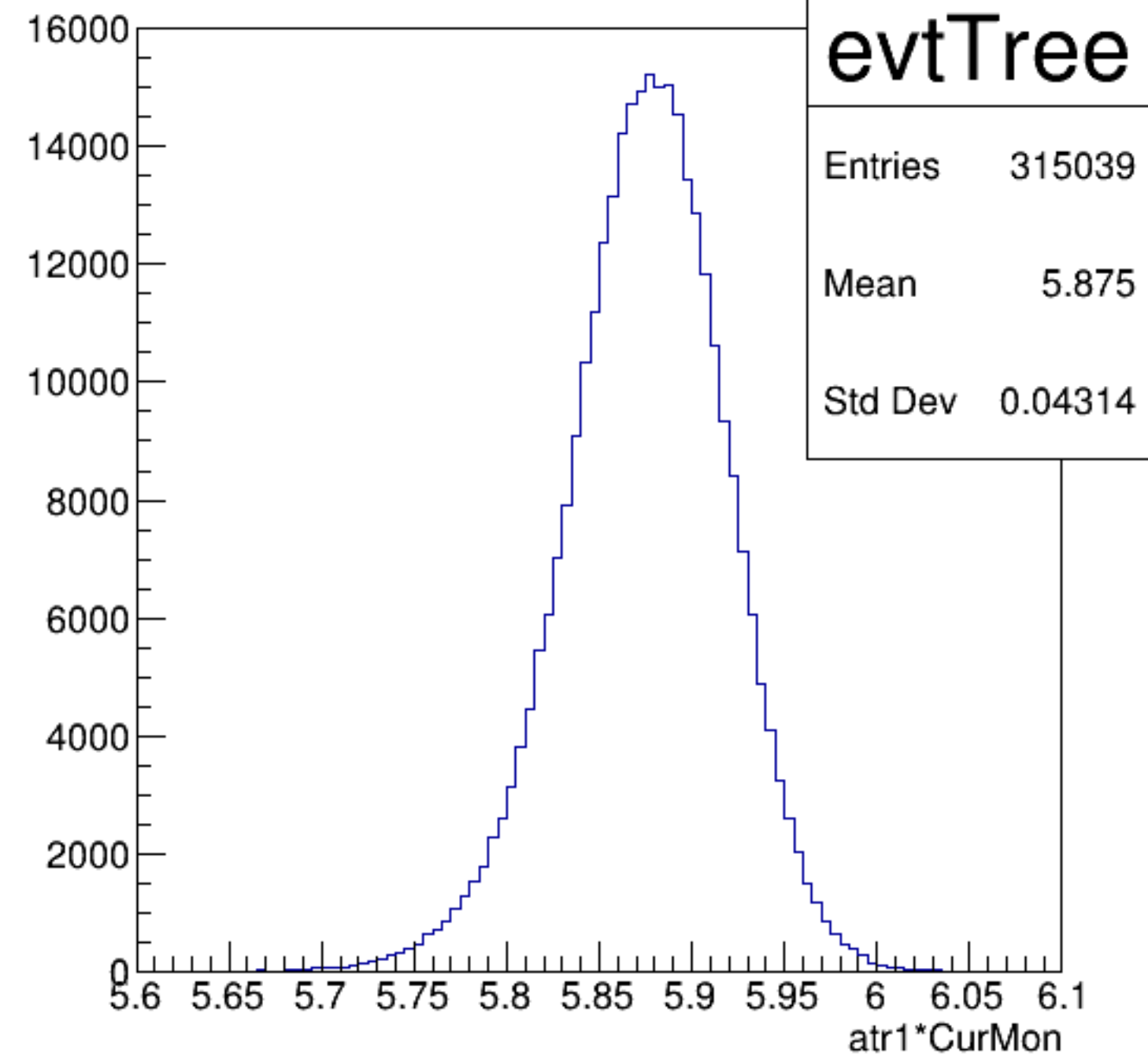
asym_atl2



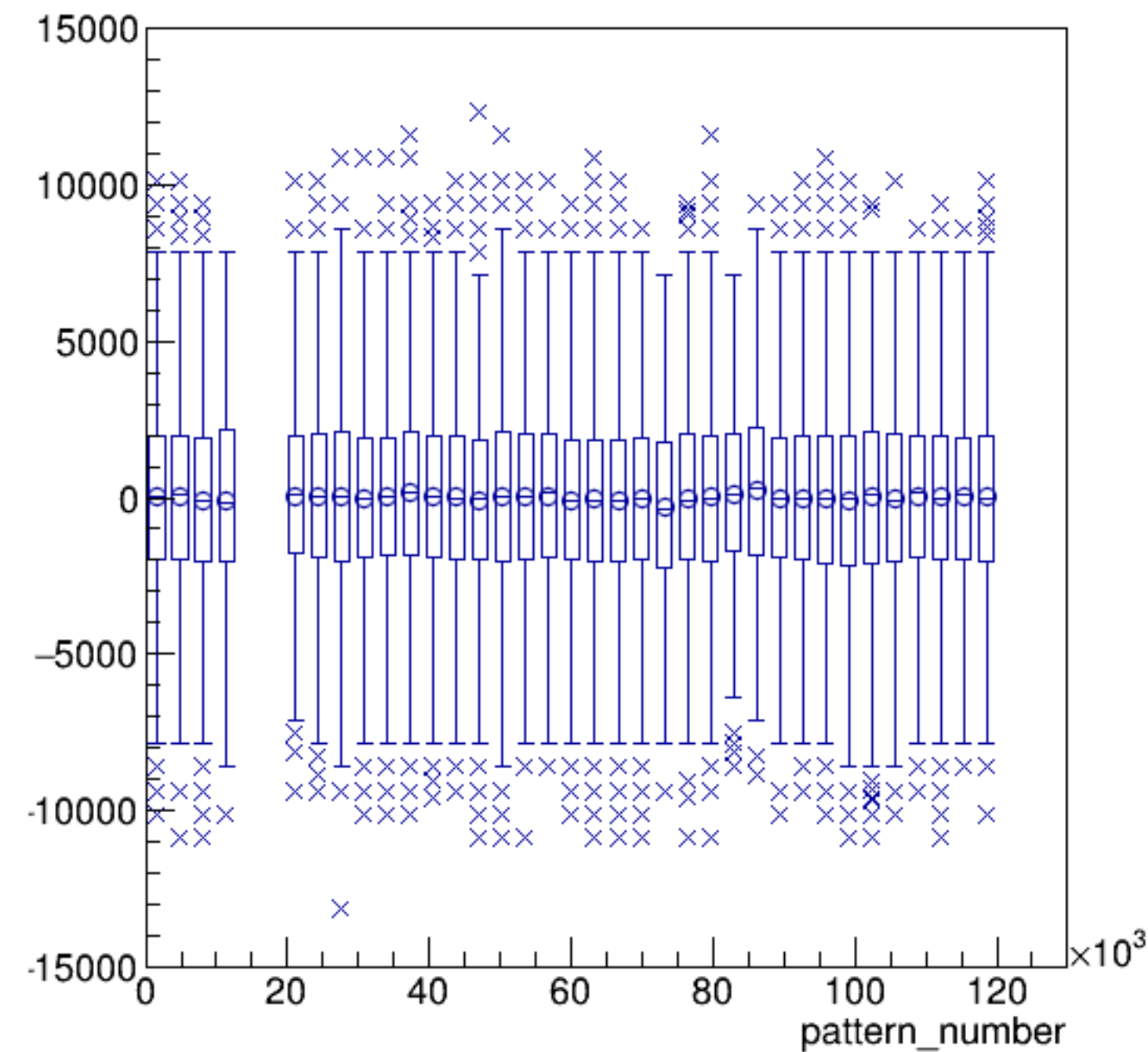
atr1*CurMon:Entry\$



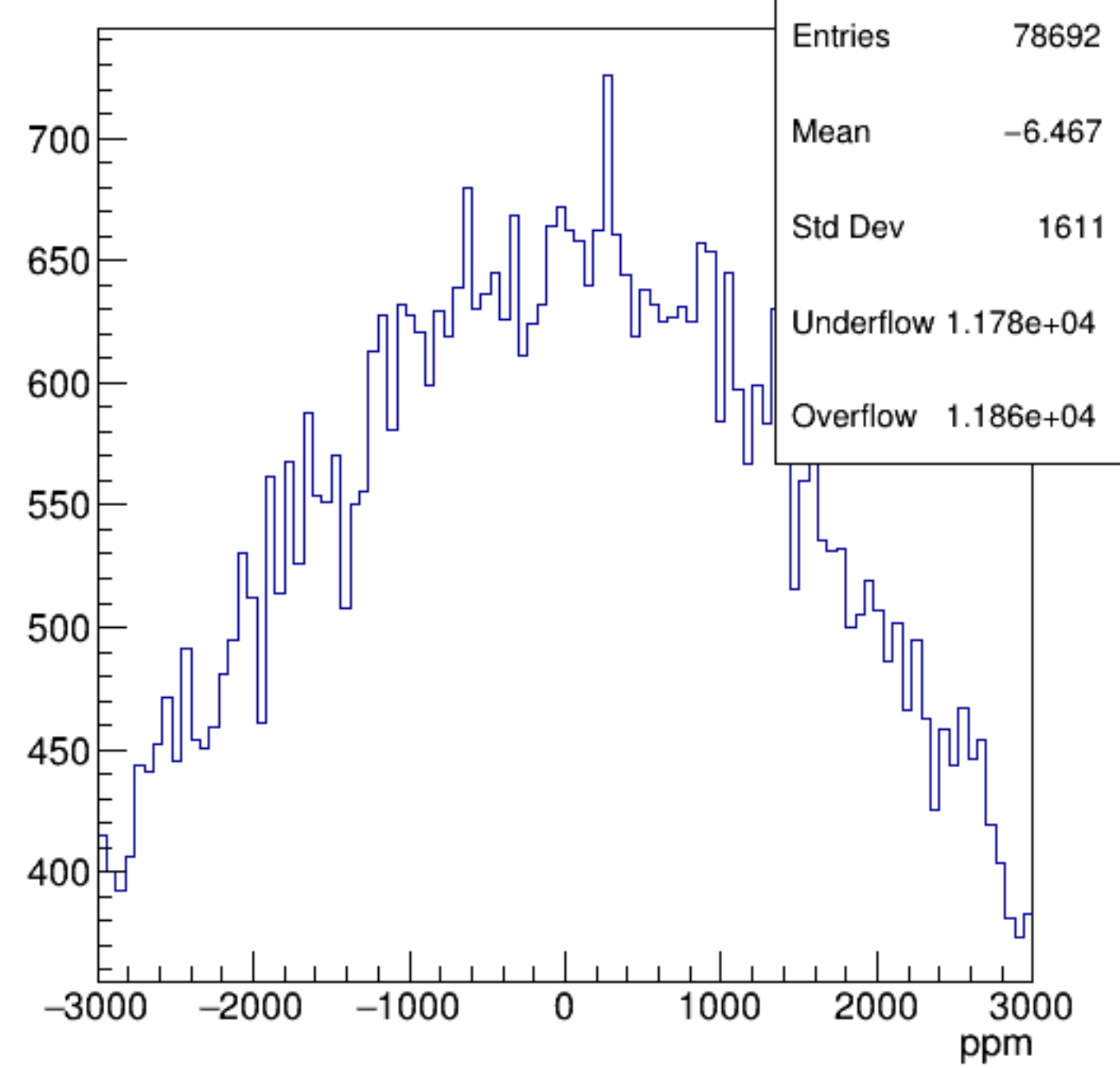
atr1*CurMon {ErrorFlag==0}



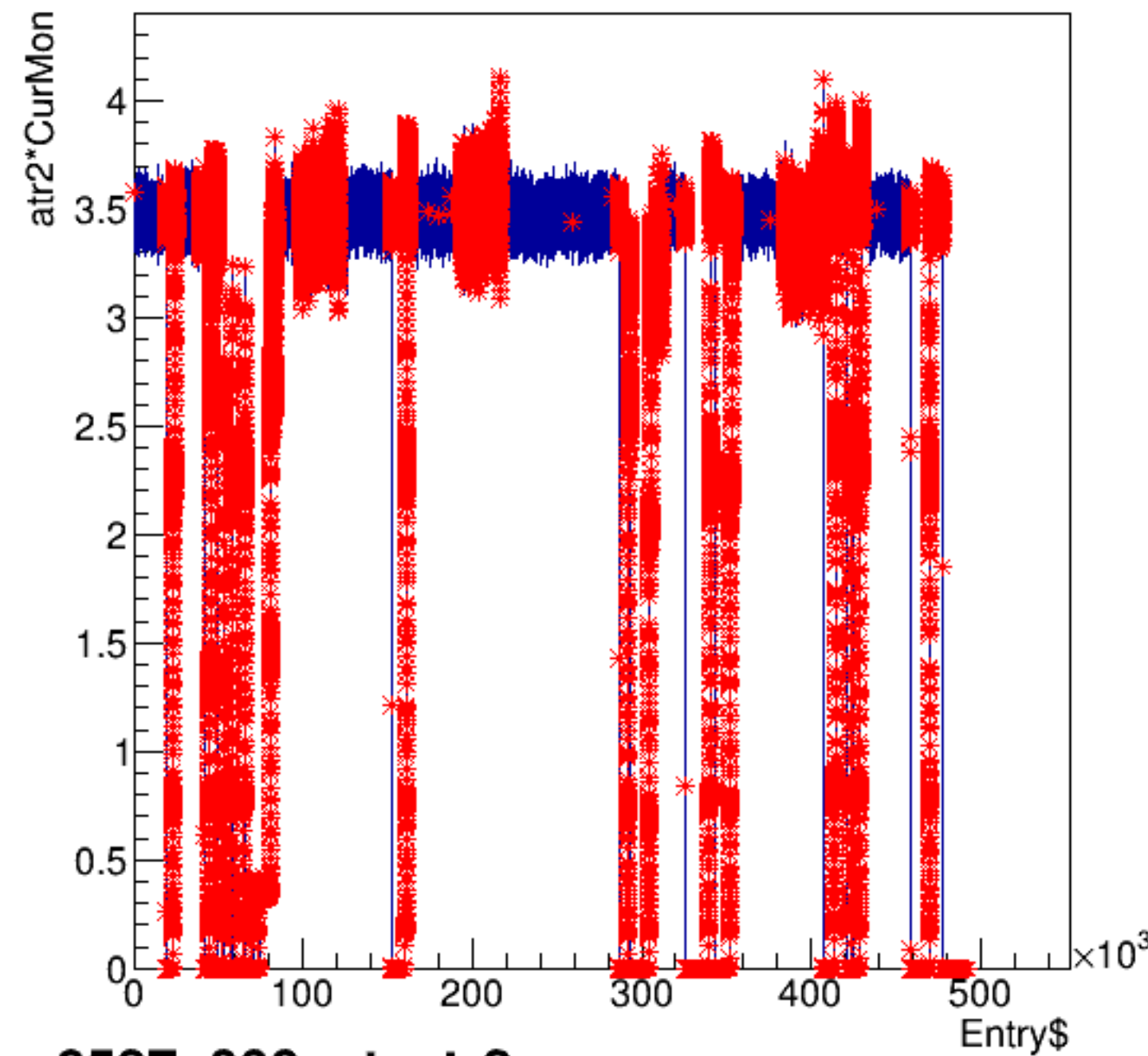
asym_atr1/ppm:pattern_number {ErrorFlag==0}



asym_atr1

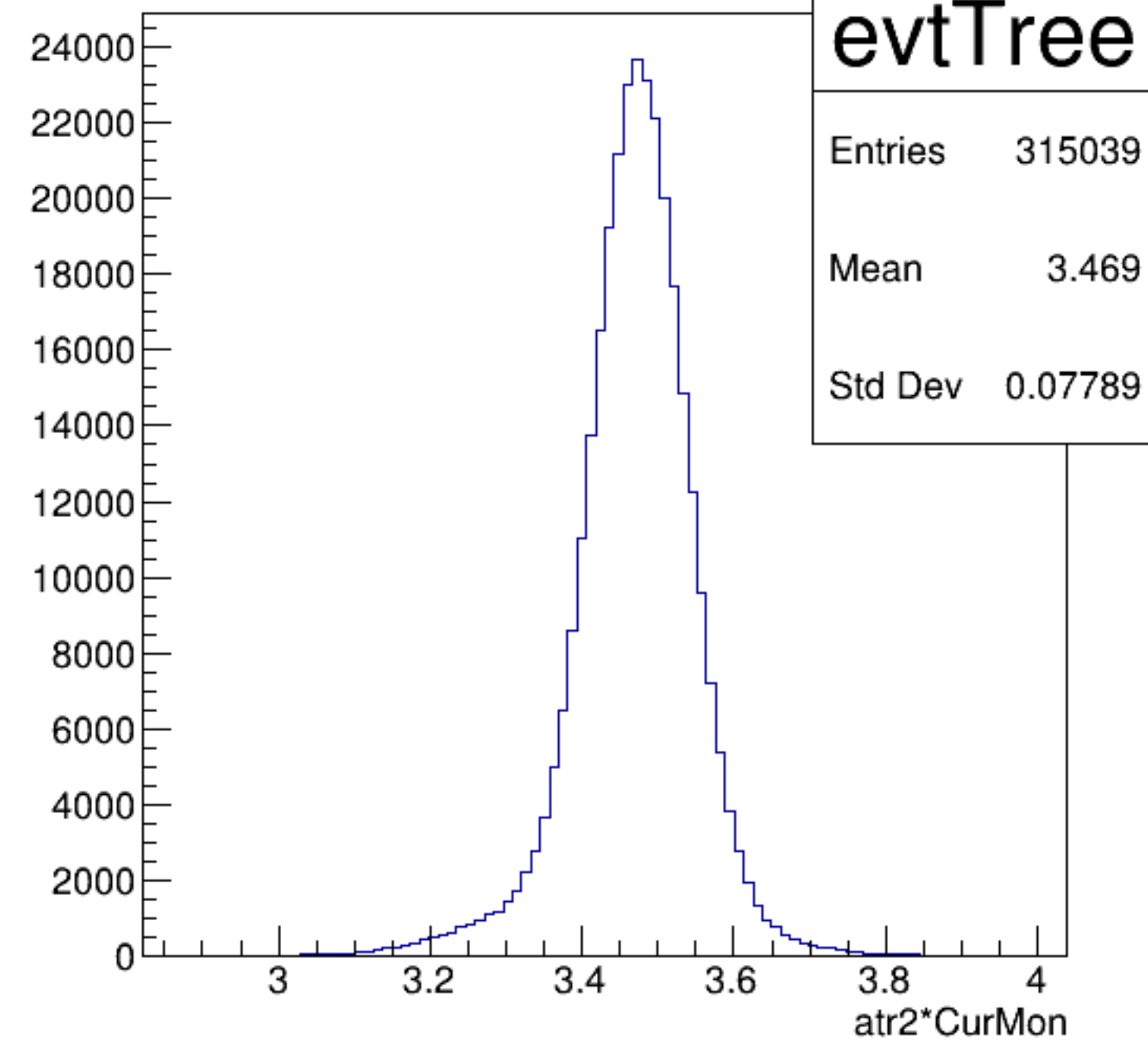


atr2*CurMon:Entry\$

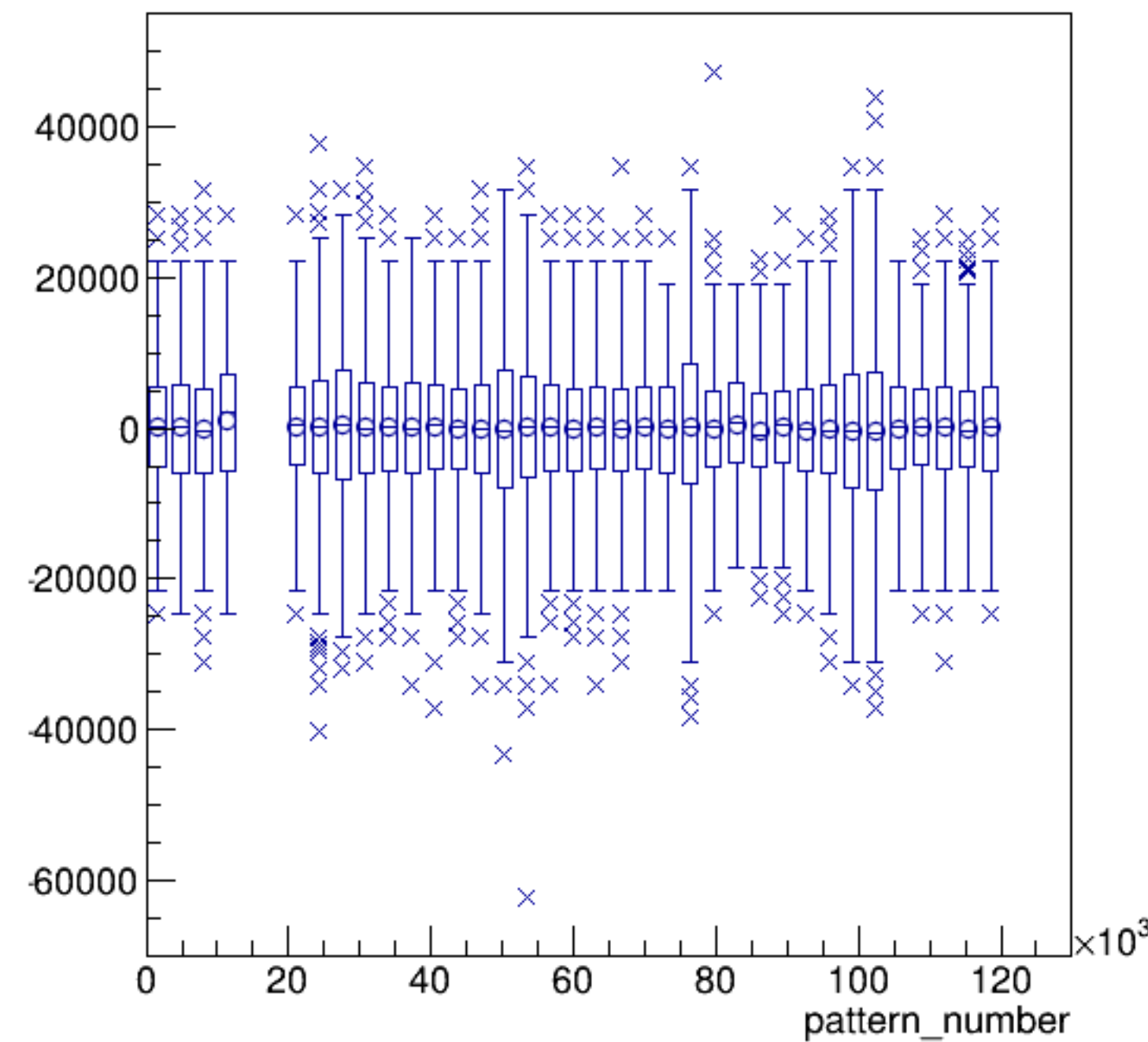


run6587_000_at_atr2.png

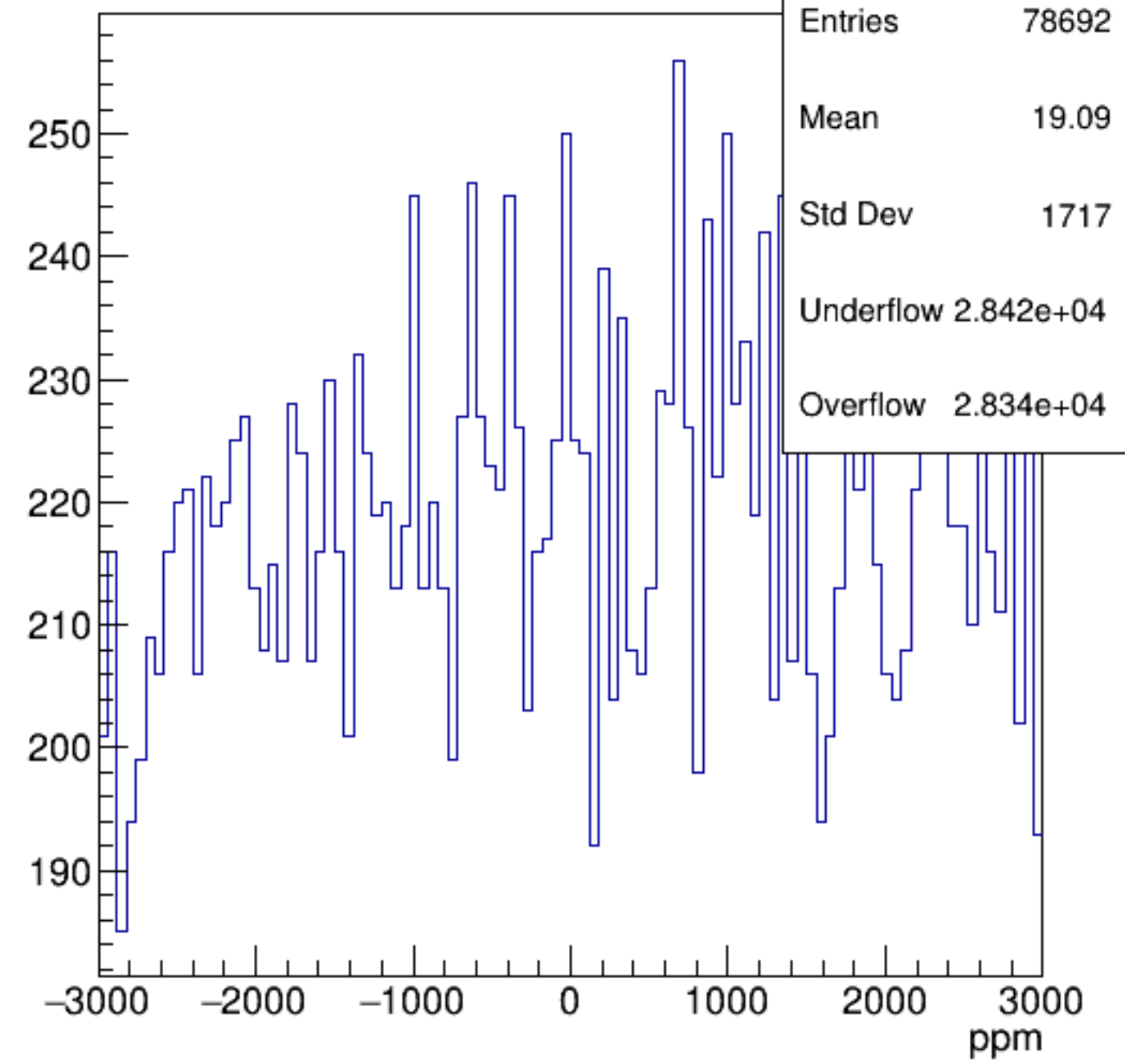
atr2*CurMon {ErrorFlag==0}



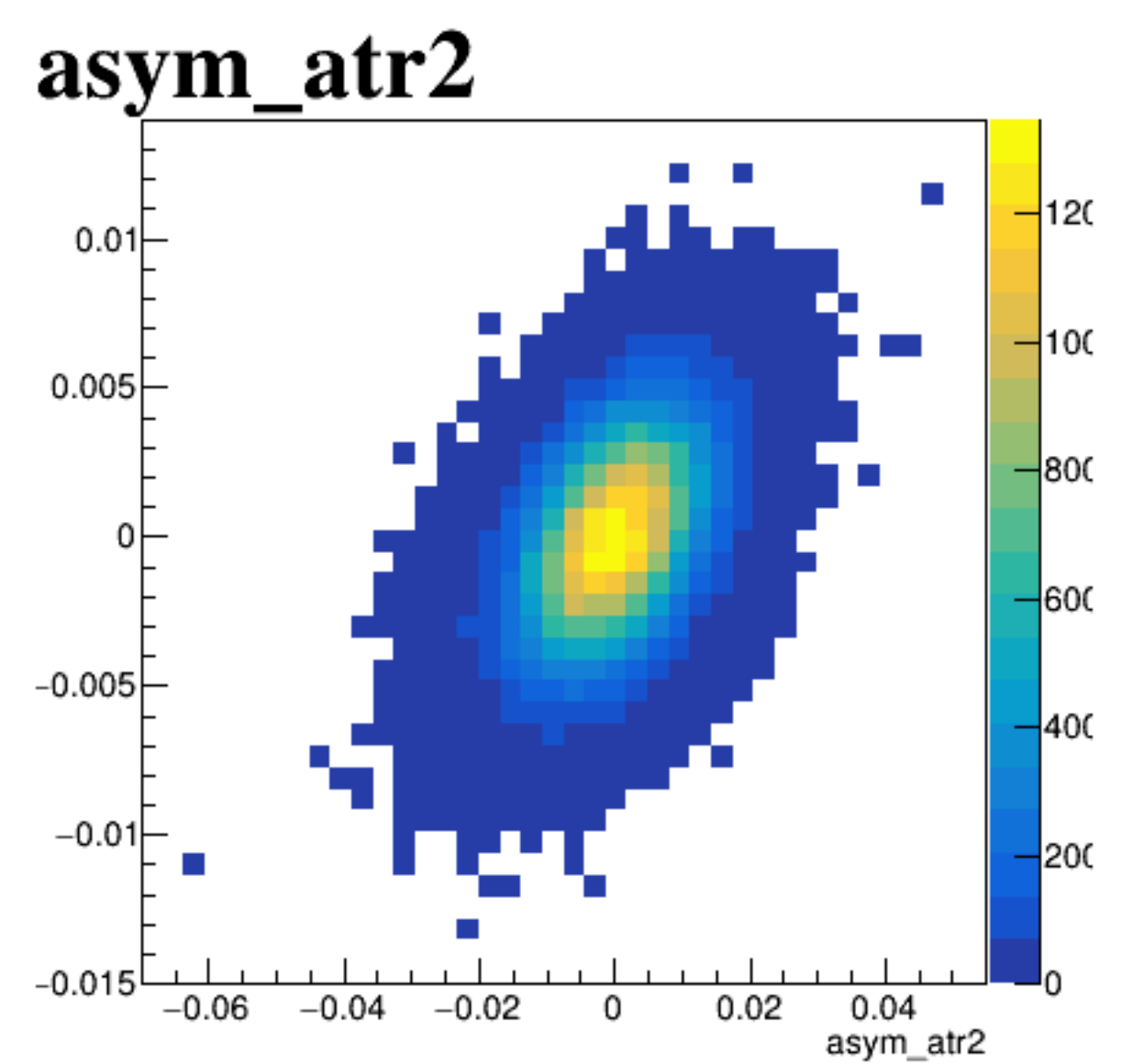
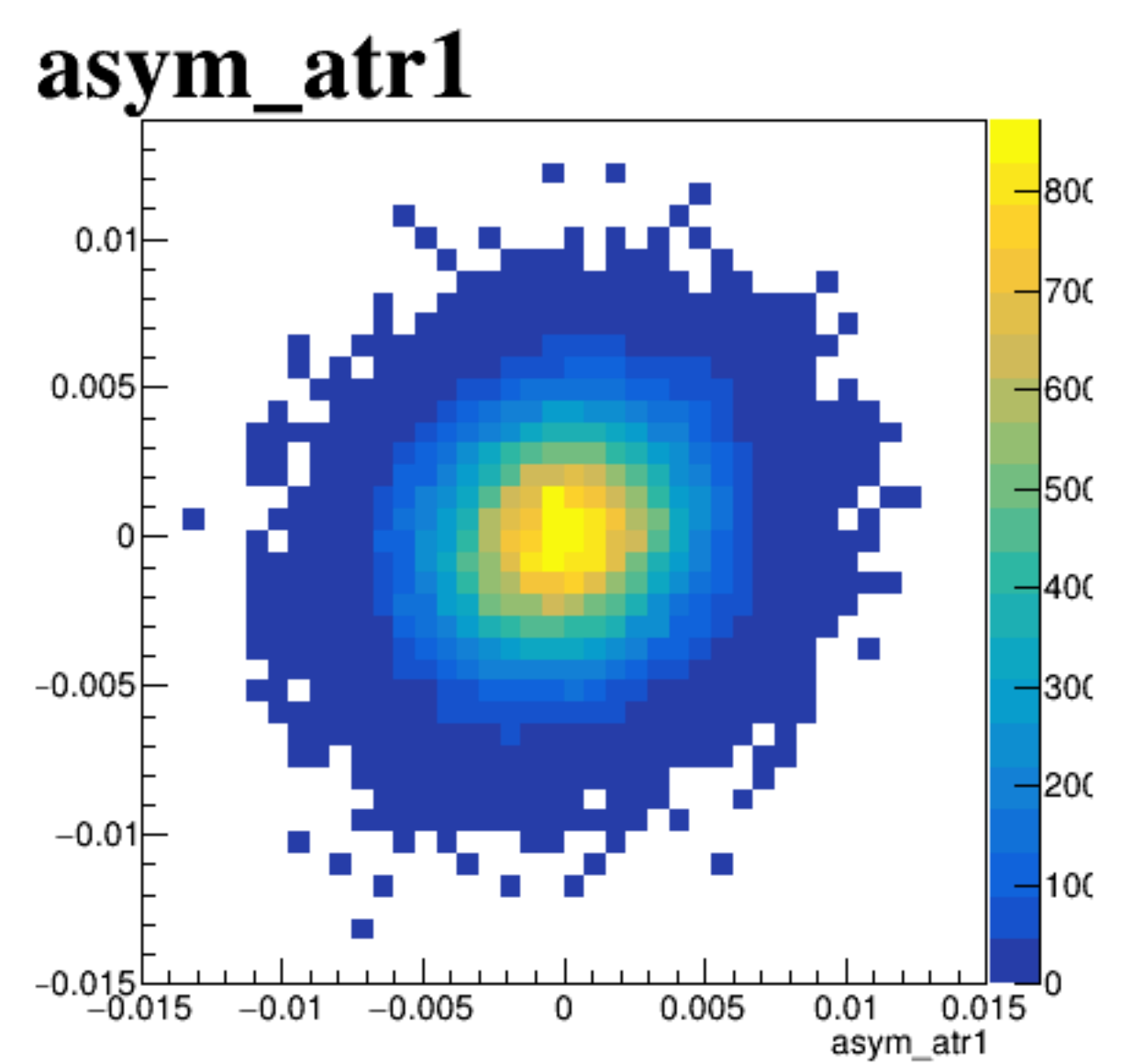
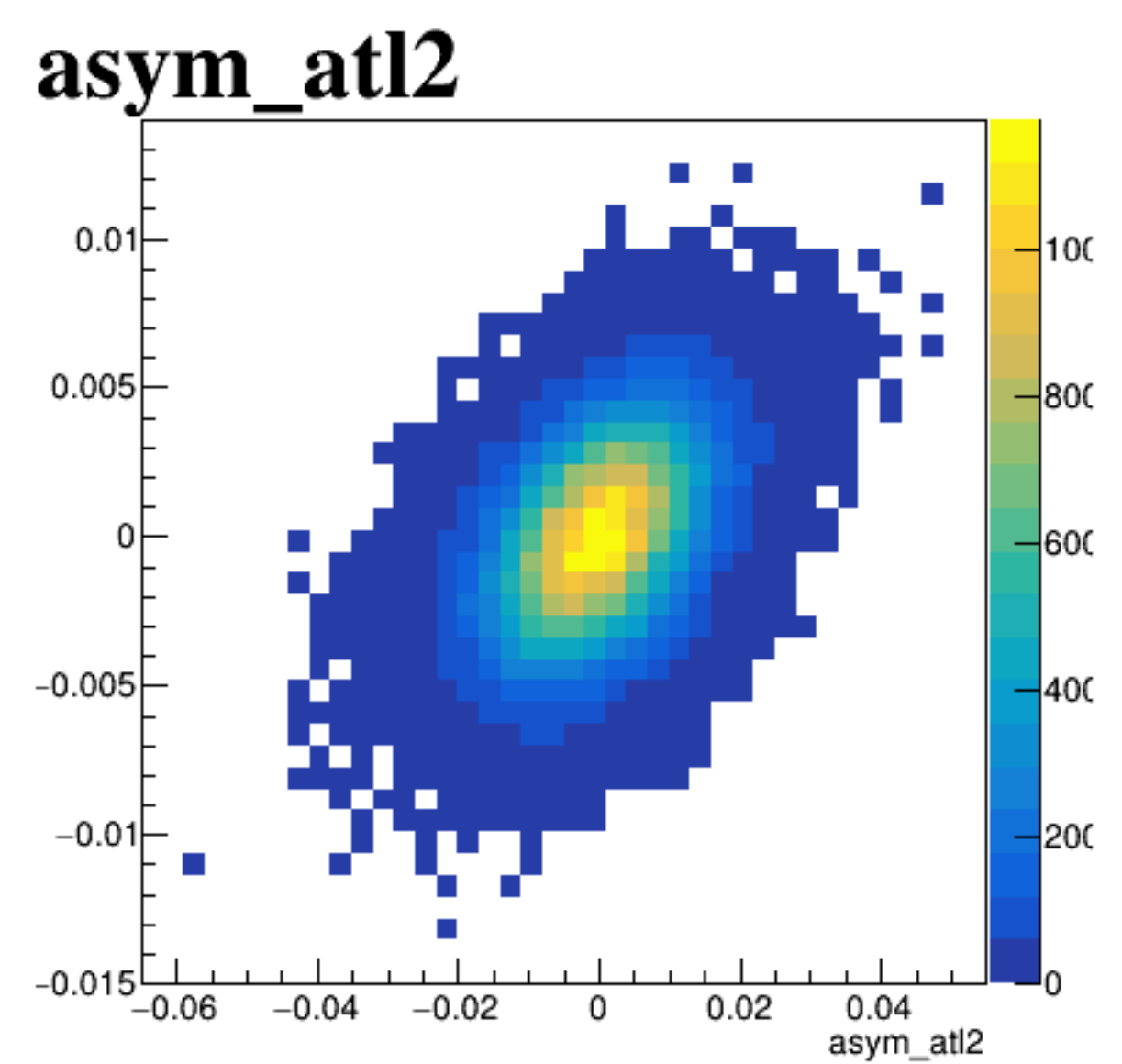
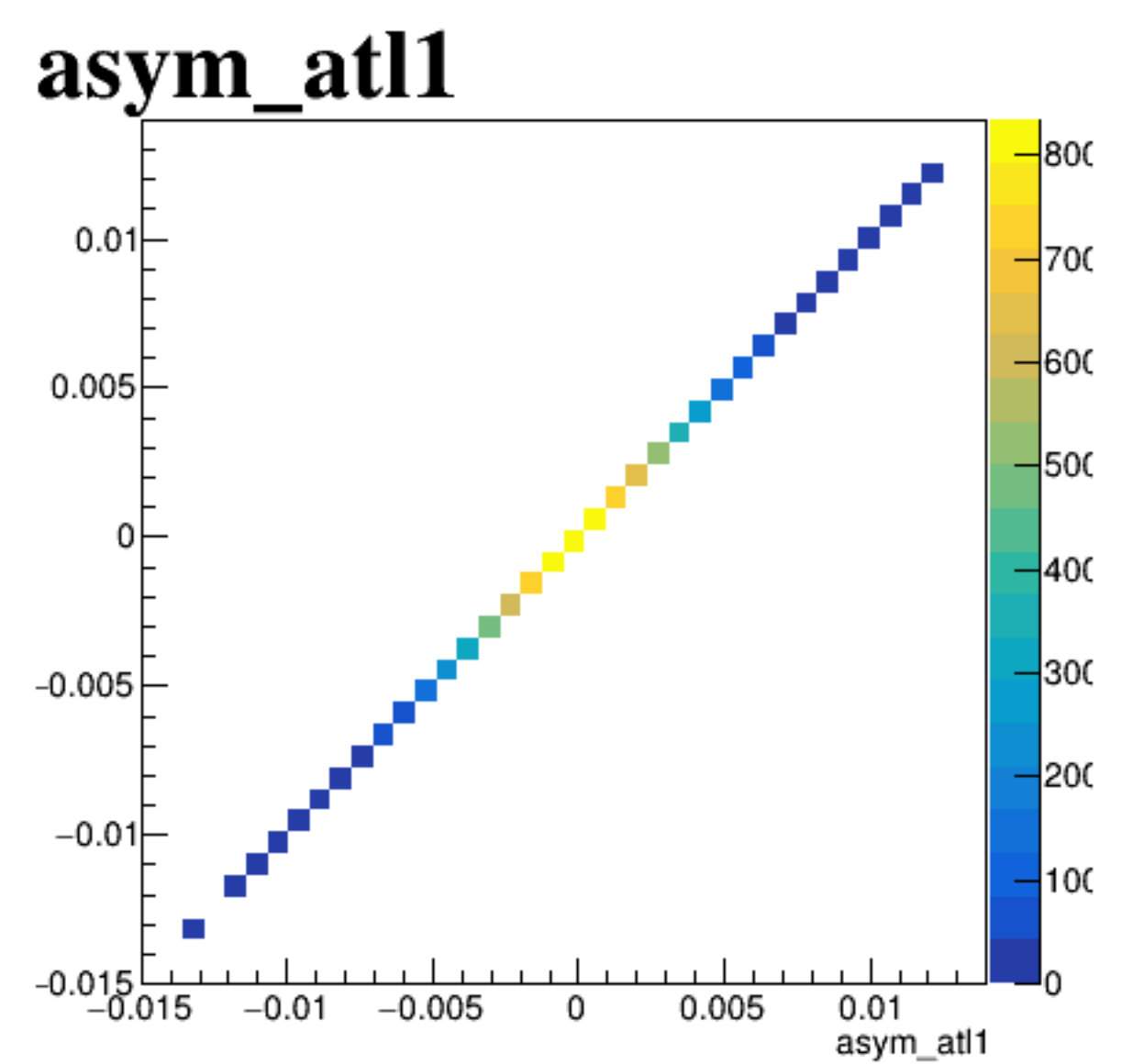
asym_atr2/ppm:pattern_number {ErrorFlag==0}



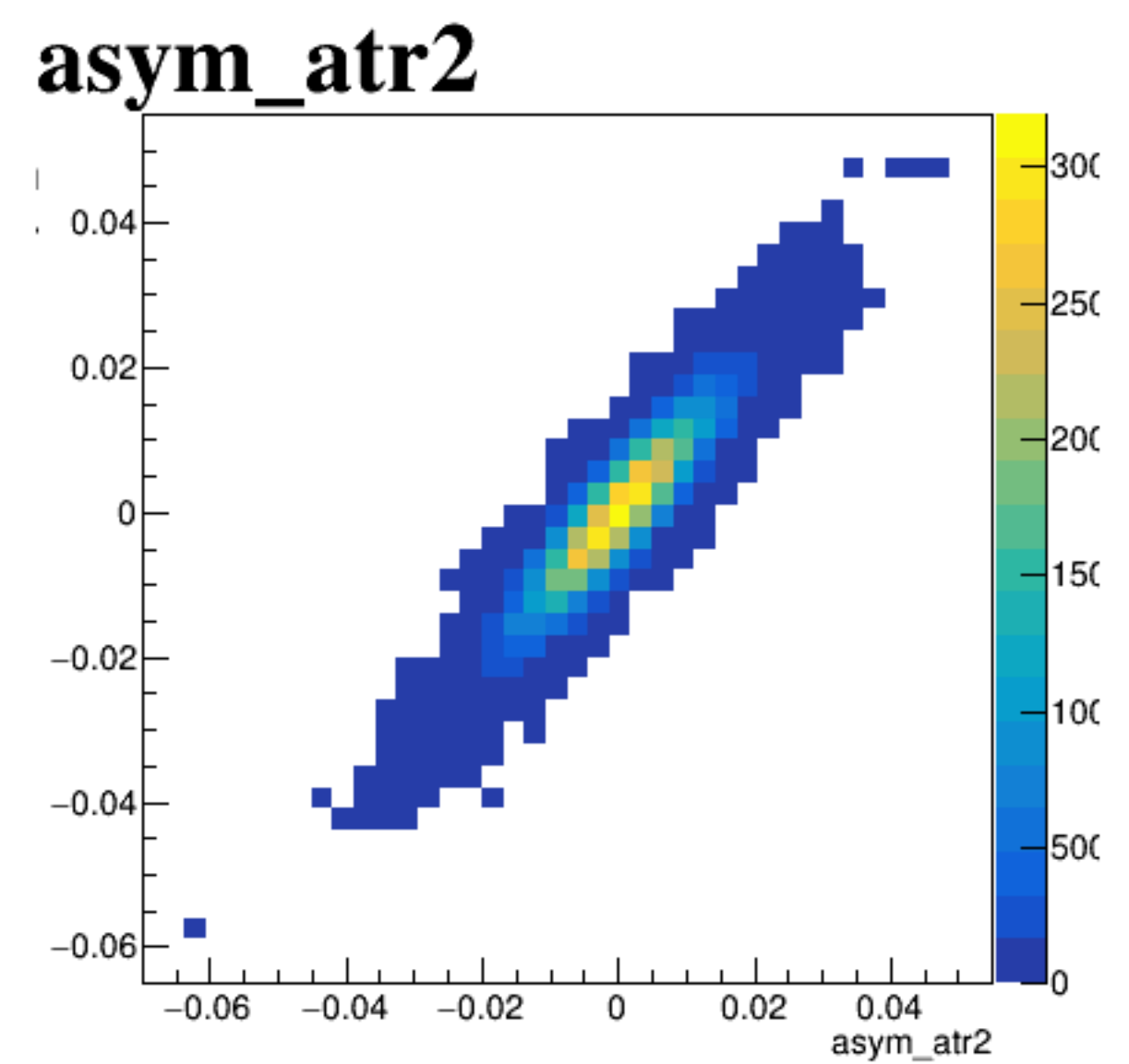
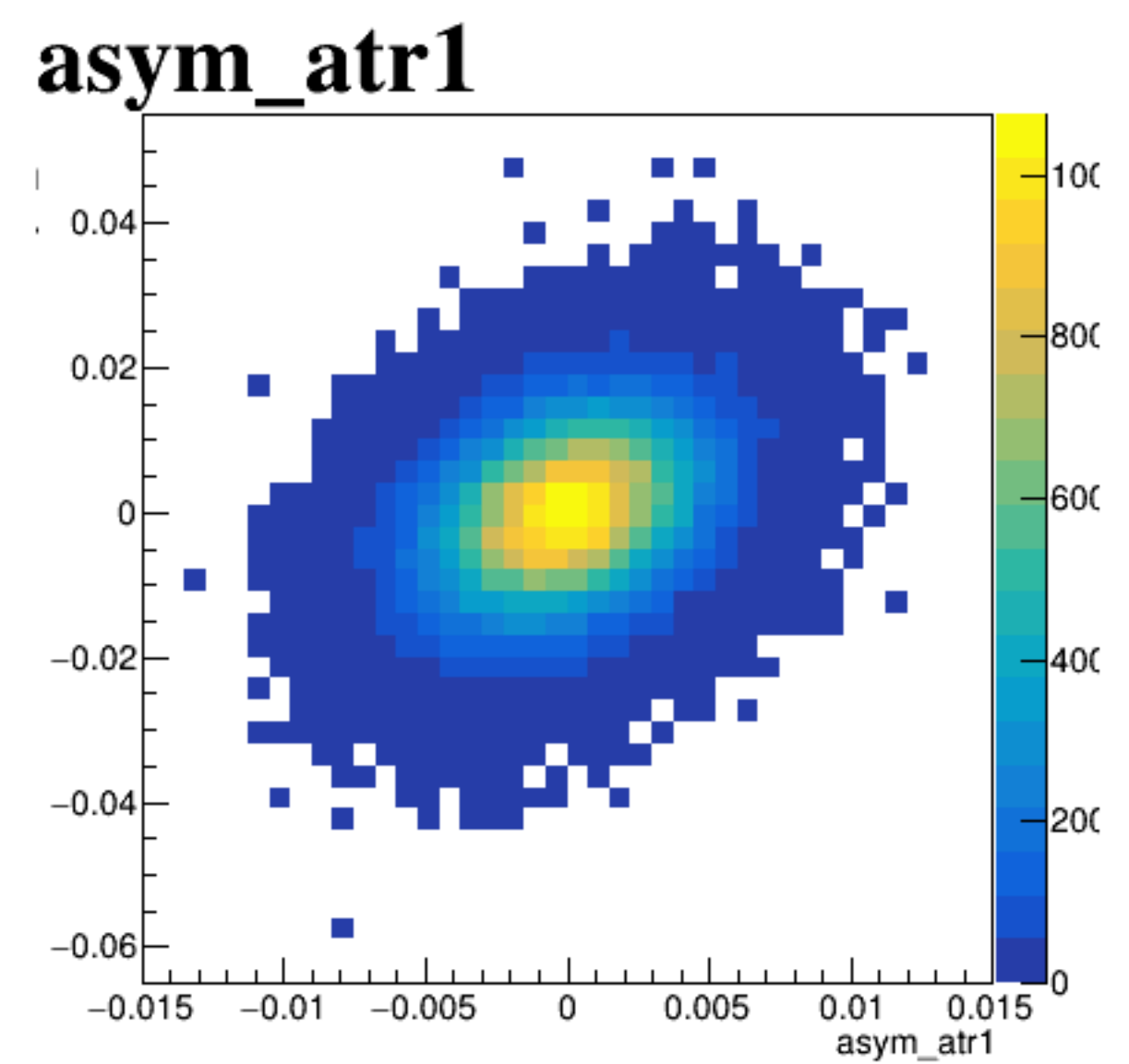
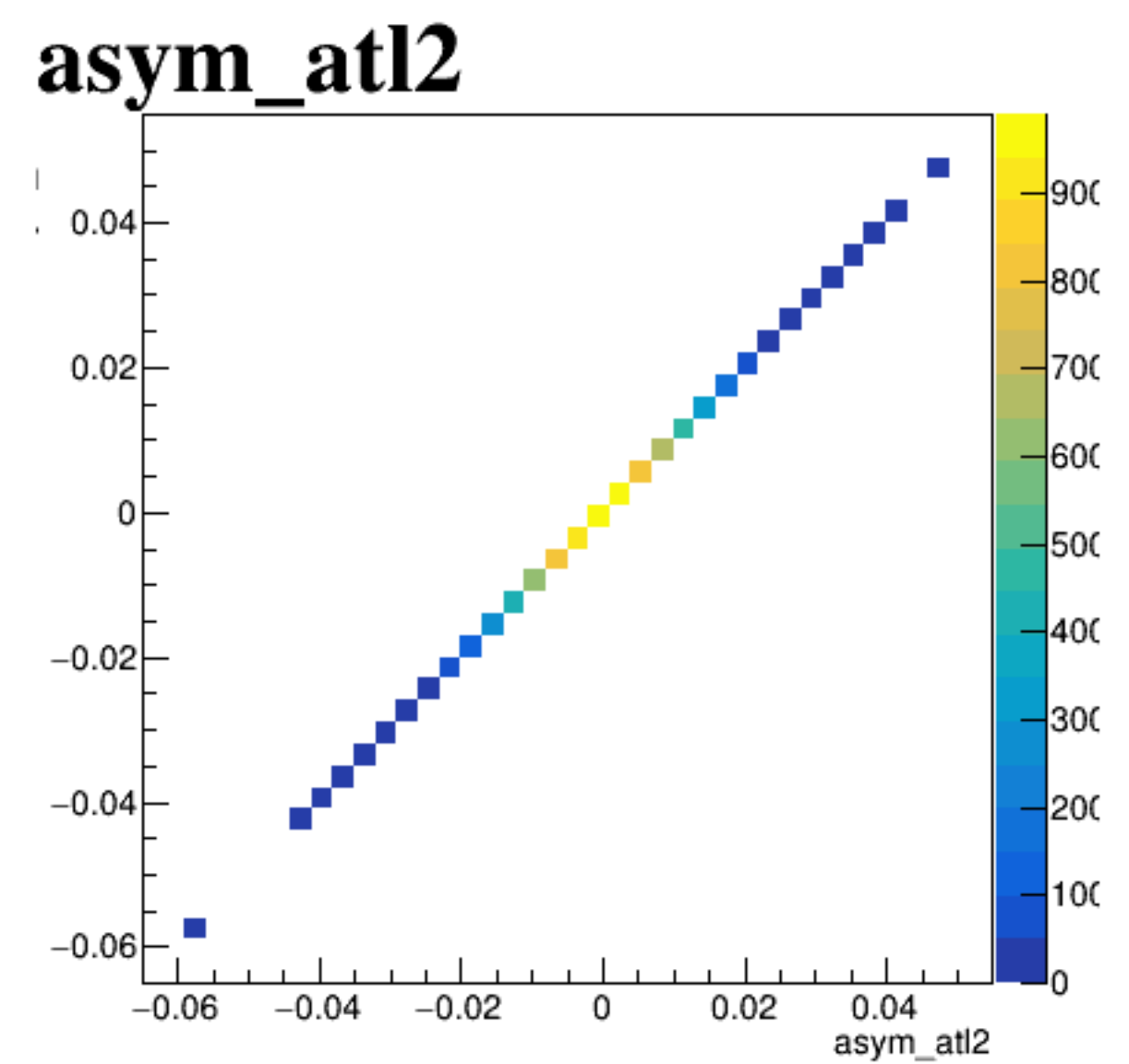
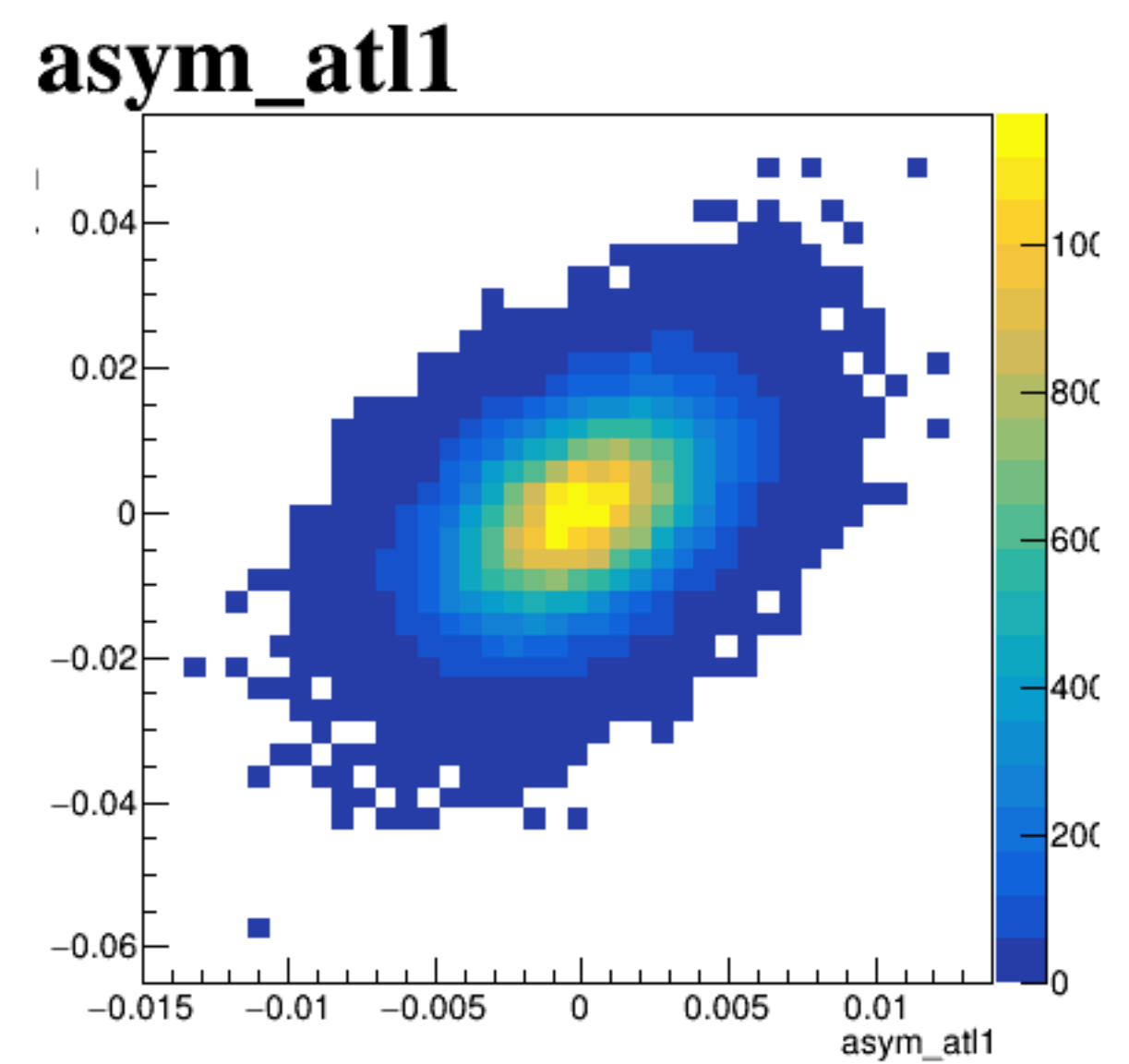
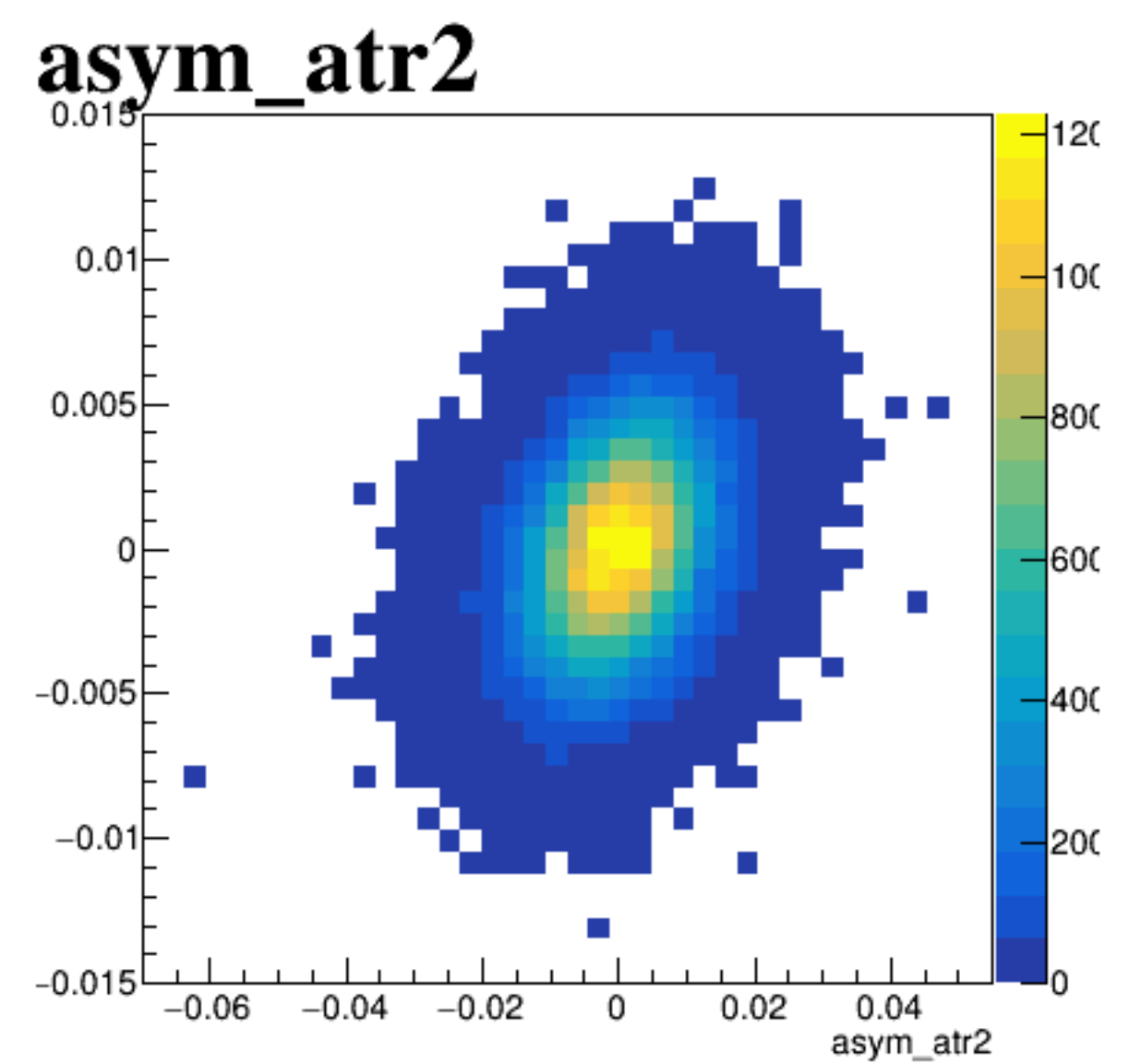
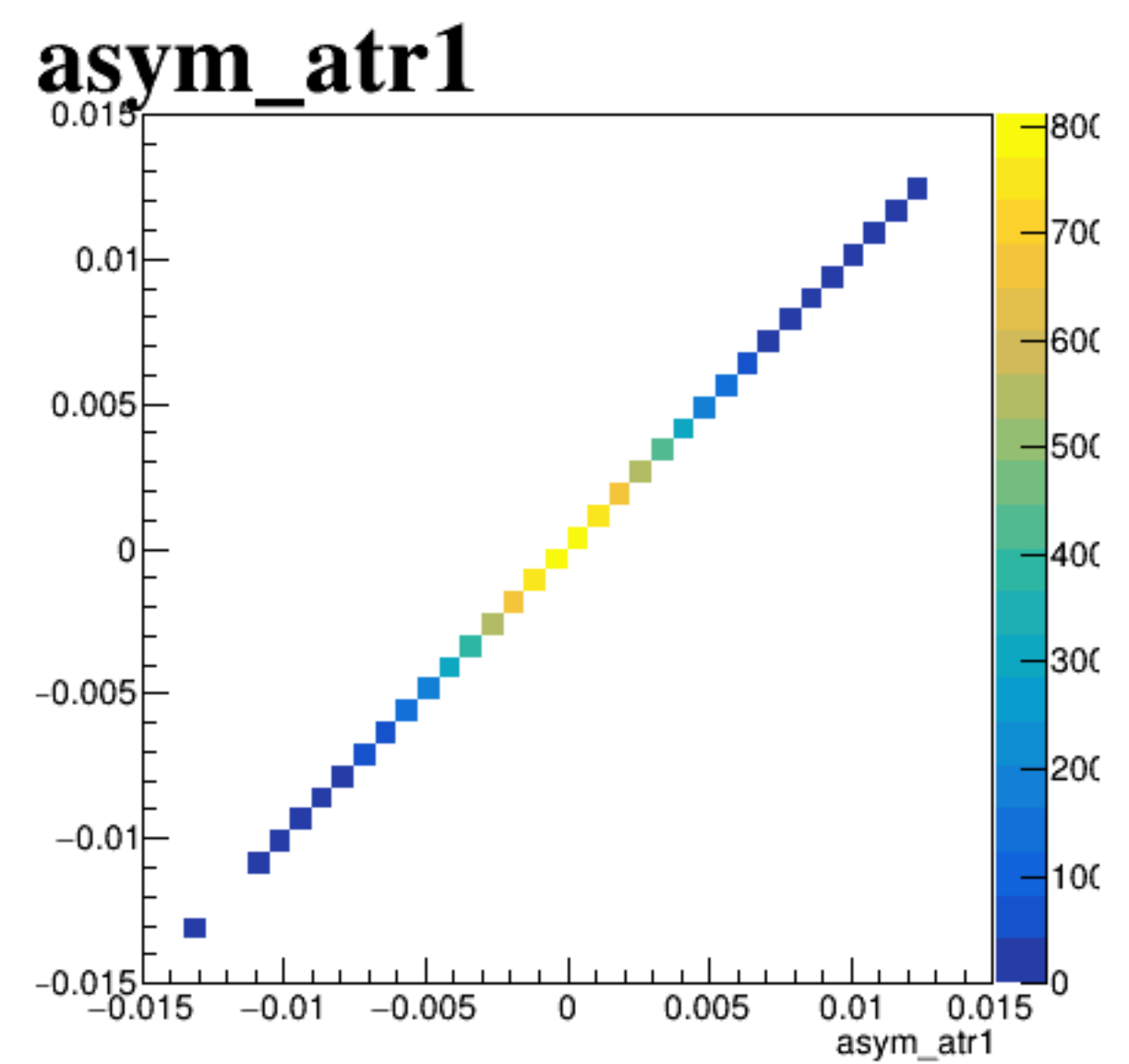
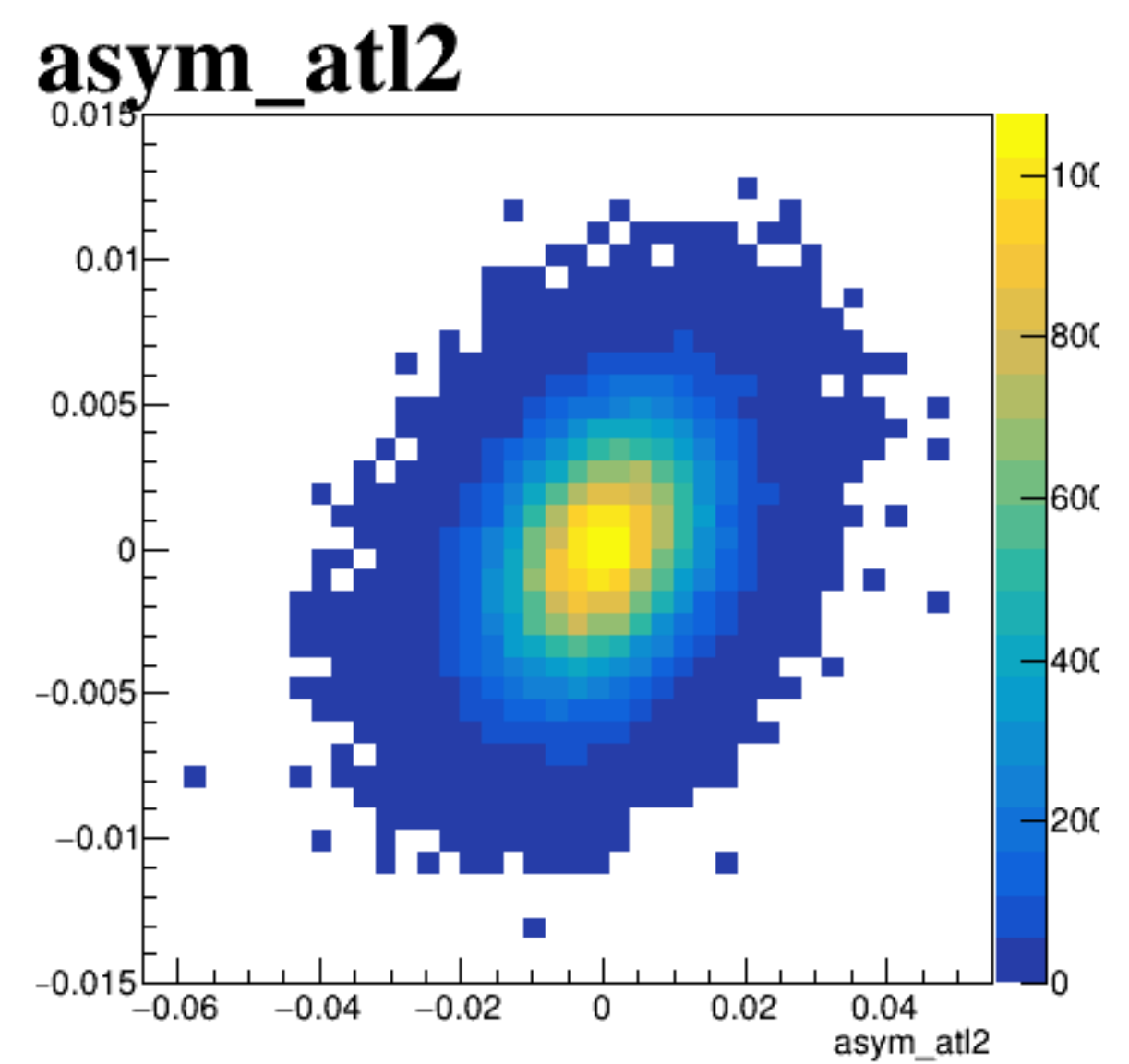
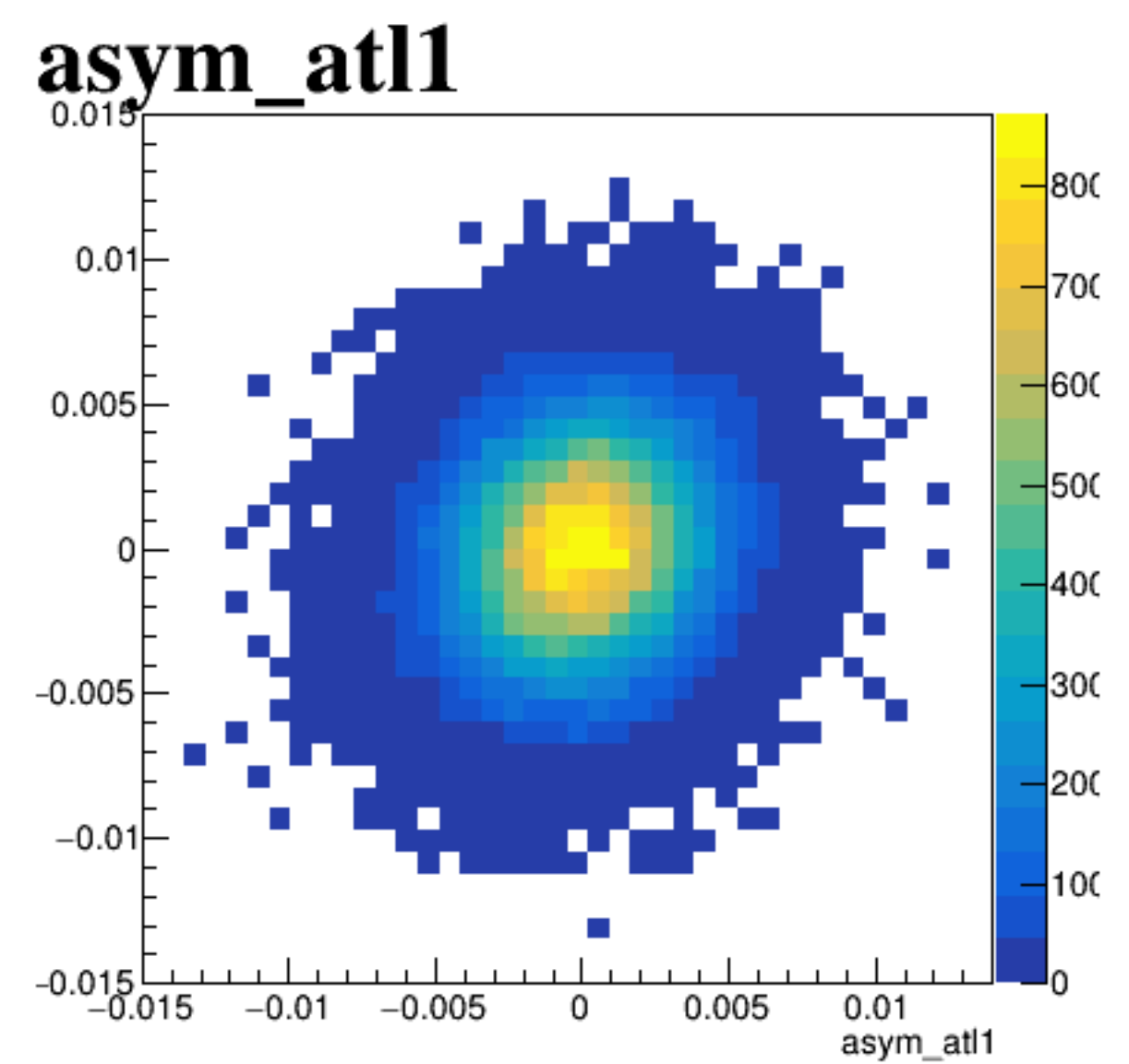
asym_atr2



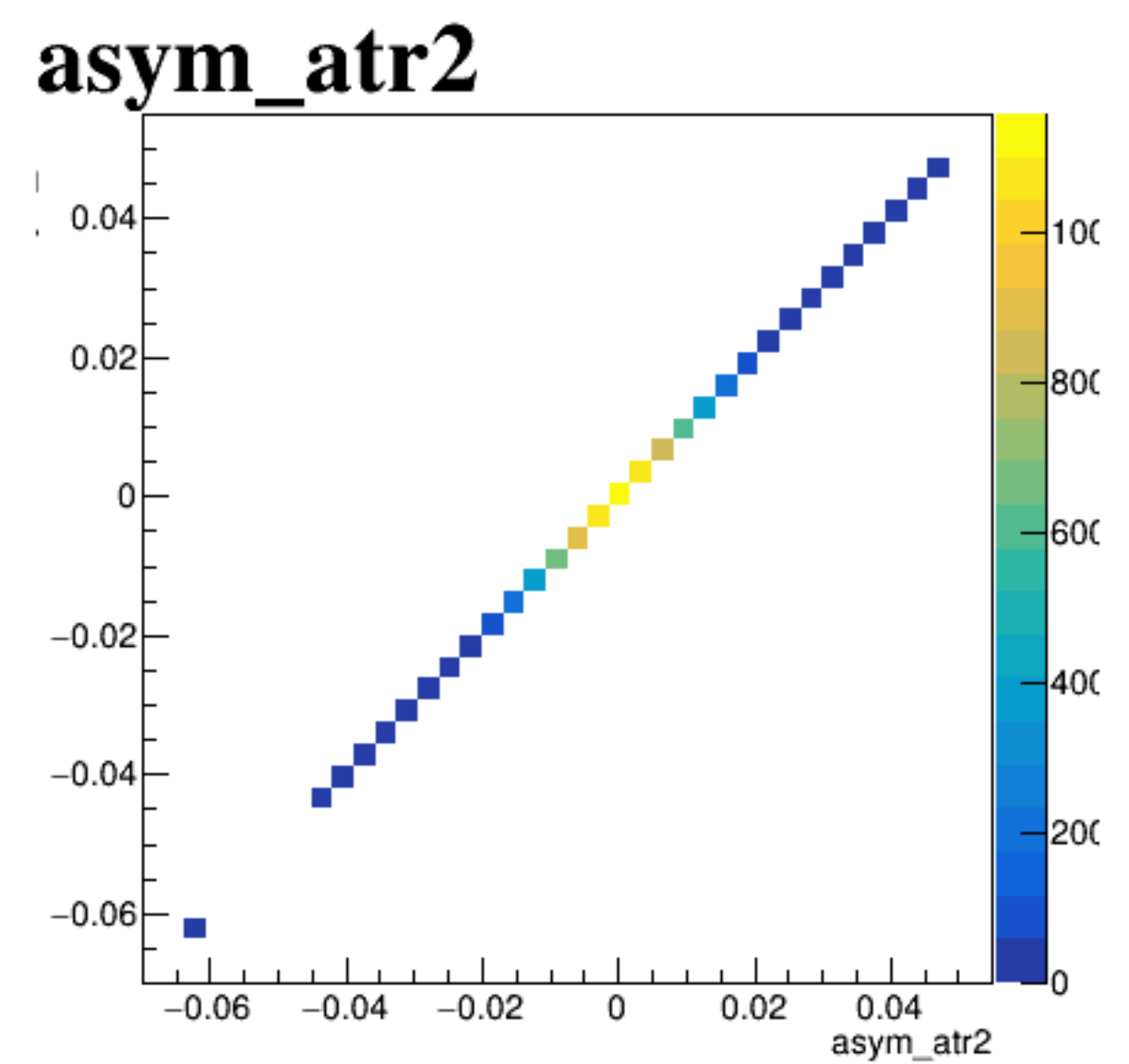
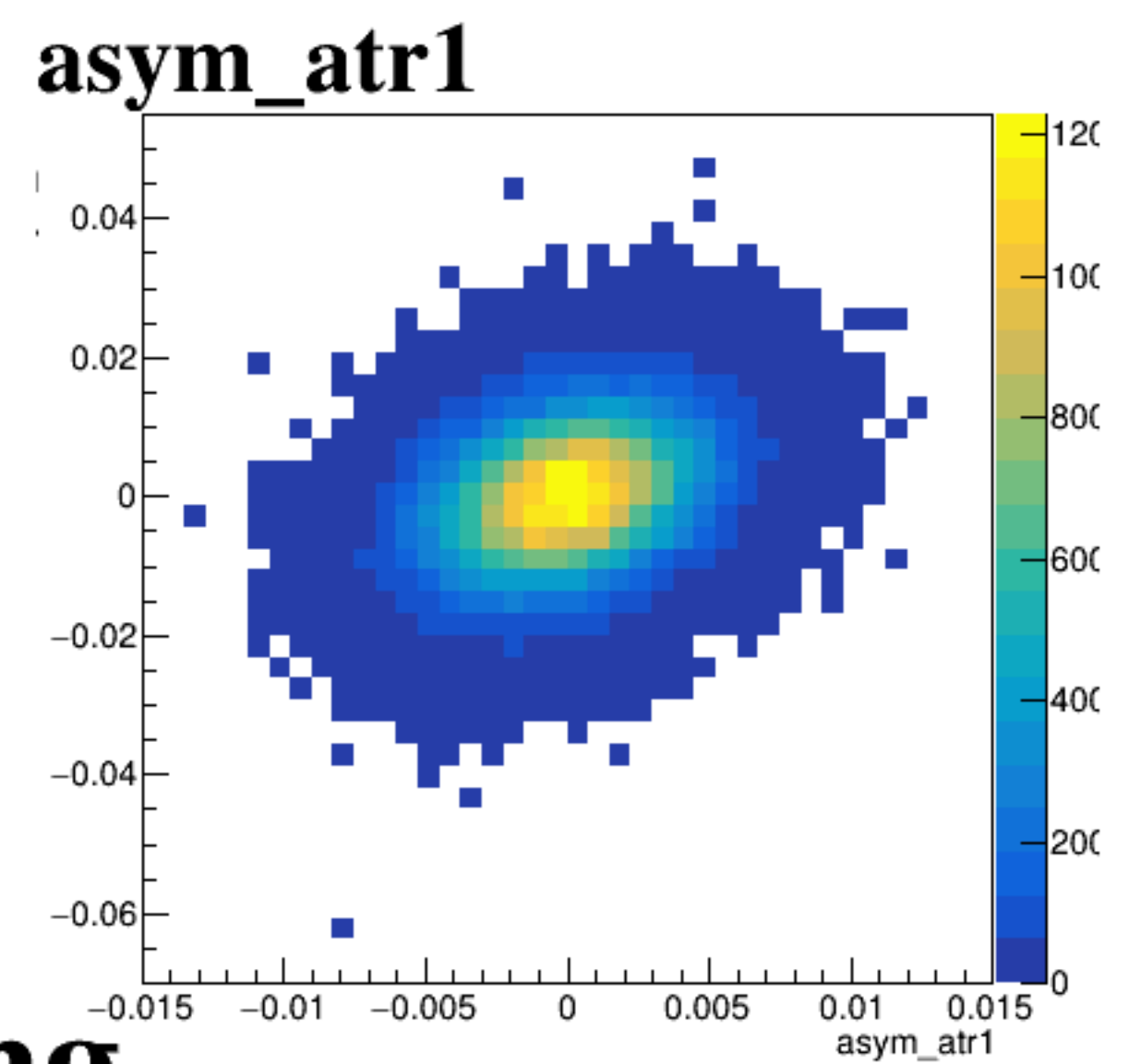
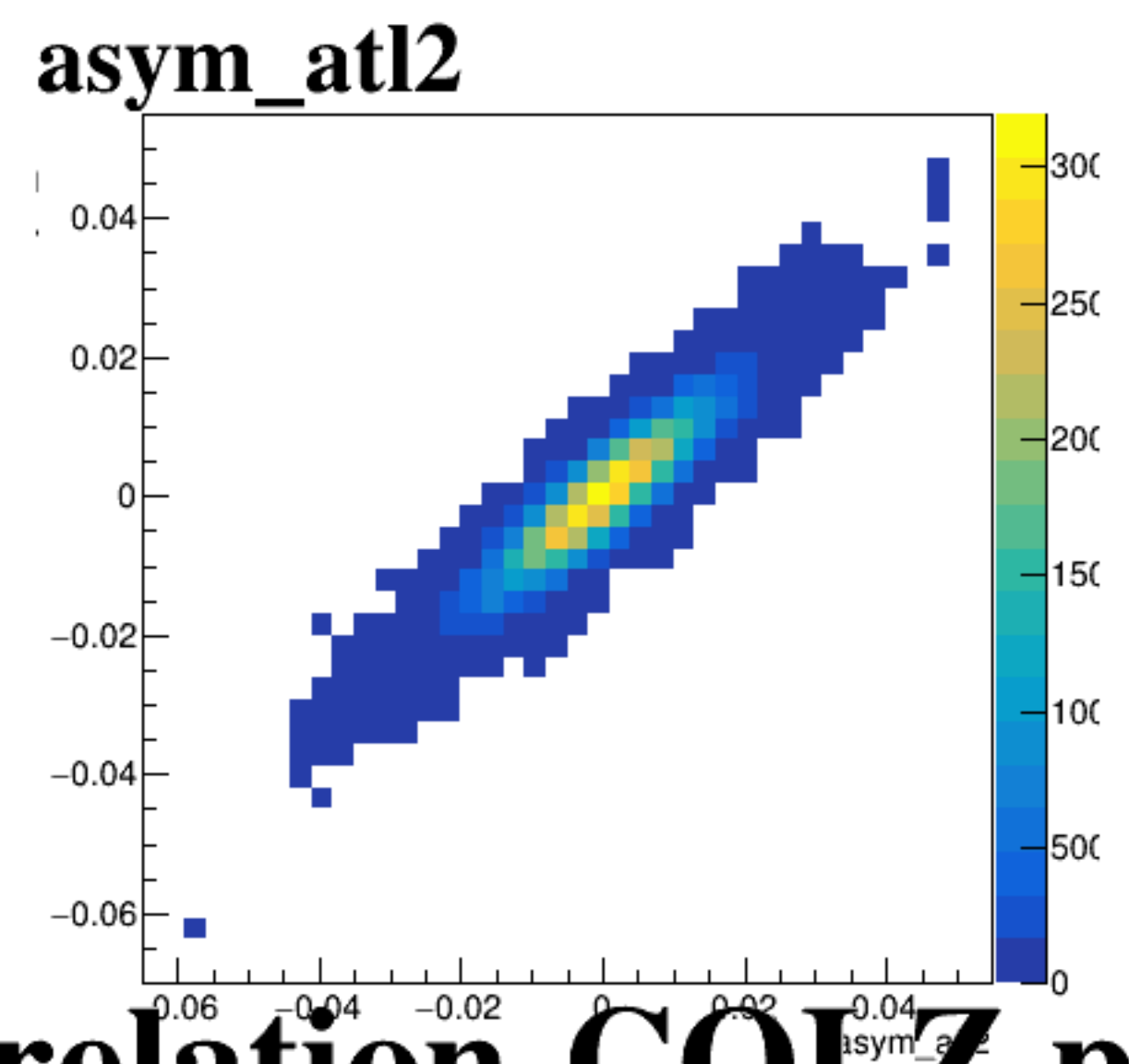
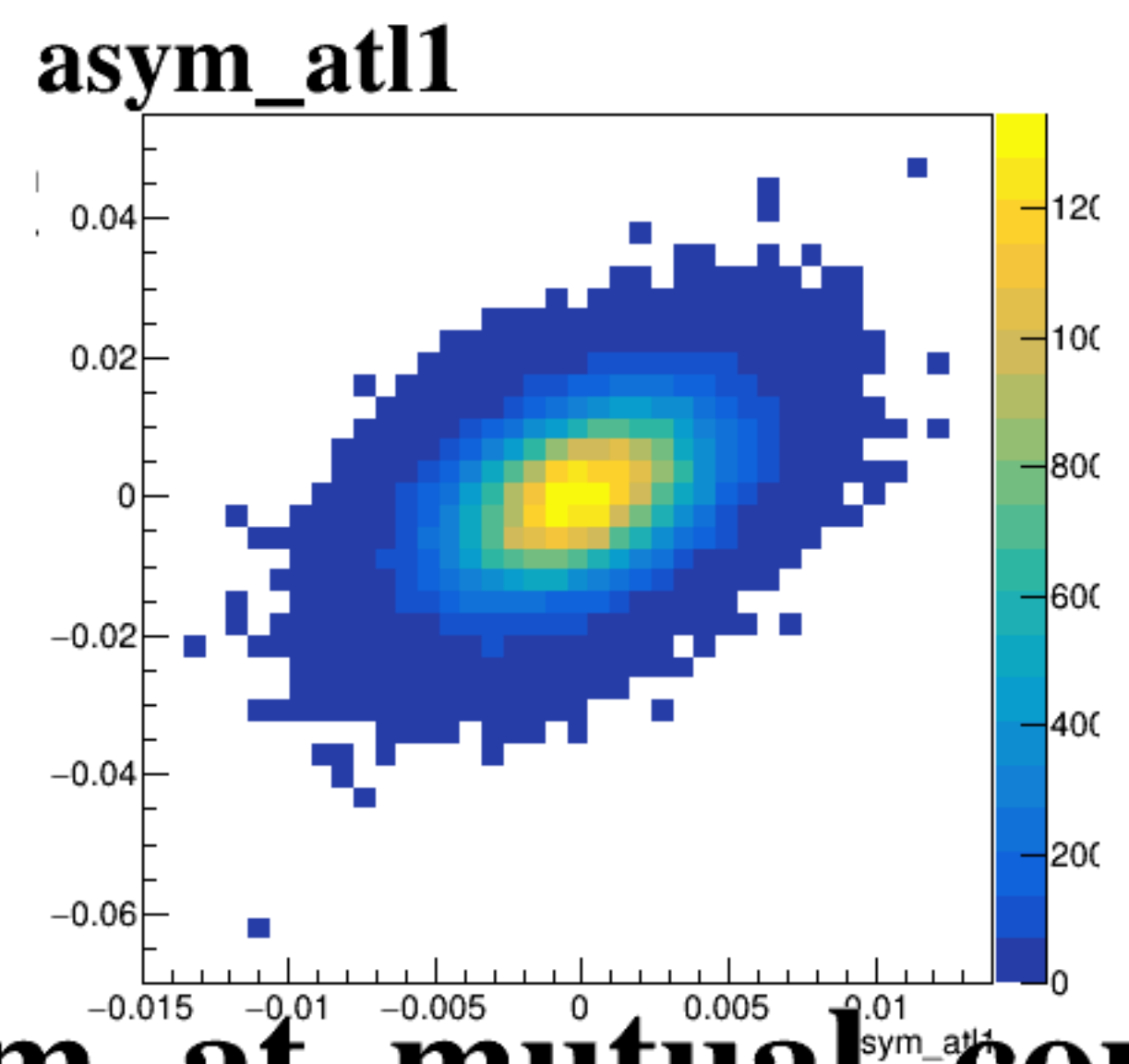
asym_atl1



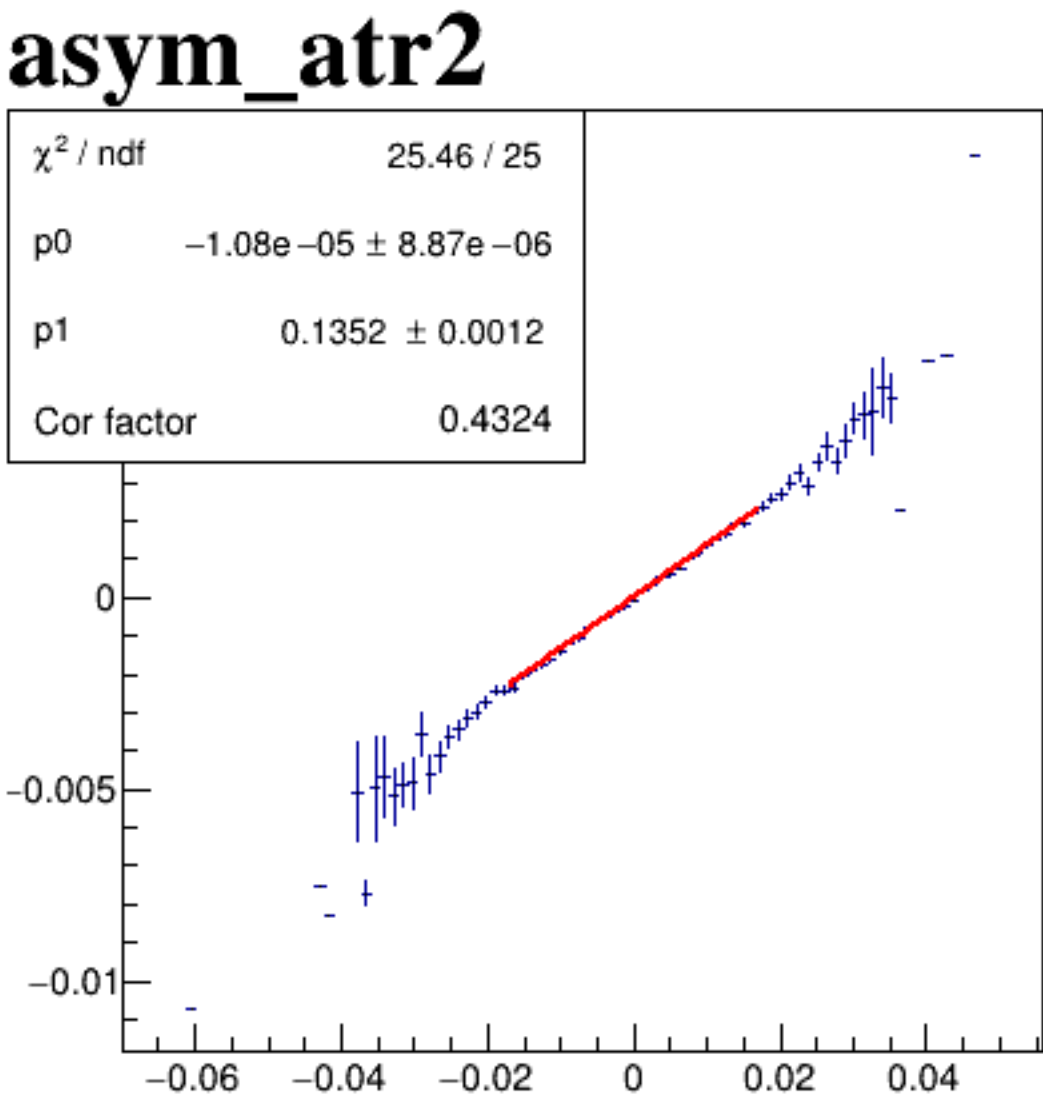
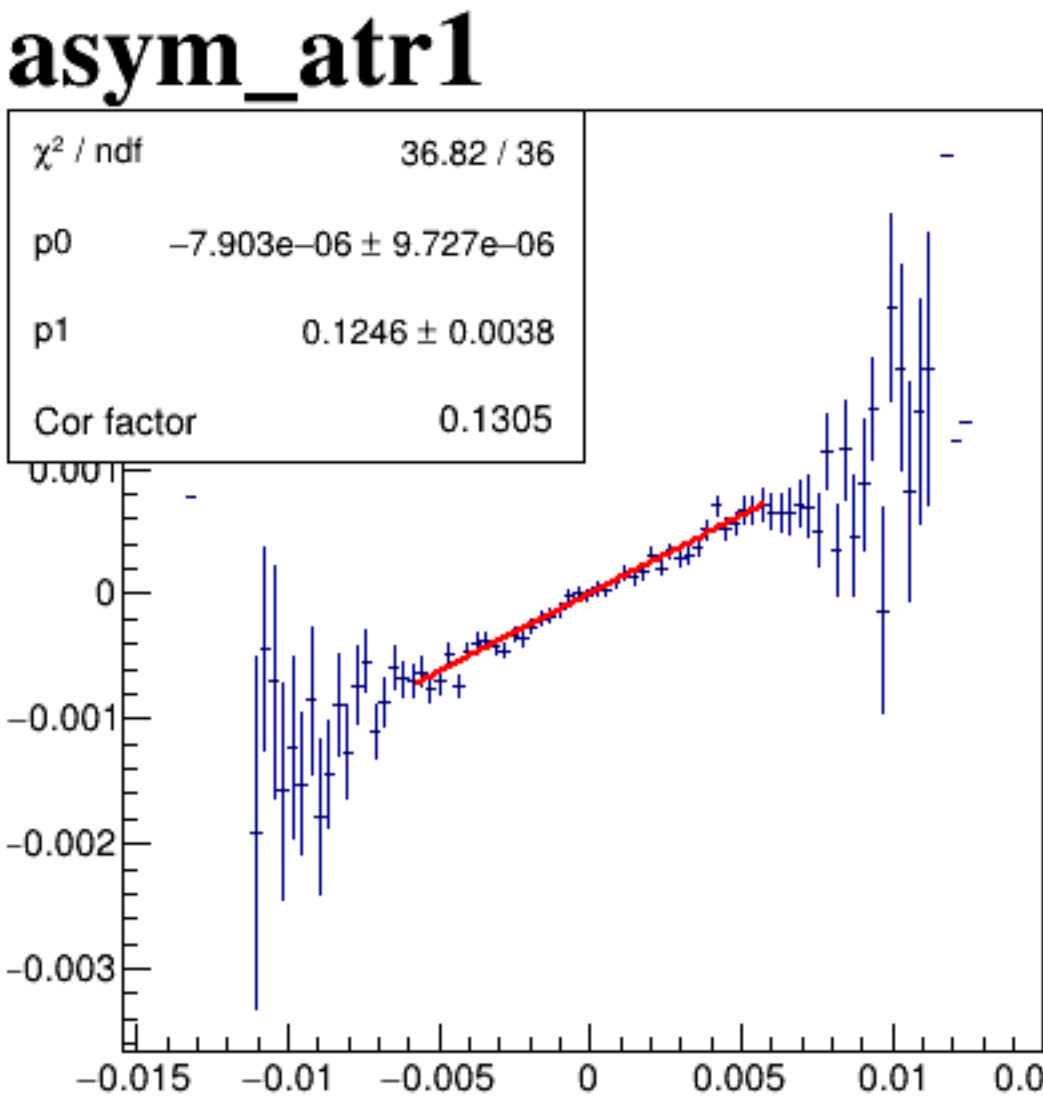
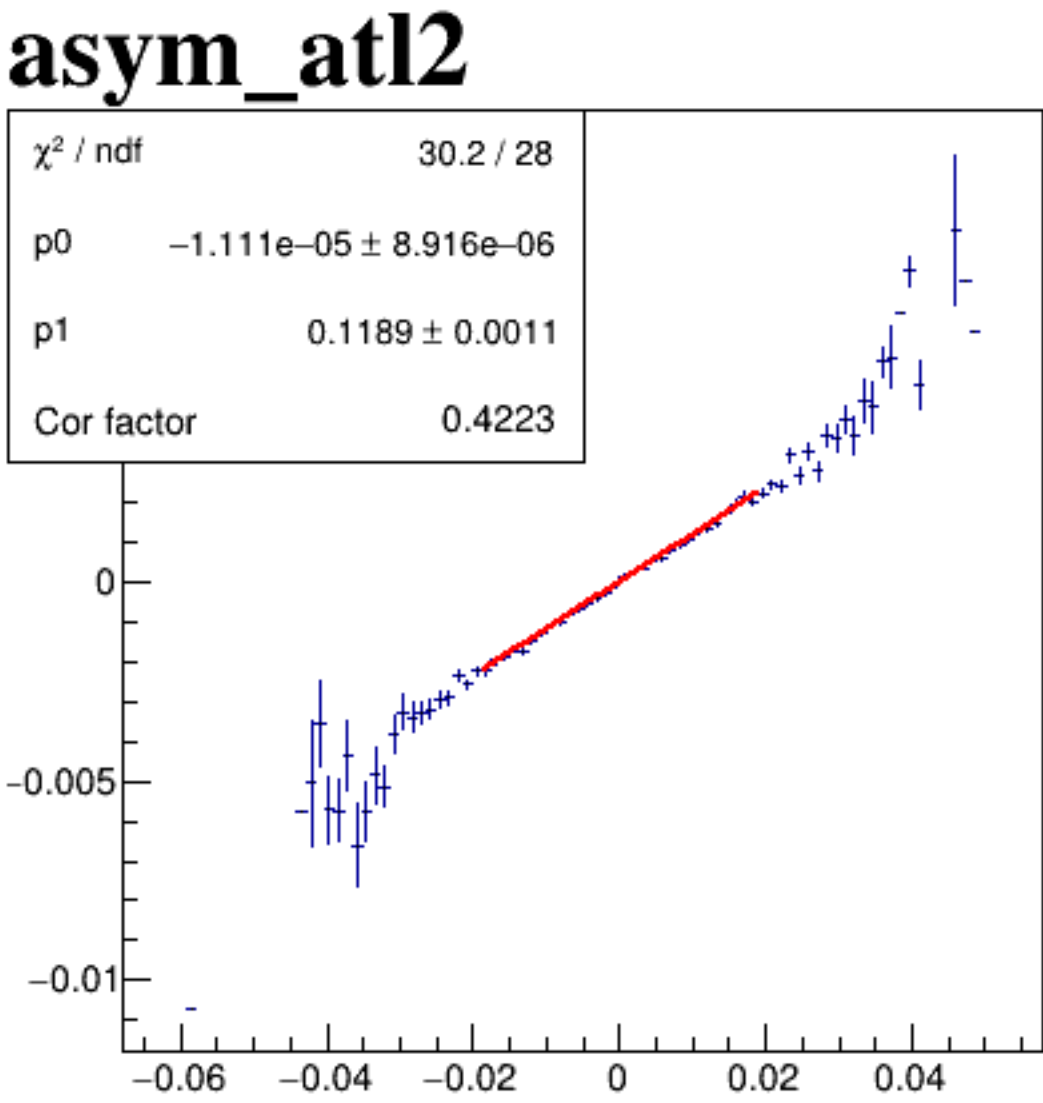
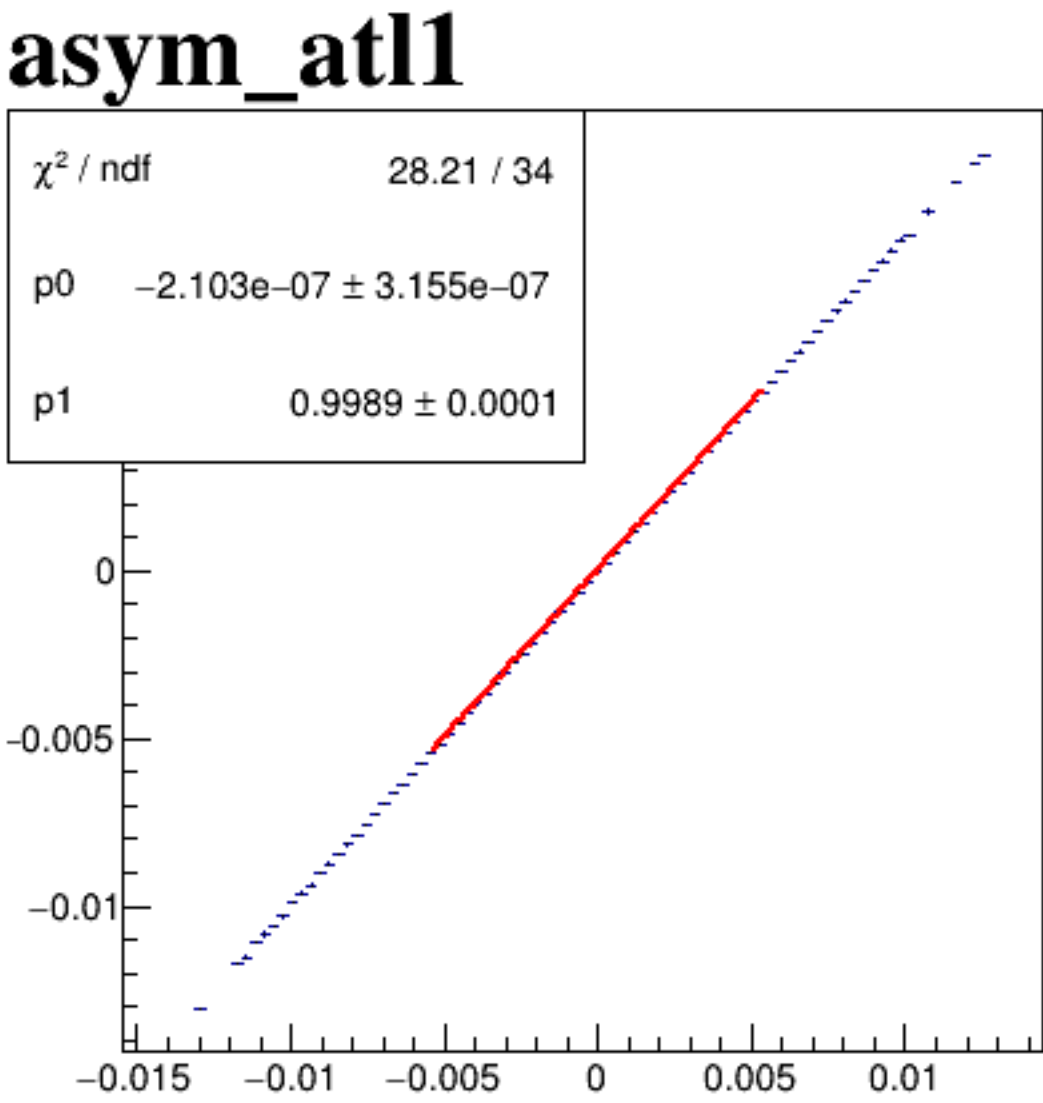
asym_atl2

**asym_atr1**

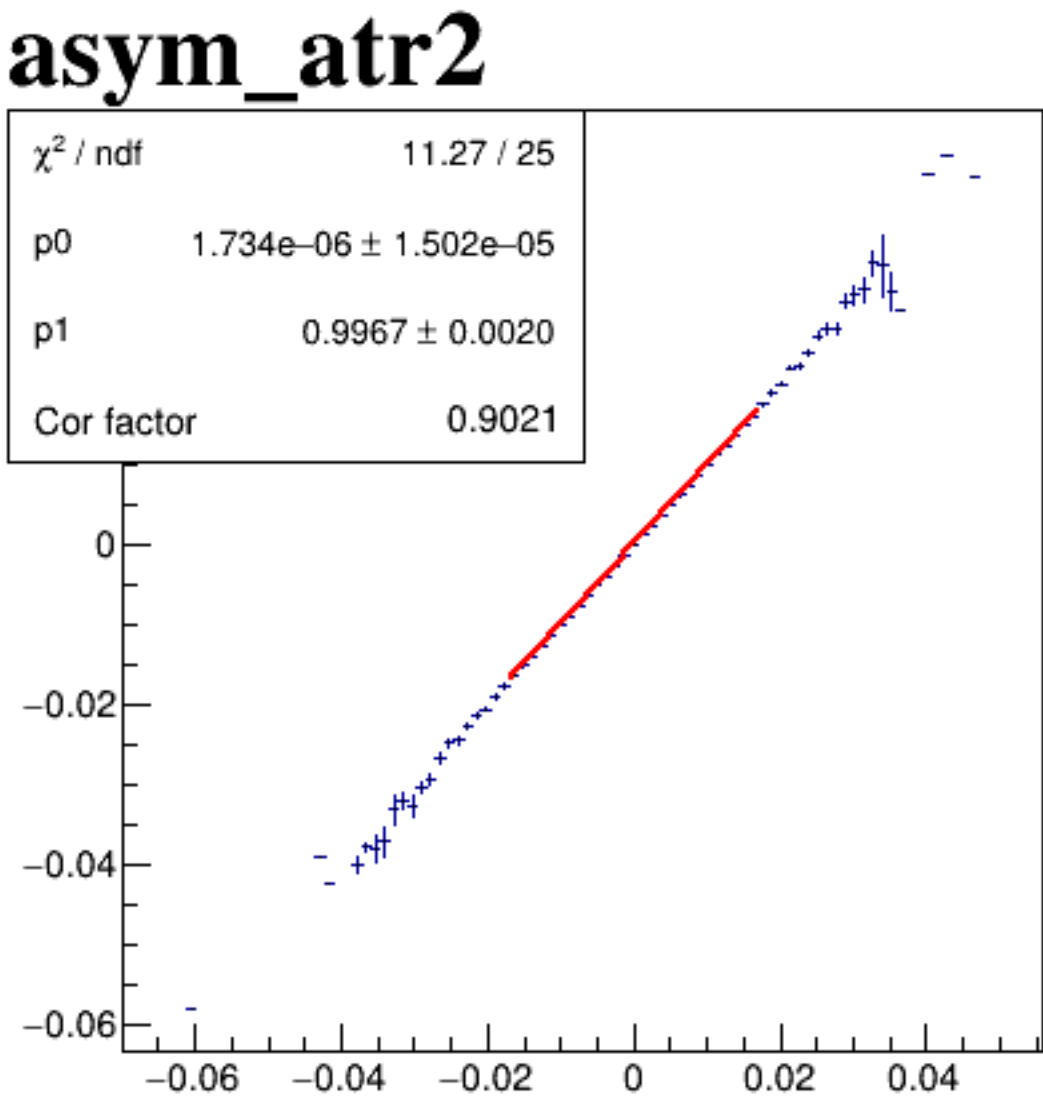
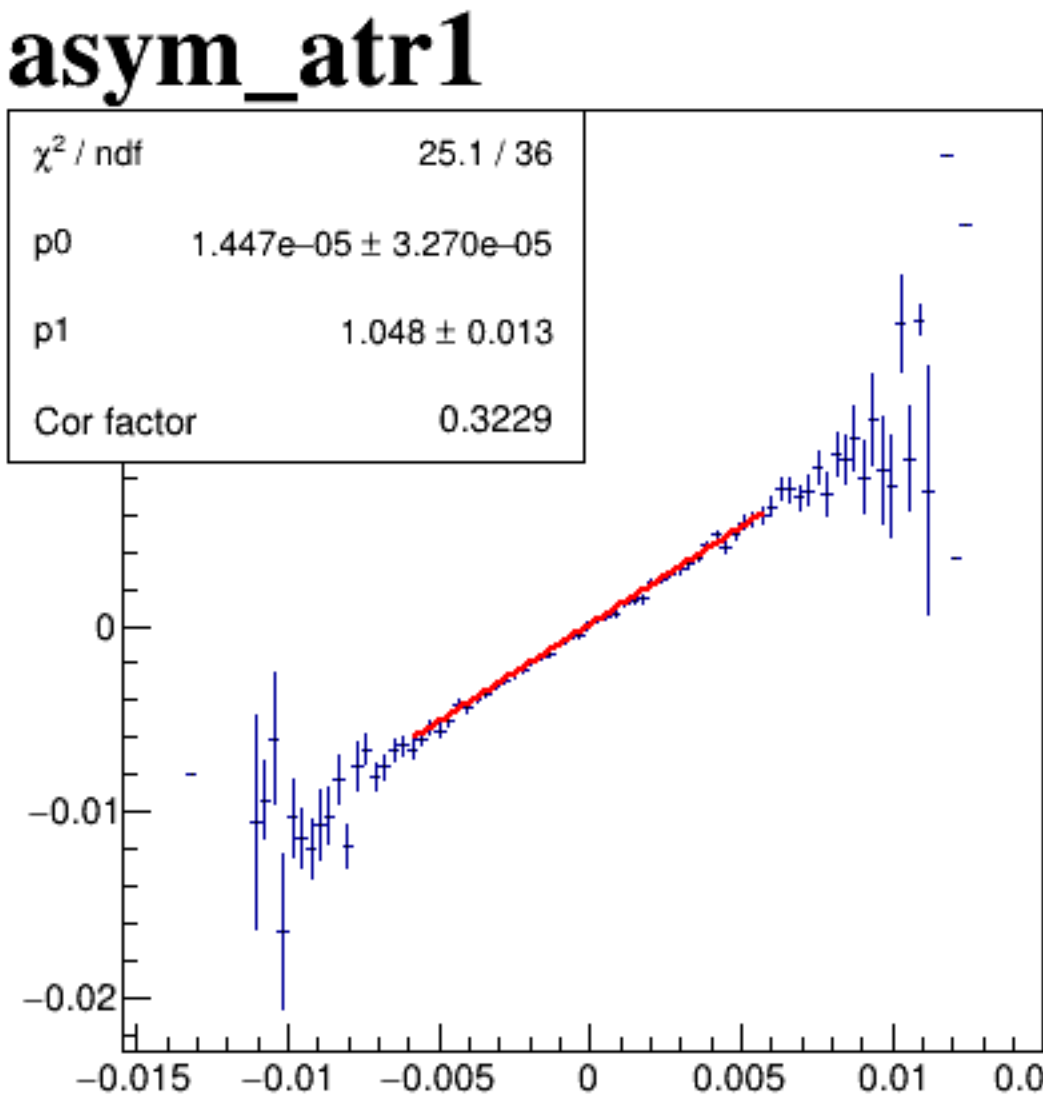
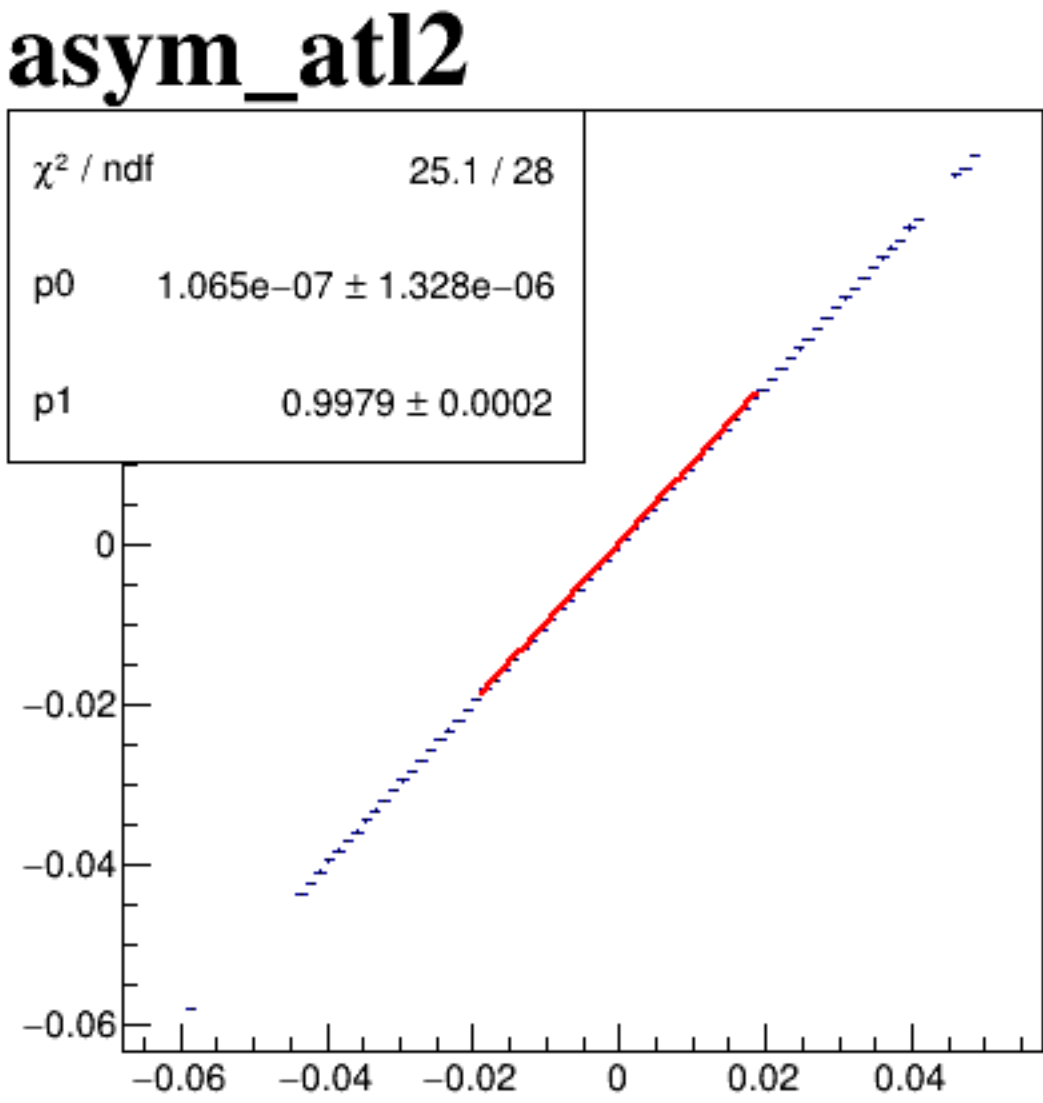
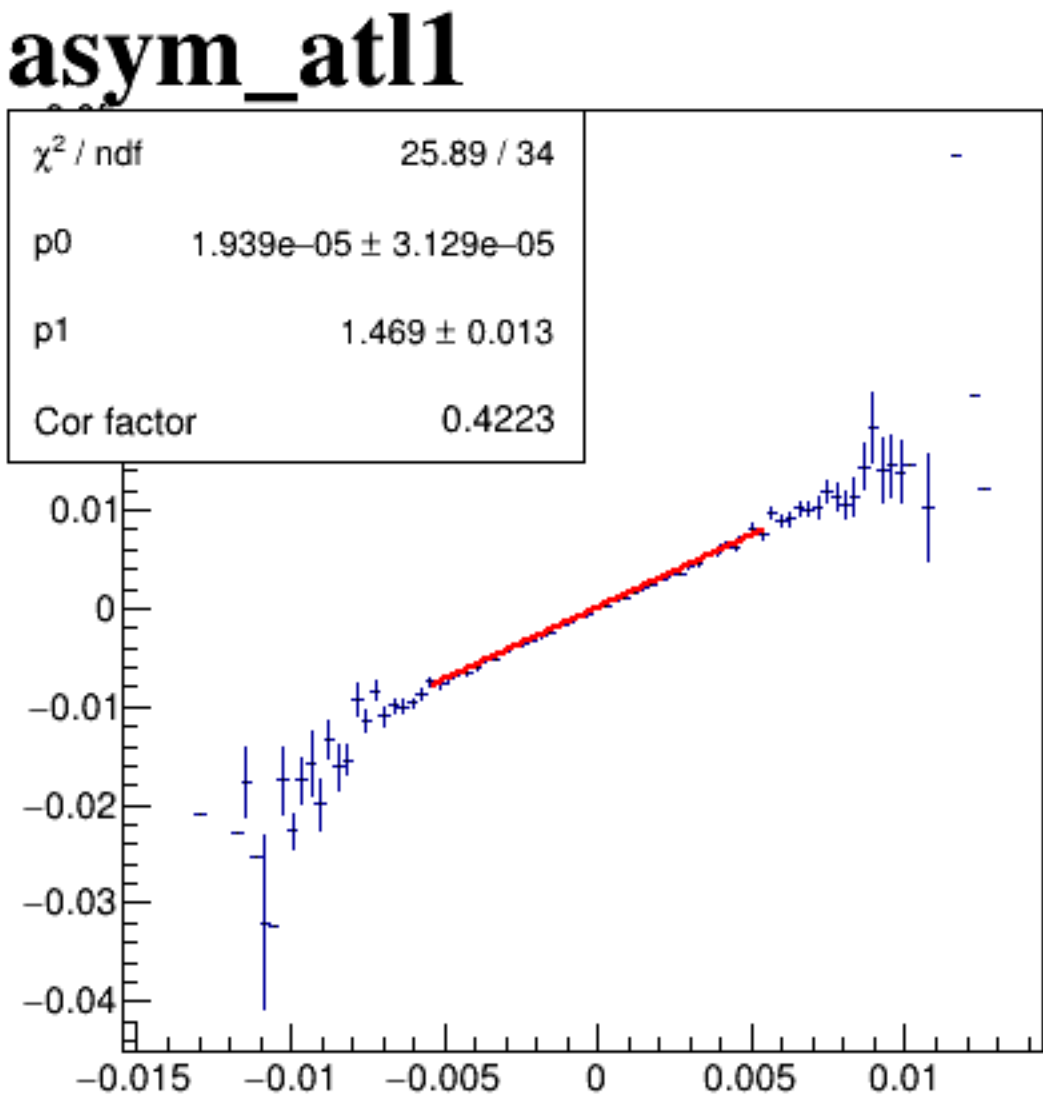
asym_atr2



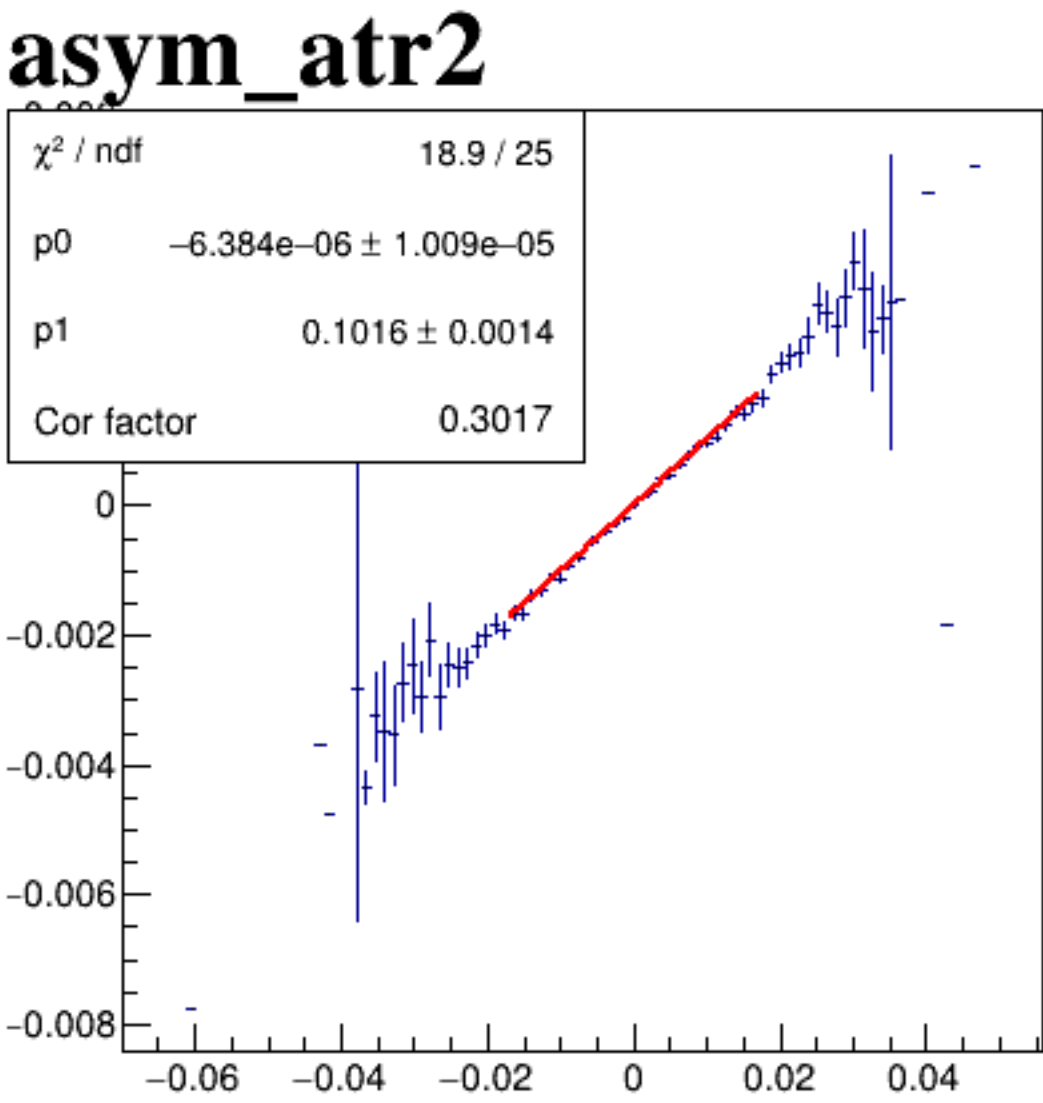
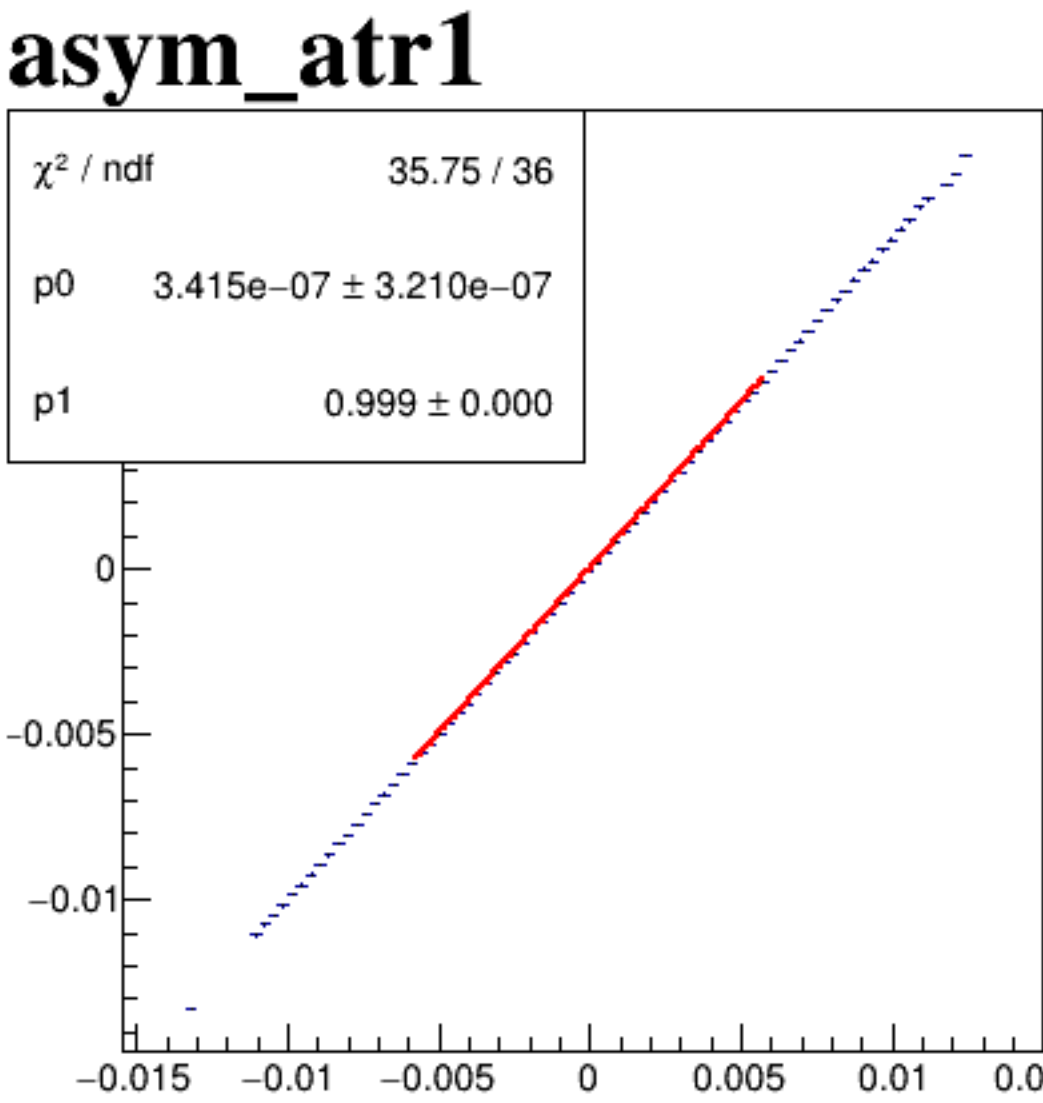
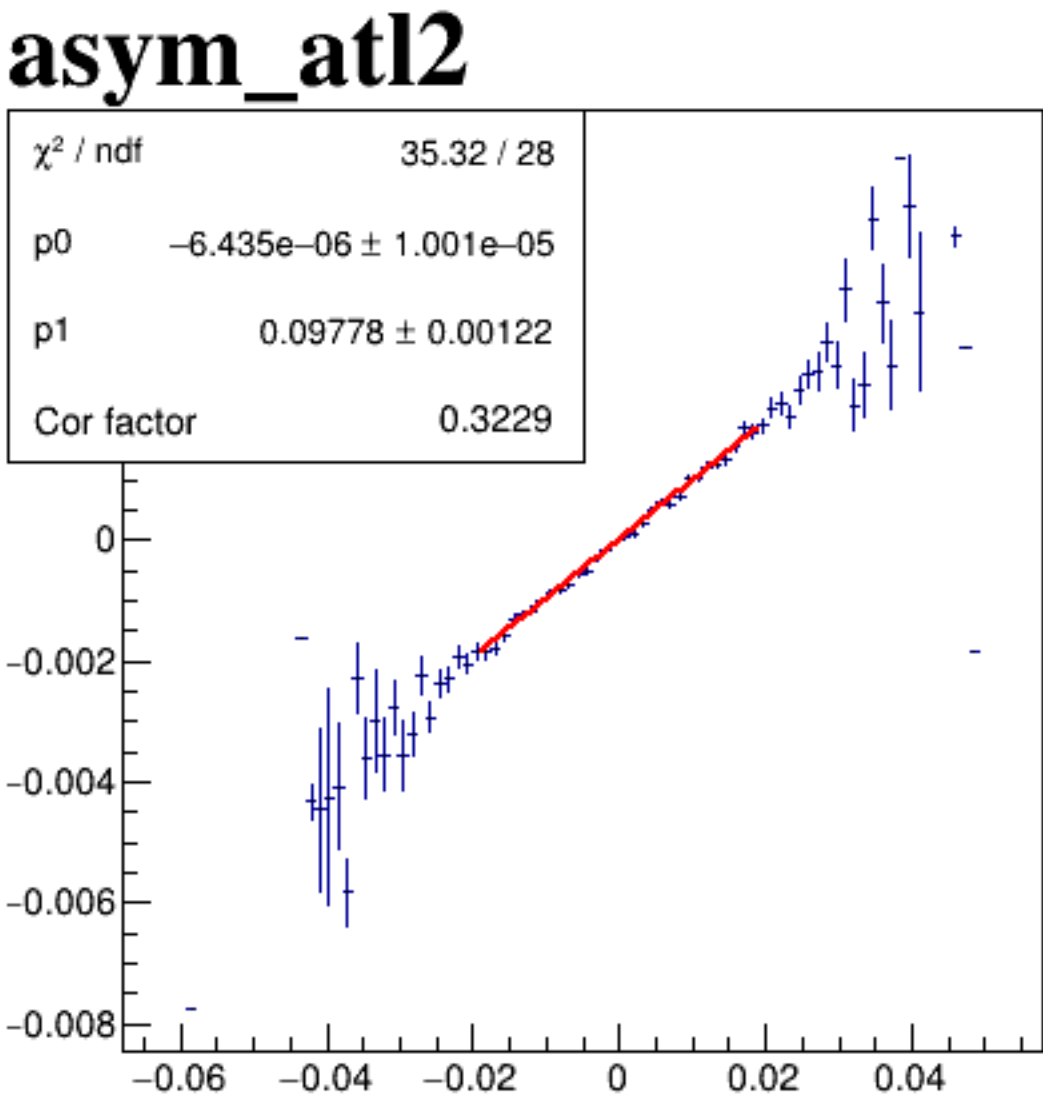
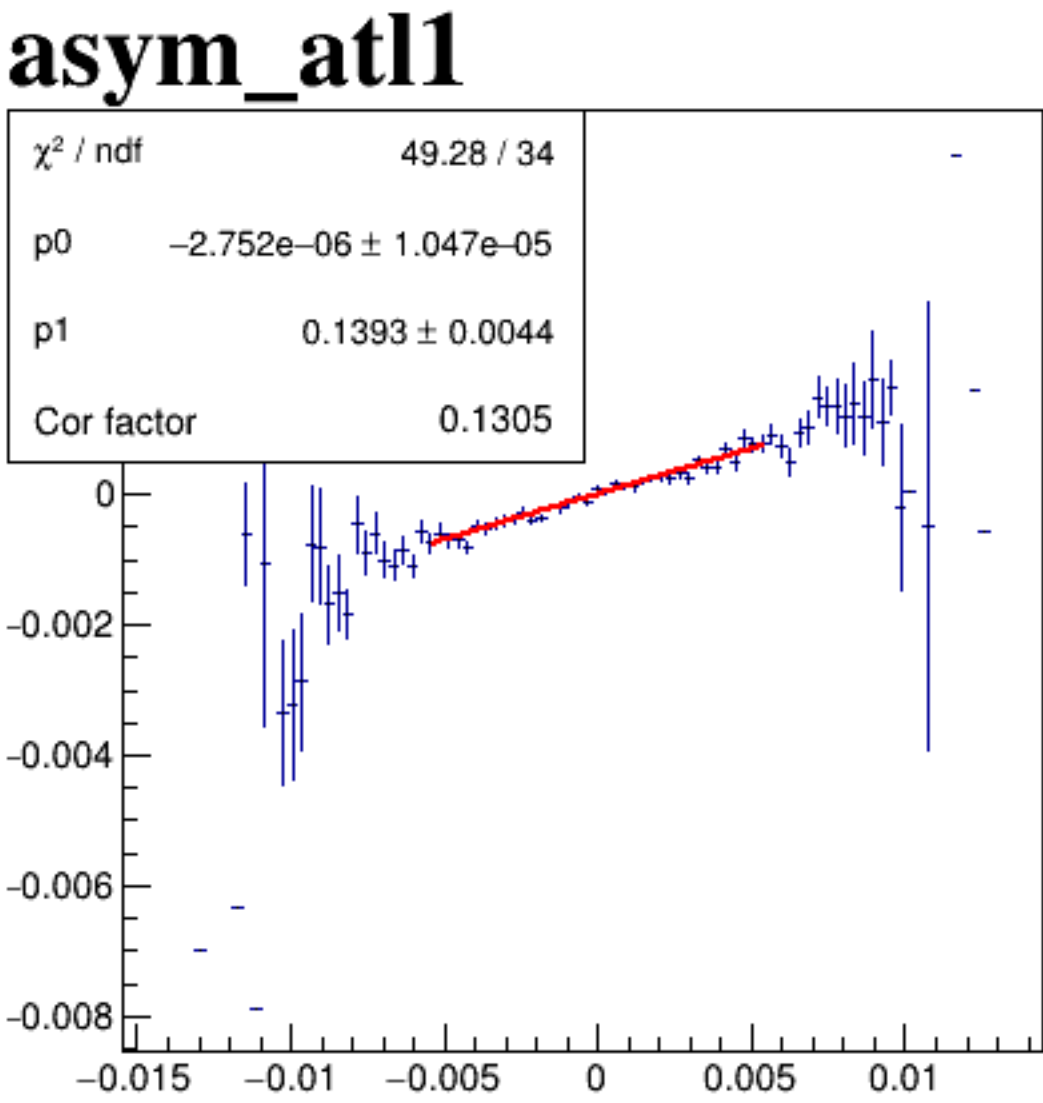
asym_atl1



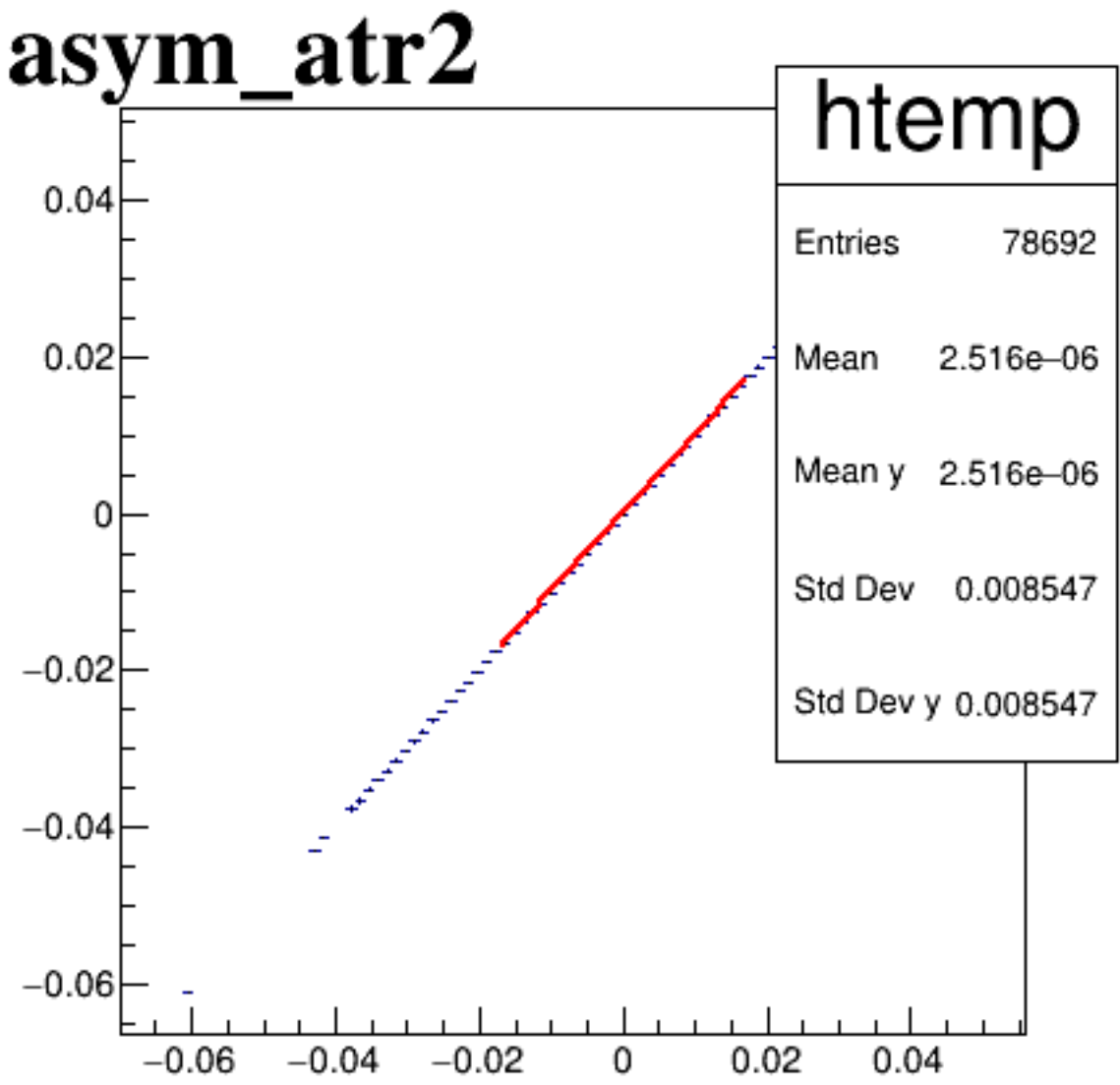
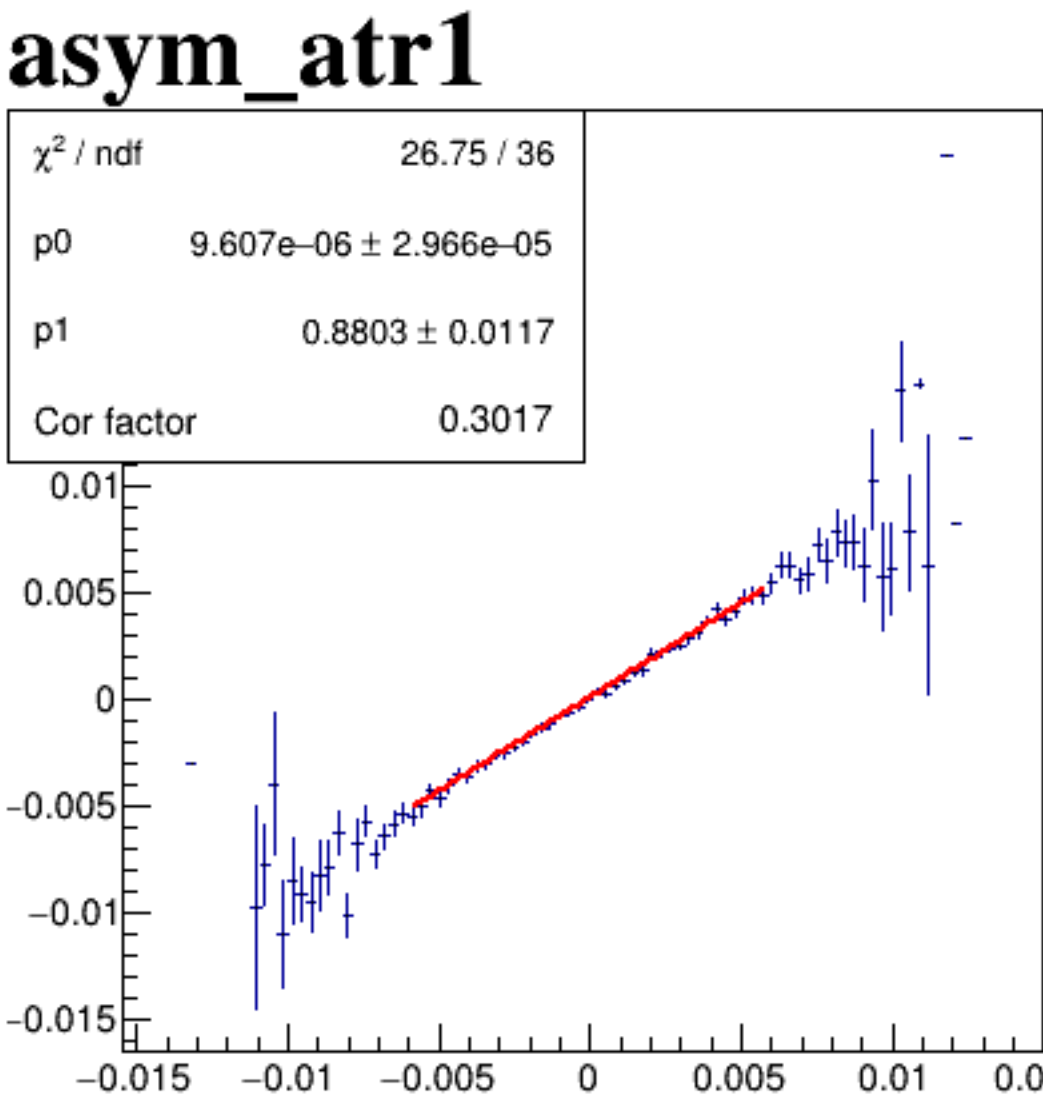
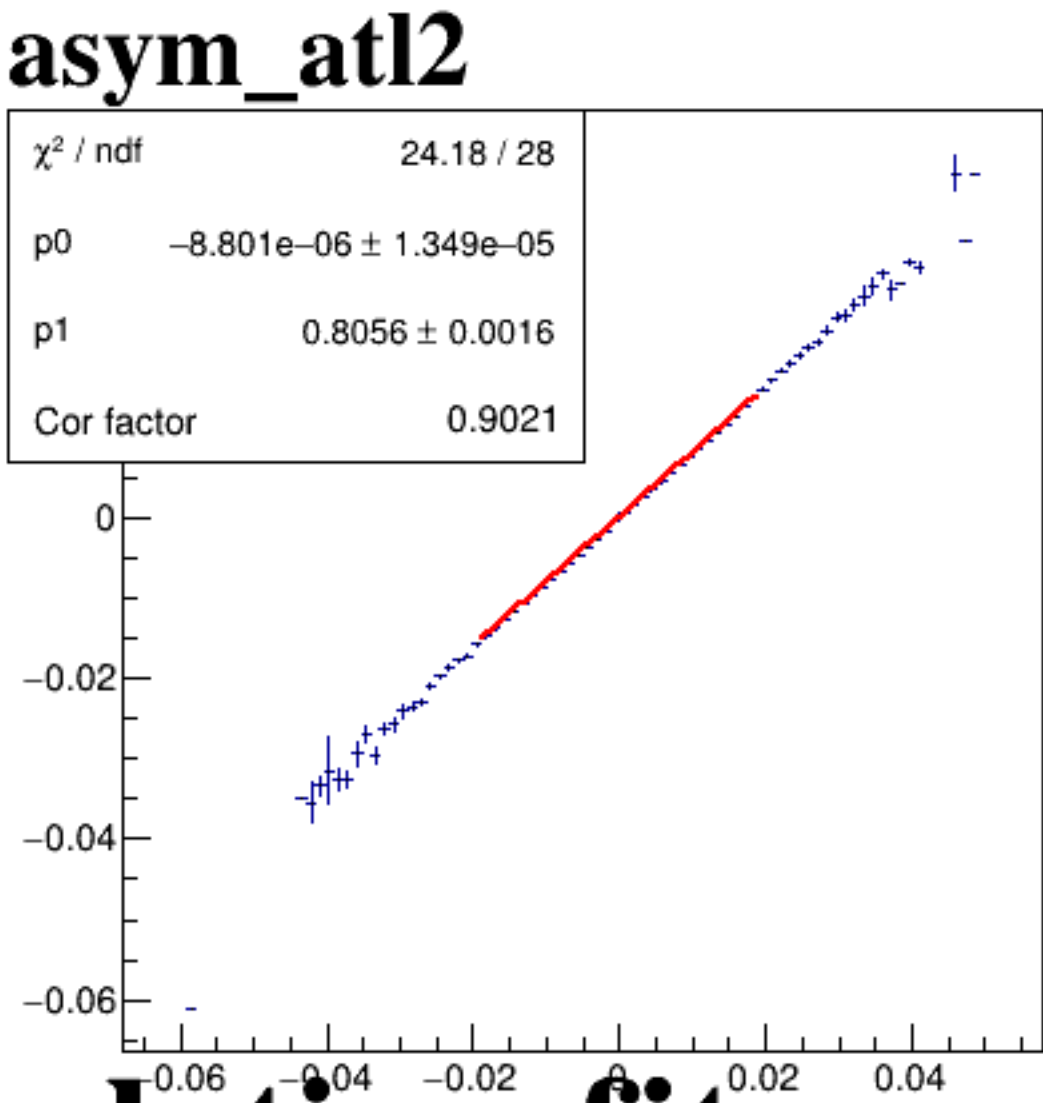
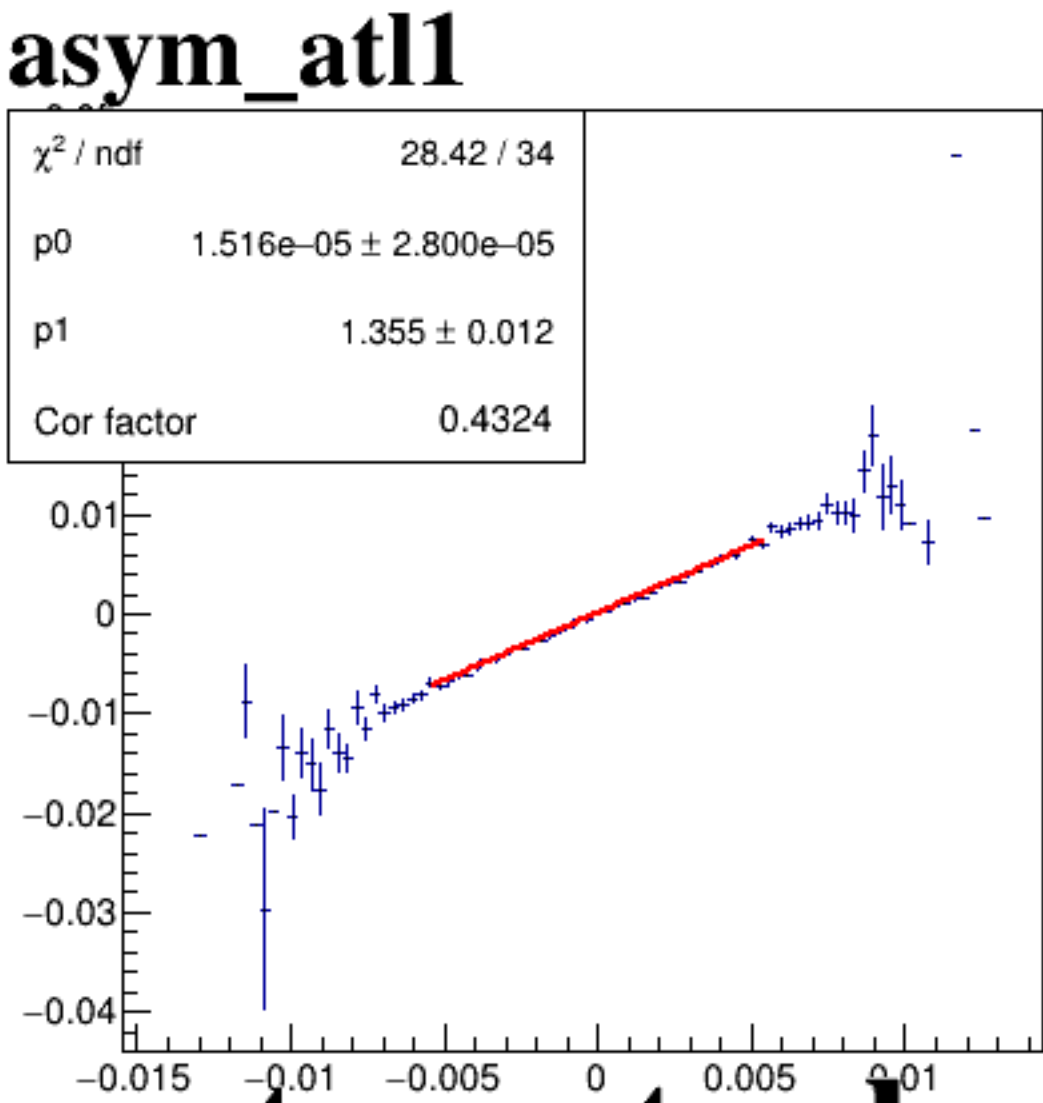
asym_atl2



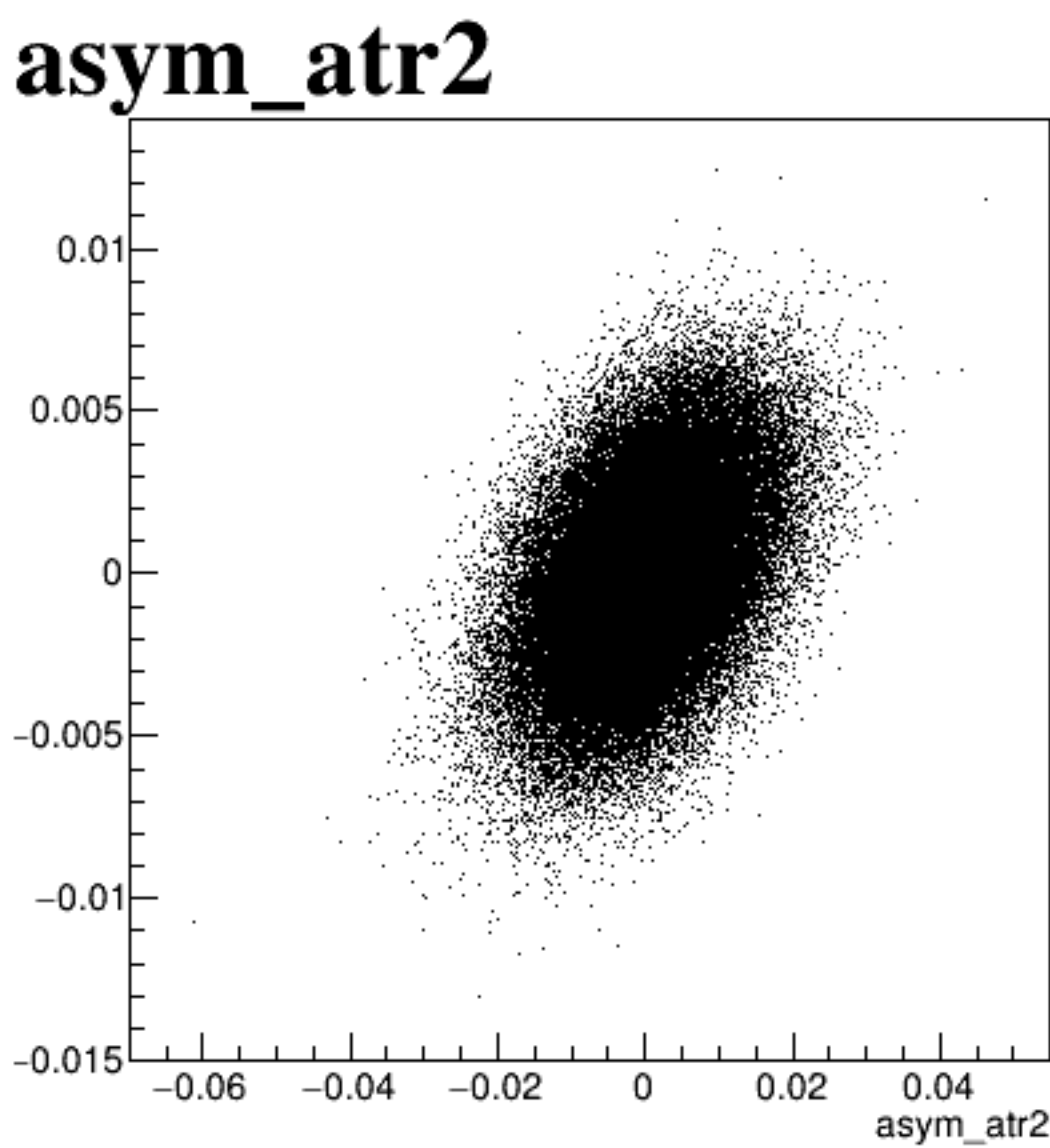
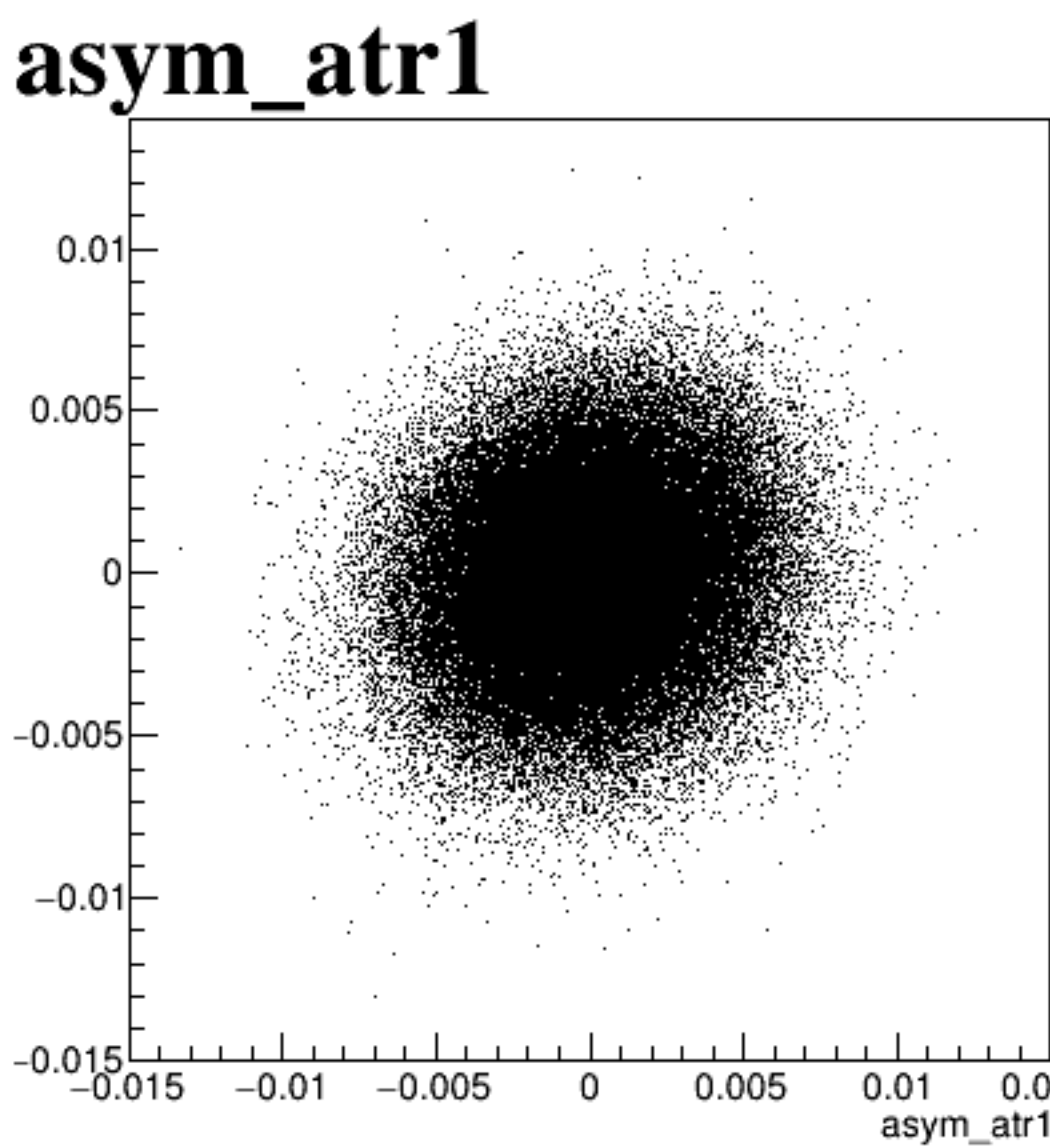
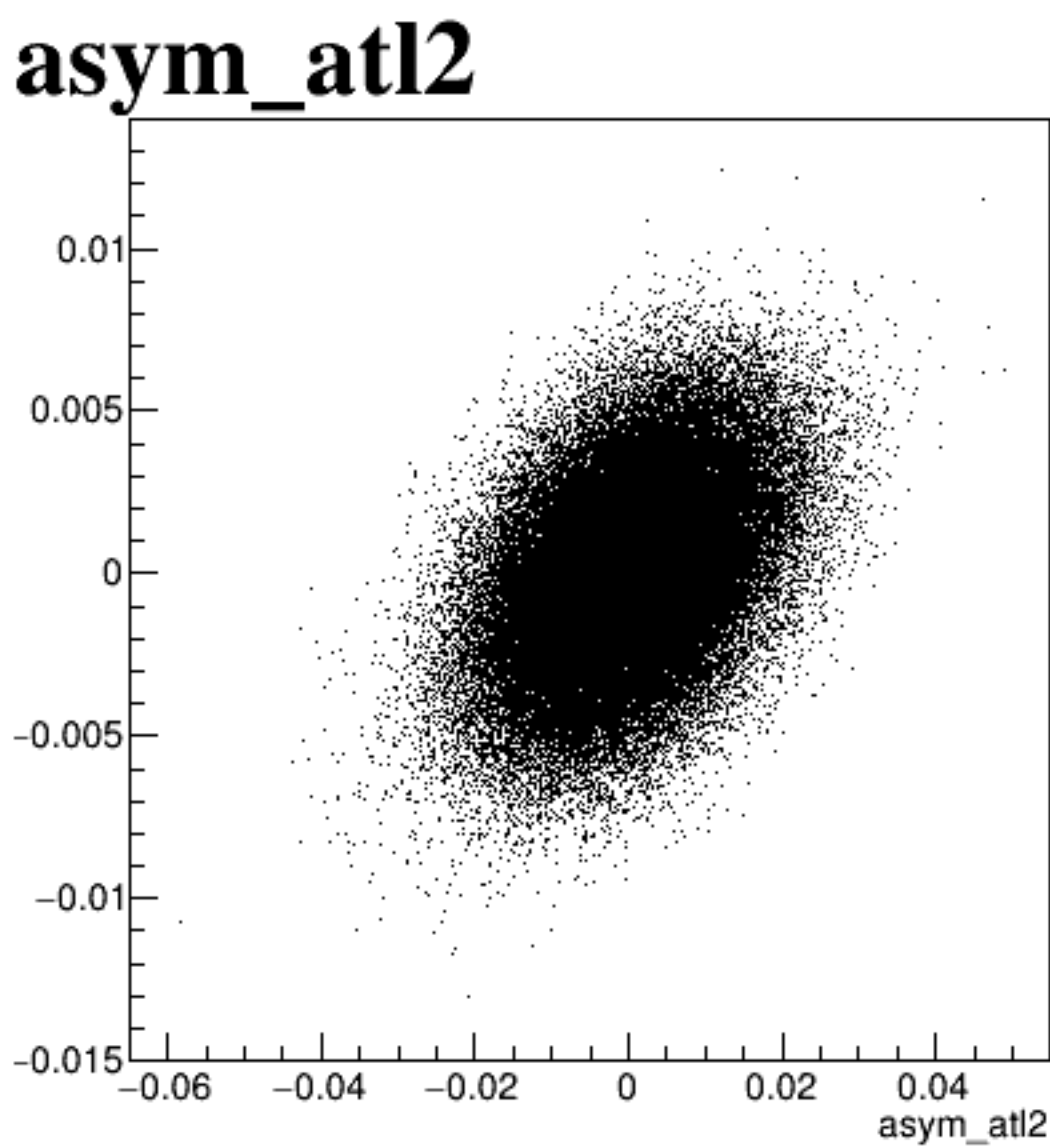
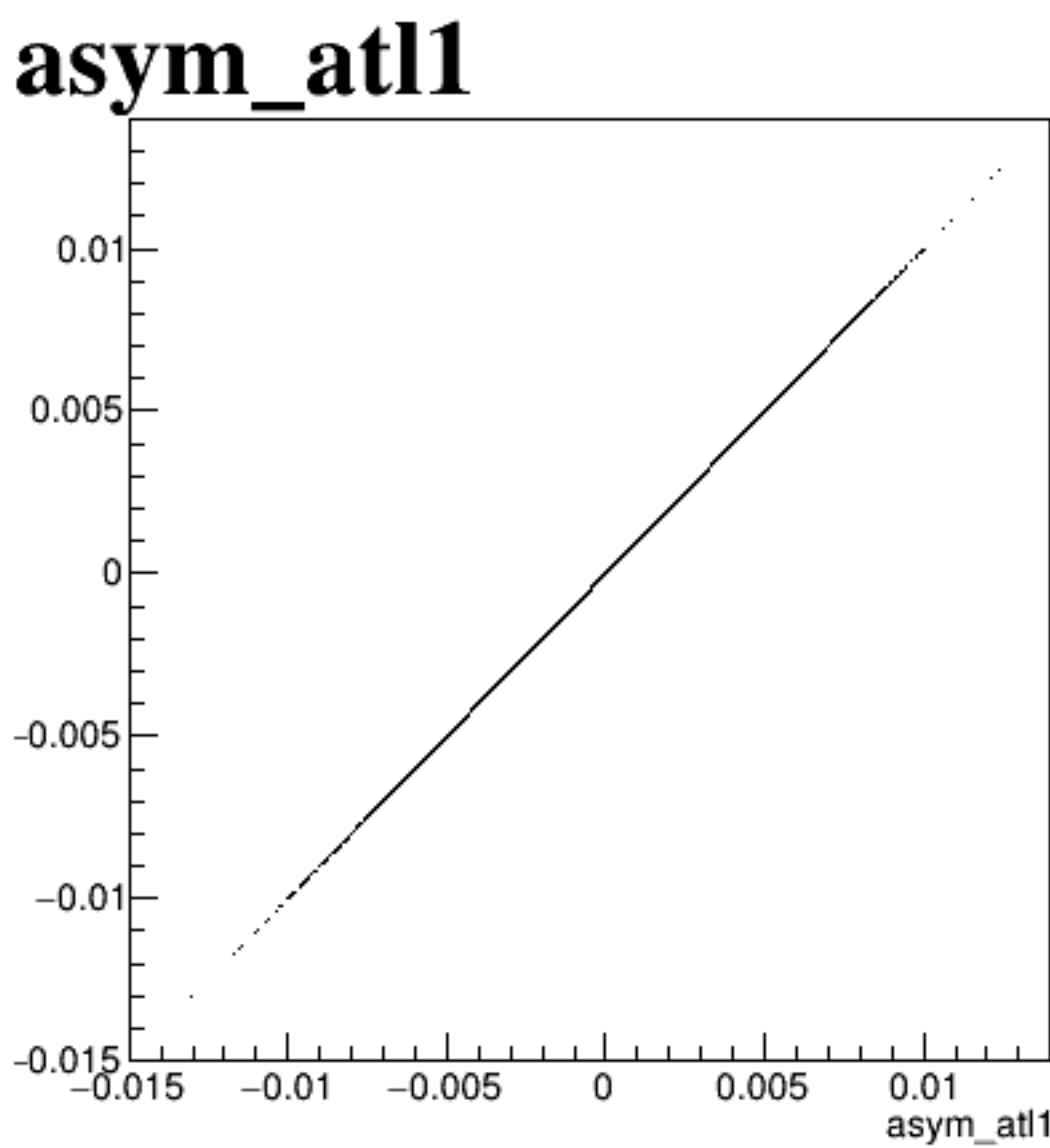
asym_atr1



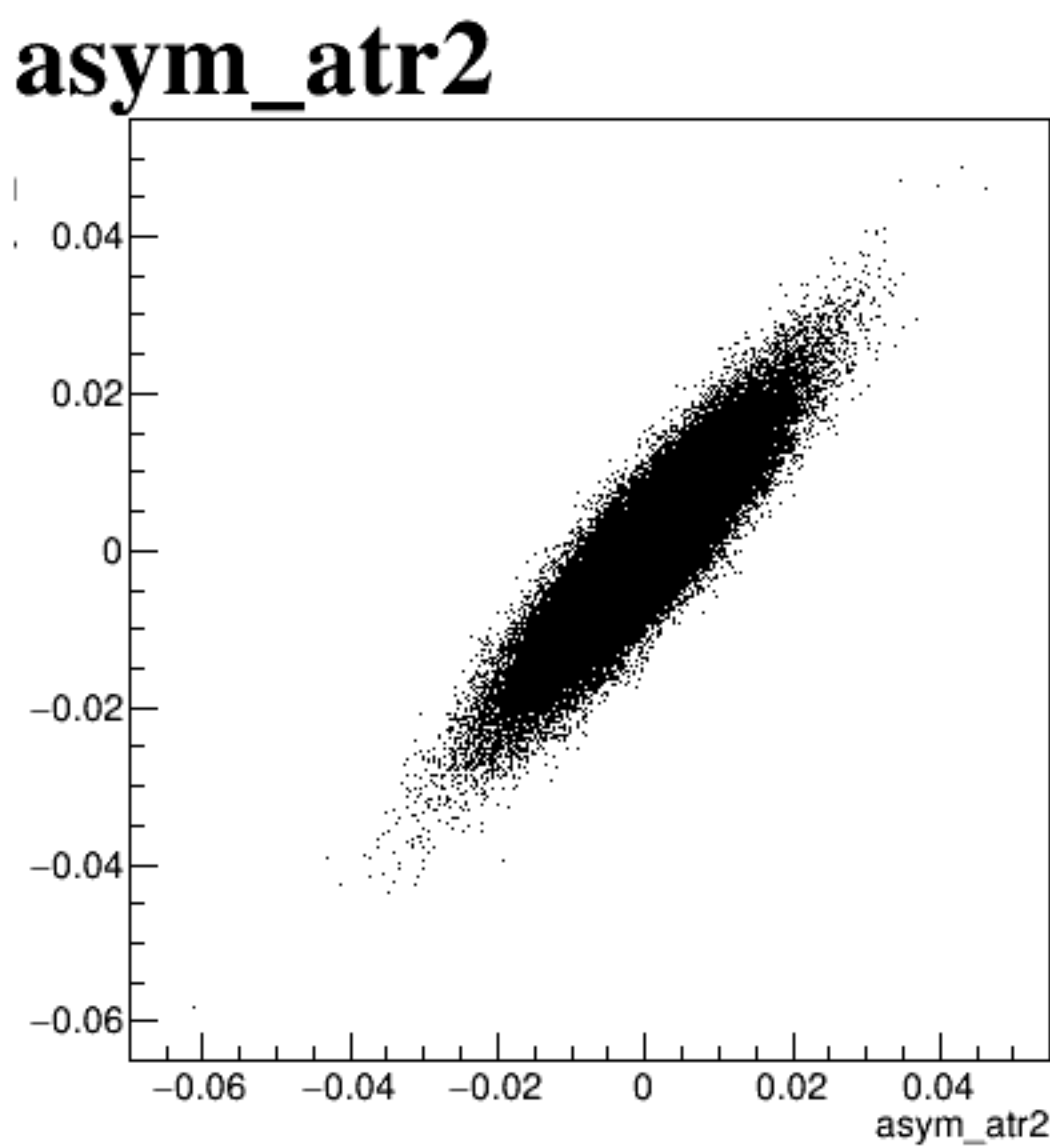
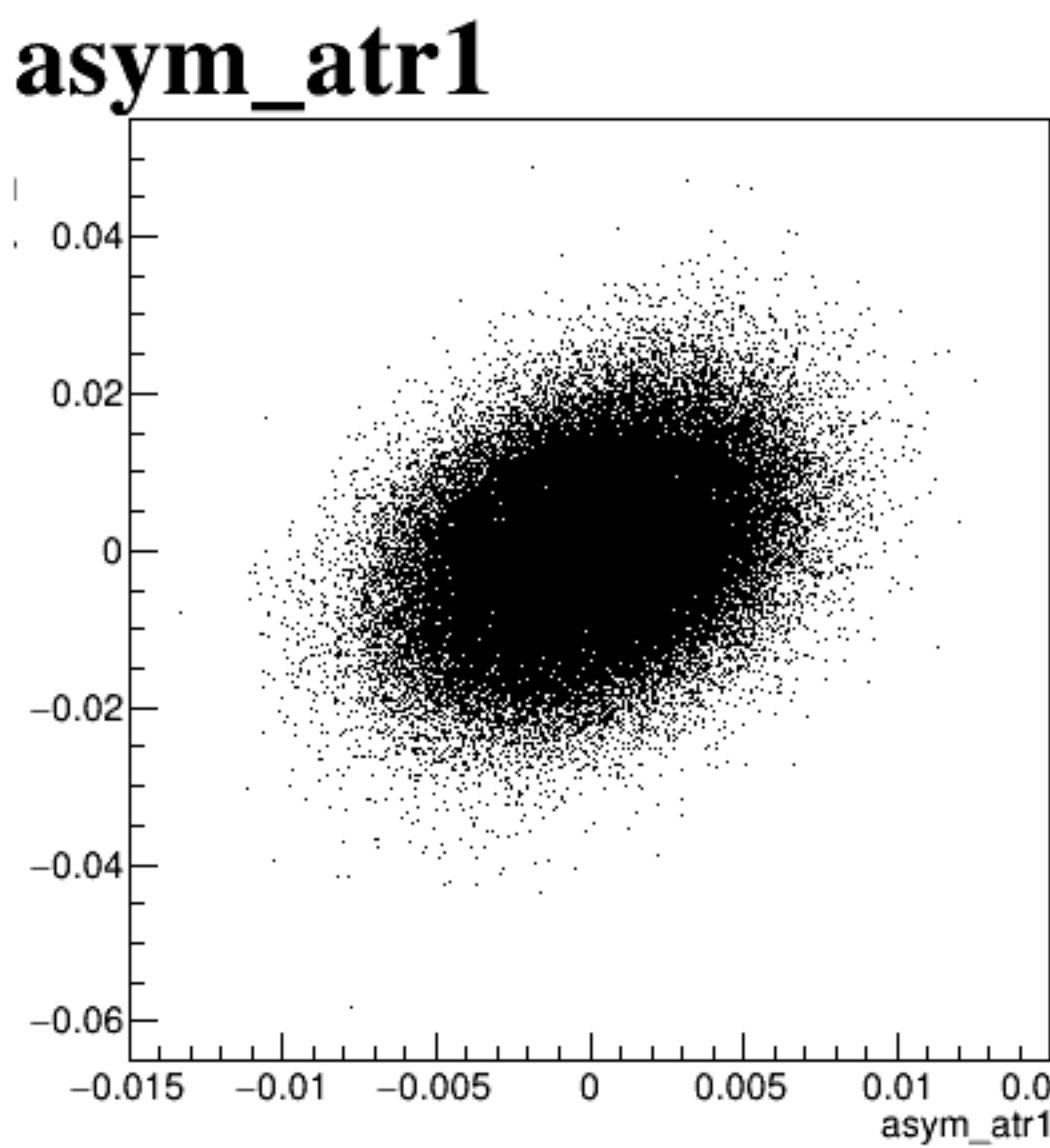
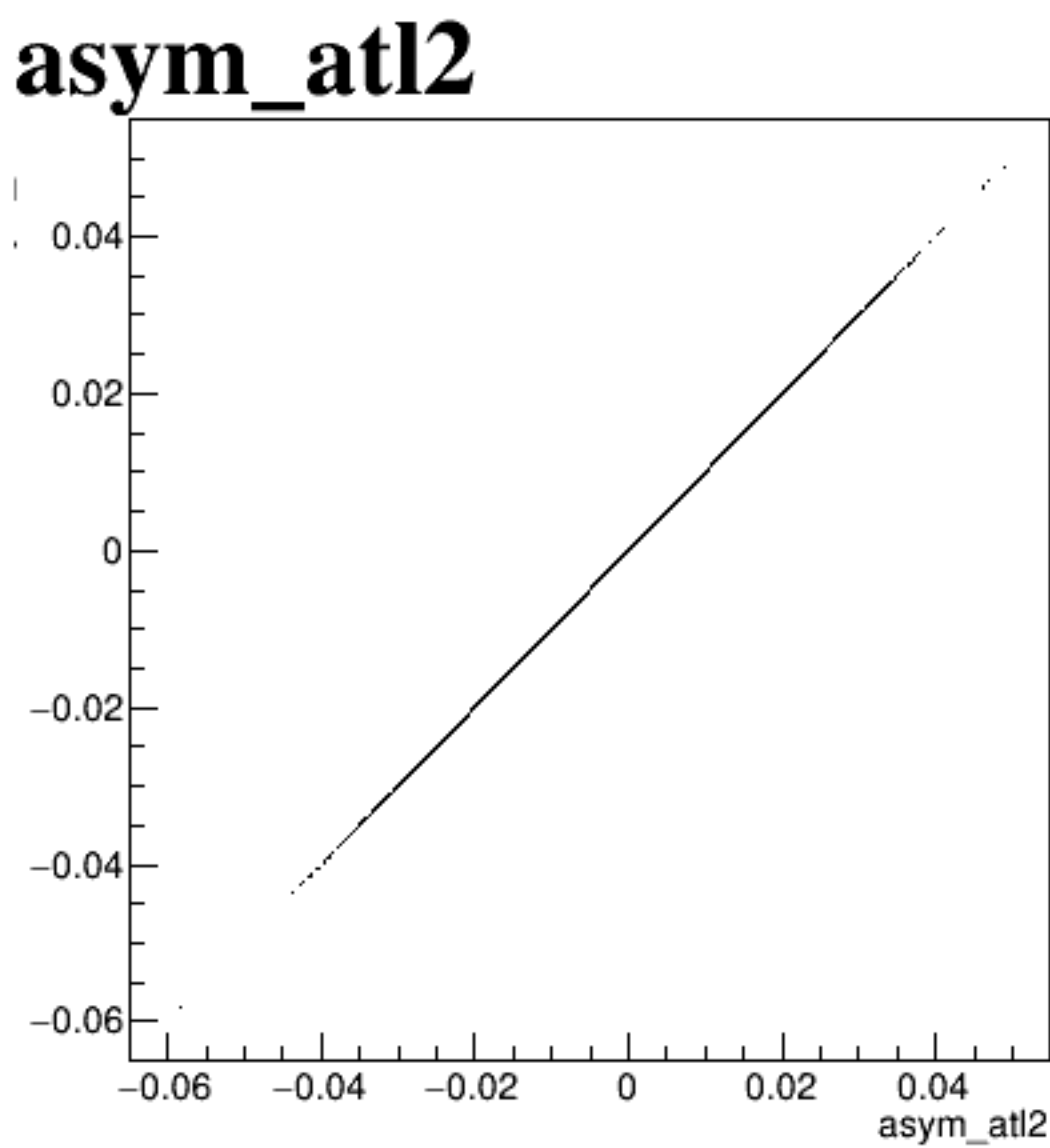
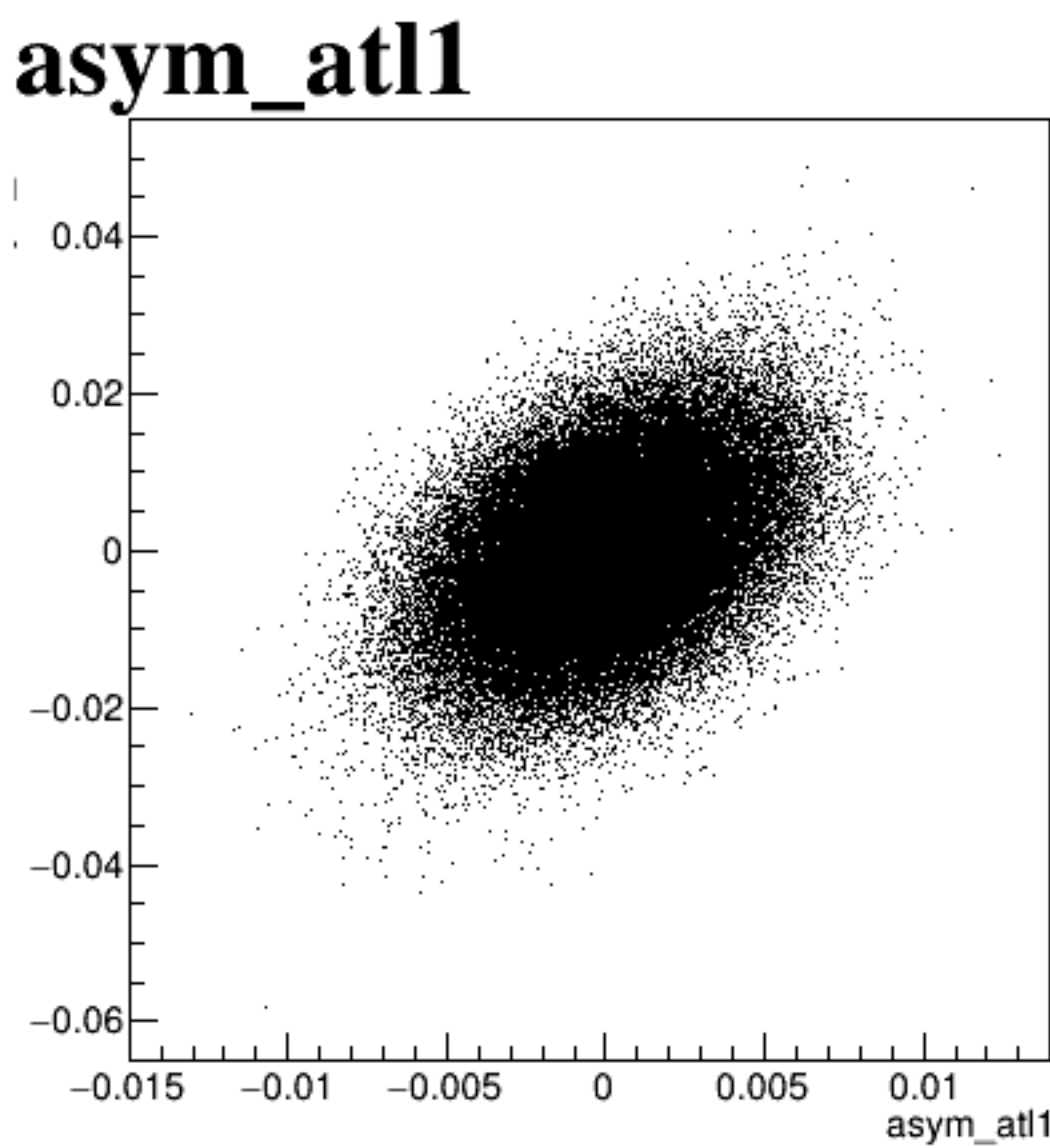
asym_atr2



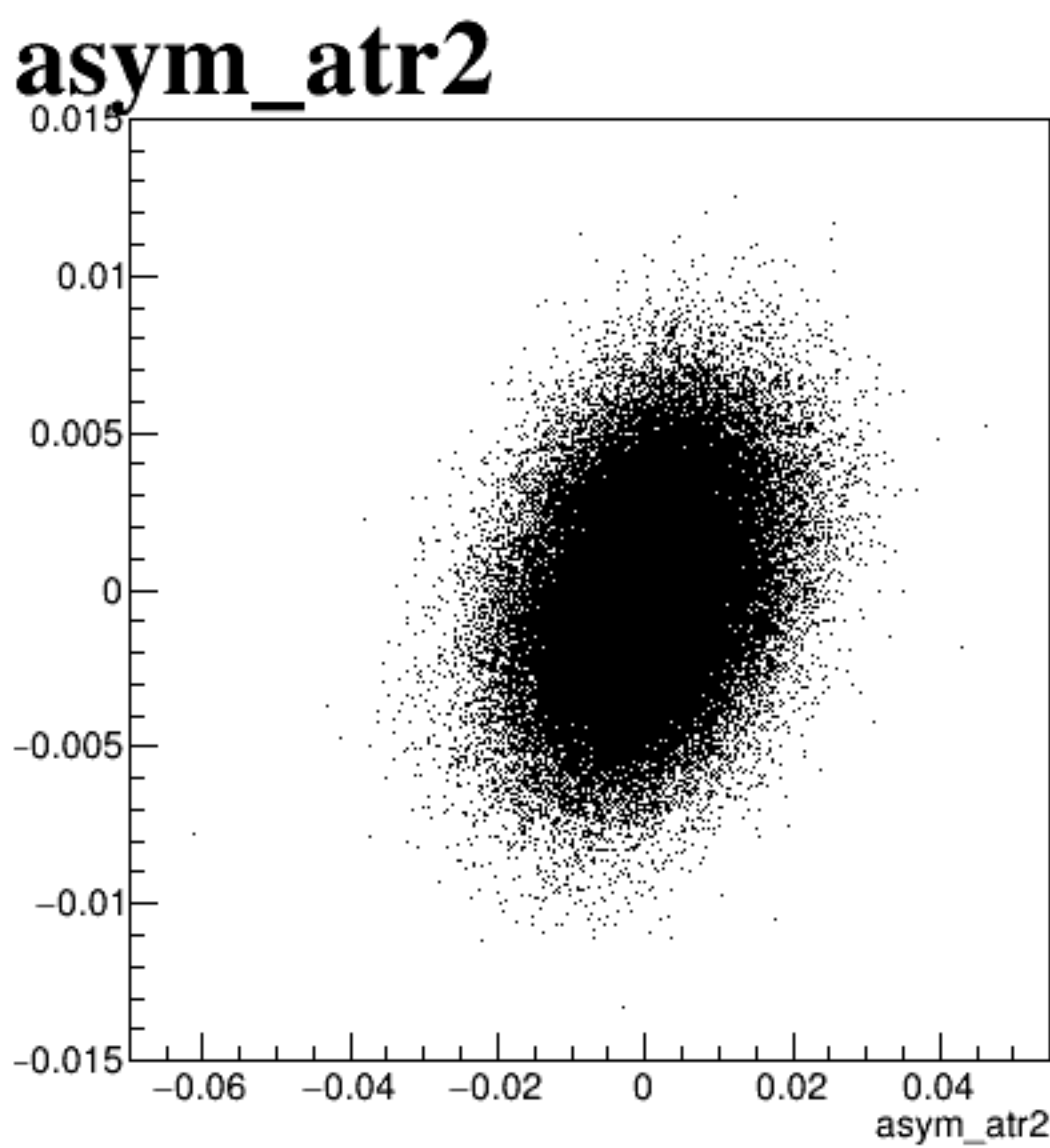
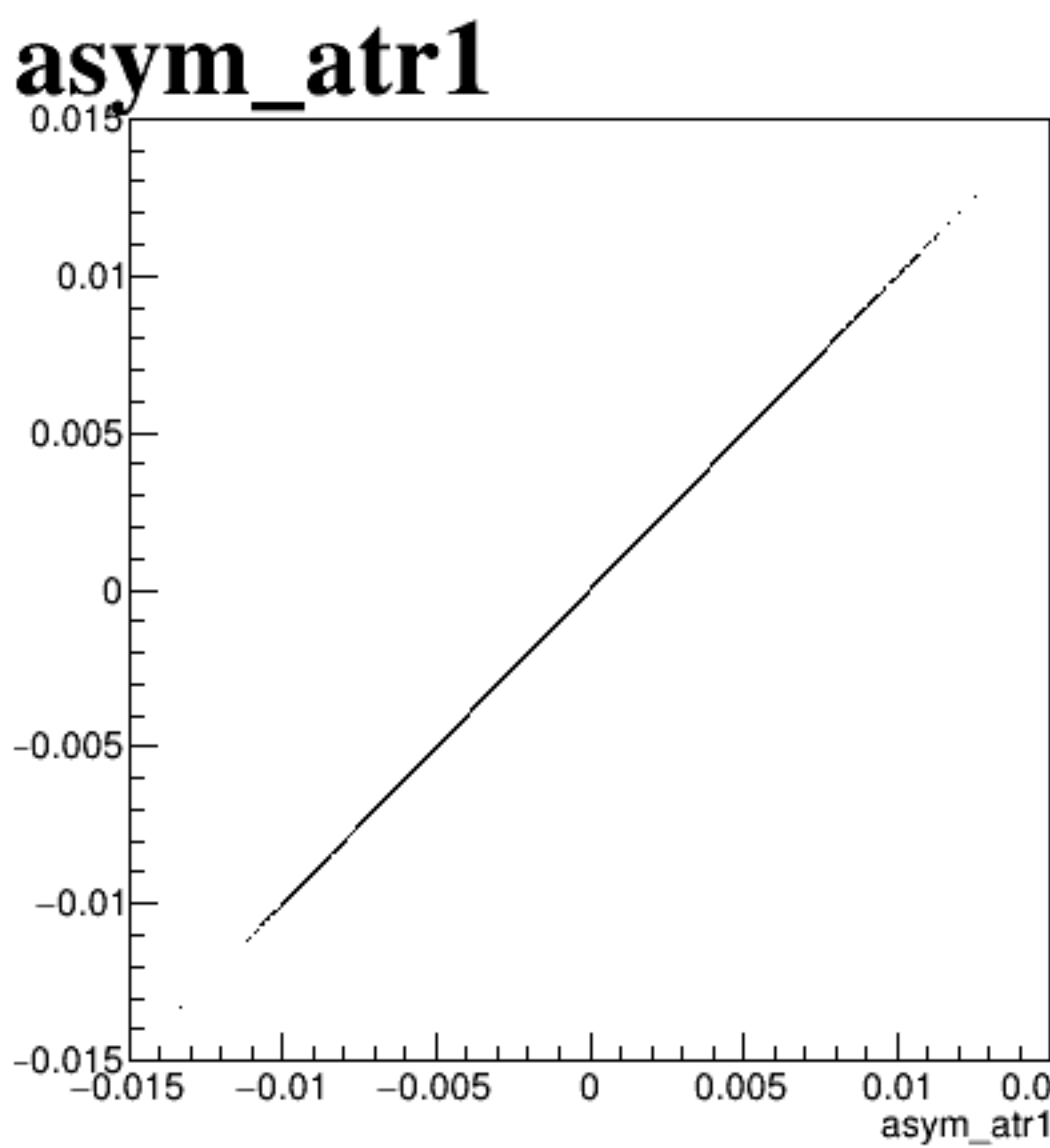
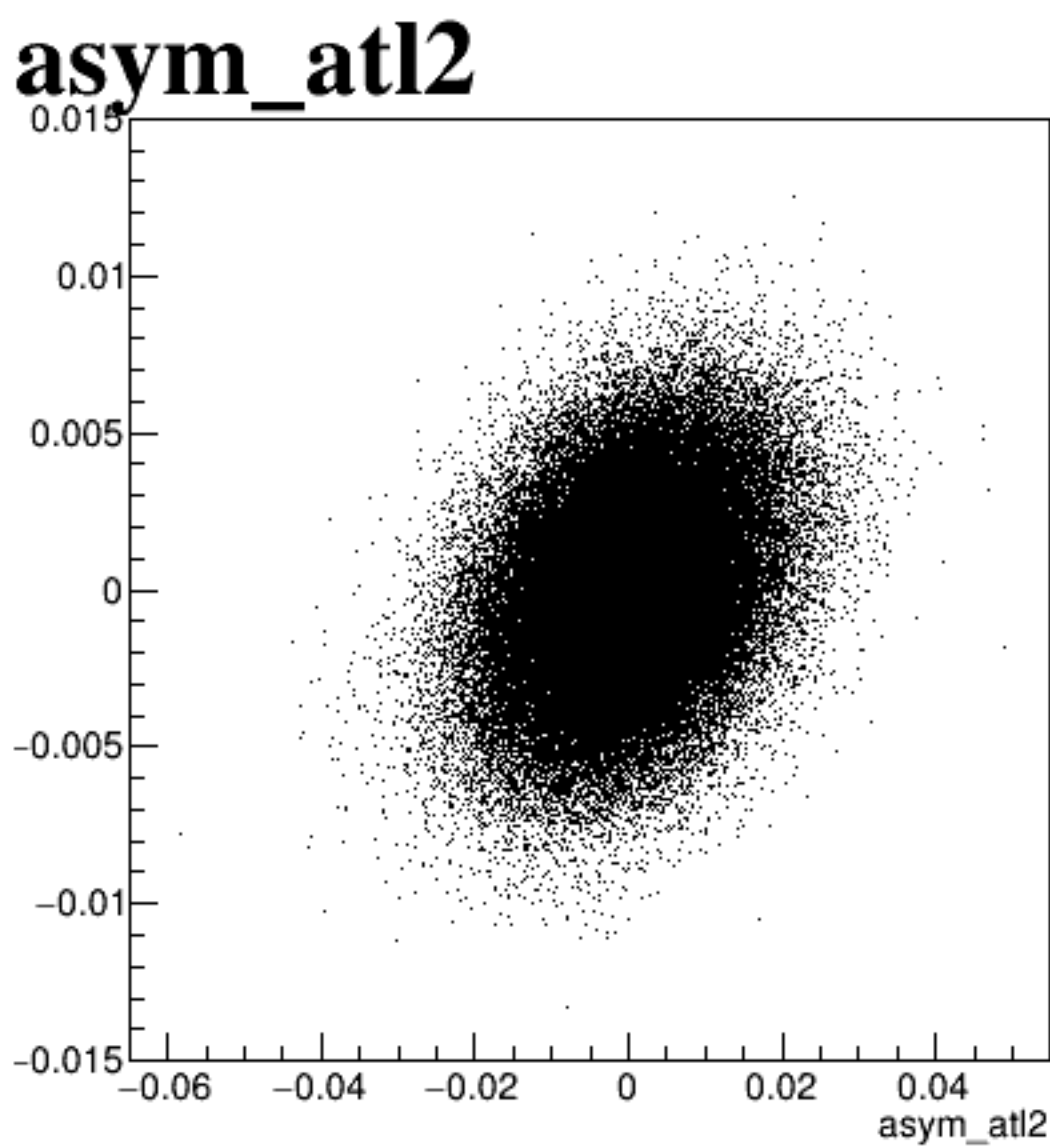
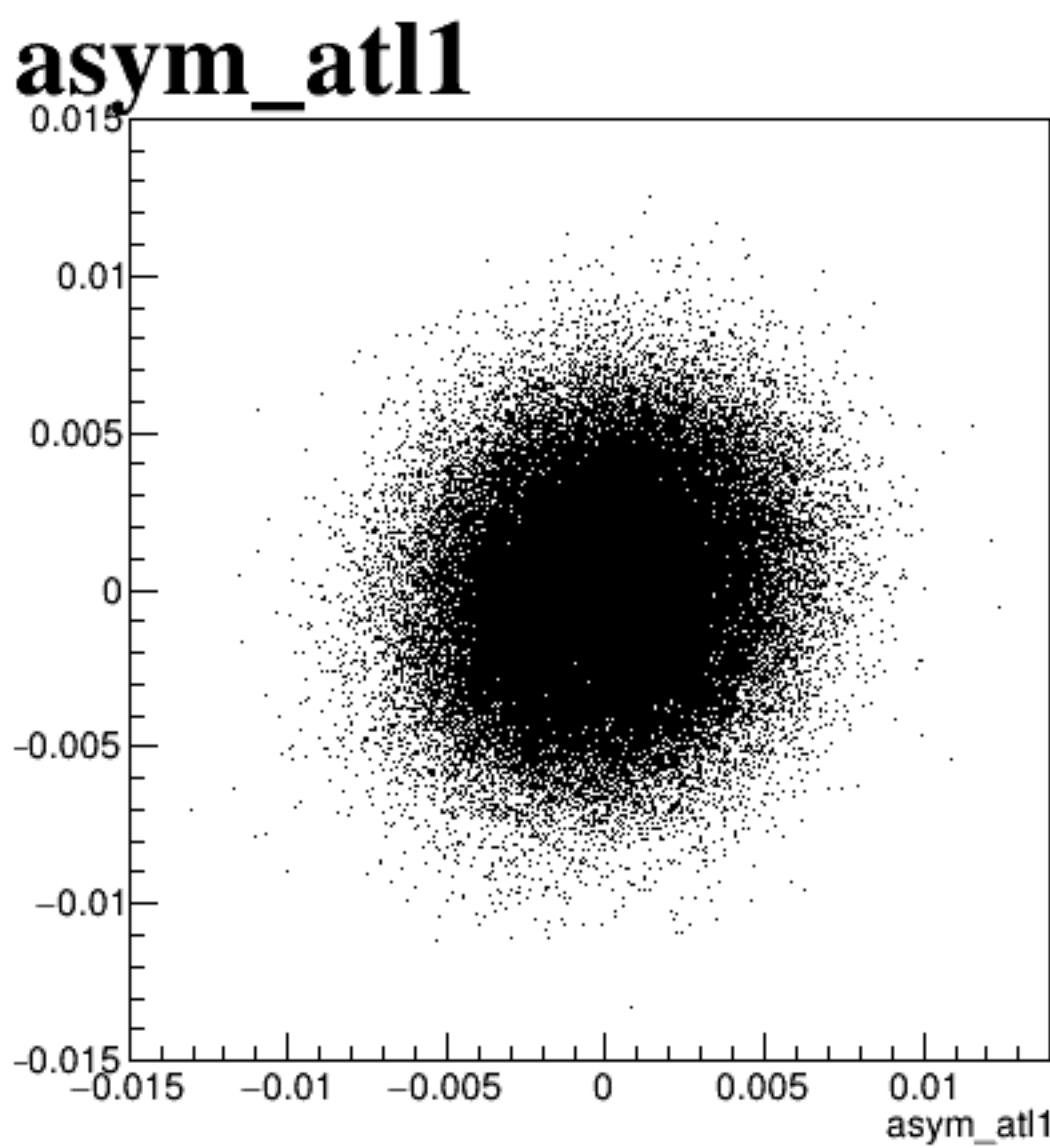
asym_atl1



asym_atl2



asym_atr1



asym_atr2

