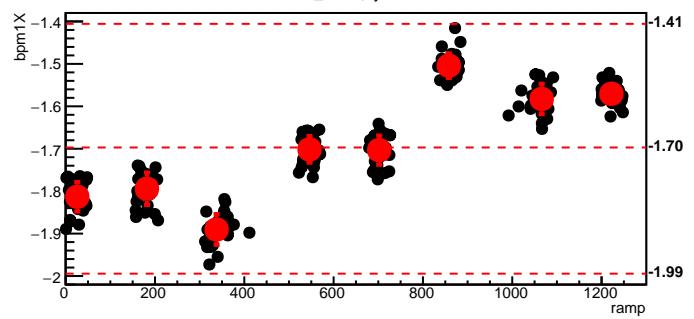
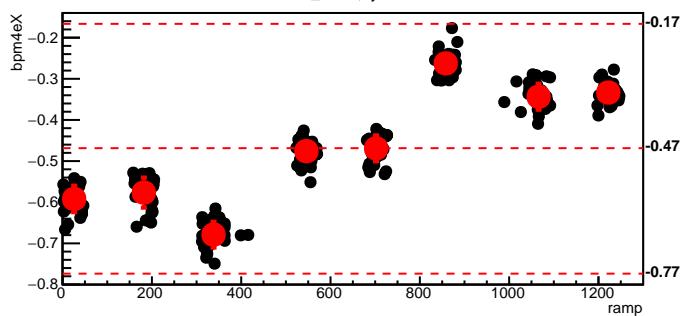


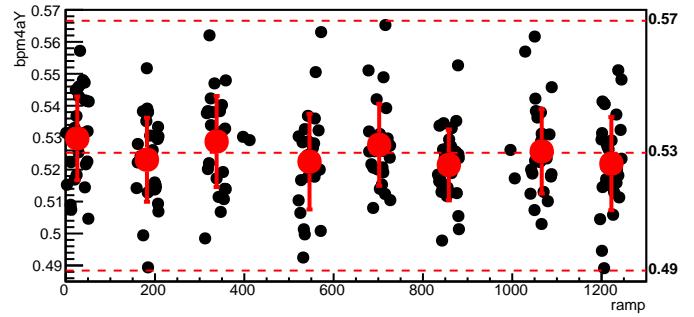
bmod Trim1,cyc=6565



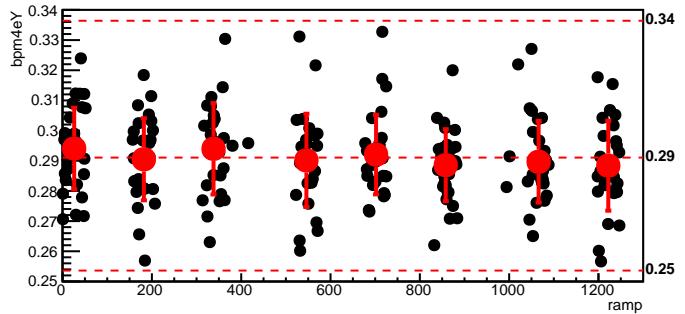
bmod Trim1,cyc=6565



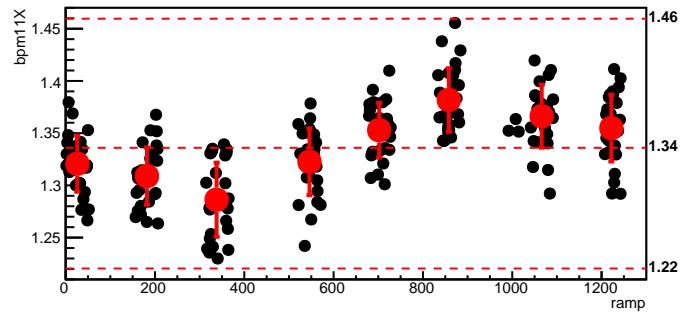
bmod Trim1,cyc=6565



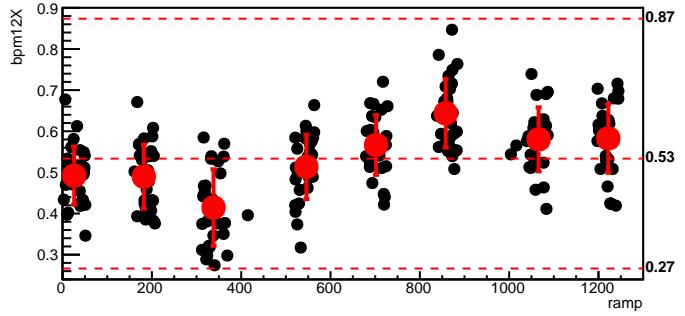
bmod Trim1,cyc=6565



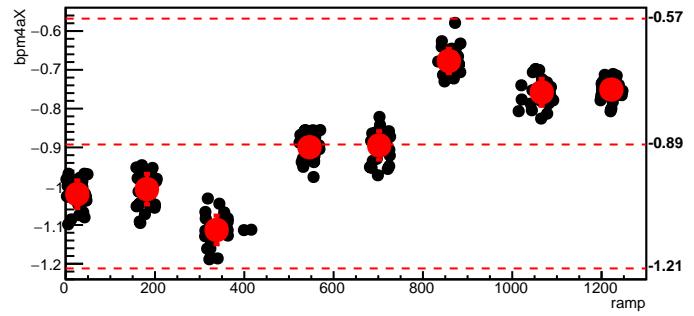
bmod Trim1,cyc=6565



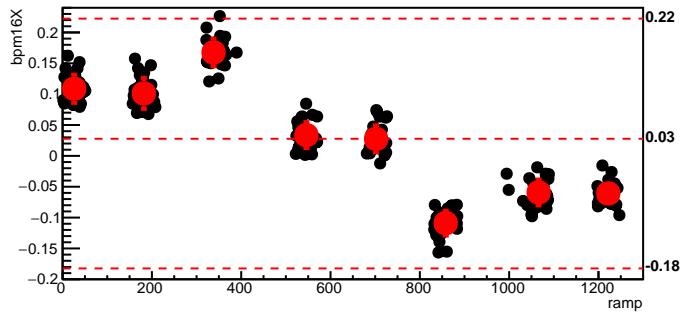
bmod Trim1,cyc=6565



bmod Trim1,cyc=6565

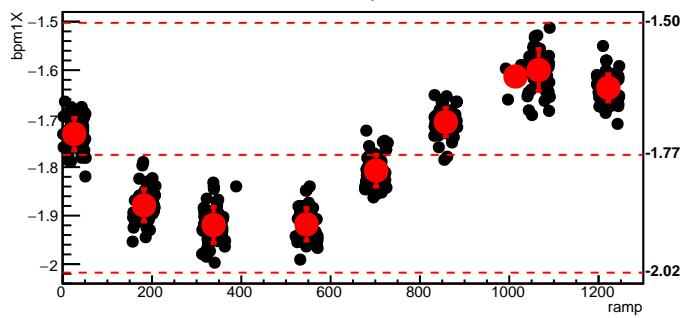


bmod Trim1,cyc=6565

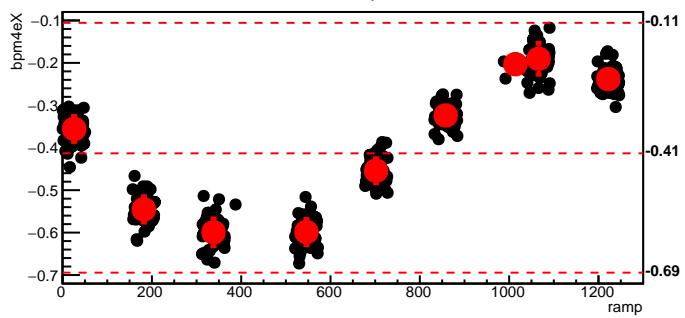




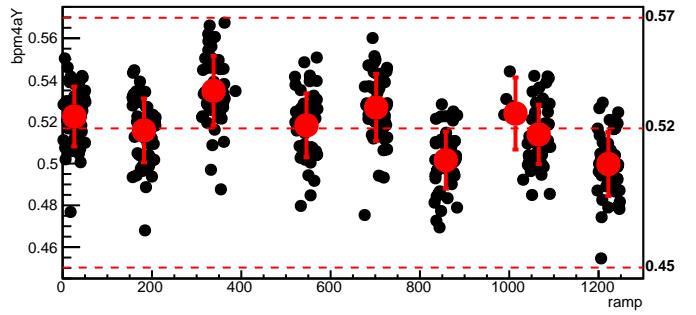
bmod Trim3,cyc=6565



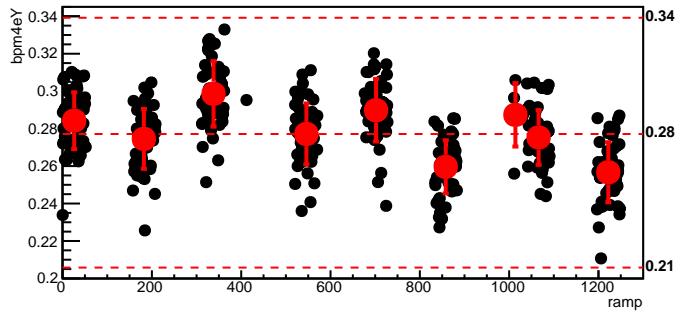
bmod Trim3,cyc=6565



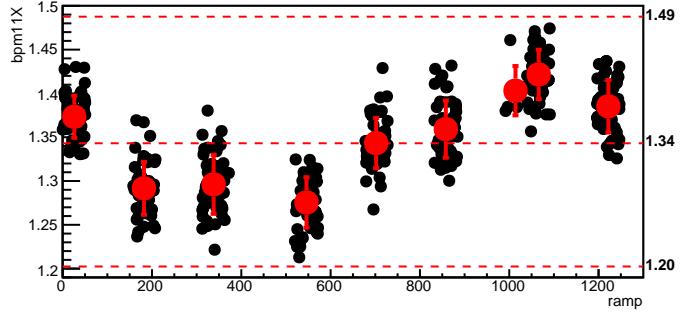
bmod Trim3,cyc=6565



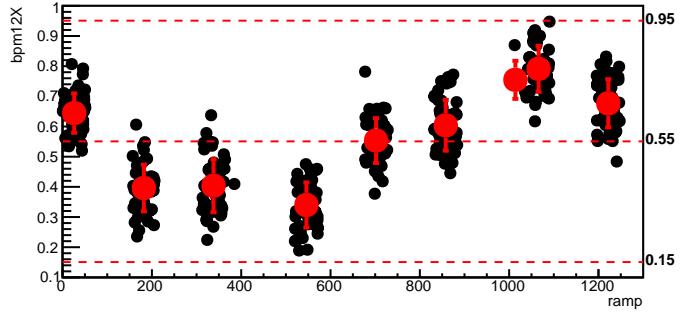
bmod Trim3,cyc=6565



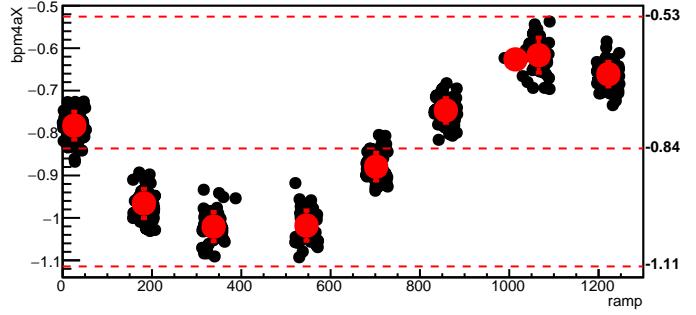
bmod Trim3,cyc=6565



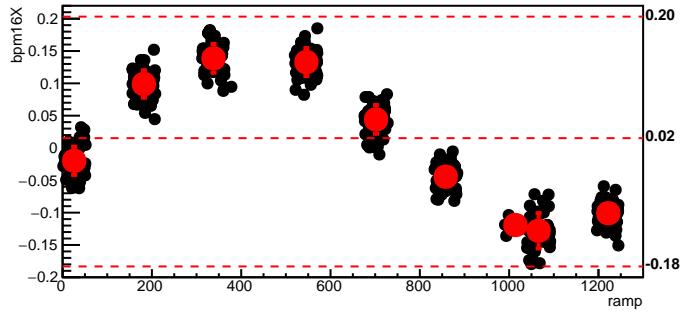
bmod Trim3,cyc=6565



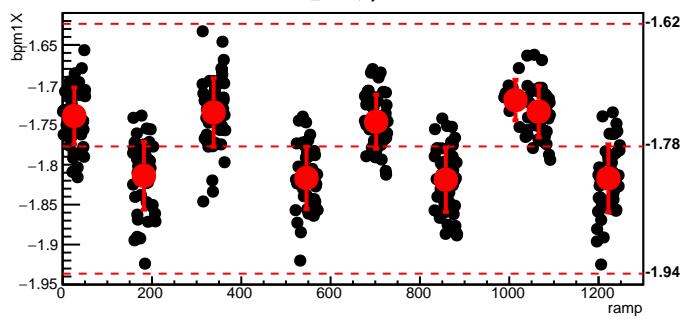
bmod Trim3,cyc=6565



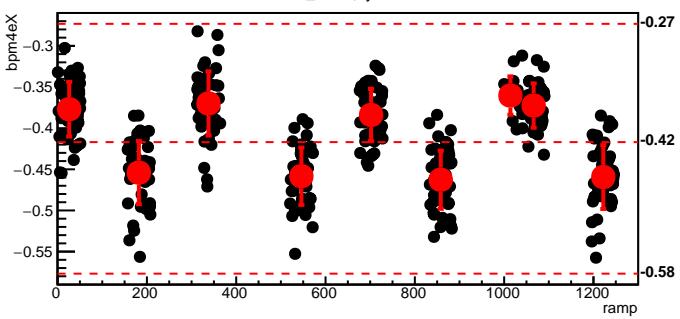
bmod Trim3,cyc=6565



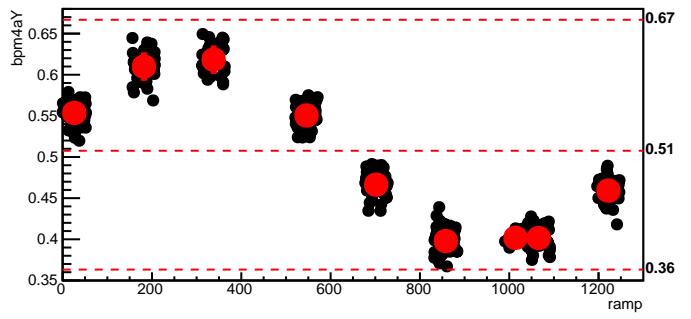
bmod Trim4,cyc=6565



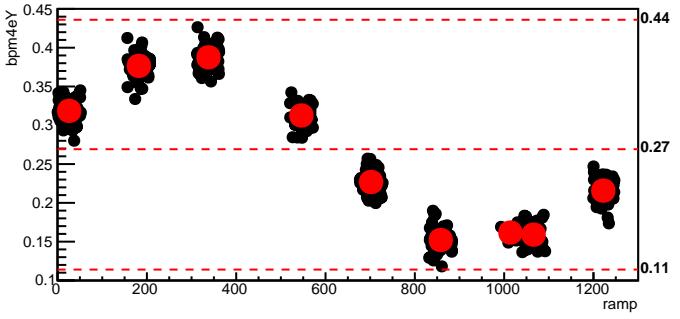
bmod Trim4,cyc=6565



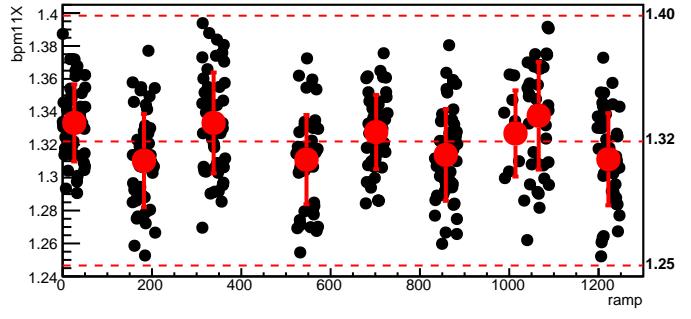
bmod Trim4,cyc=6565



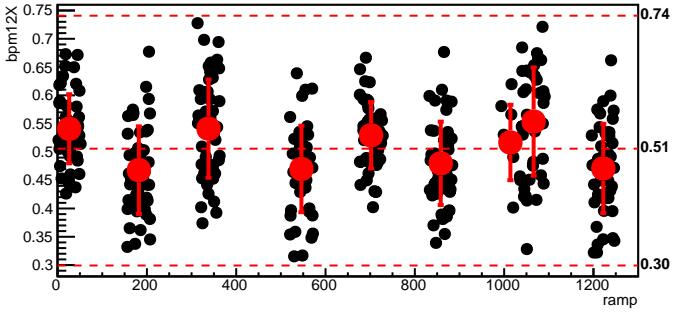
bmod Trim4,cyc=6565



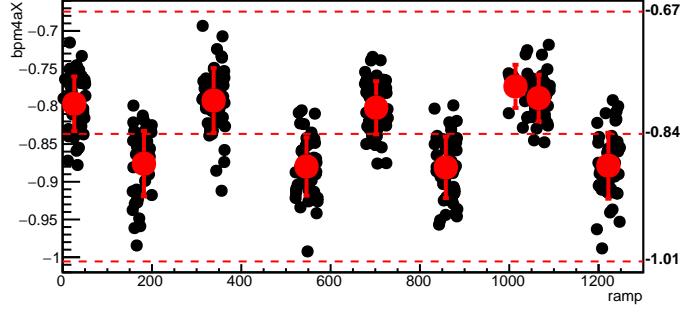
bmod Trim4,cyc=6565



bmod Trim4,cyc=6565



bmod Trim4,cyc=6565



bmod Trim4,cyc=6565

