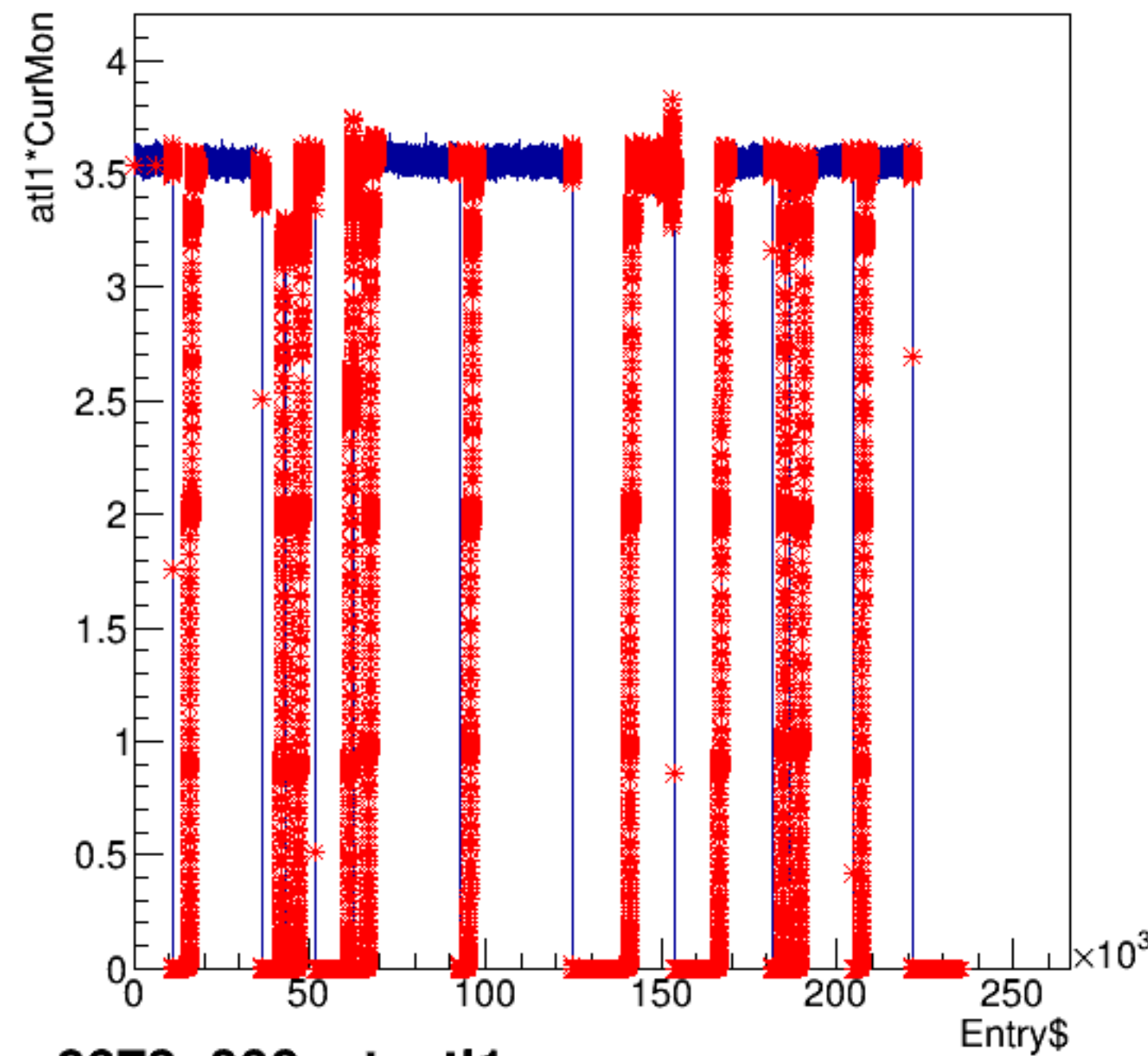
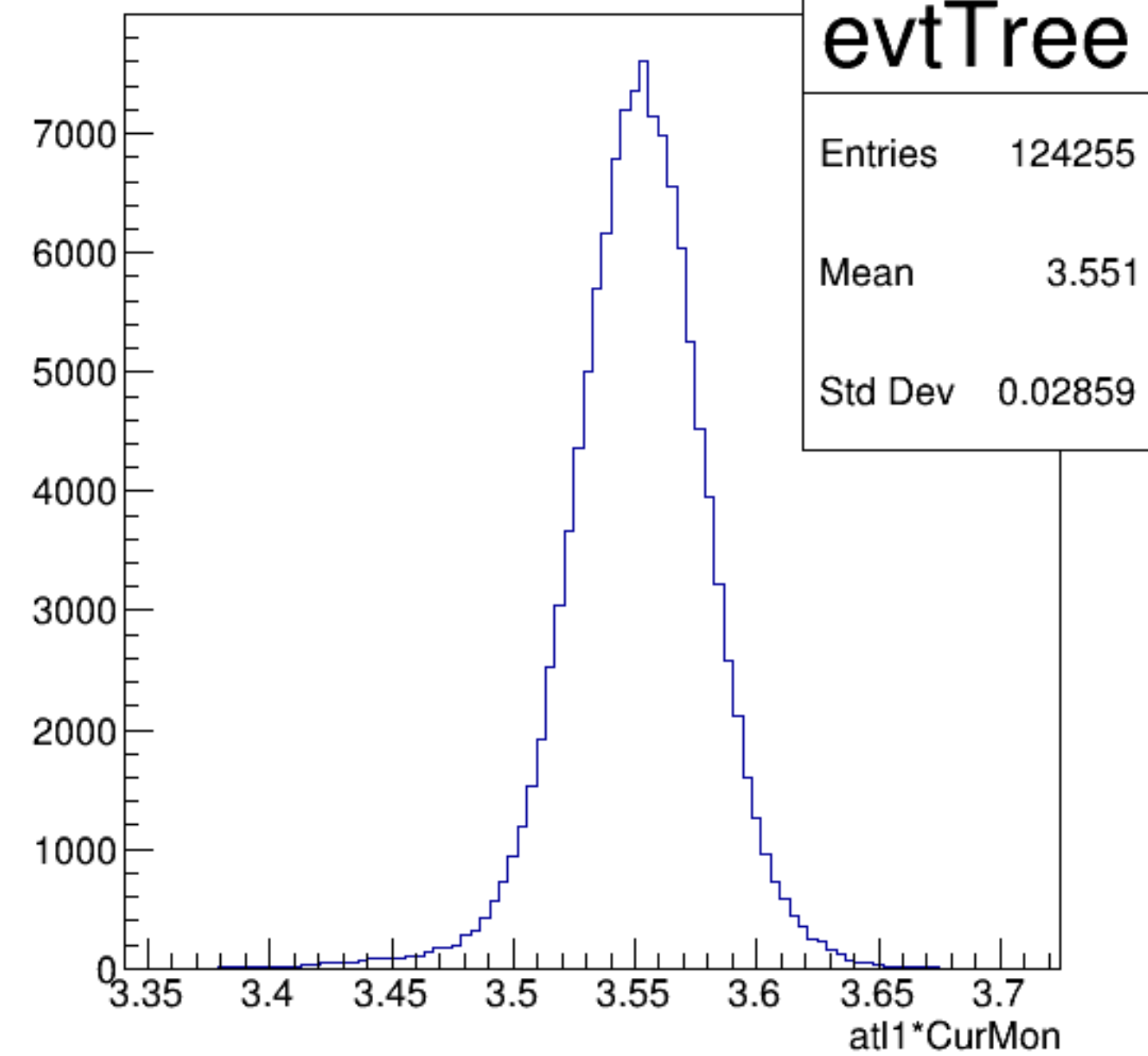


atl1*CurMon:Entry\$

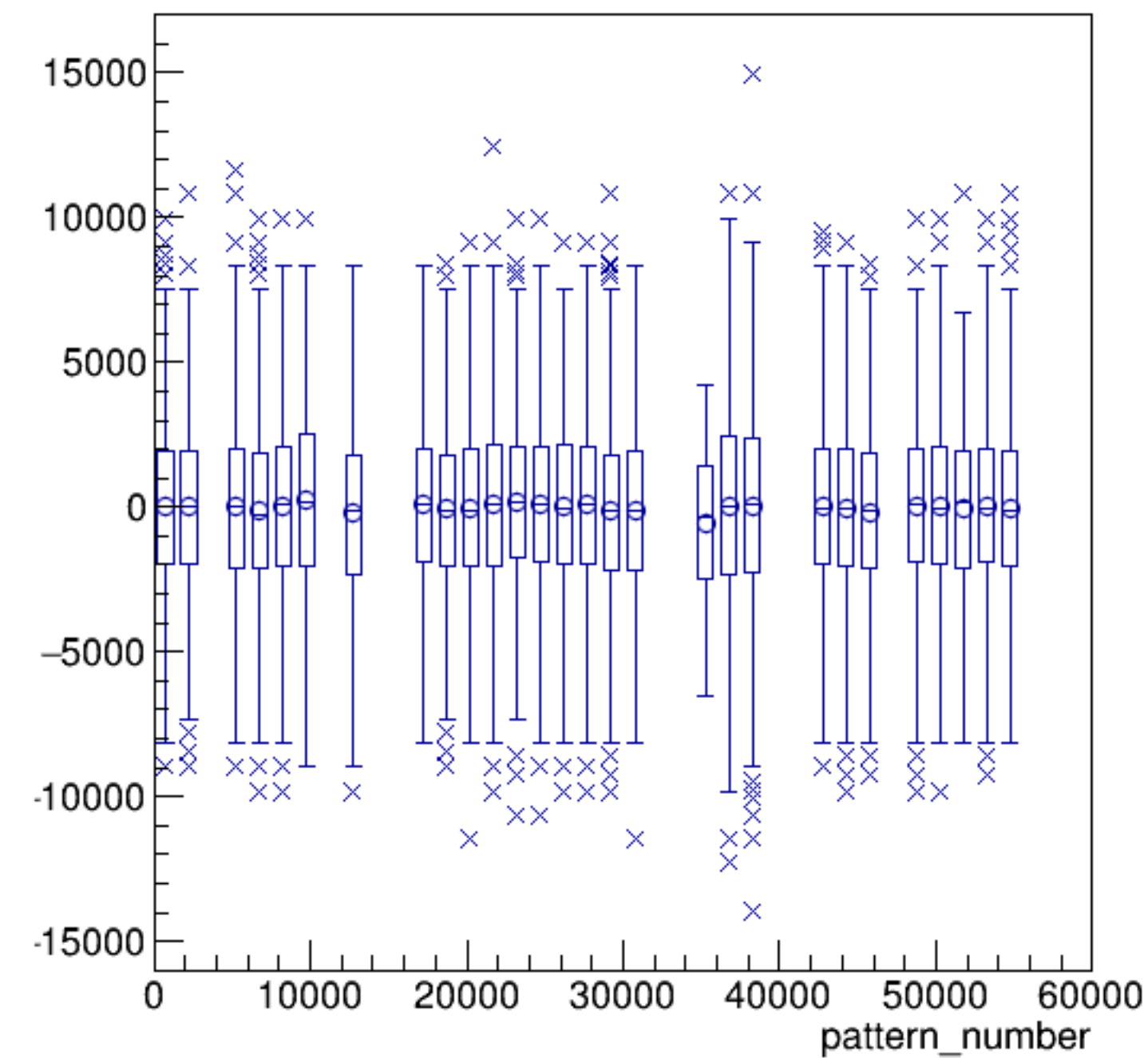


run6673_000_at_atl1.png

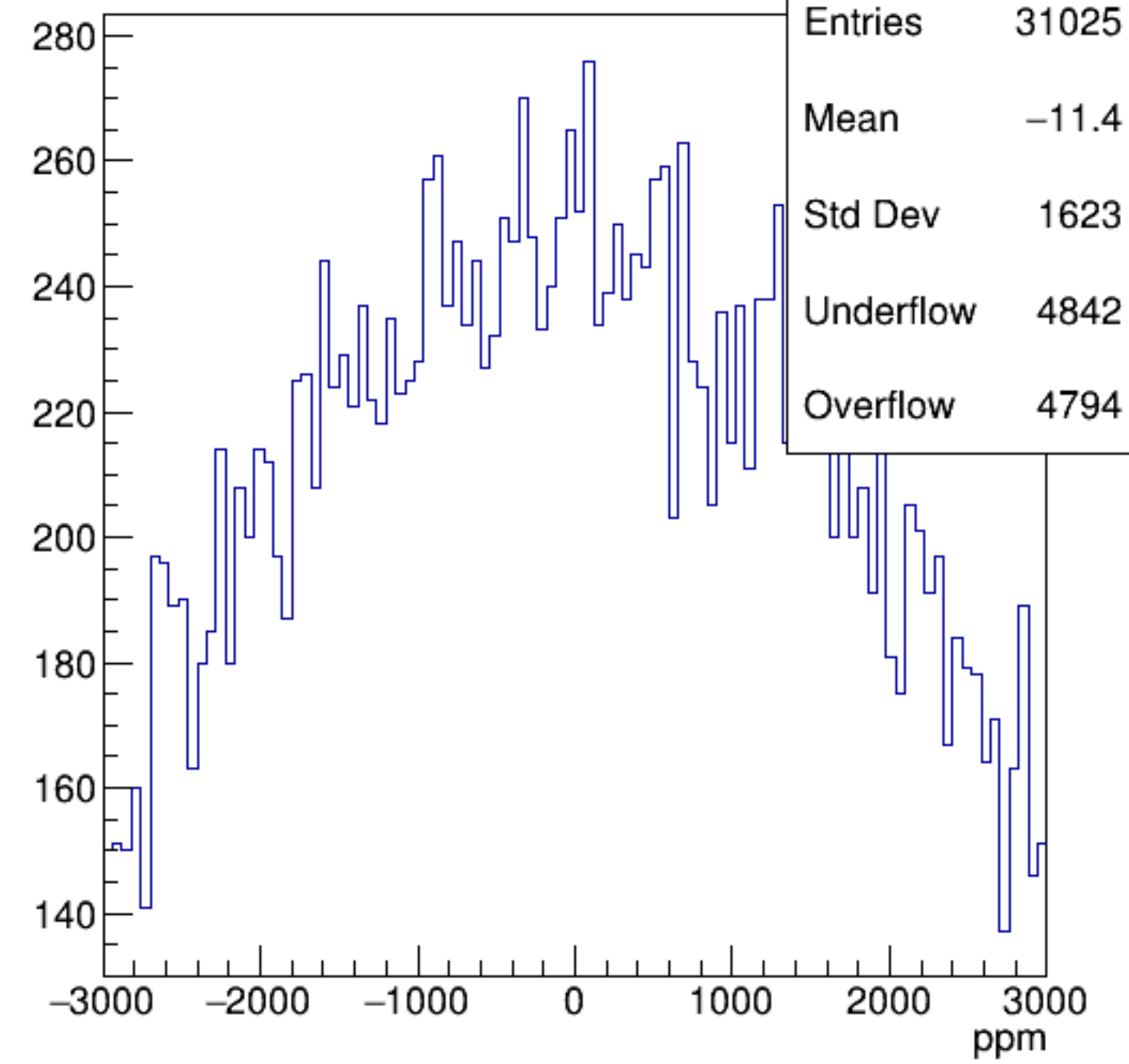
atl1*CurMon {ErrorFlag==0}



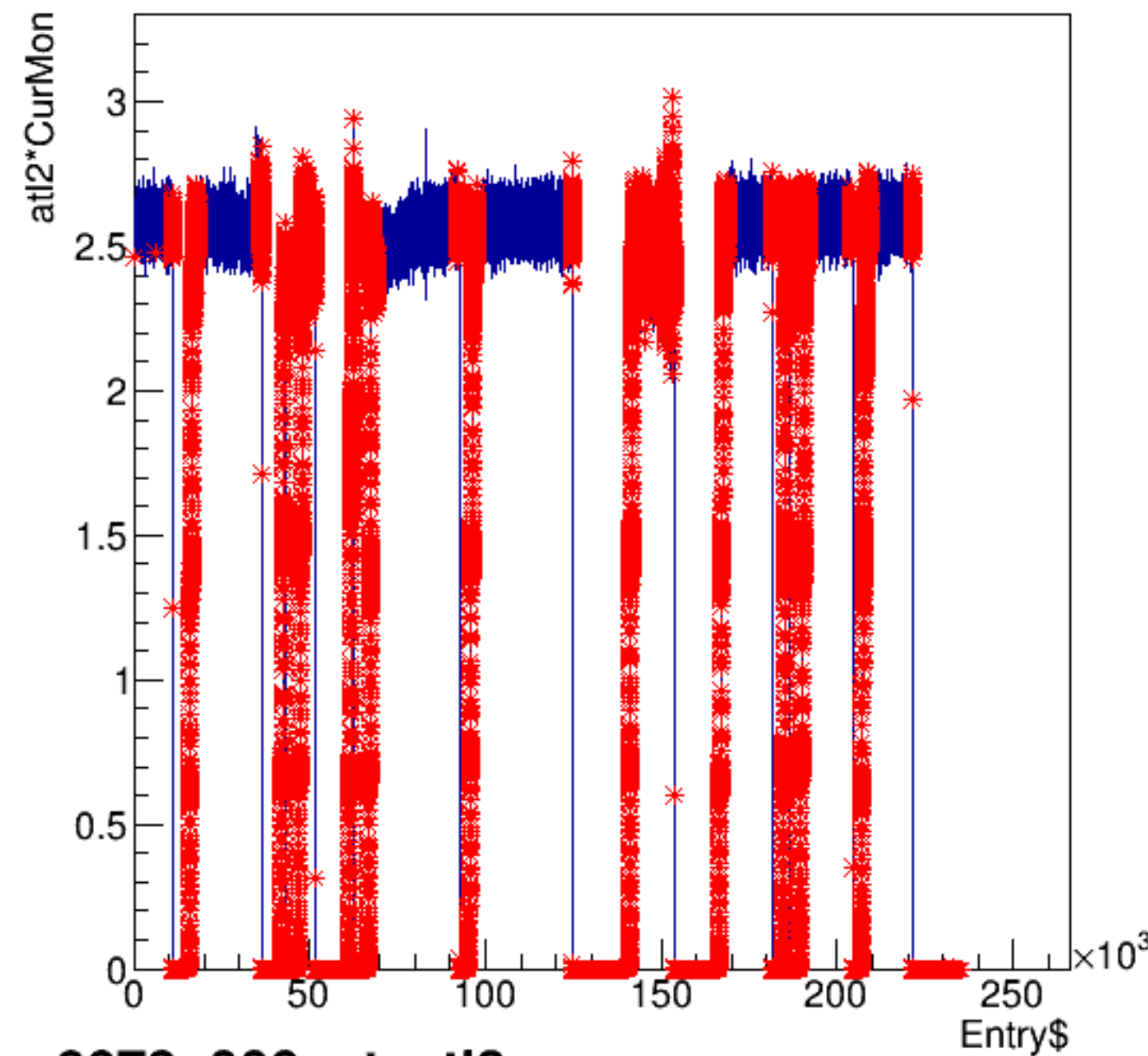
asym_atl1/ppm:pattern_number {ErrorFlag==0}



asym_atl1

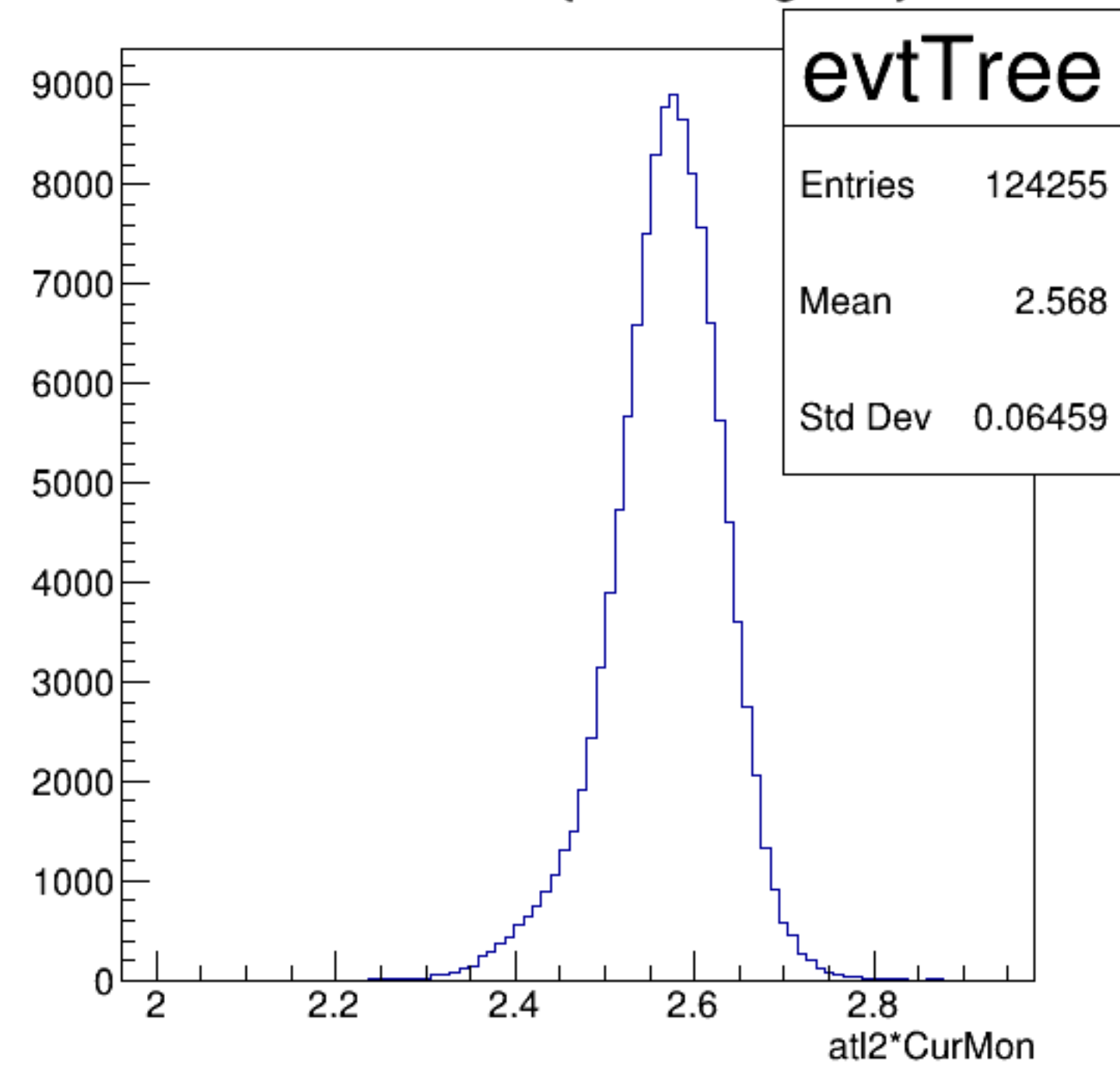


atl2*CurMon:Entry\$



run6673_000_at_atl2.png

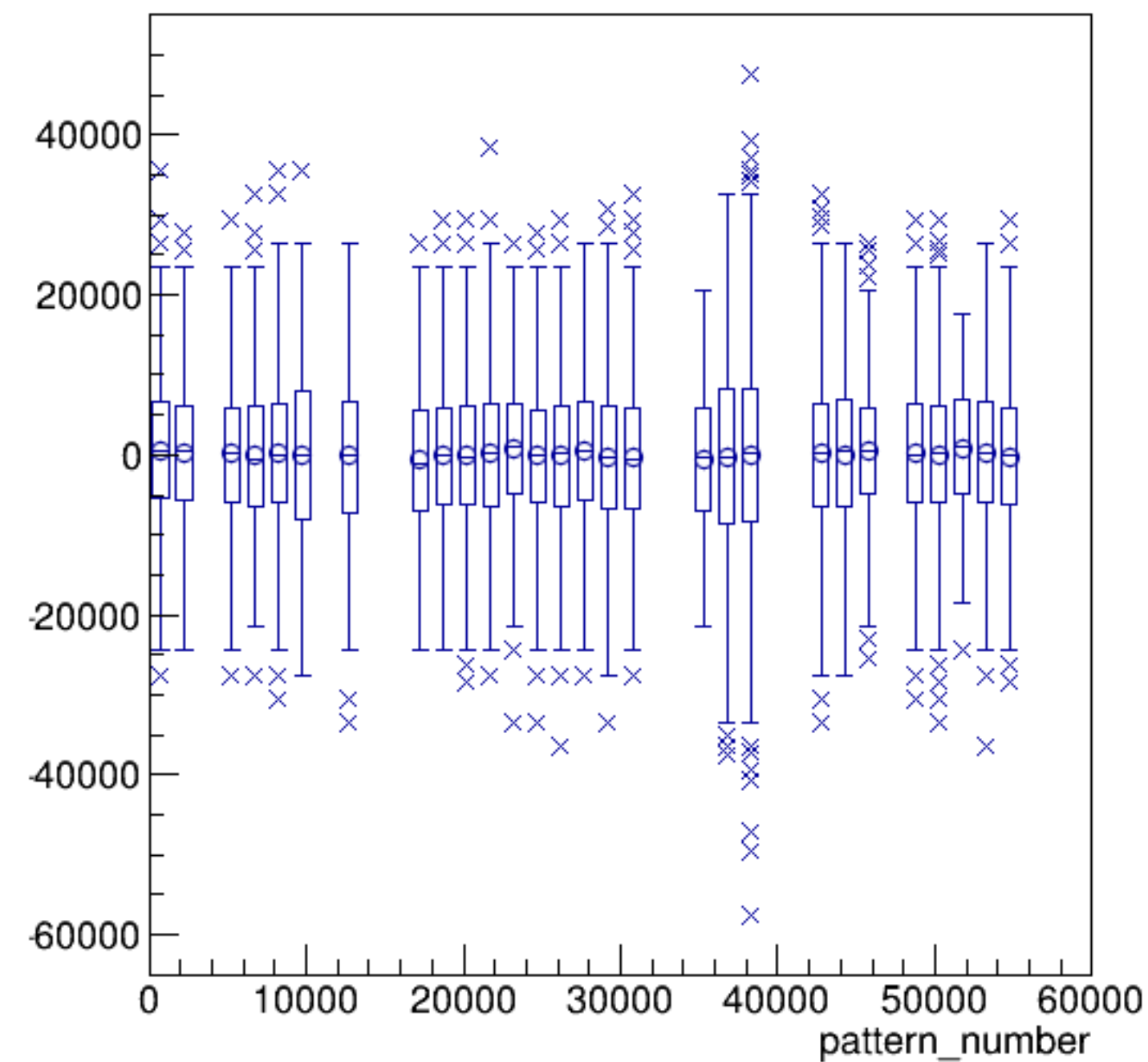
atl2*CurMon {ErrorFlag==0}



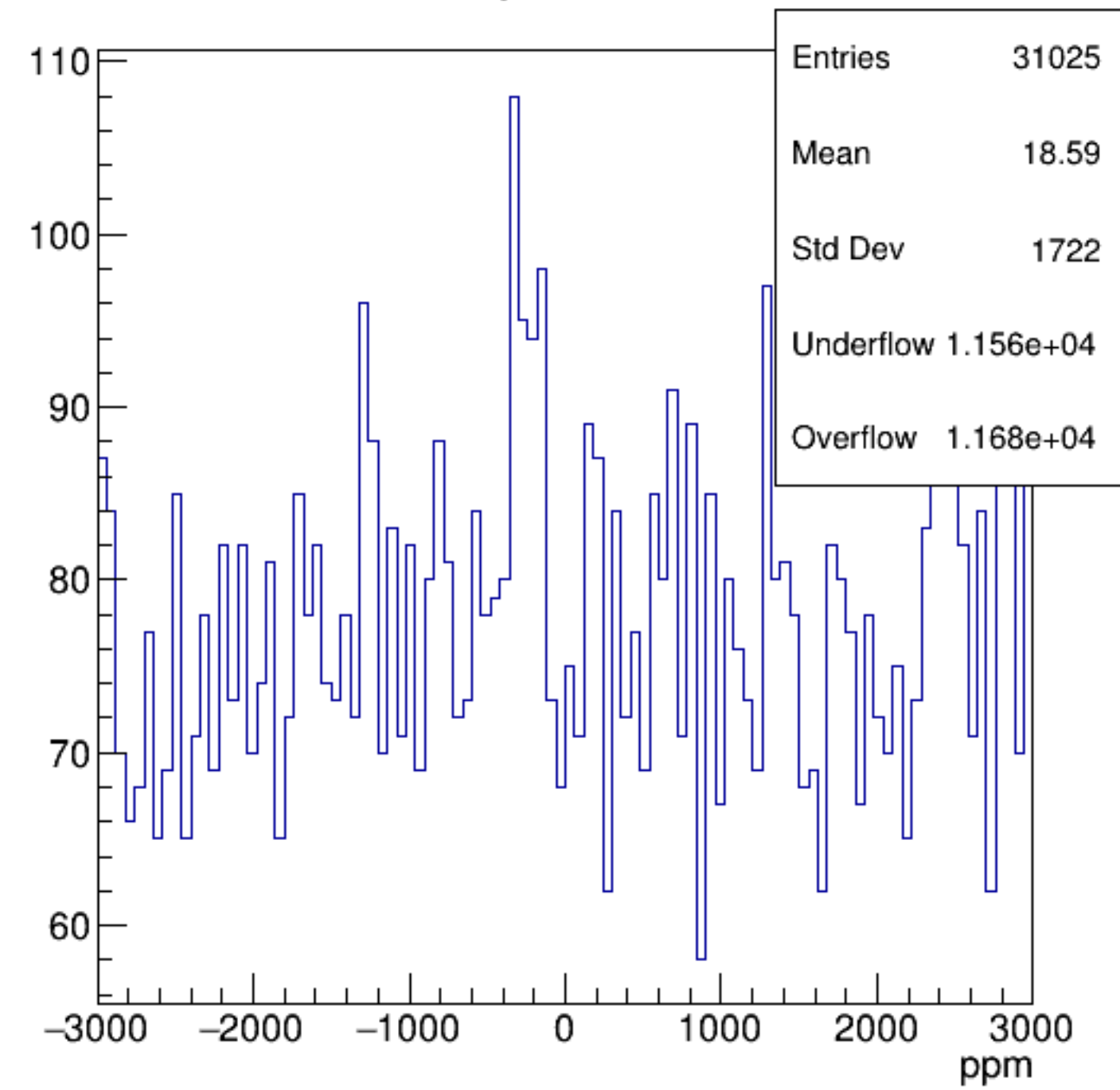
evtTree

Entries	124255
Mean	2.568
Std Dev	0.06459

asym_atl2/ppm:pattern_number {ErrorFlag==0}

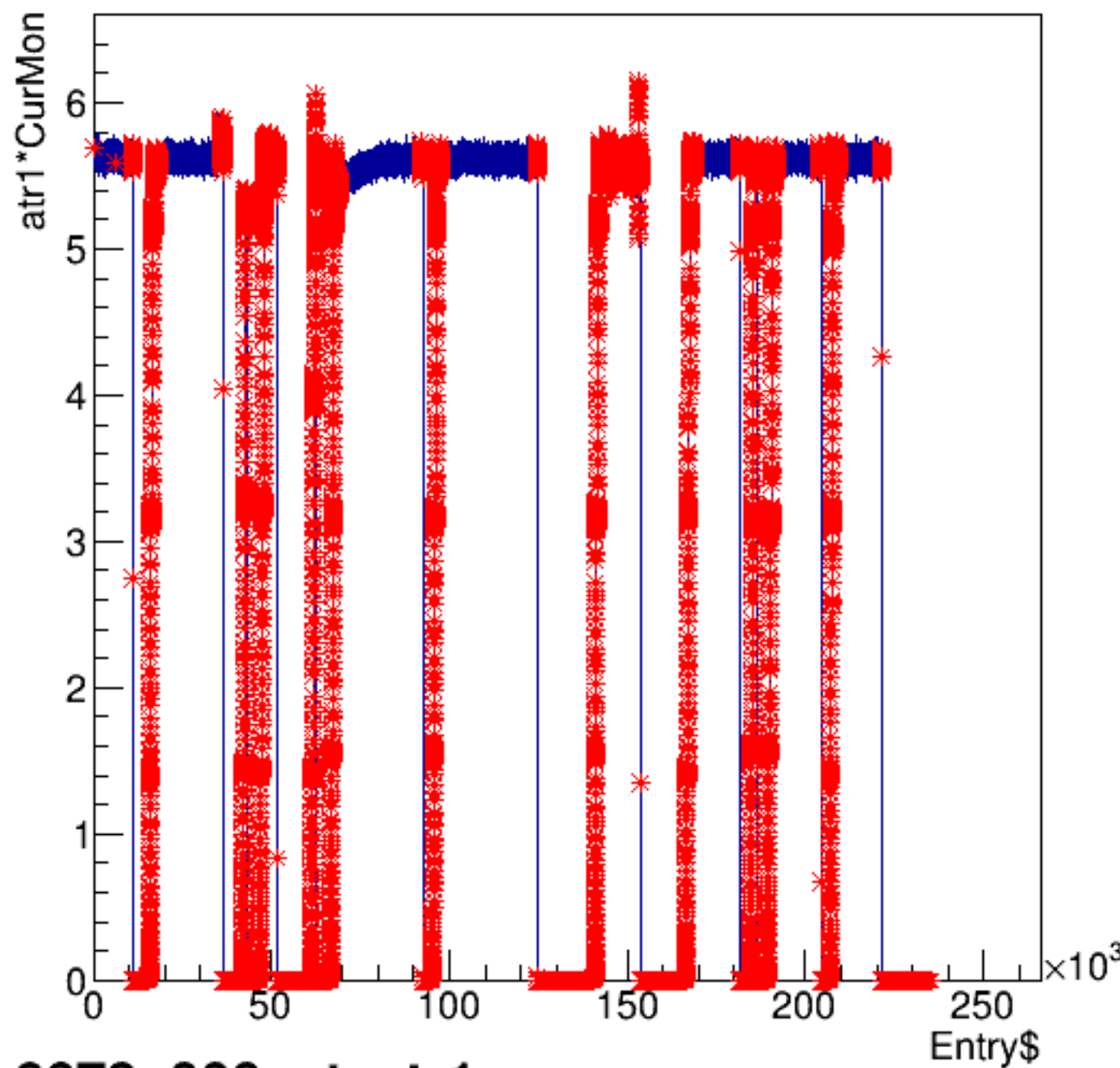


asym_atl2



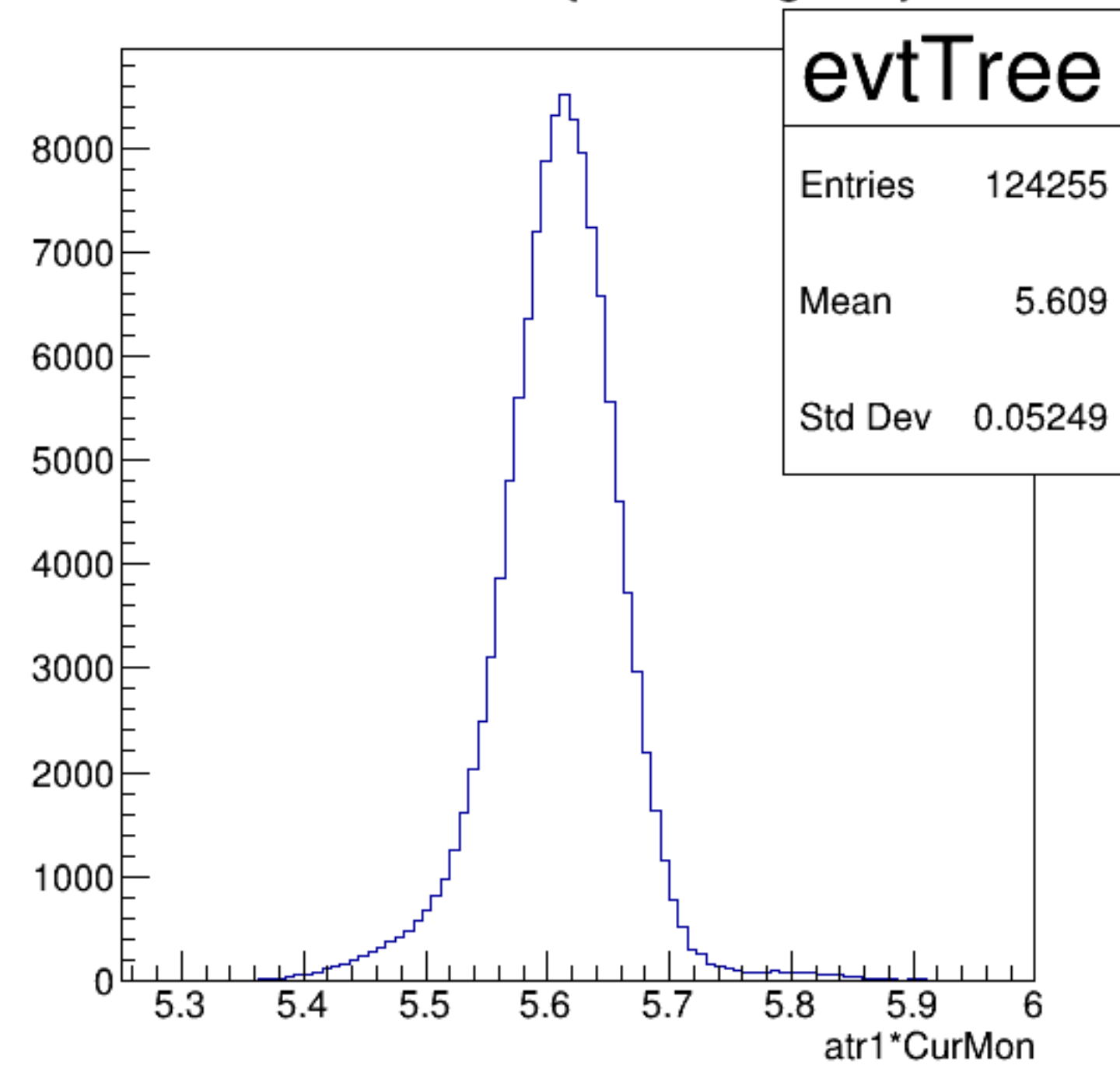
Entries	31025
Mean	18.59
Std Dev	1722
Underflow	1.156e+04
Overflow	1.168e+04

atr1*CurMon:Entry\$



run6673_000_at_atr1.png

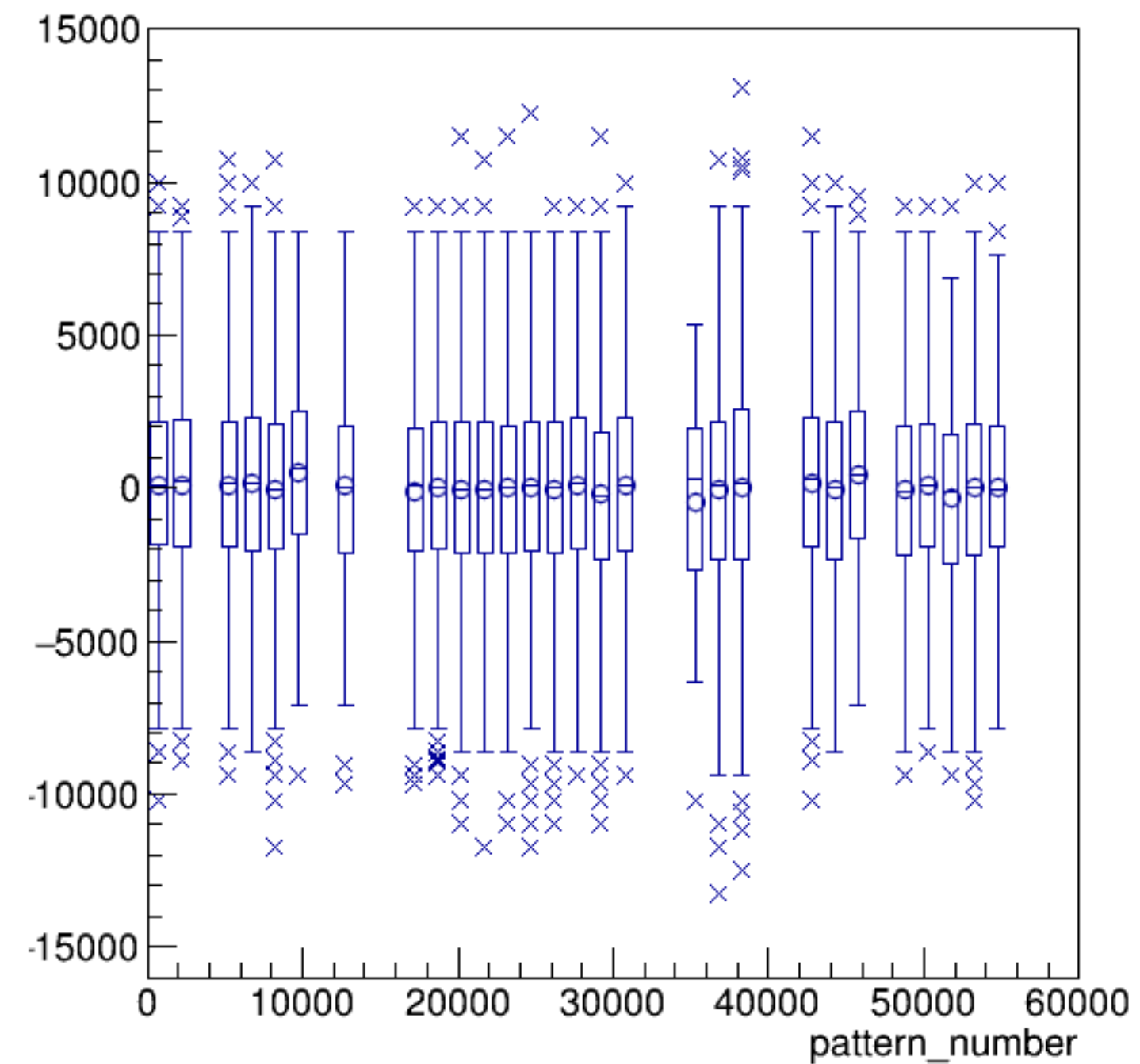
atr1*CurMon {ErrorFlag==0}



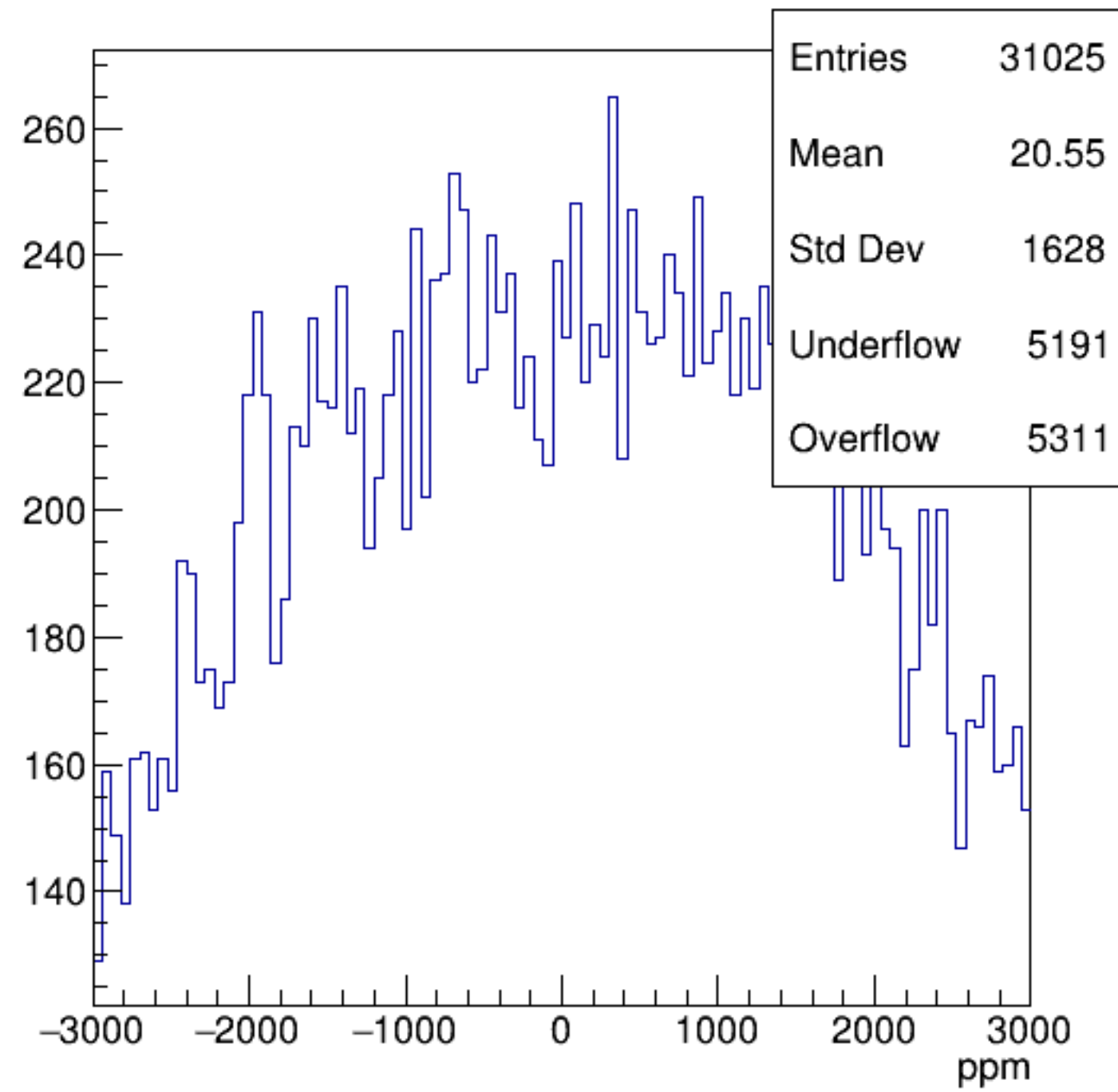
evtTree

Entries 124255
Mean 5.609
Std Dev 0.05249

asym_atr1/ppm:pattern_number {ErrorFlag==0}

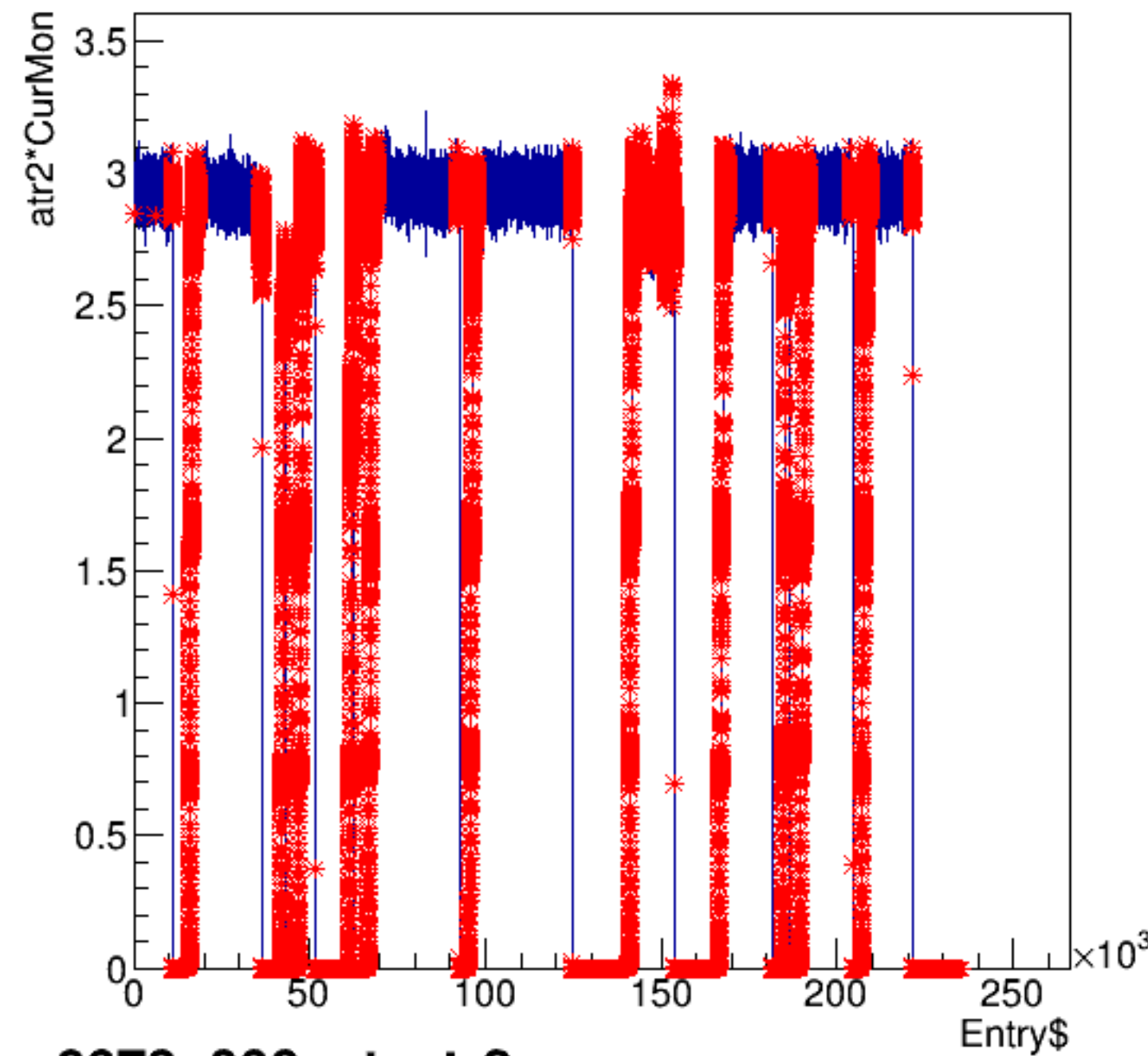


asym_atr1



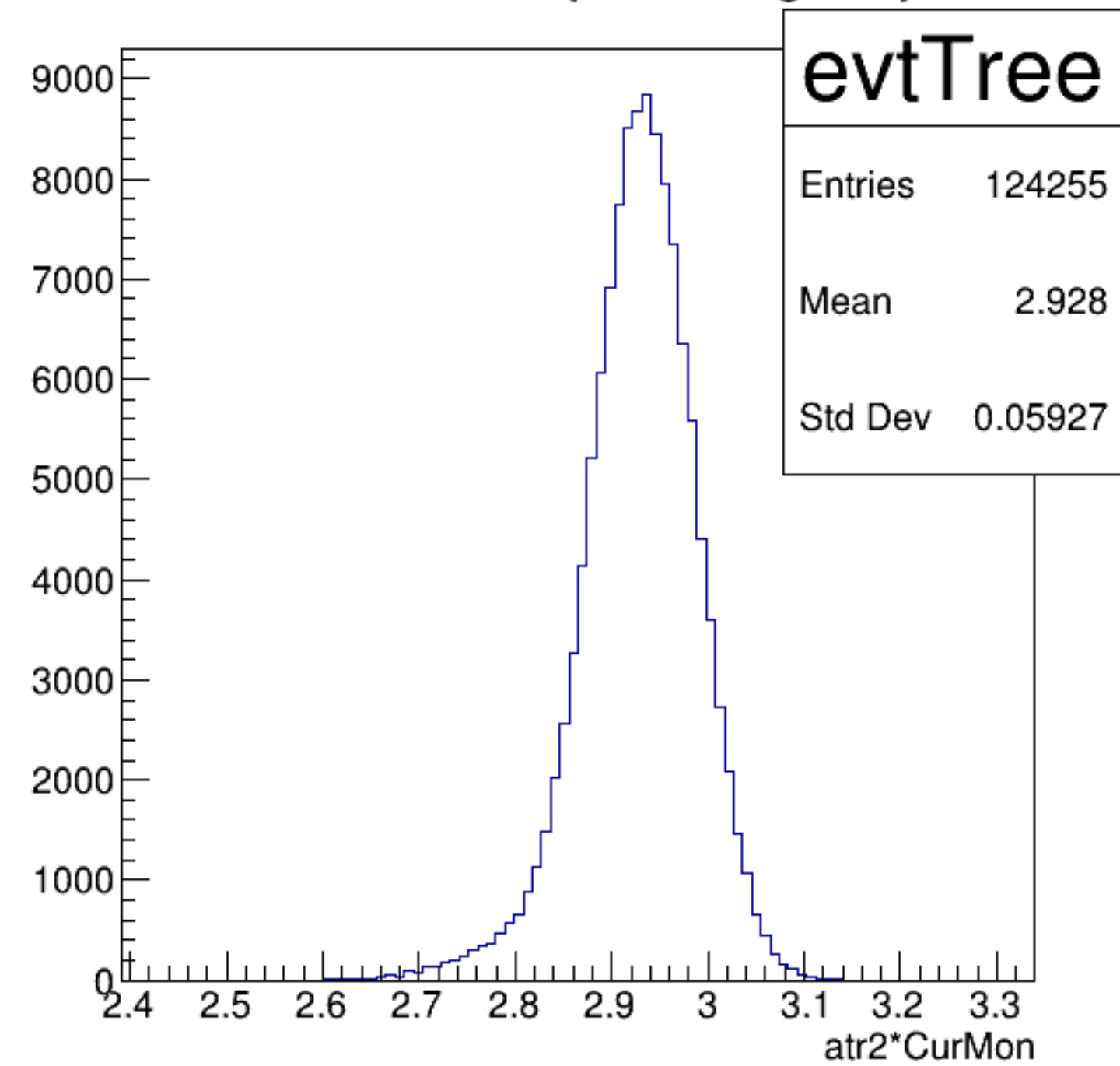
Entries 31025
Mean 20.55
Std Dev 1628
Underflow 5191
Overflow 5311

atr2*CurMon:Entry\$

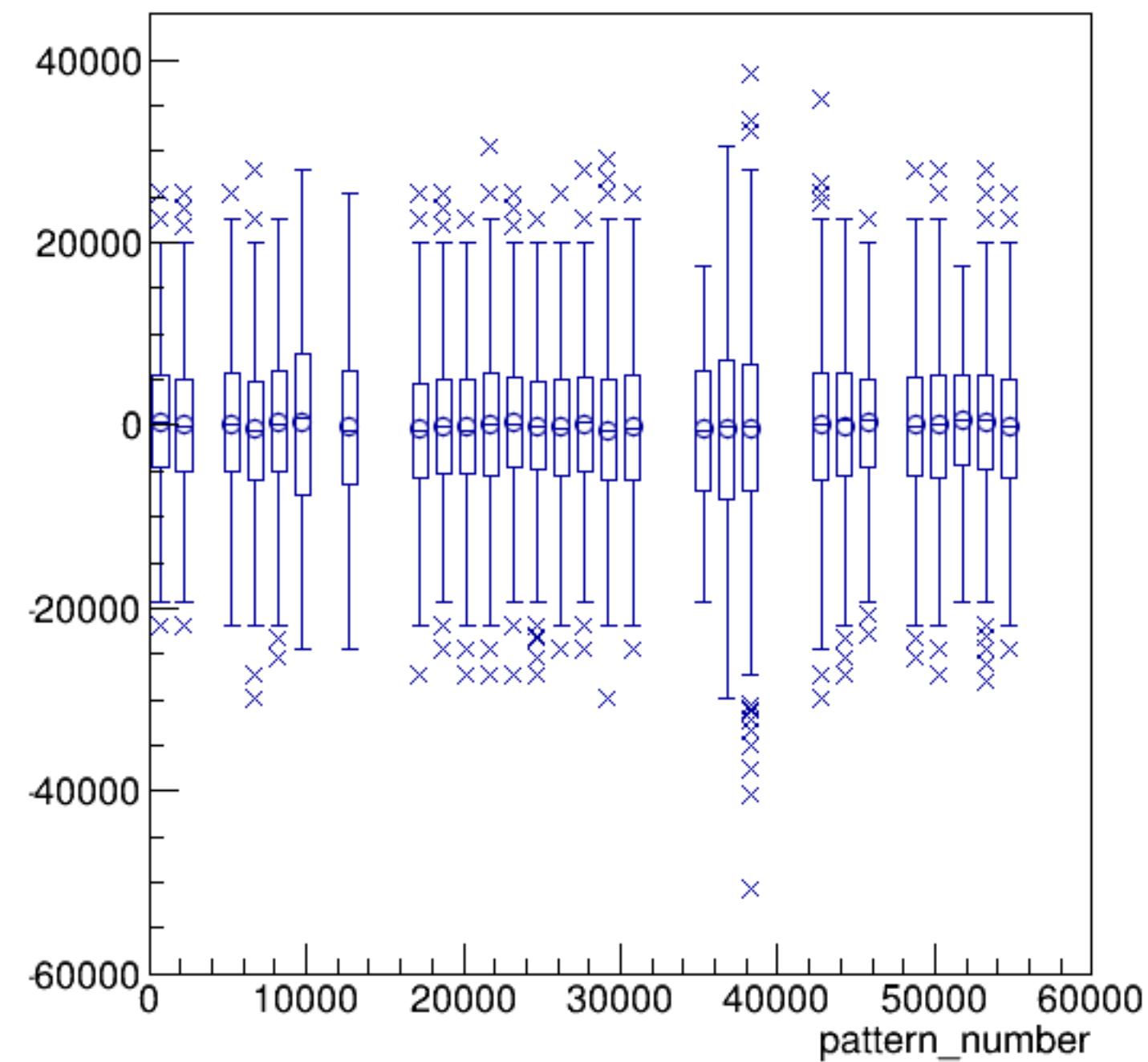


run6673_000_at_atr2.png

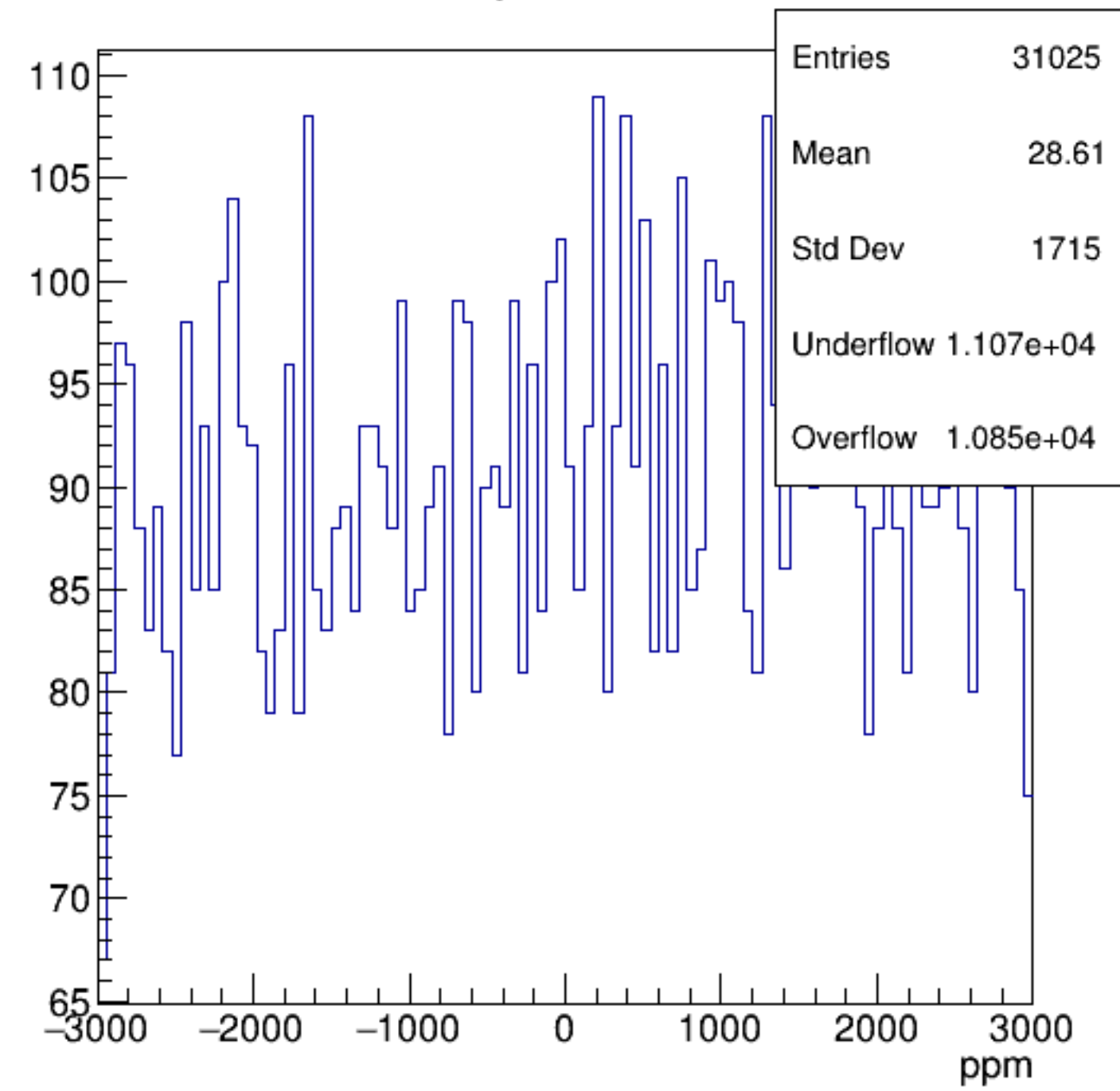
atr2*CurMon {ErrorFlag==0}



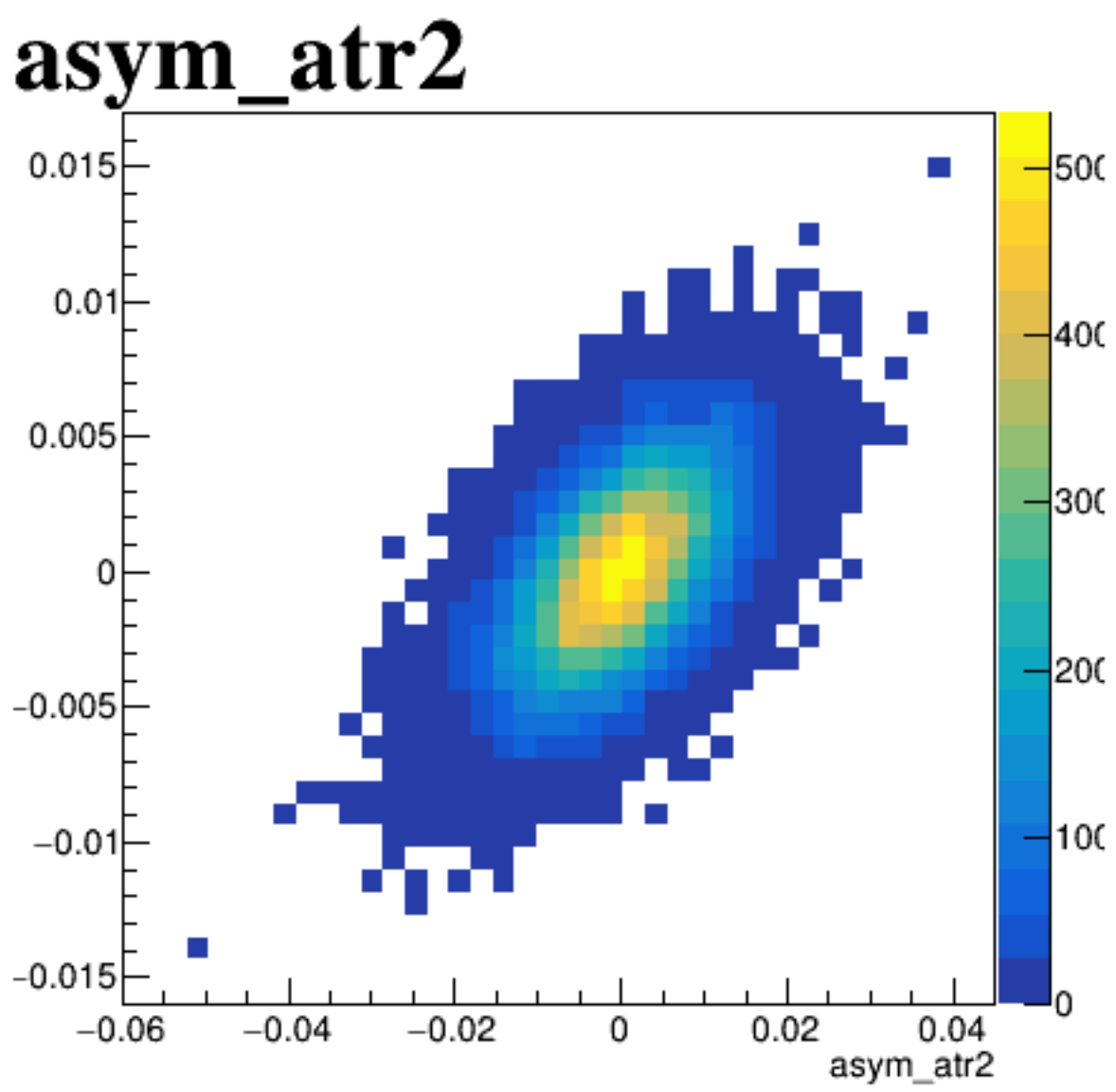
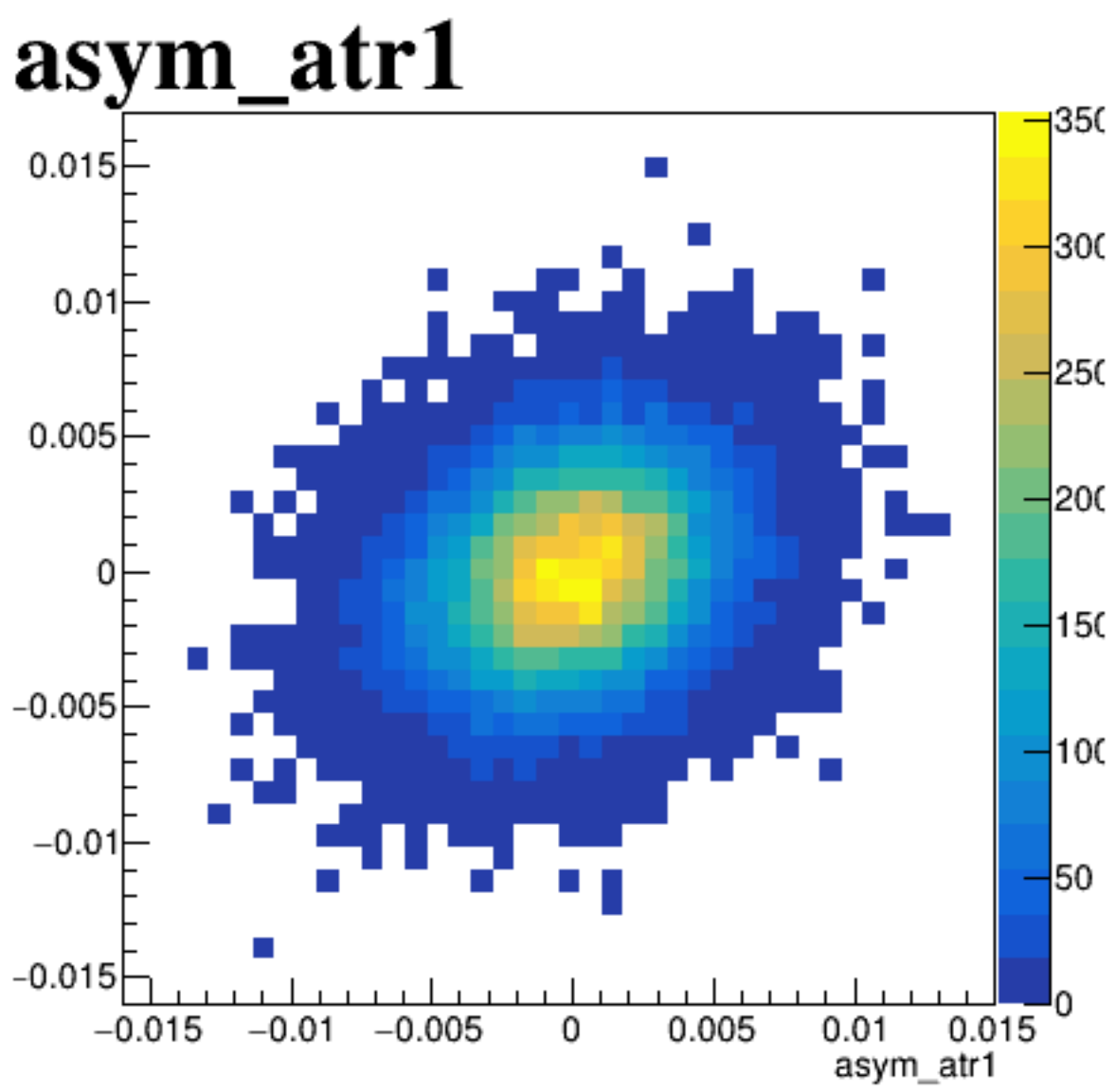
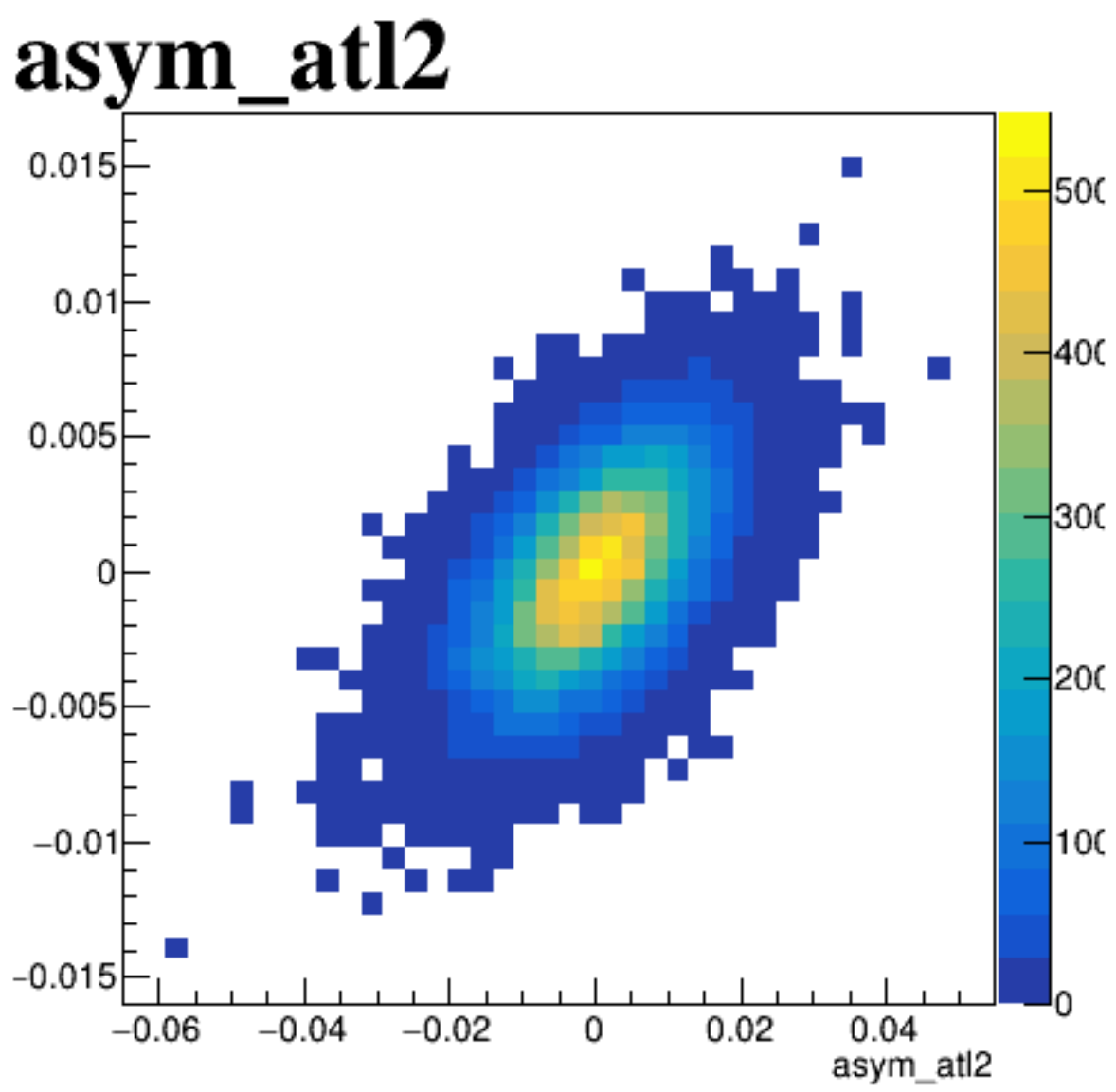
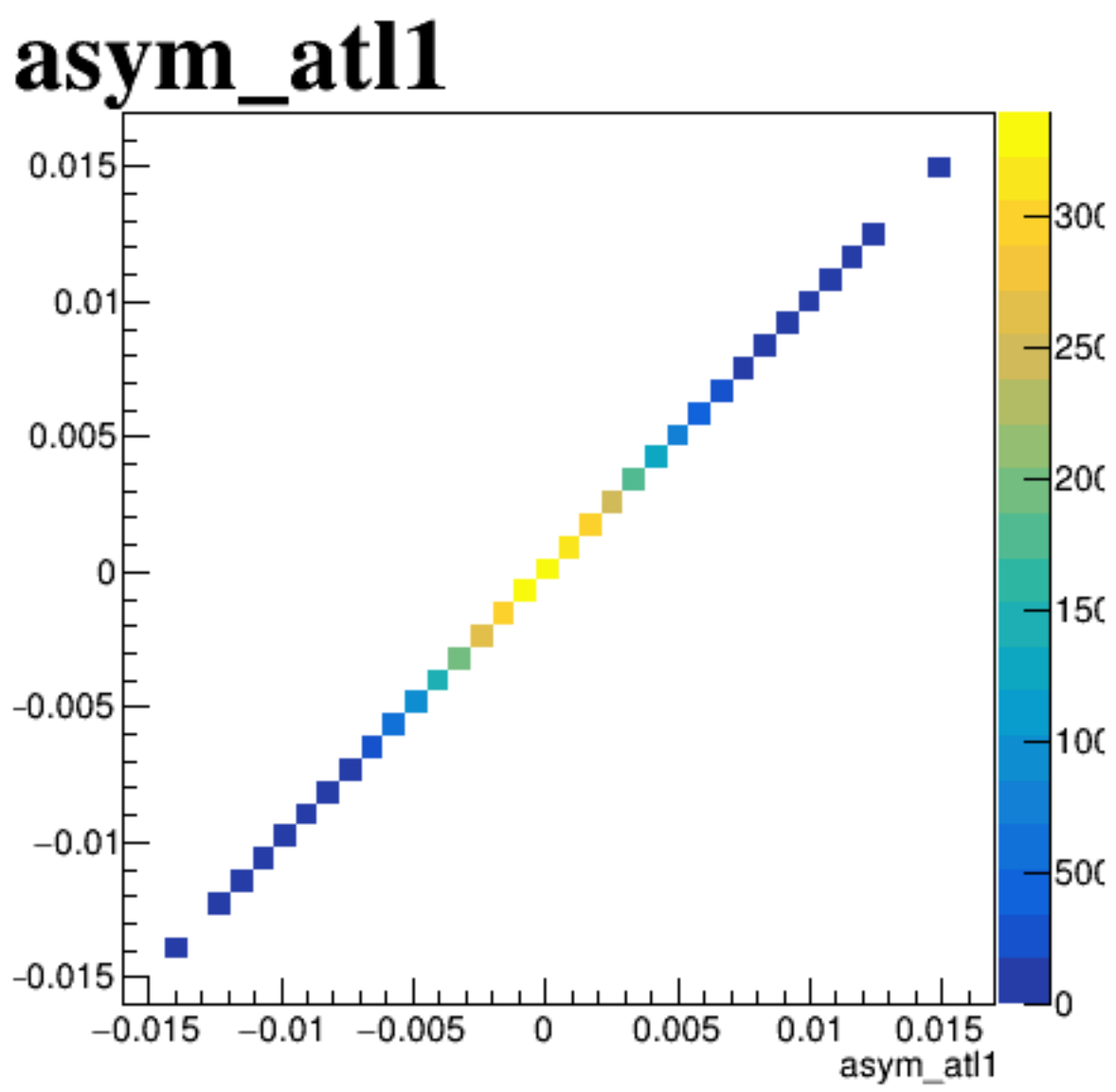
asym_atr2/ppm:pattern_number {ErrorFlag==0}



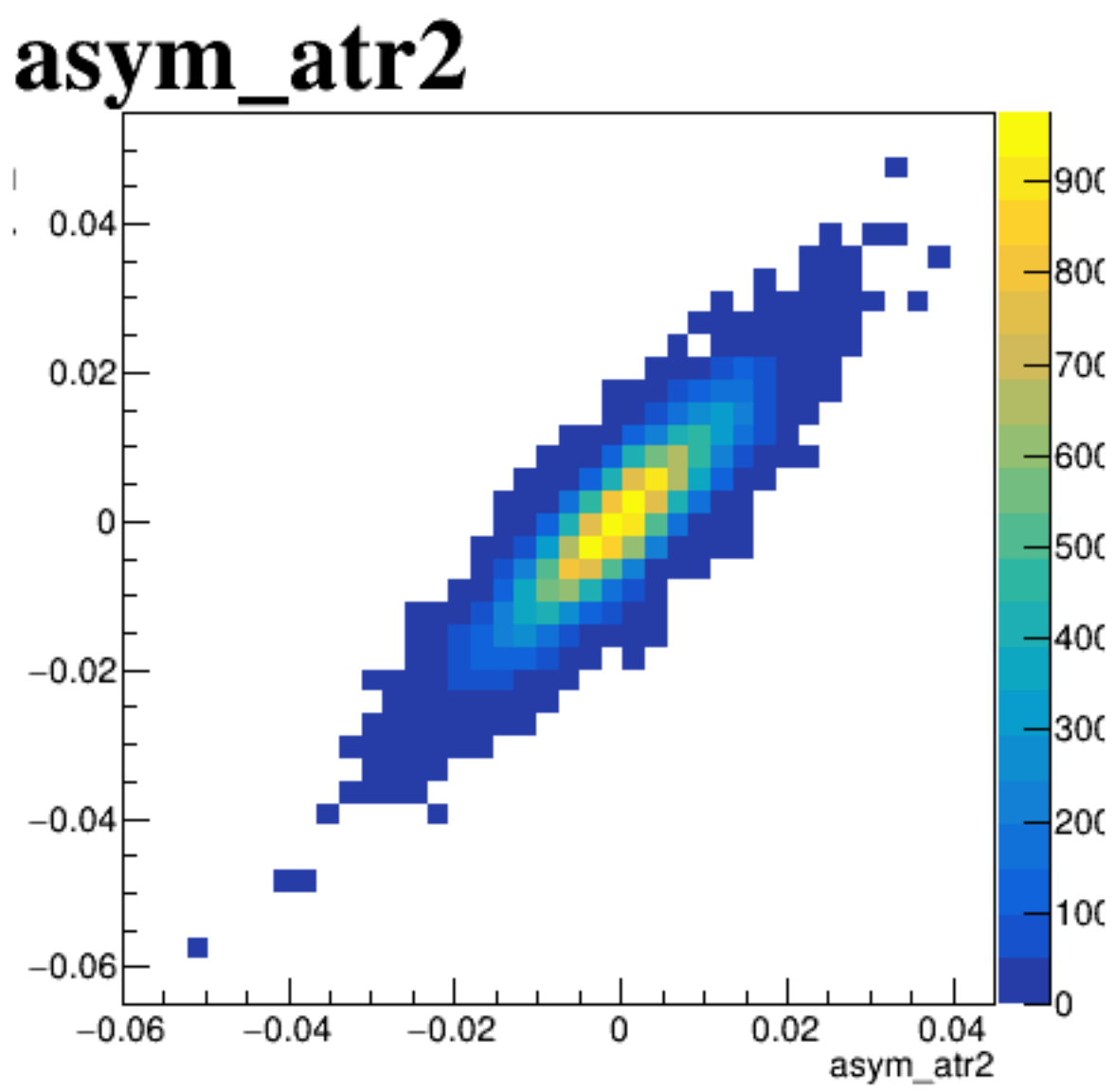
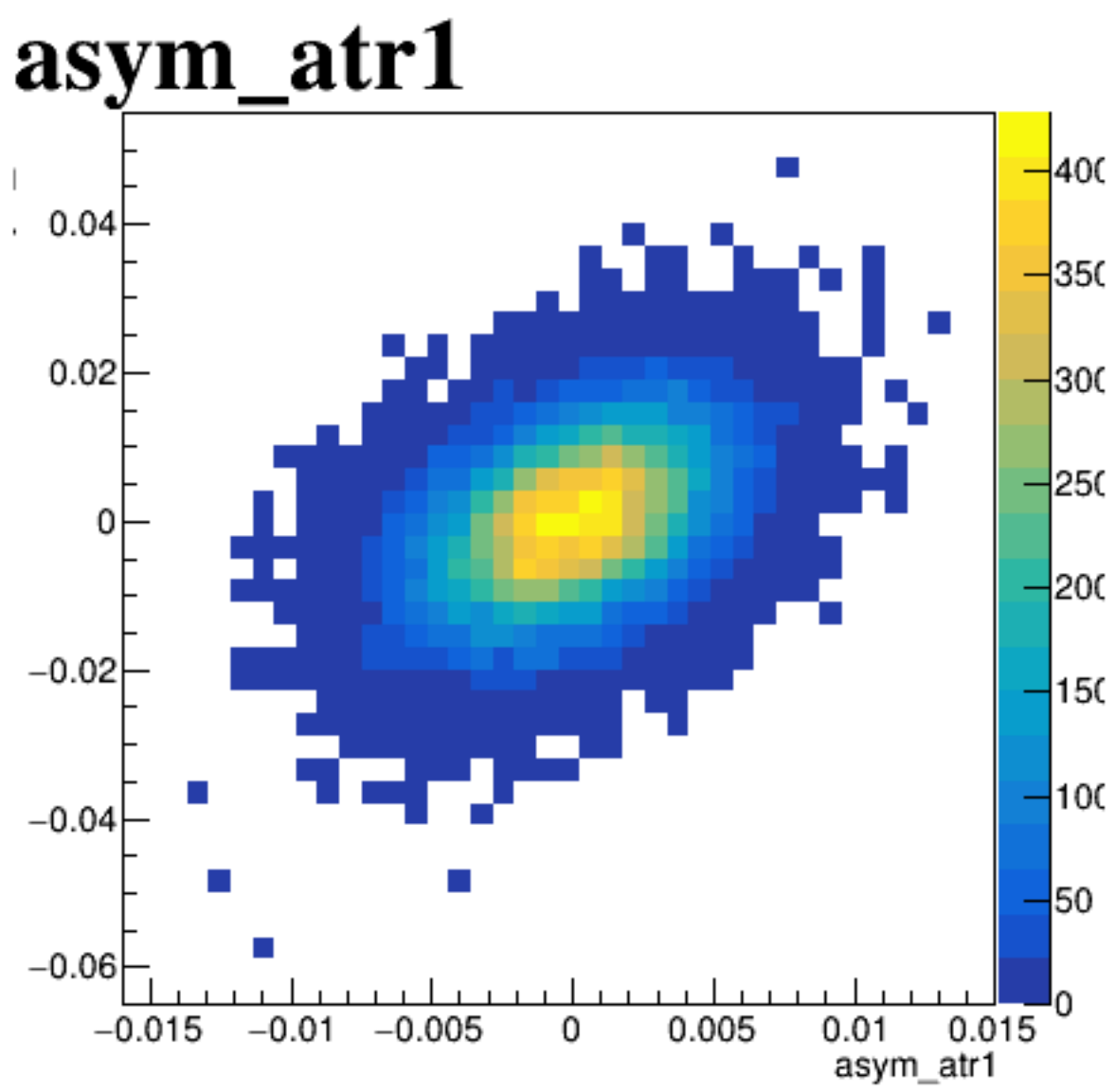
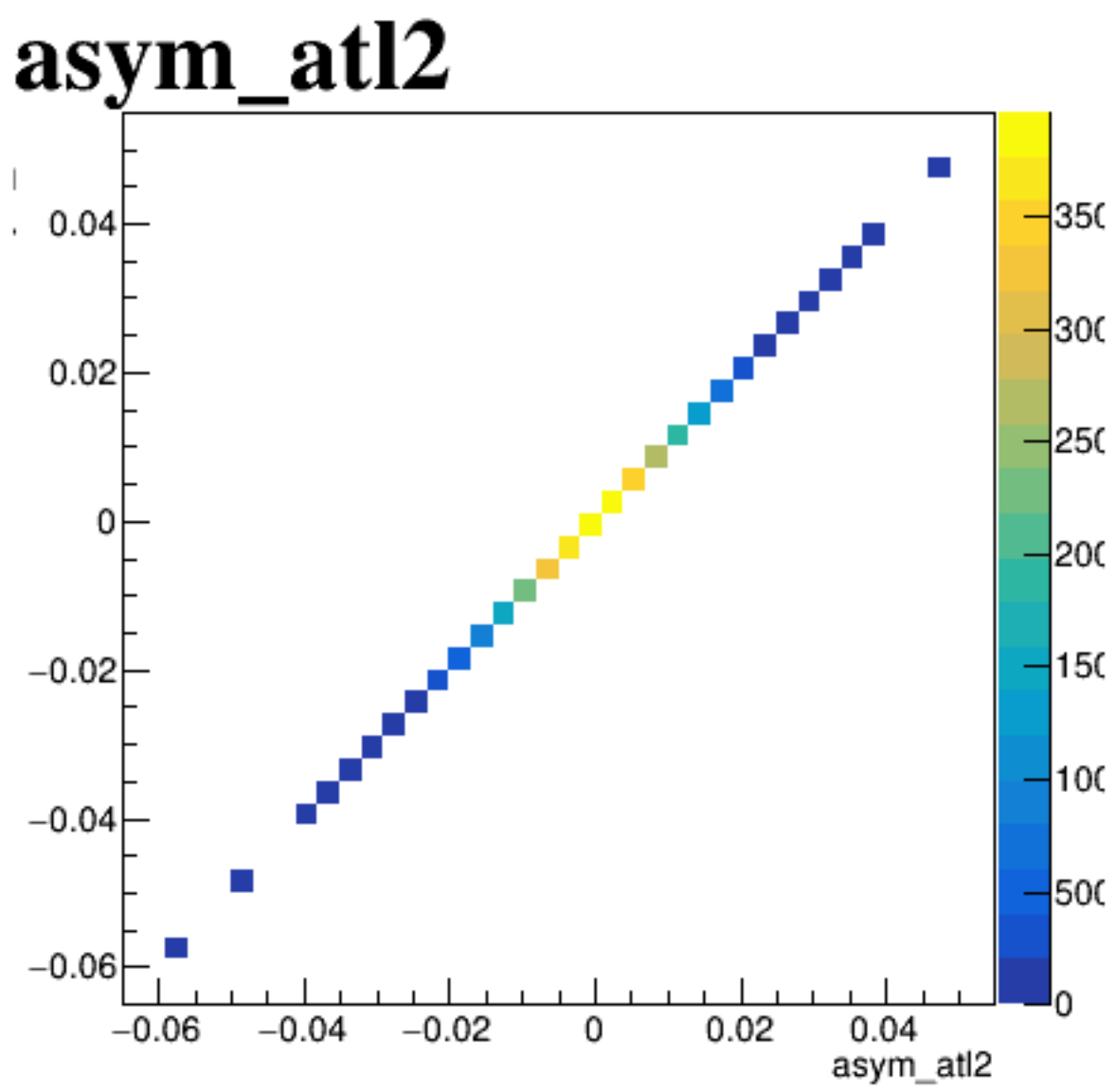
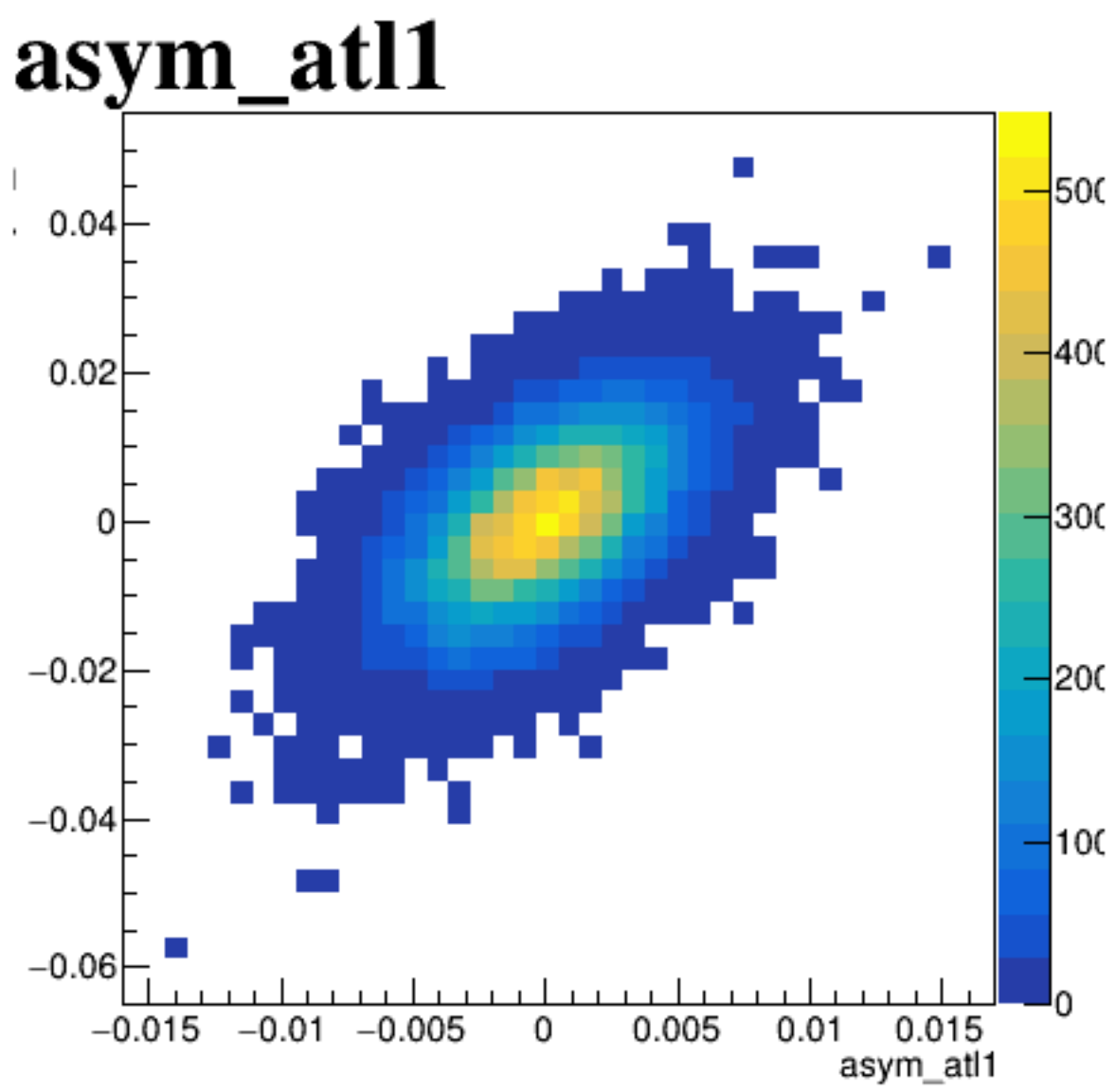
asym_atr2



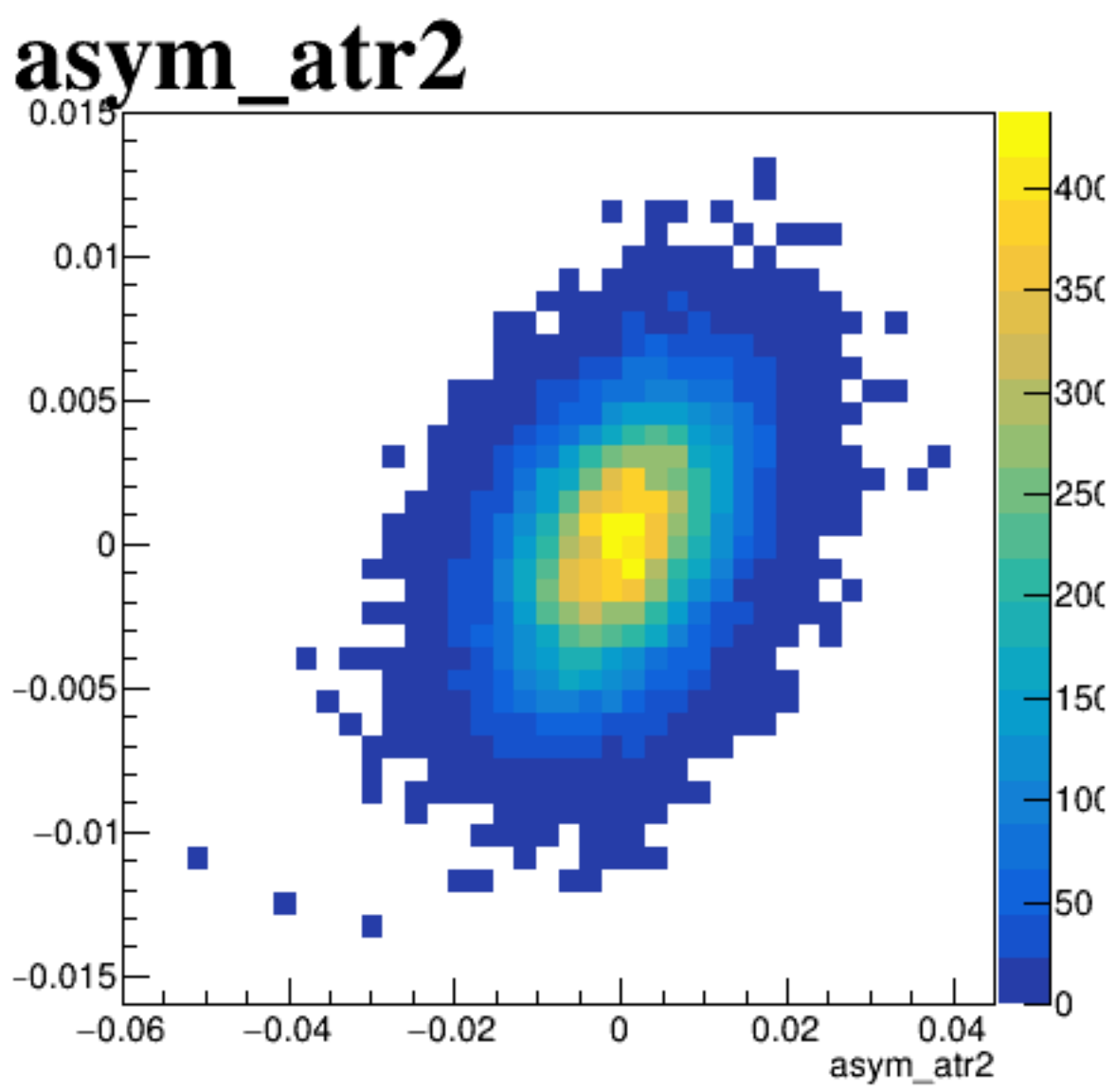
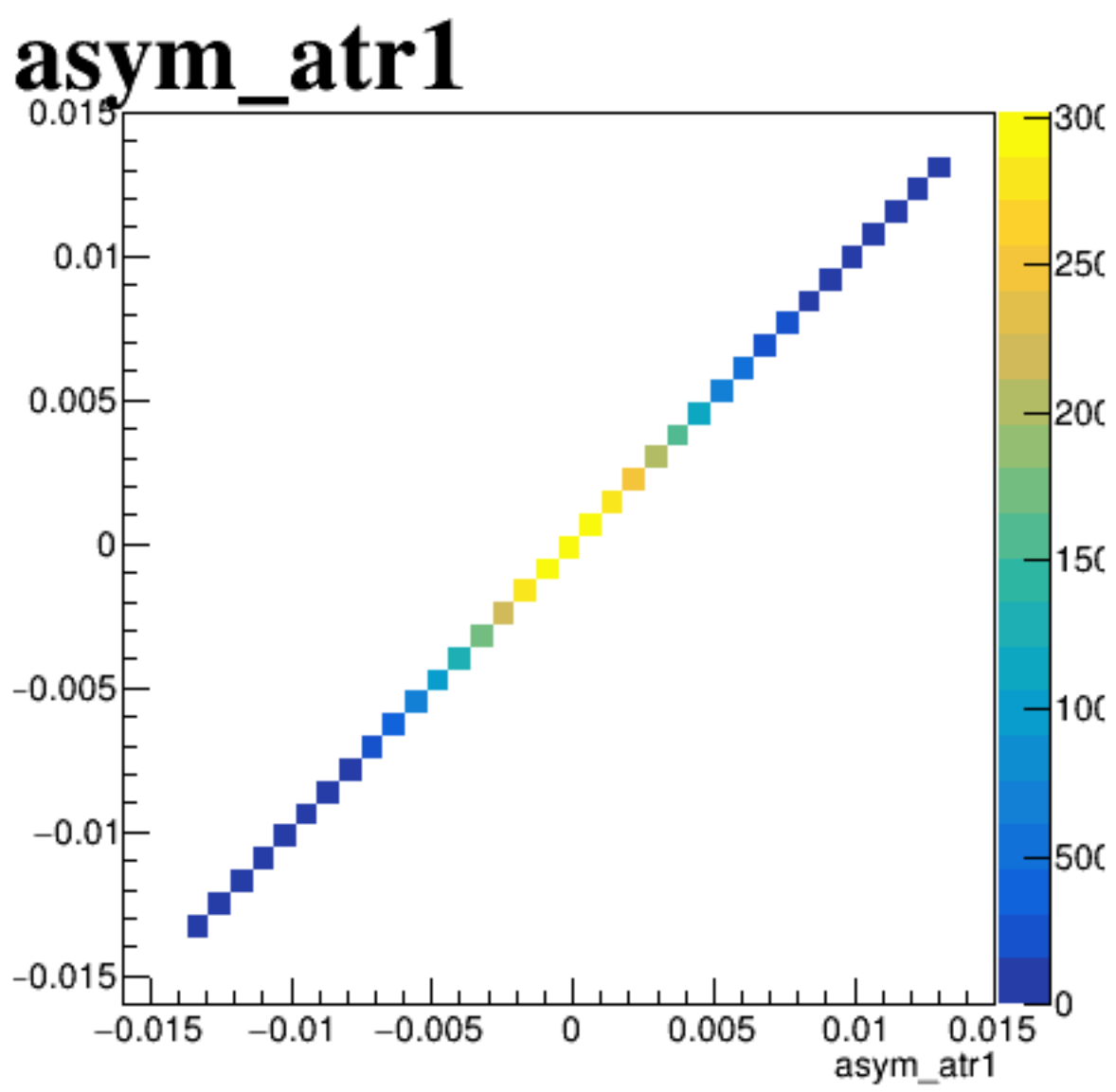
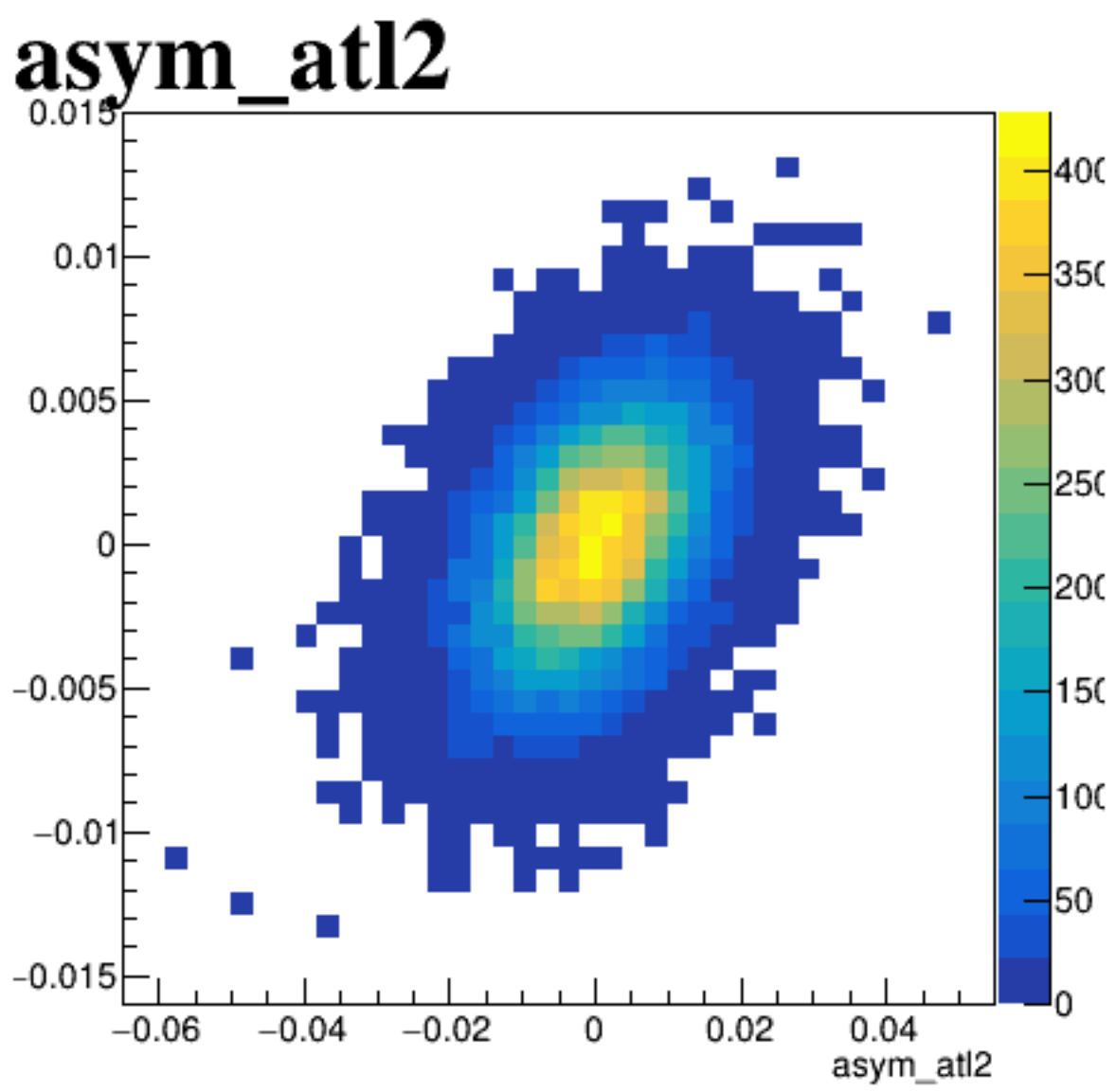
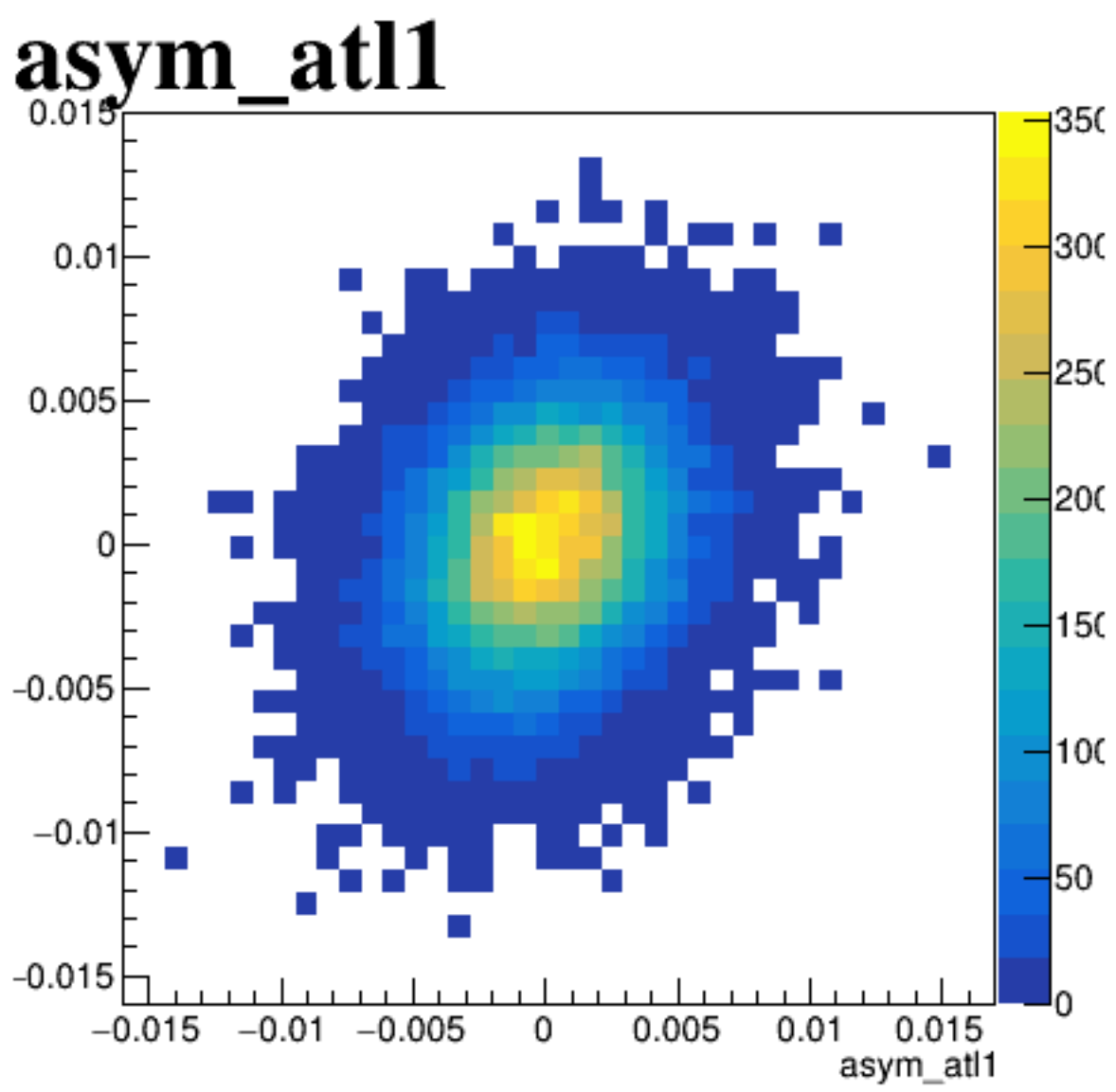
asym_atl1



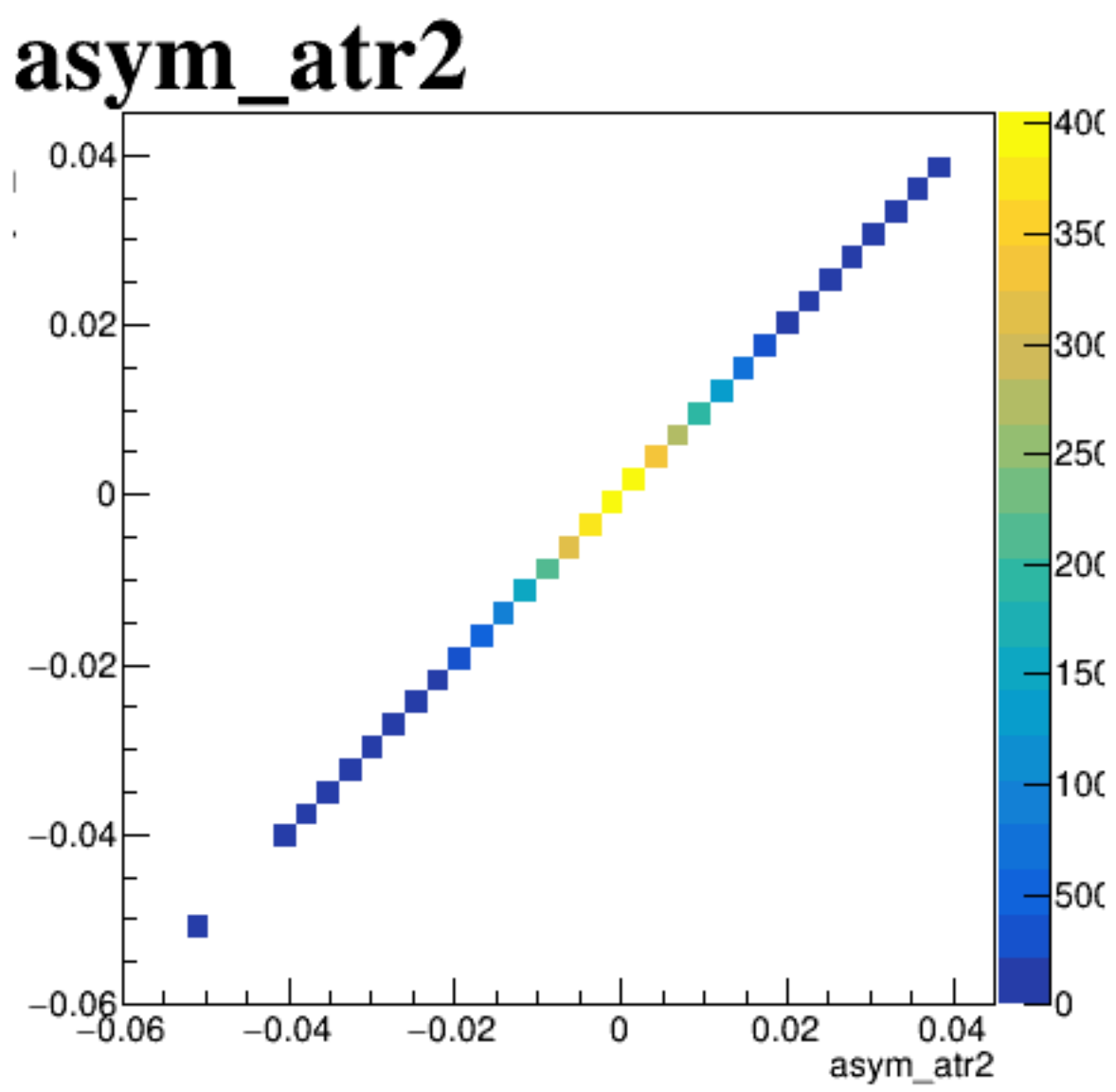
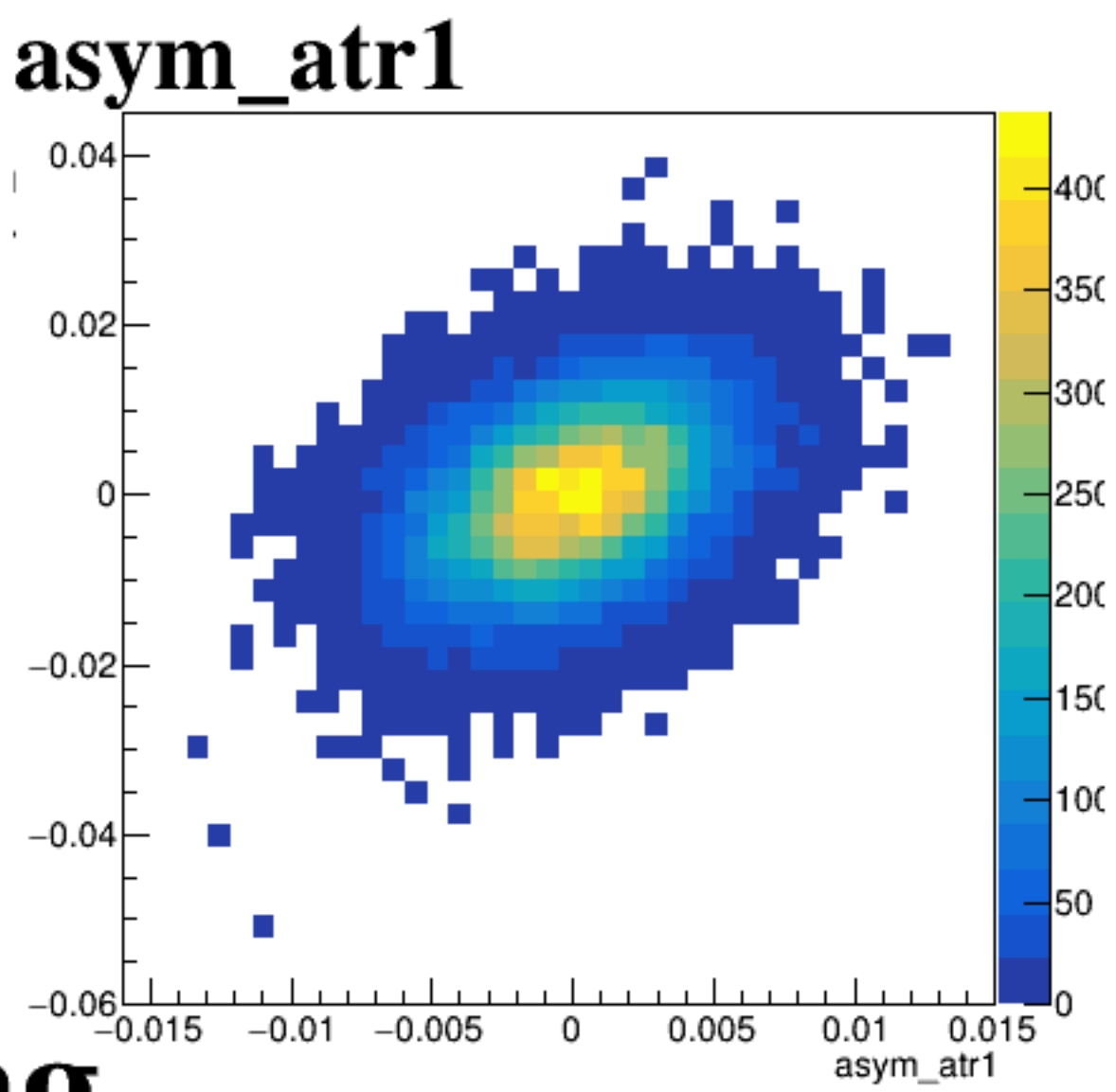
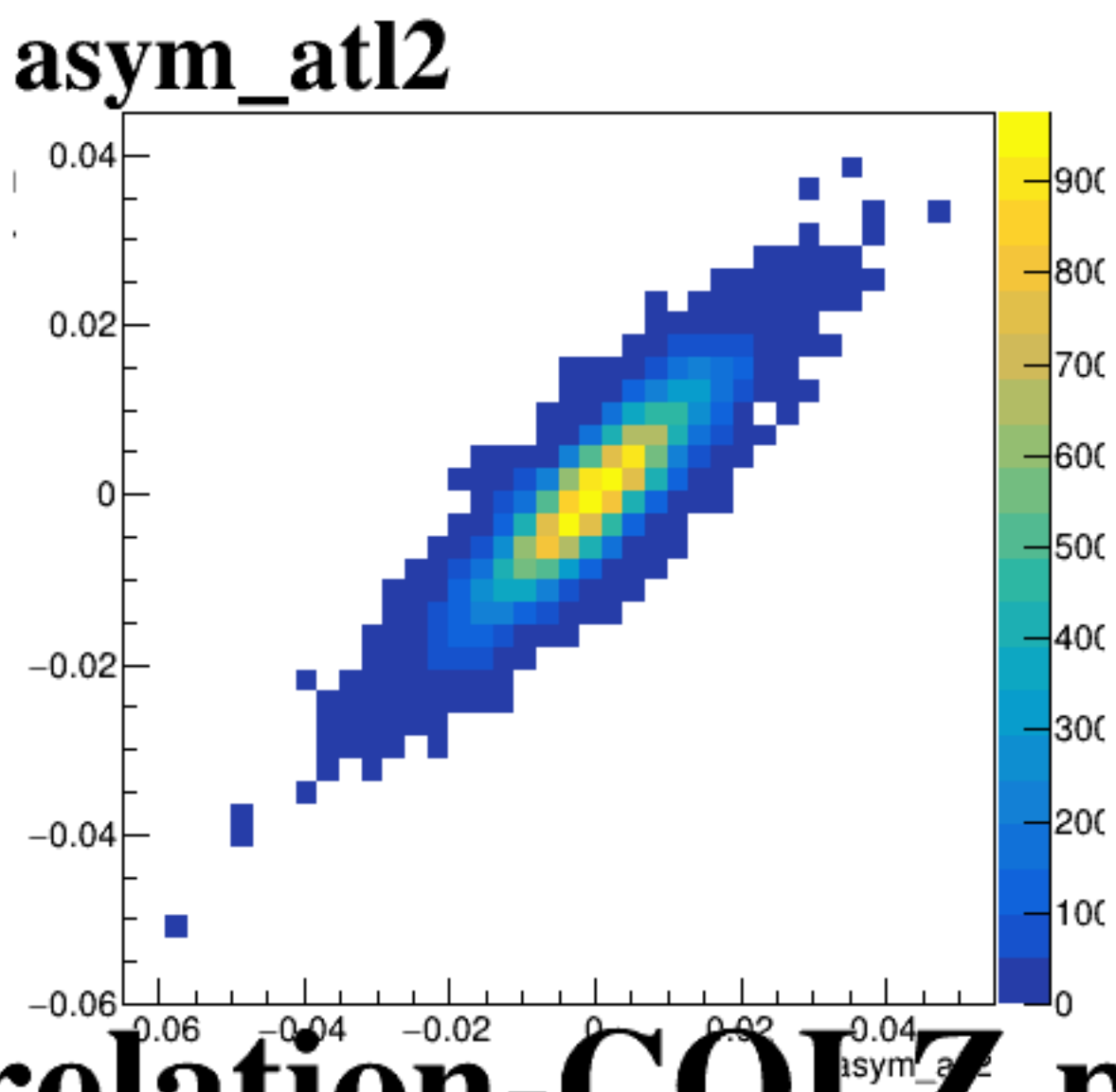
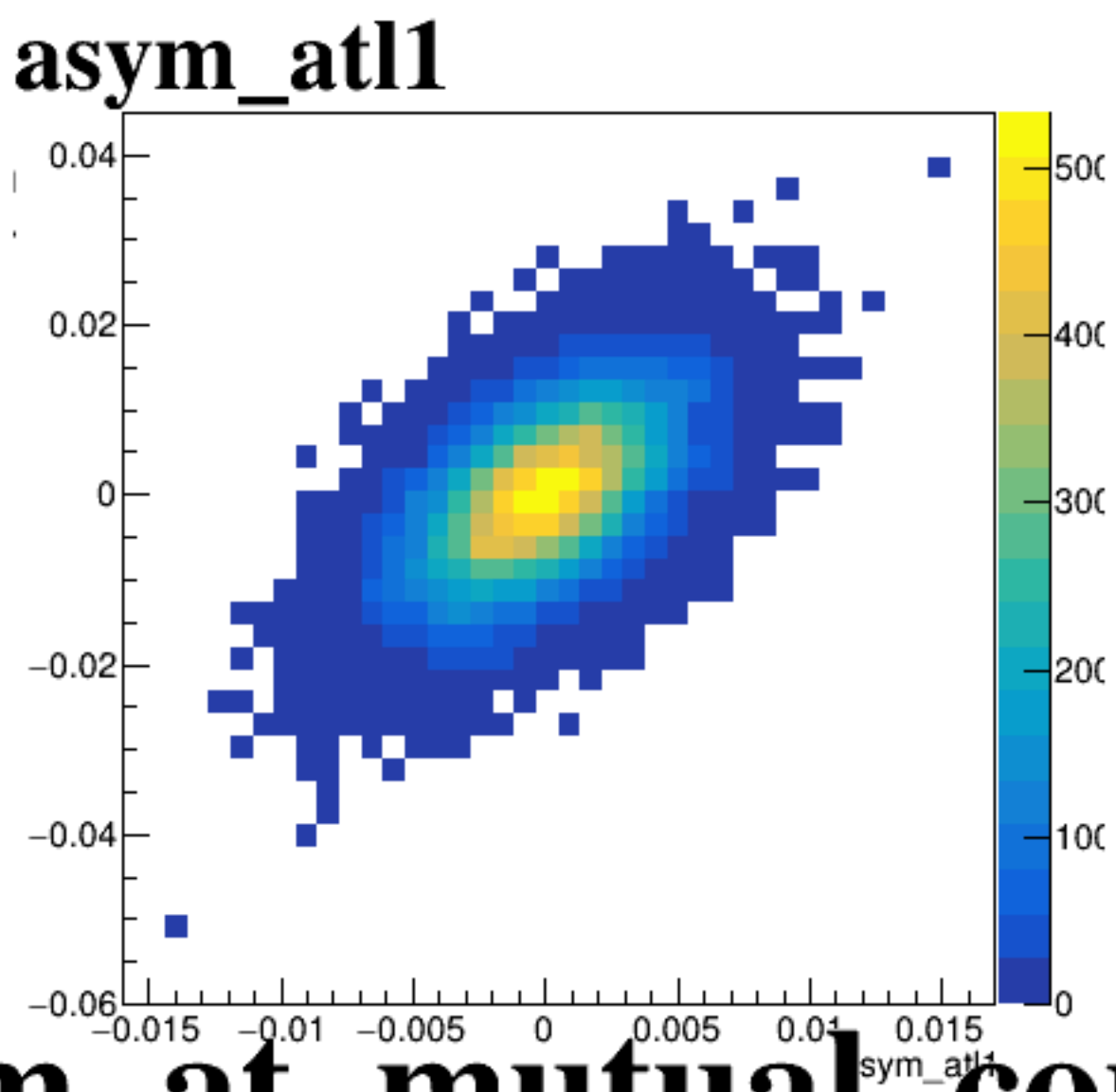
asym_atl2



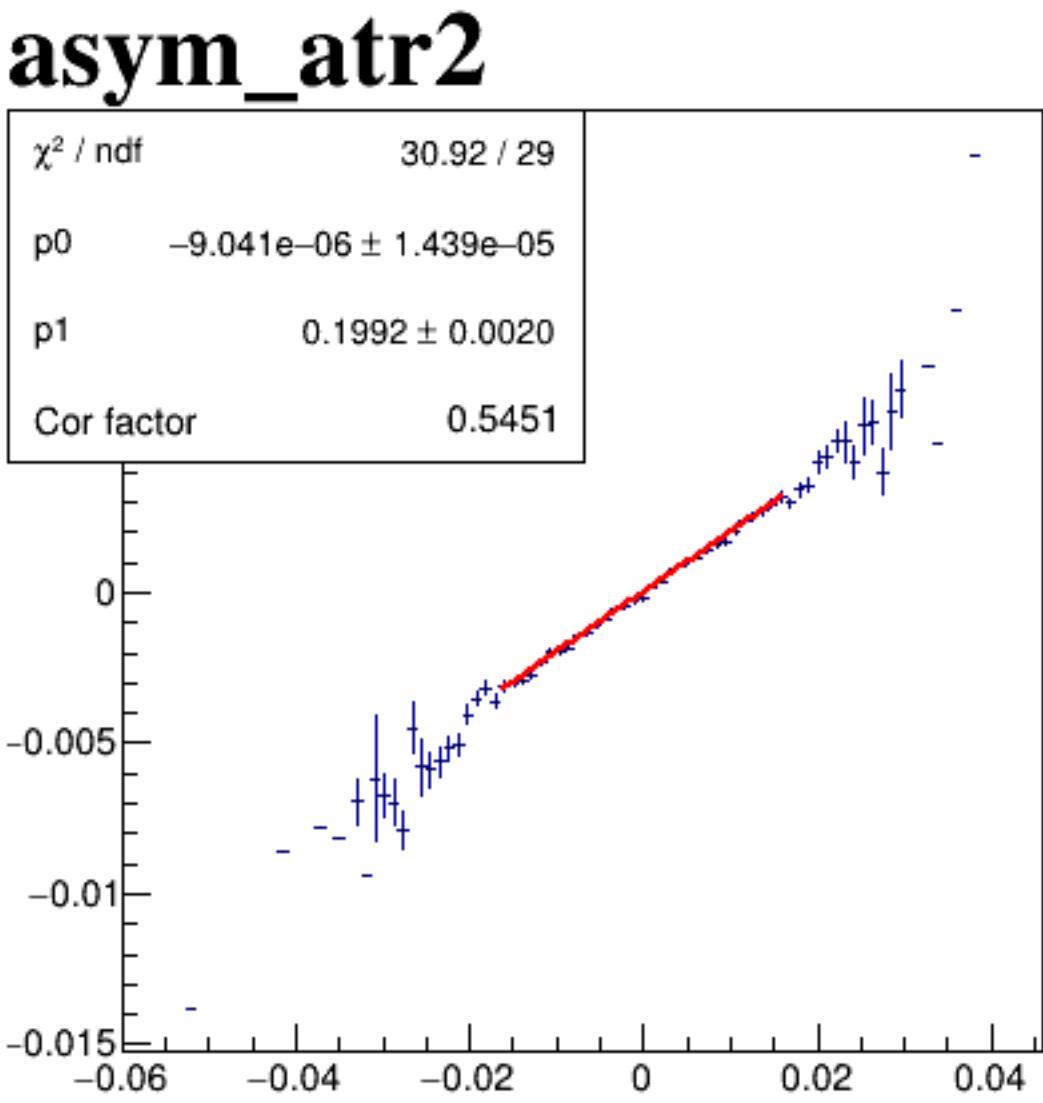
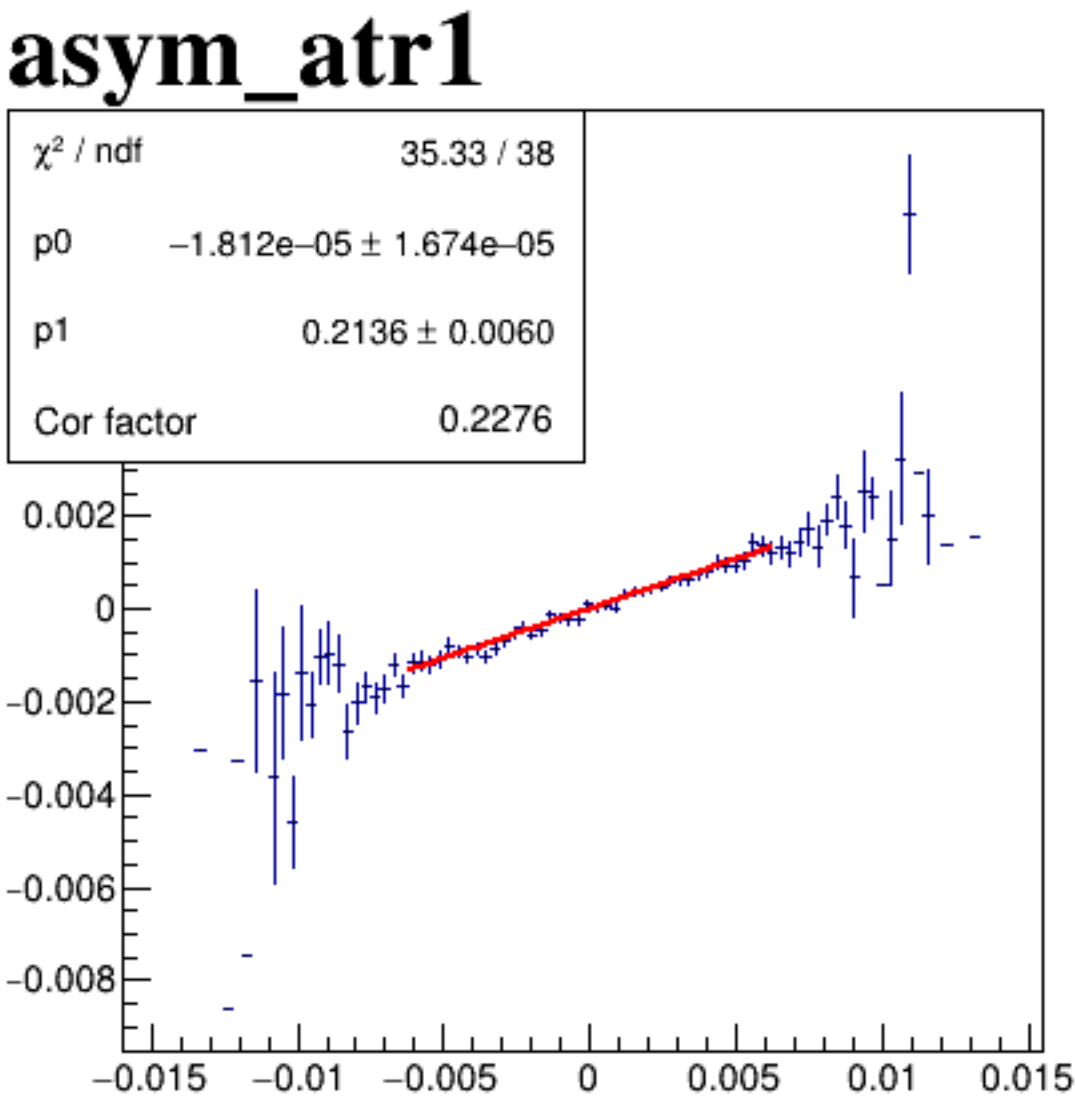
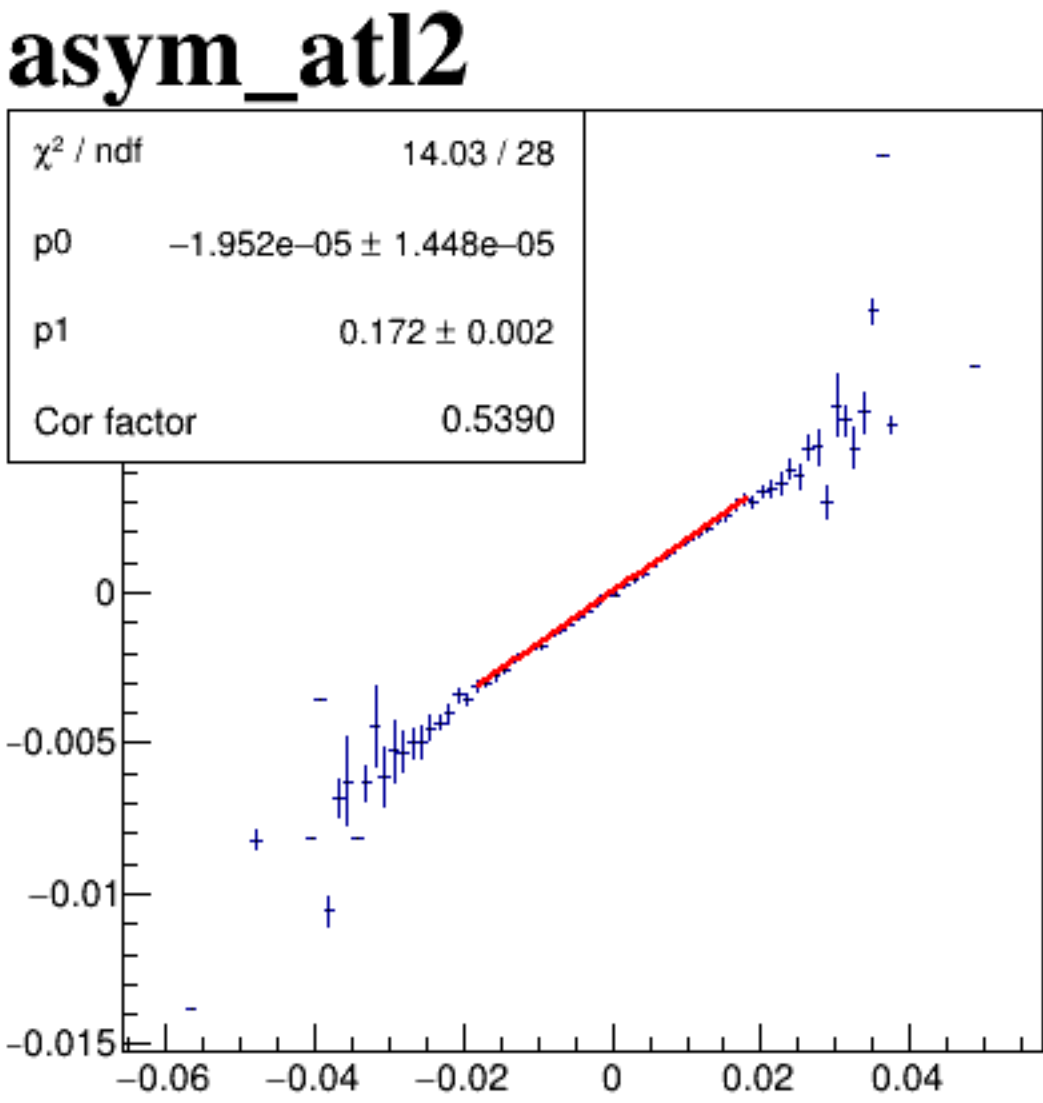
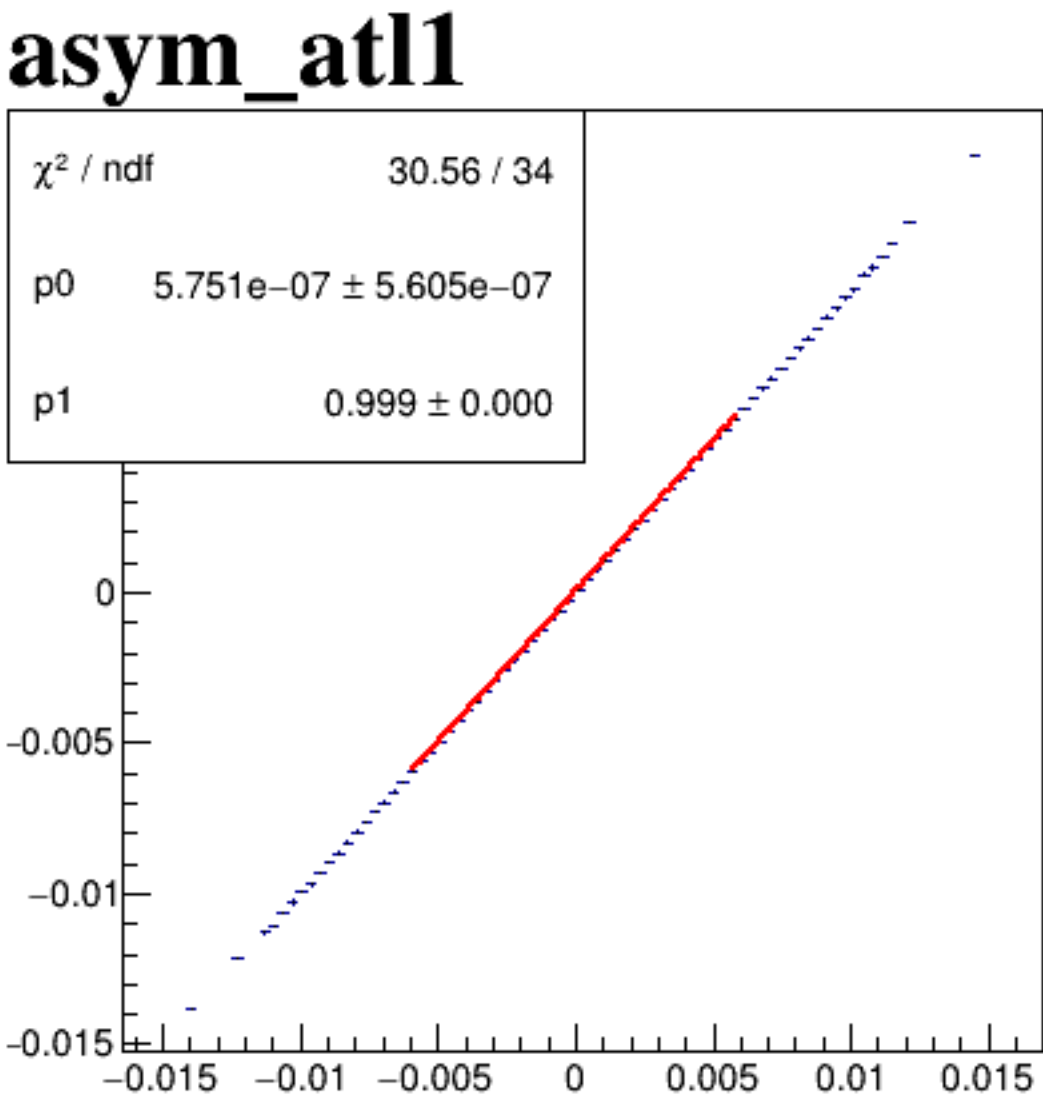
asym_atr1



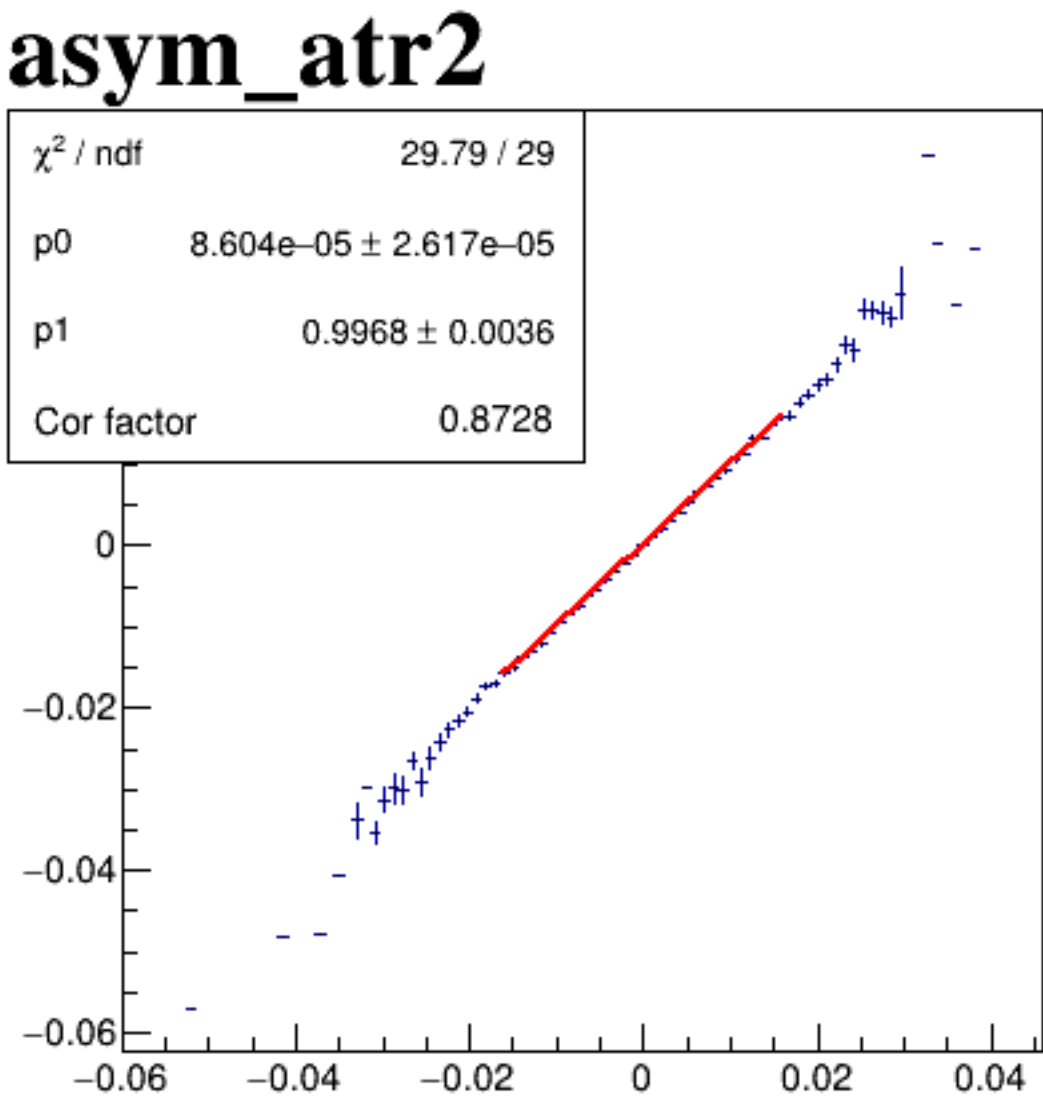
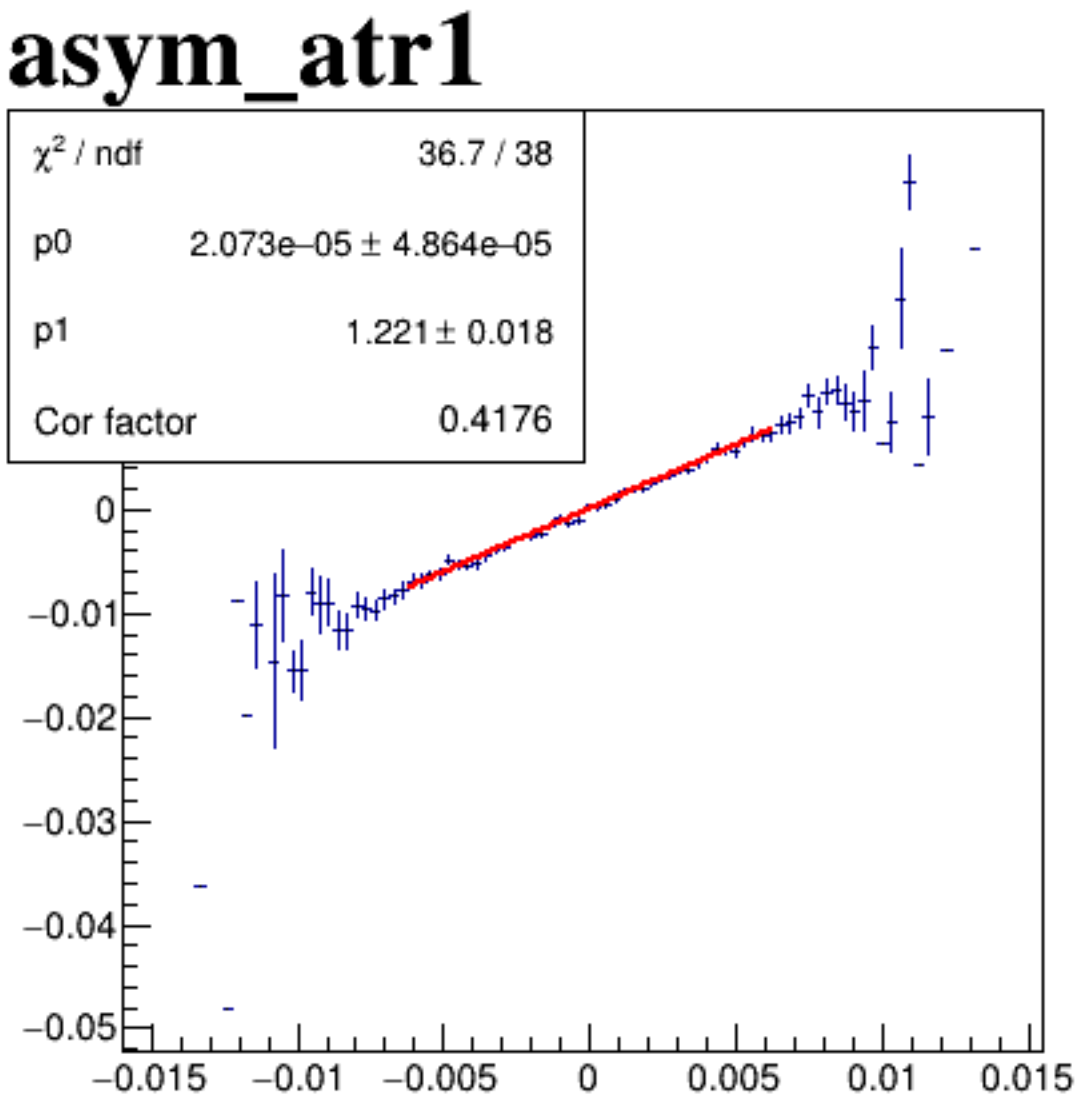
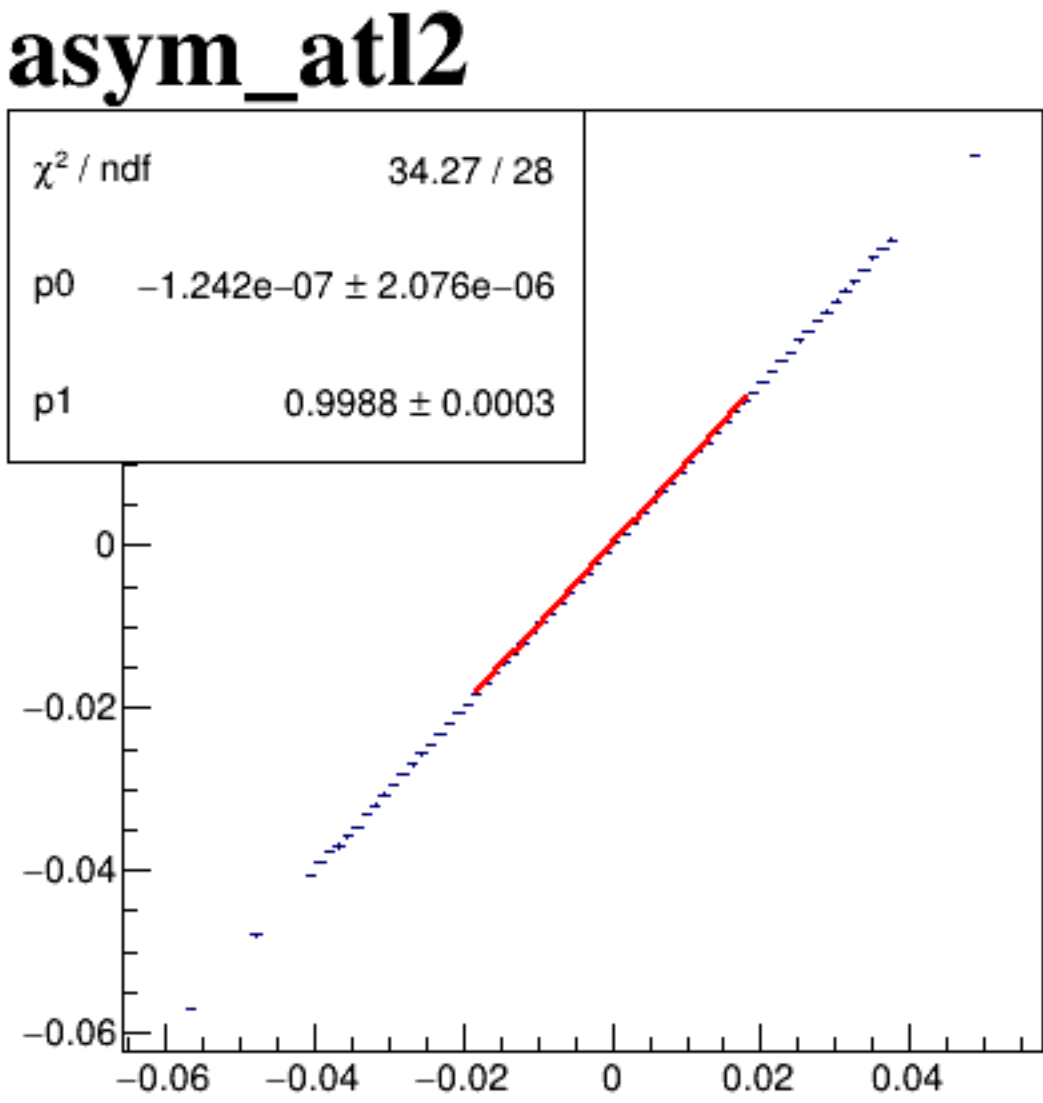
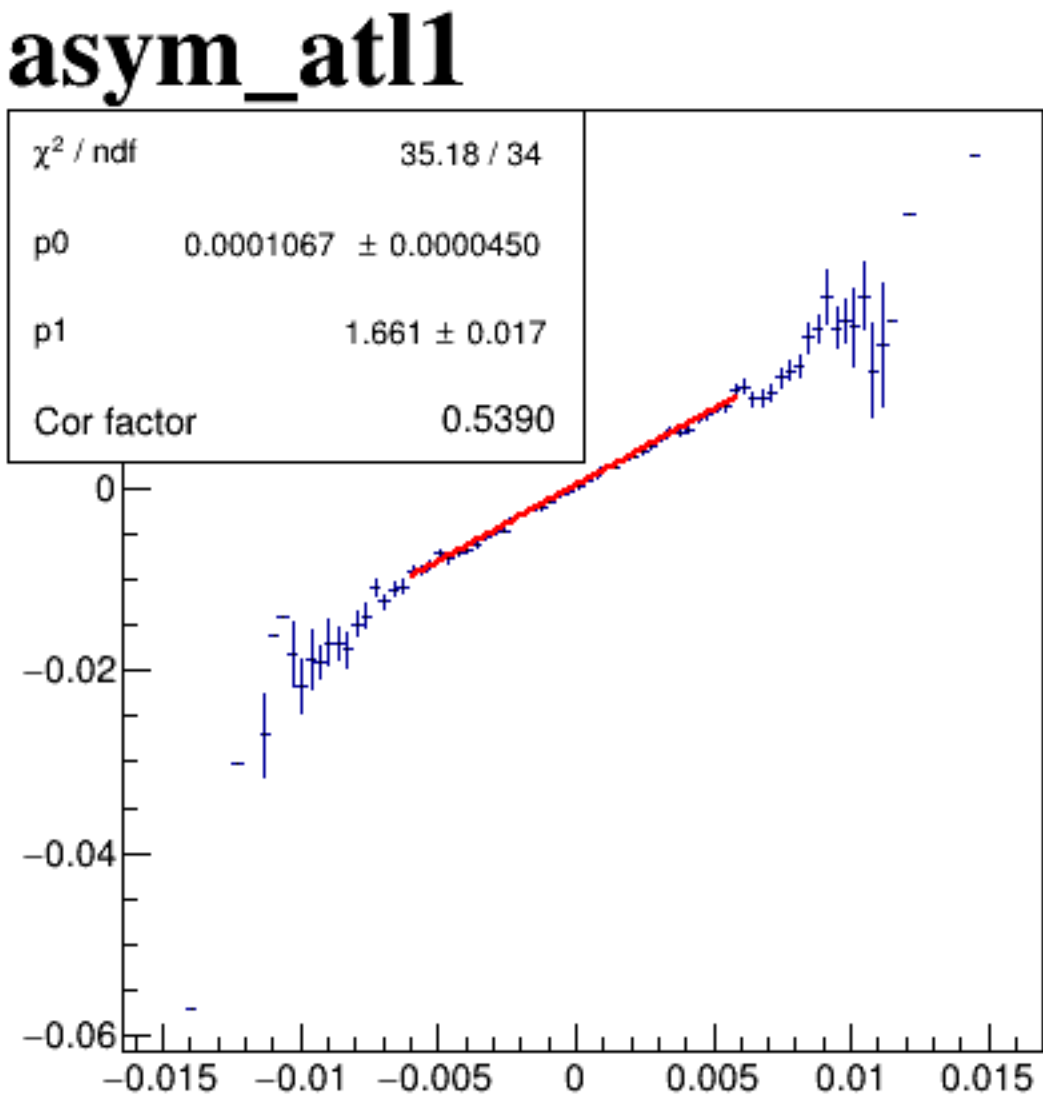
asym_atr2



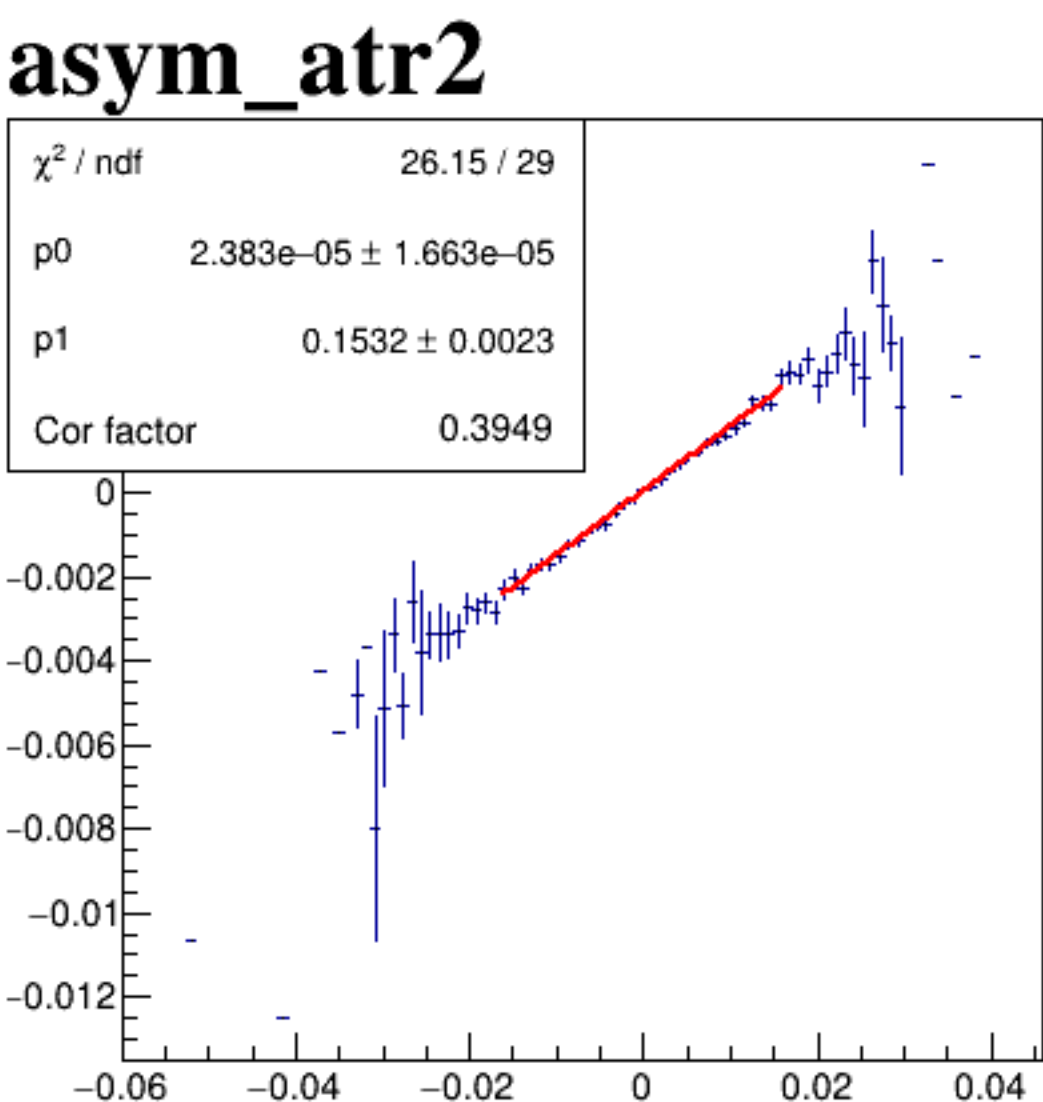
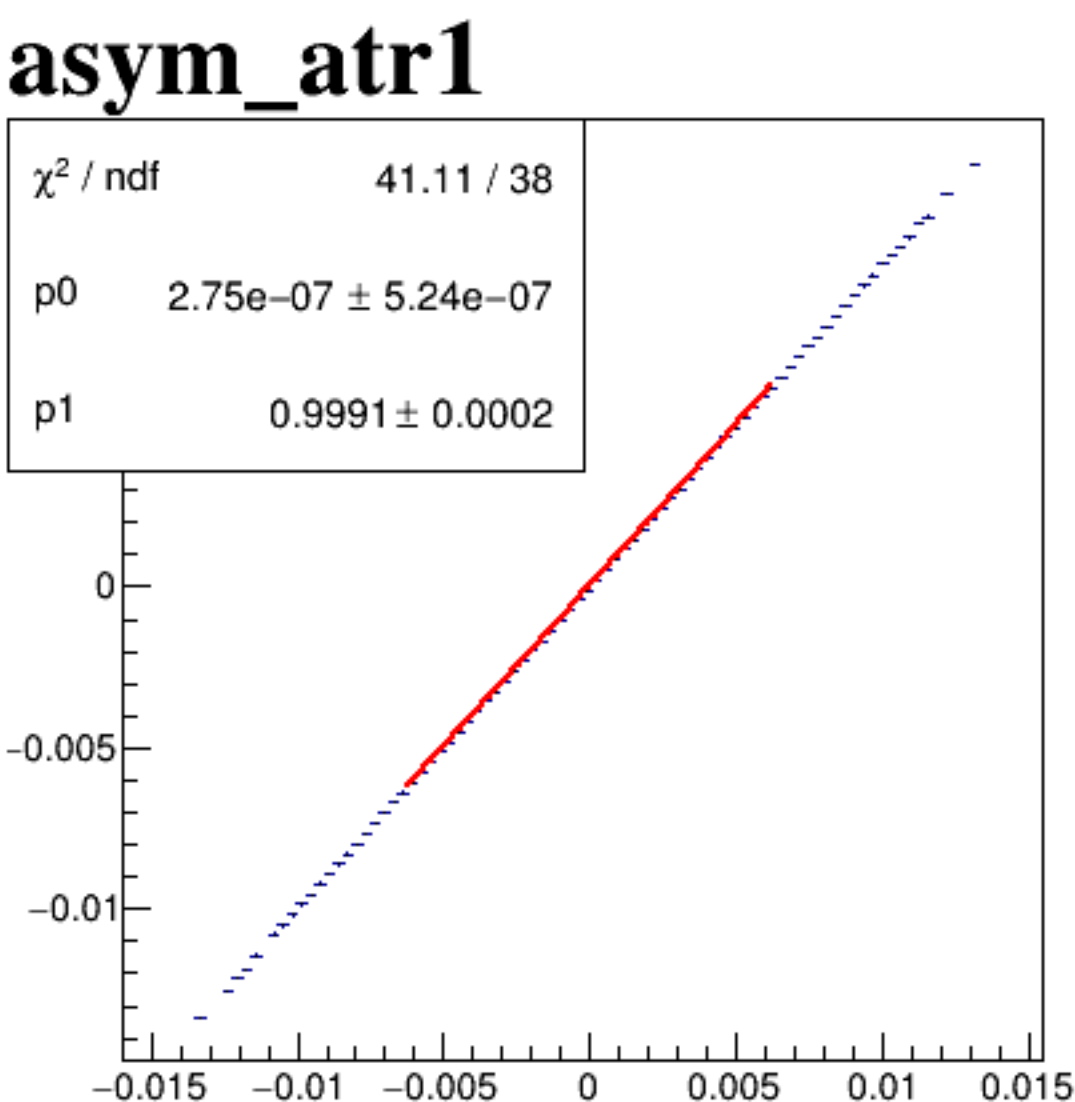
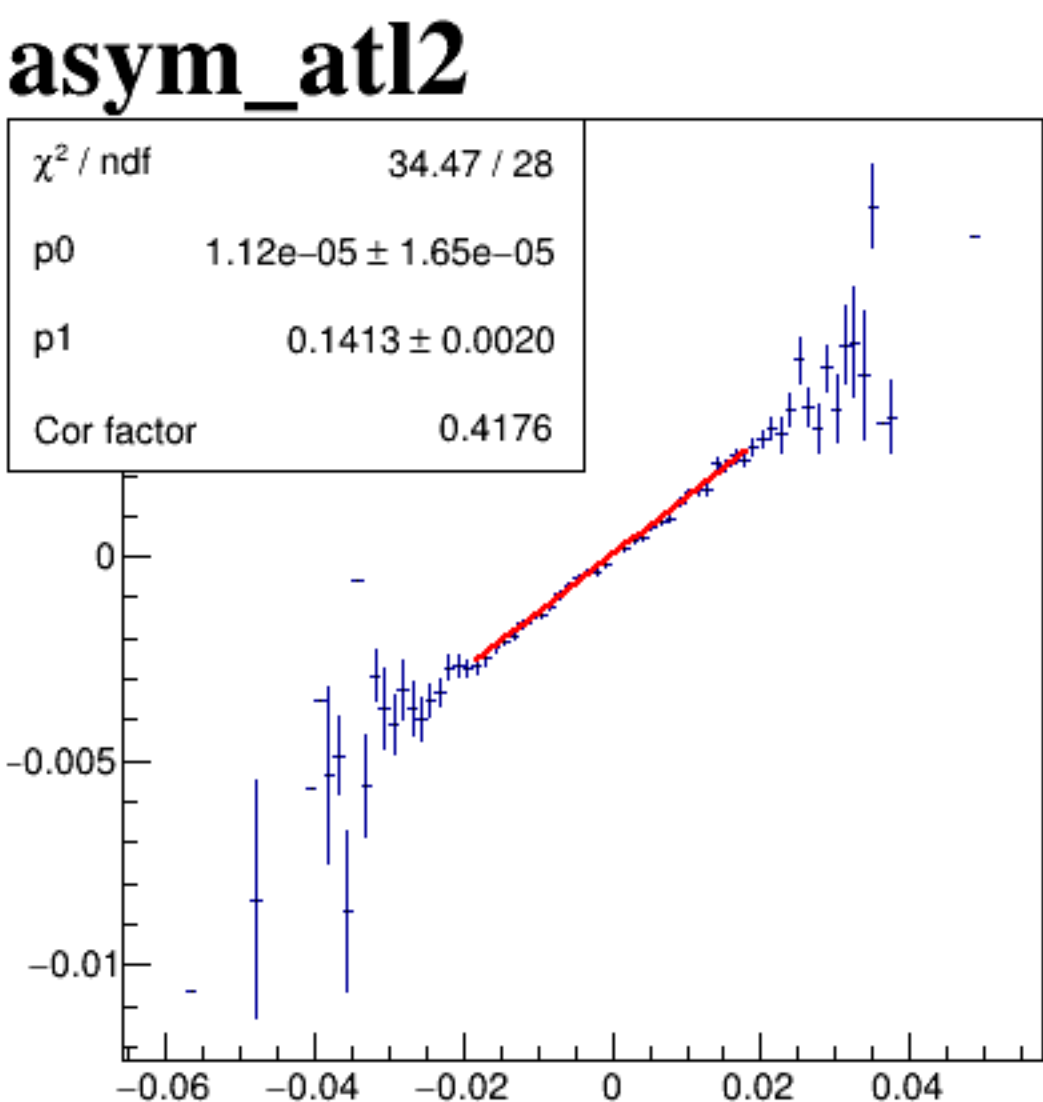
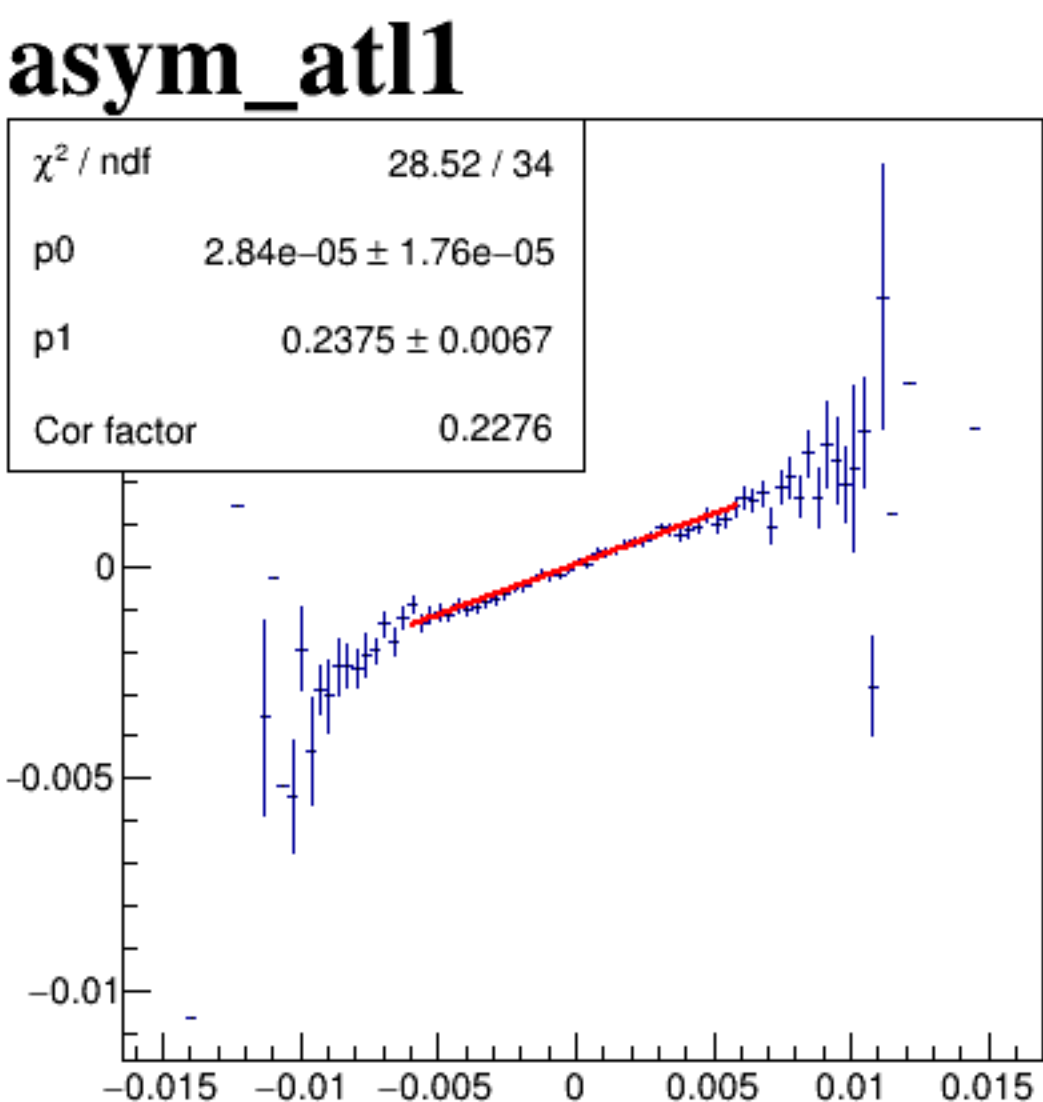
asym_atl1



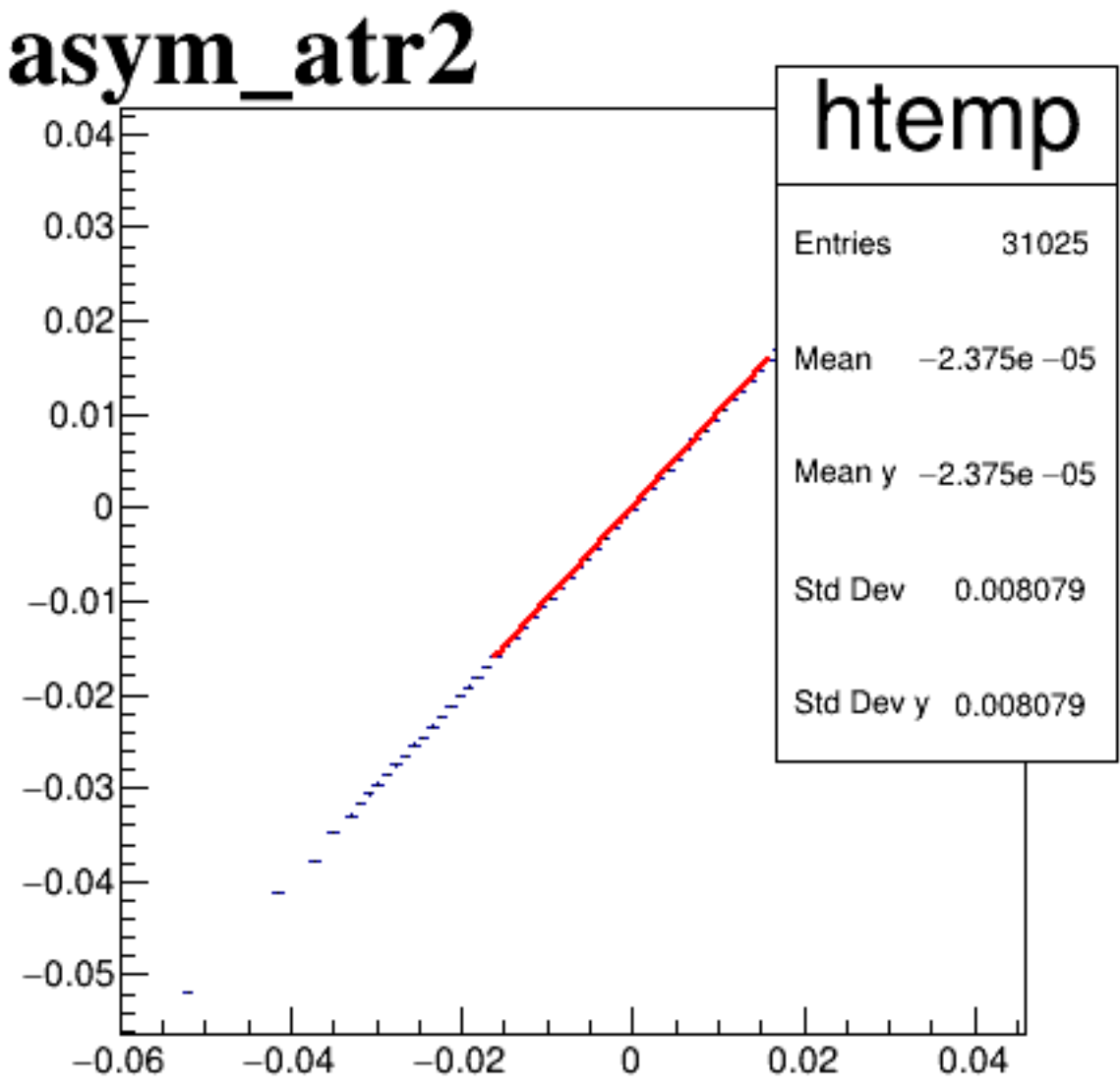
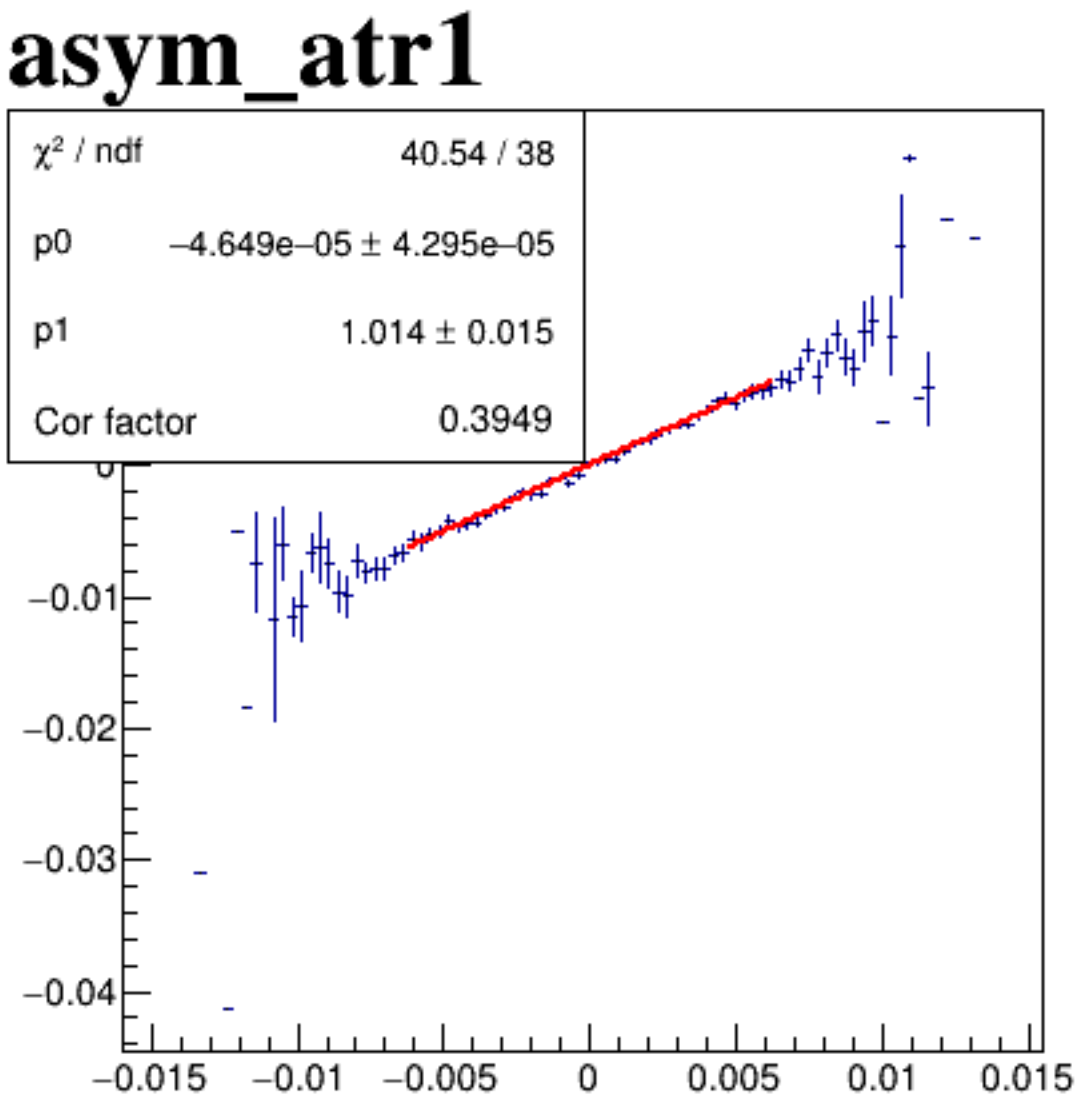
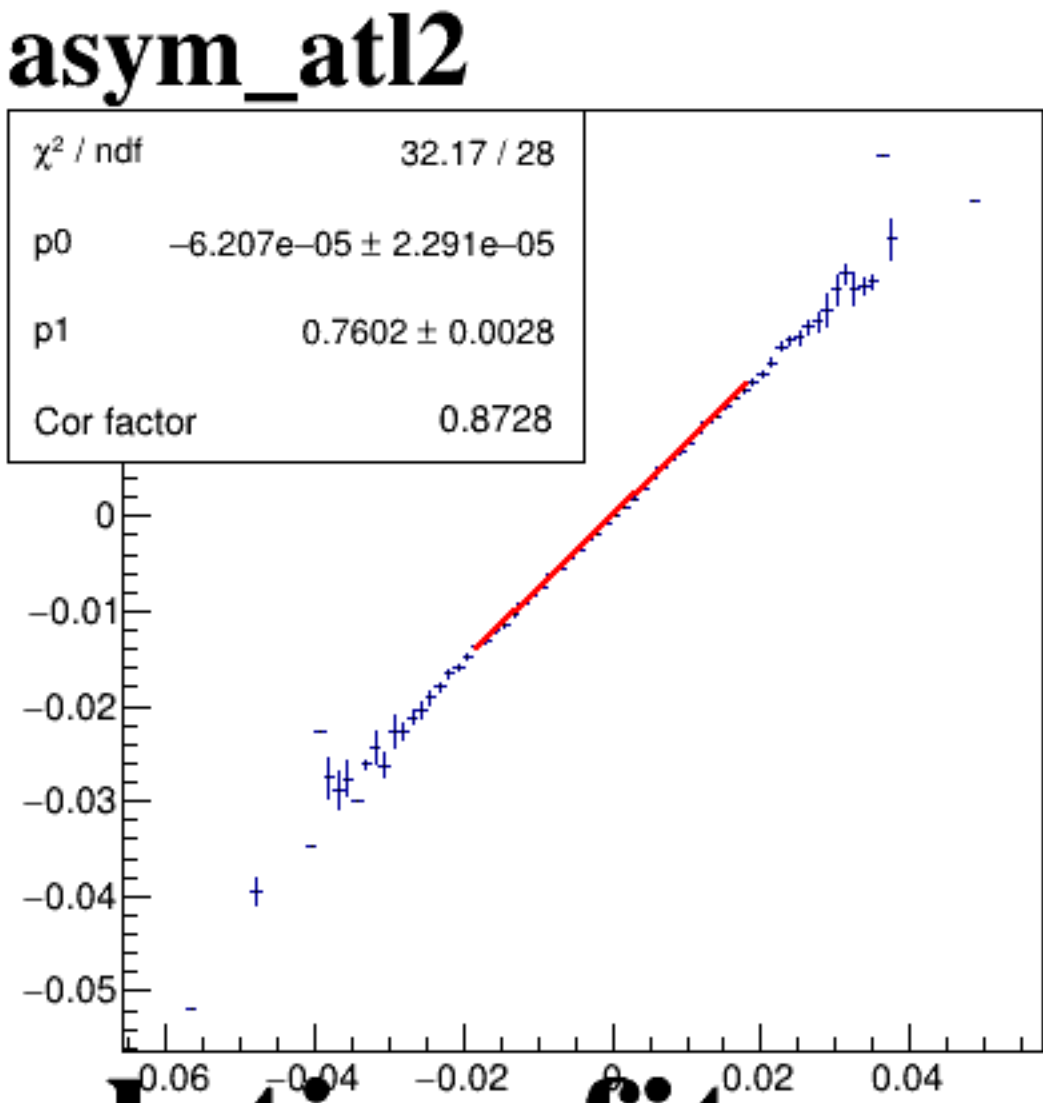
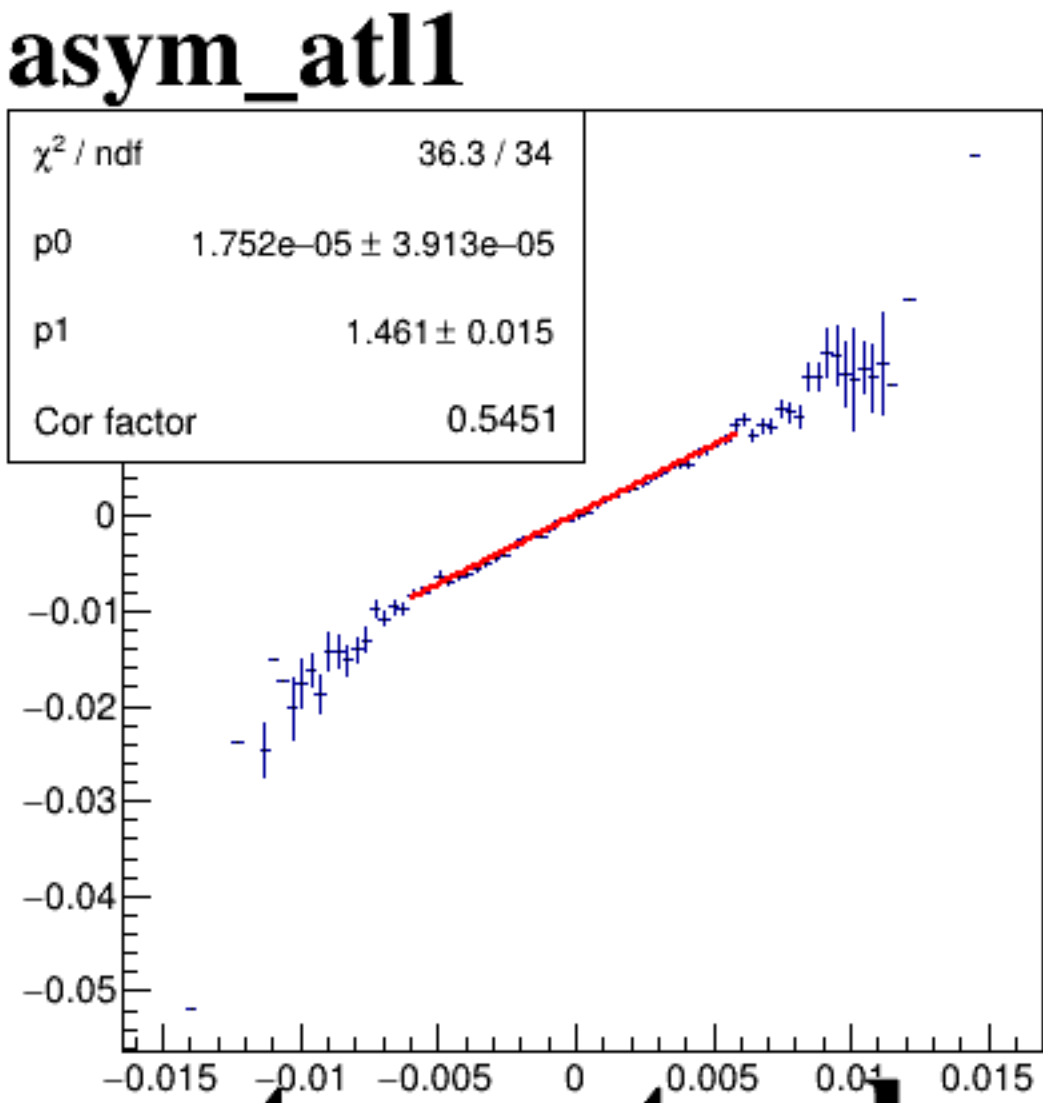
asym_atl2



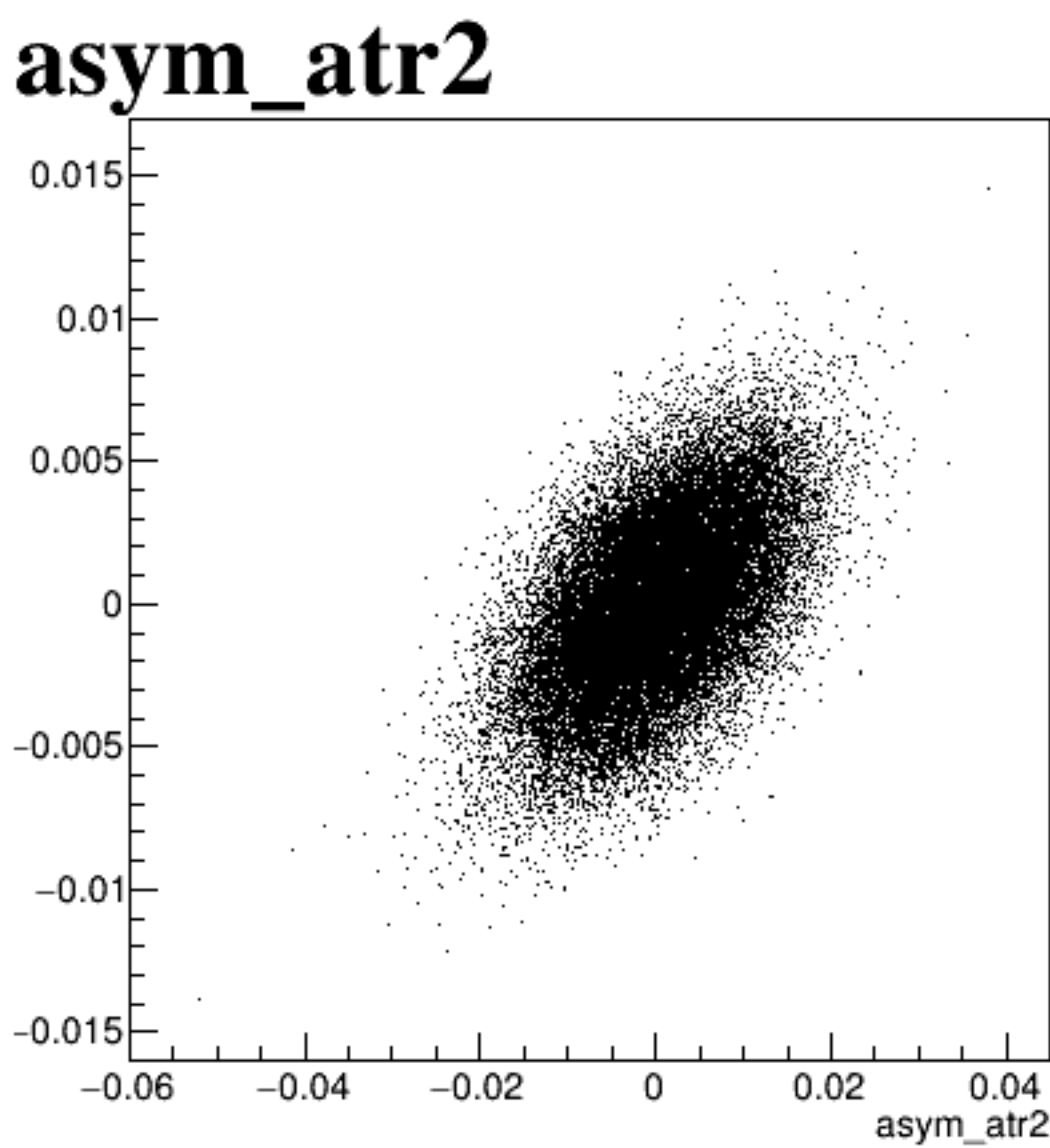
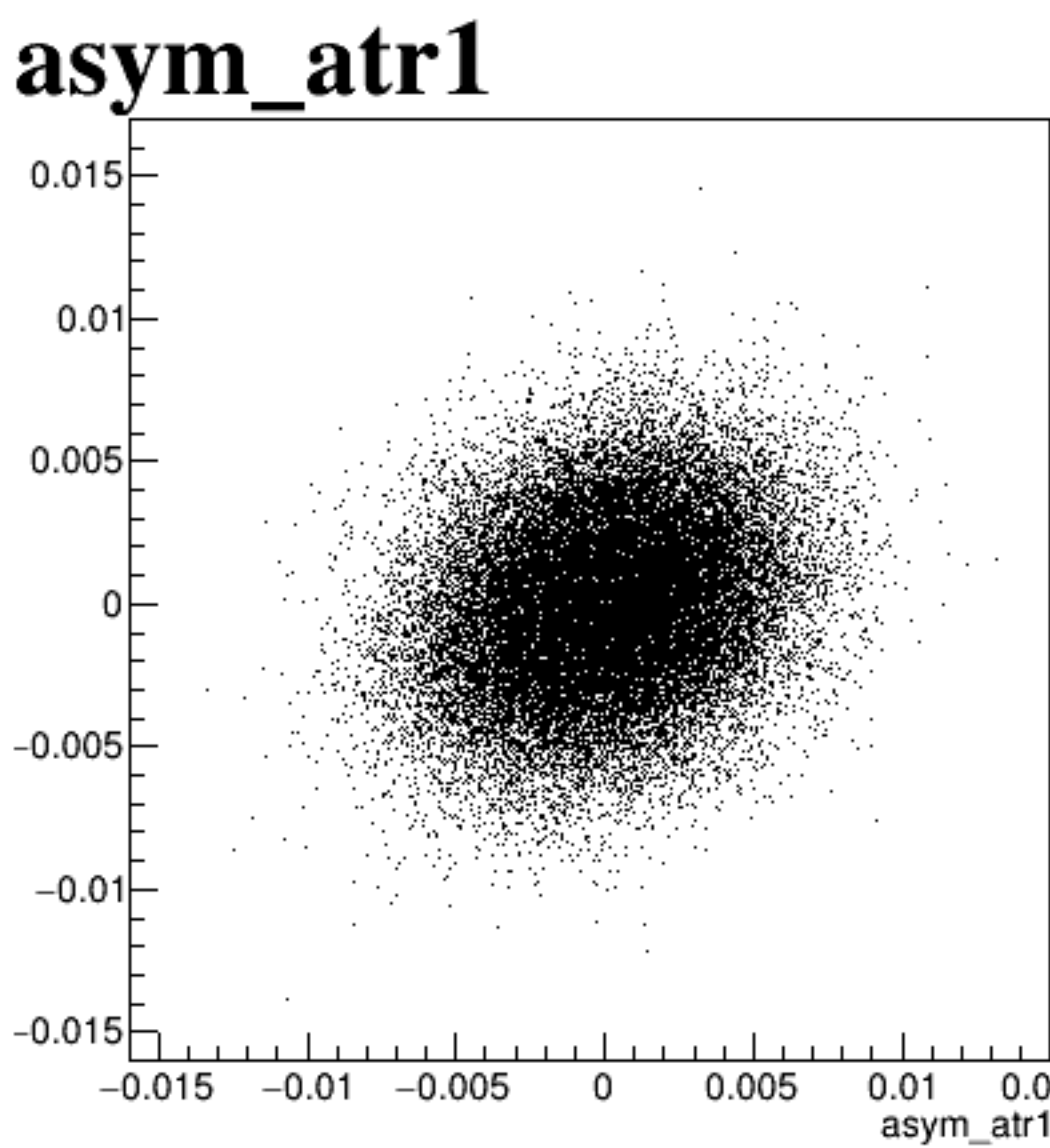
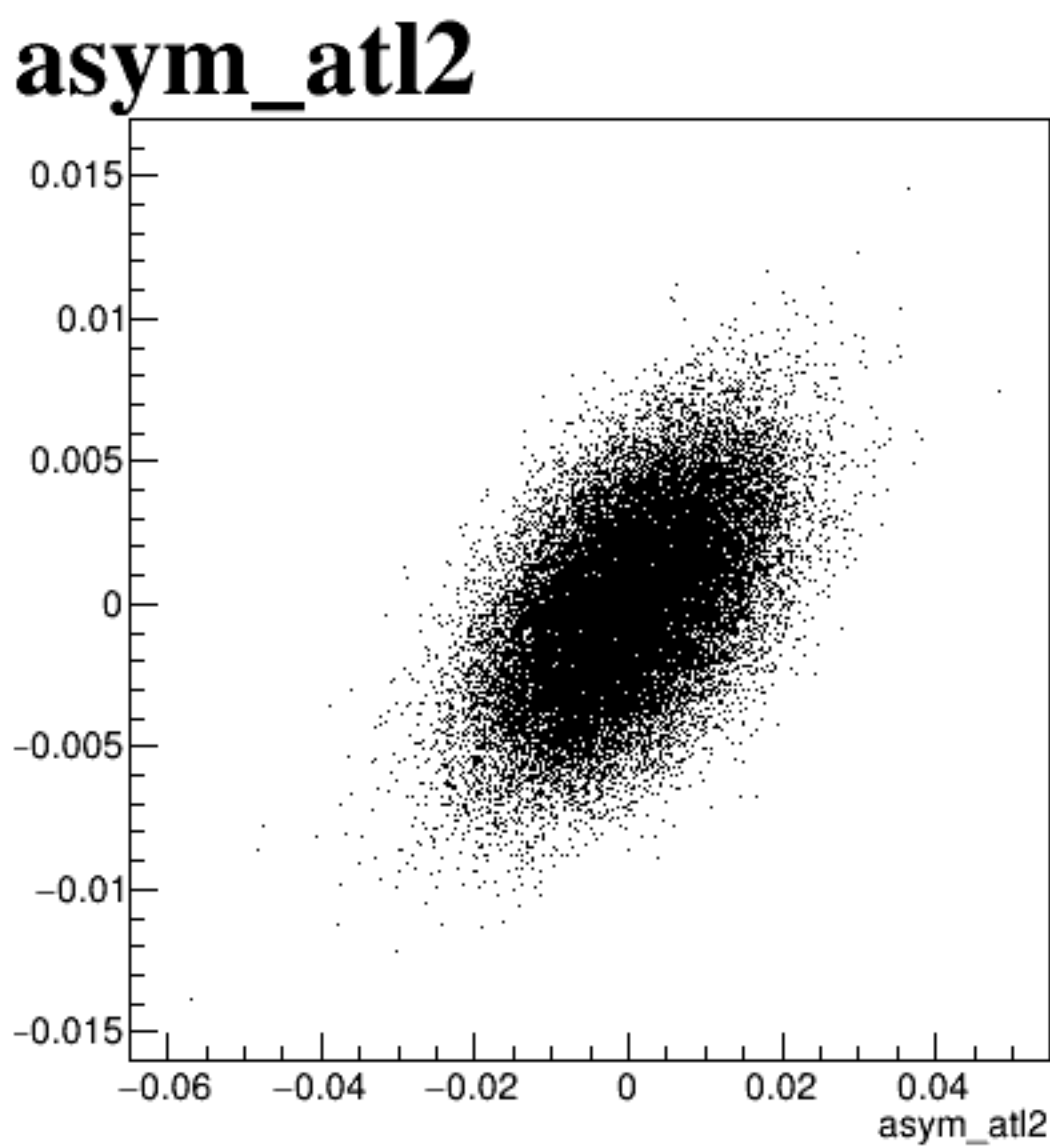
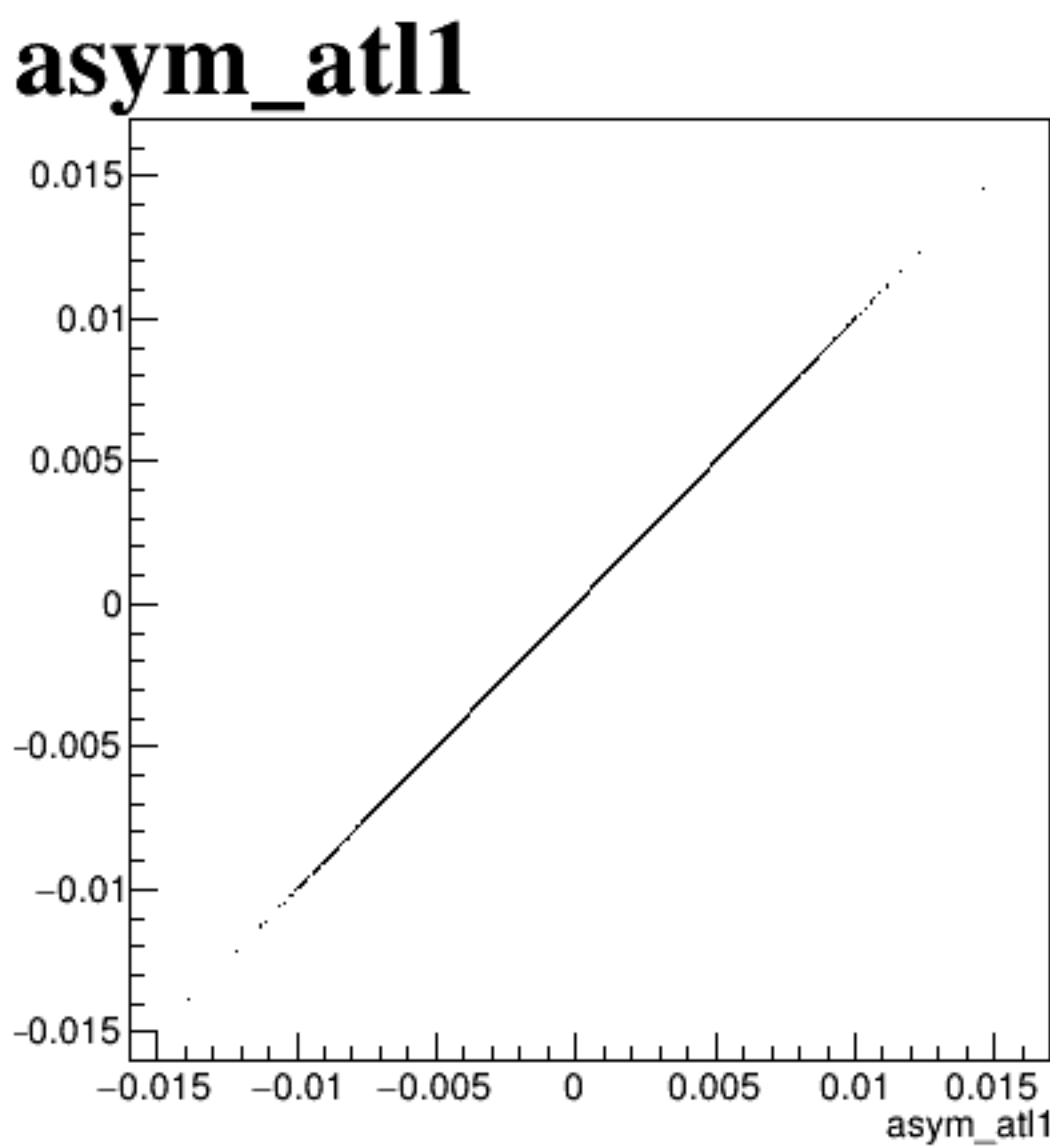
asym_atr1



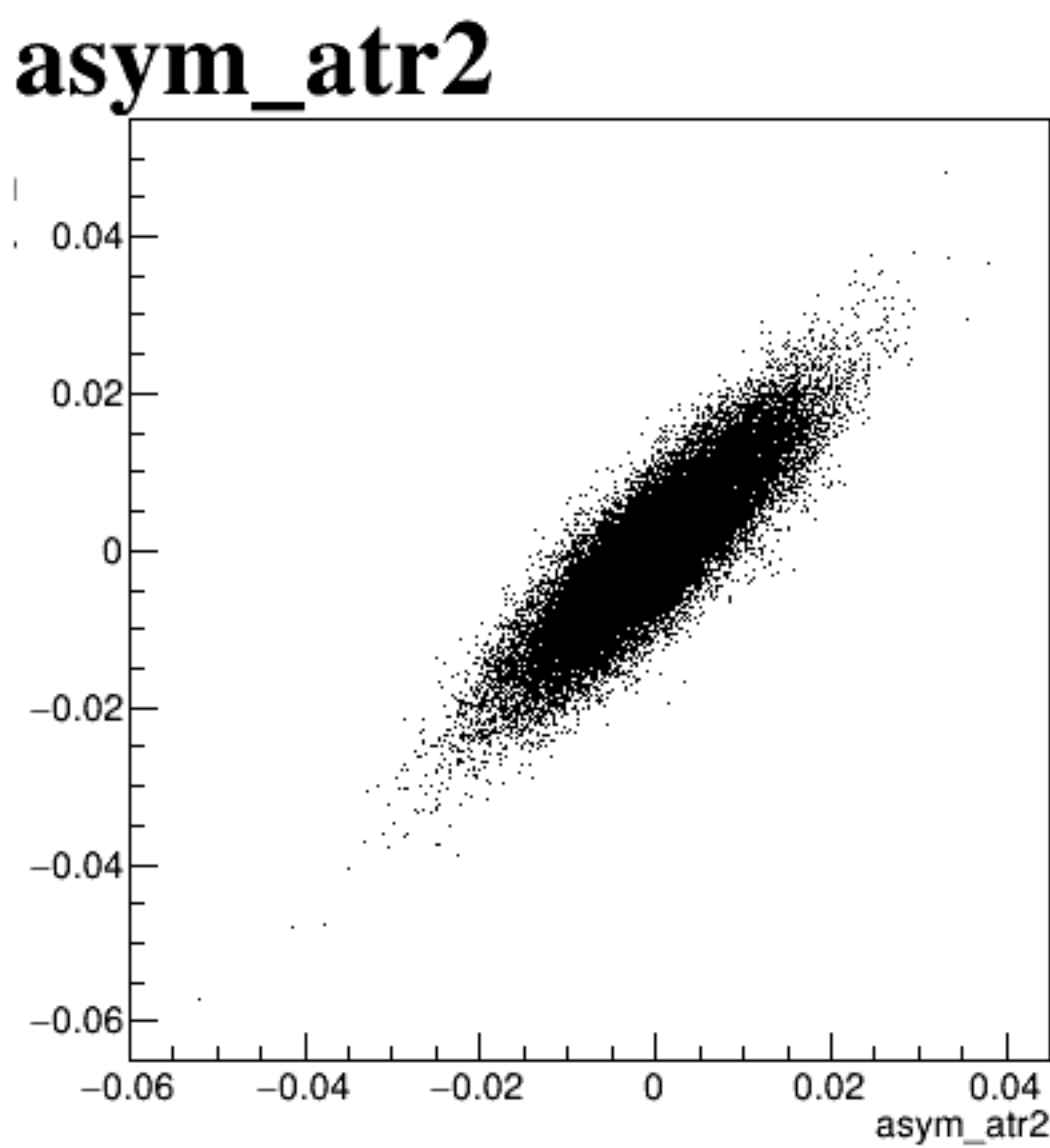
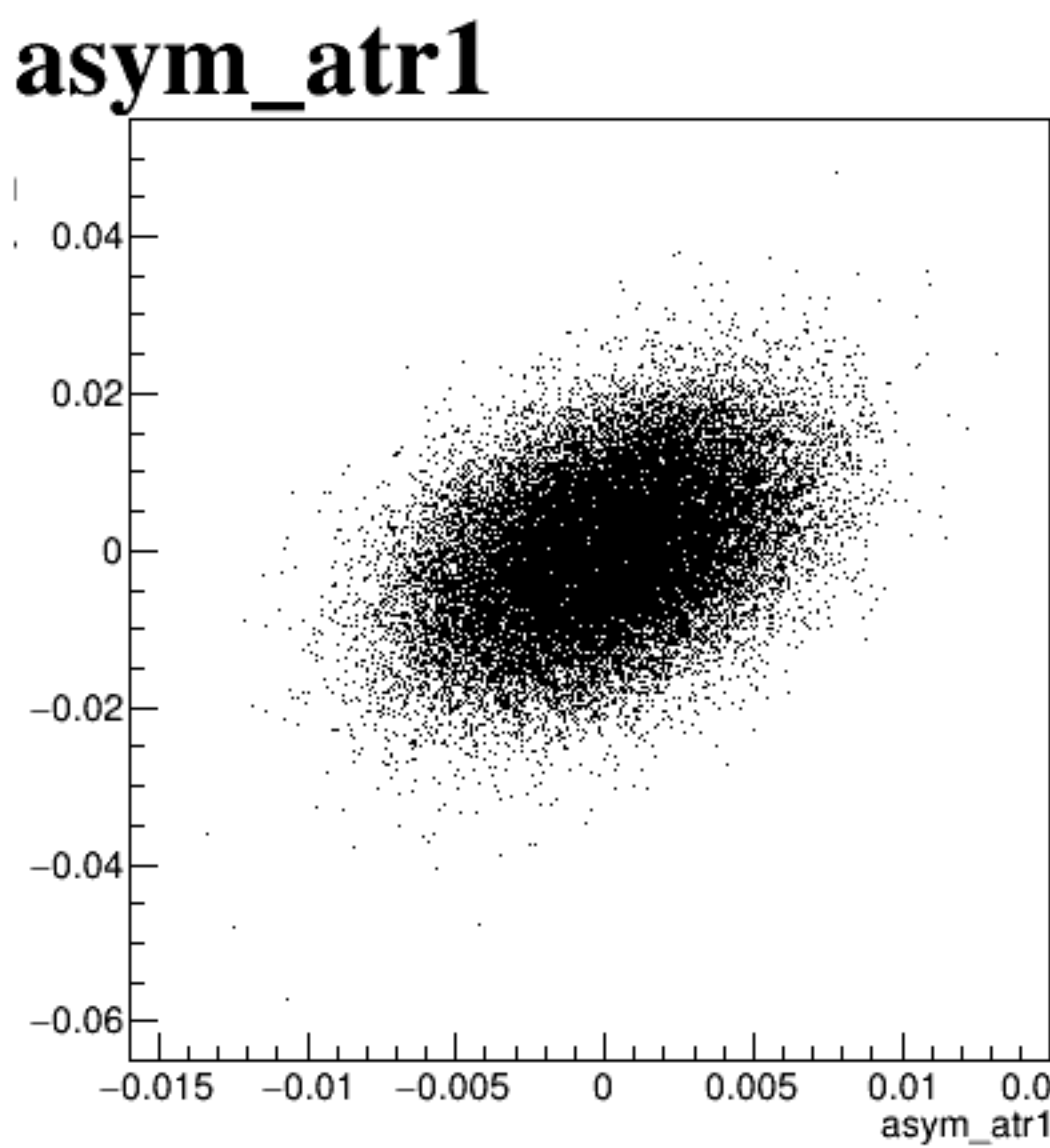
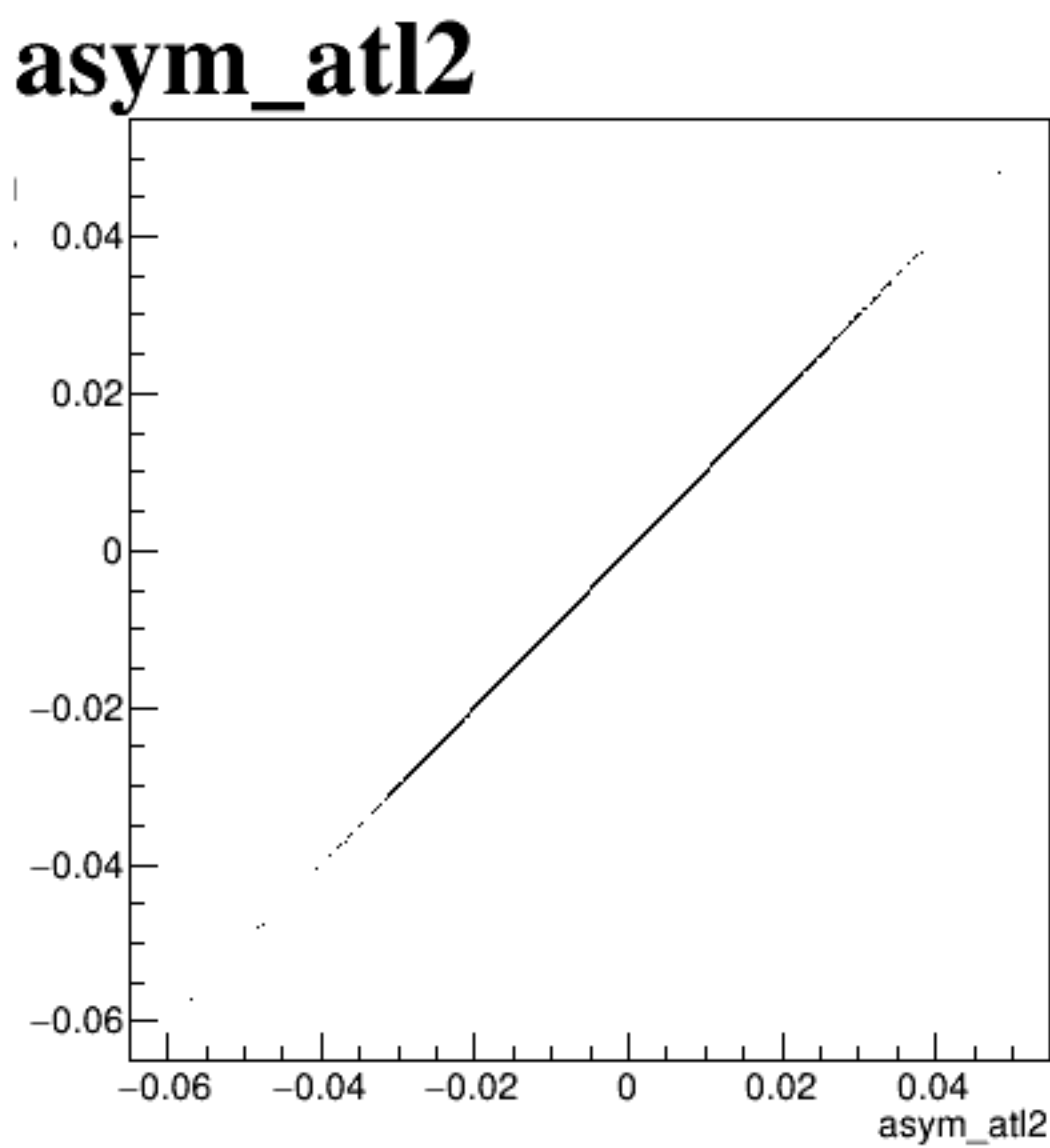
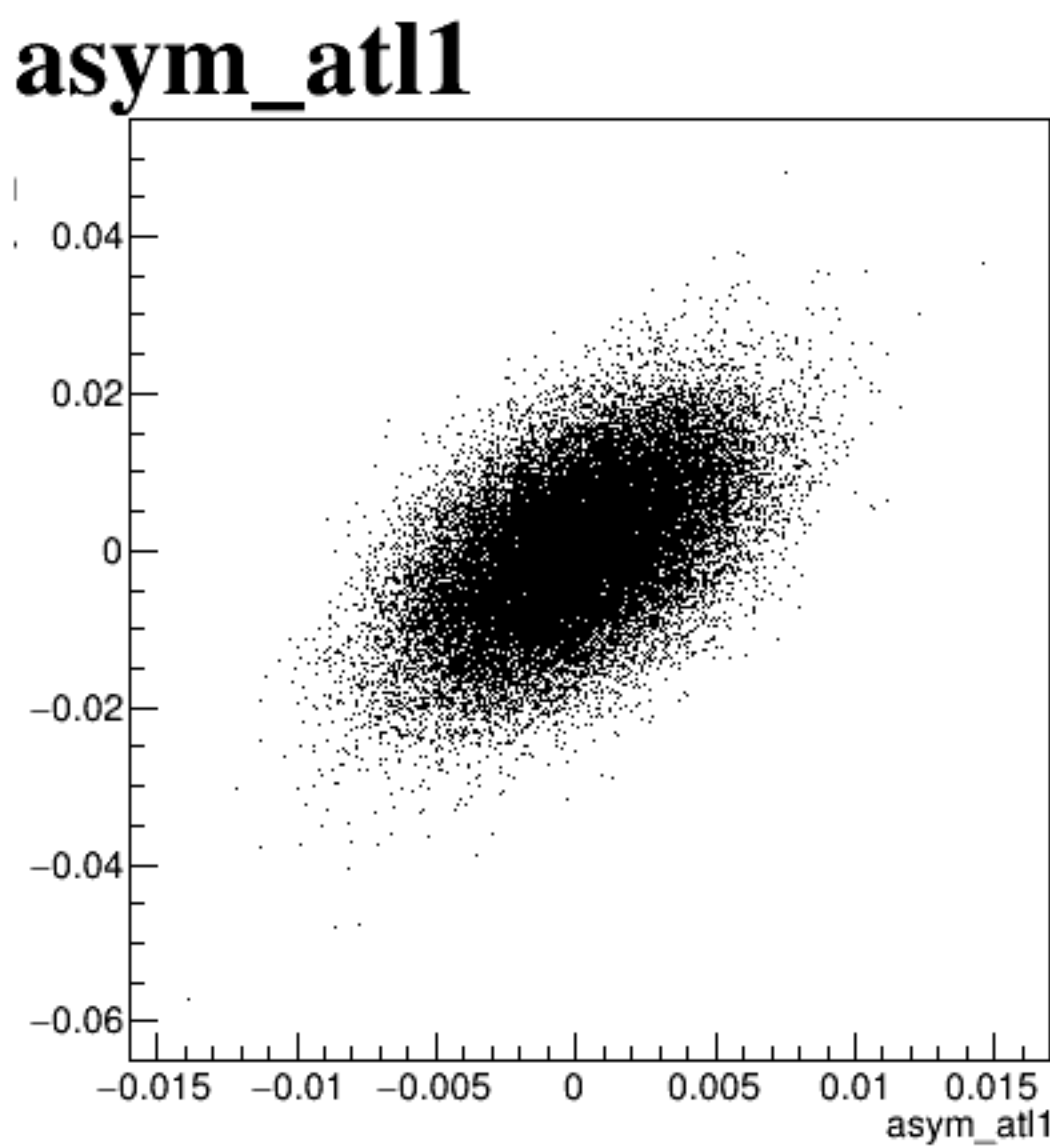
asym_atr2



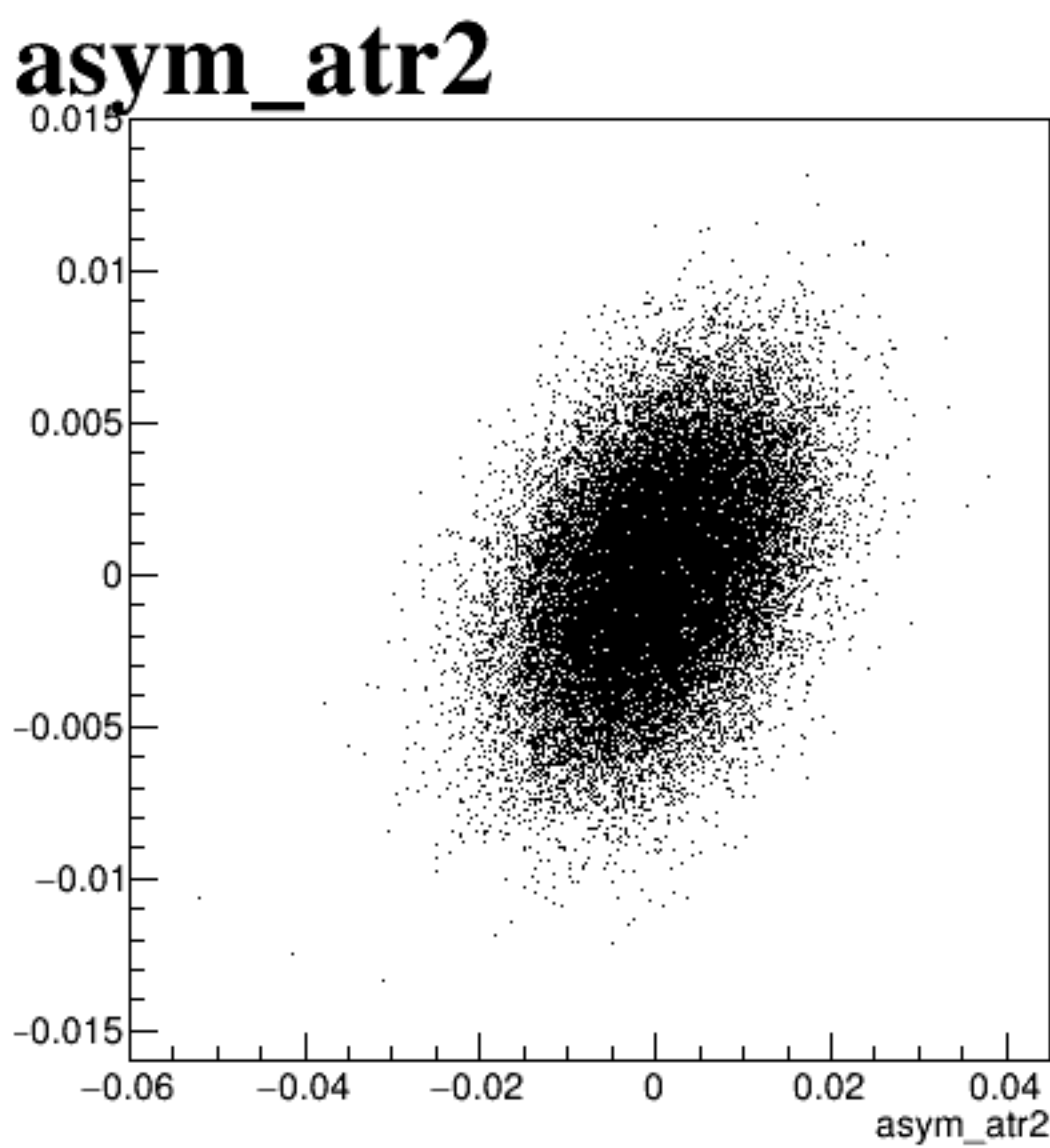
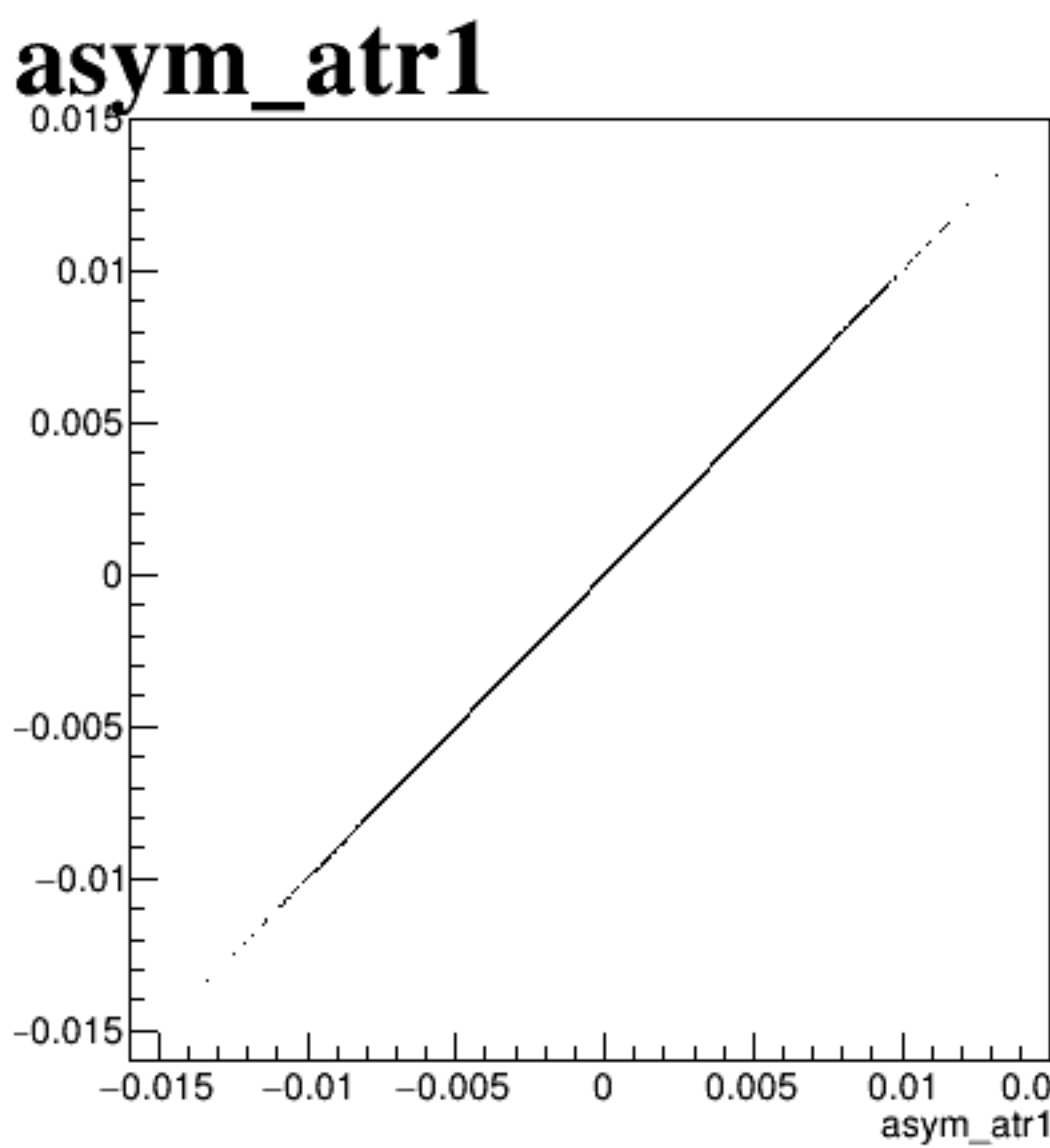
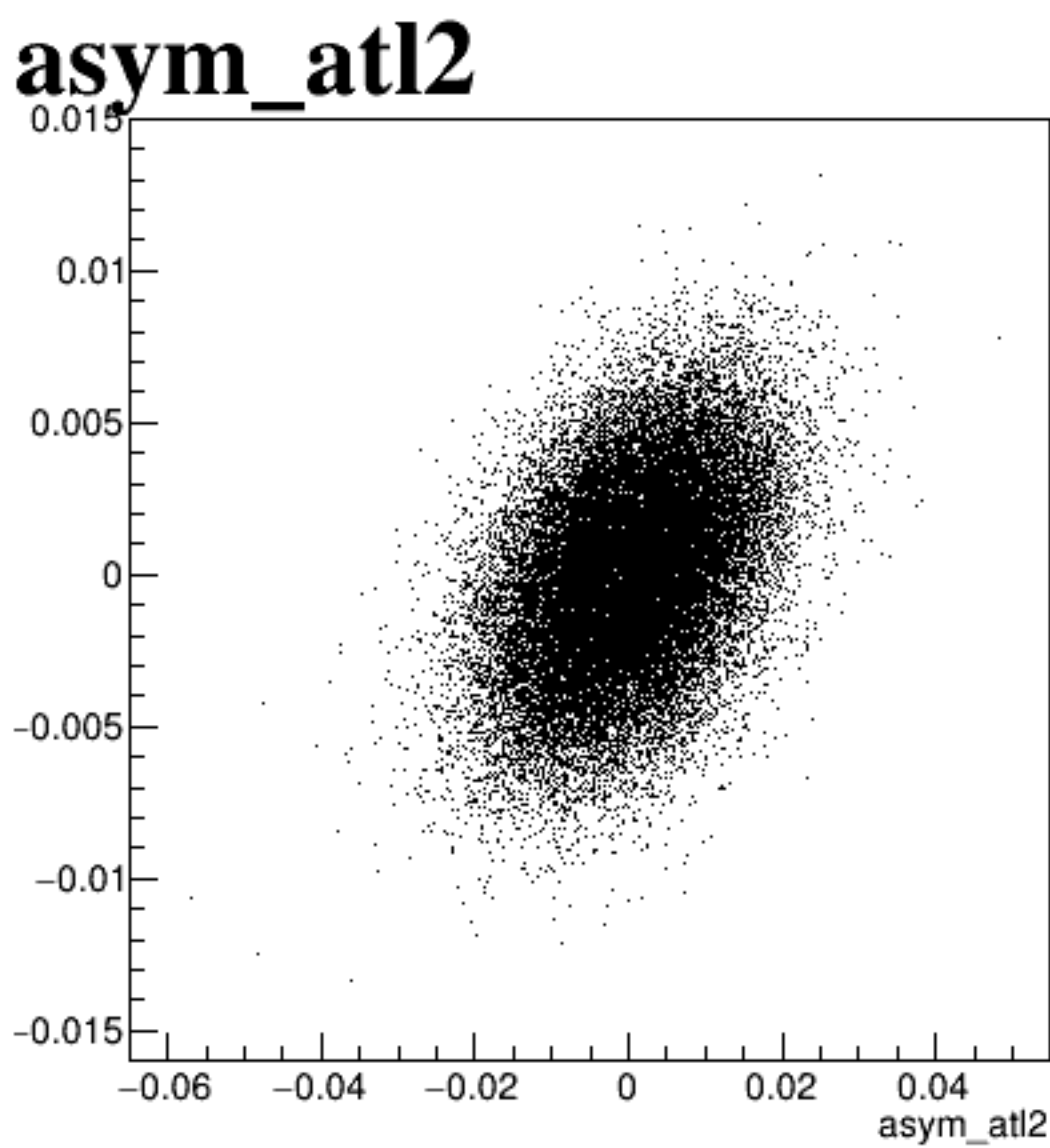
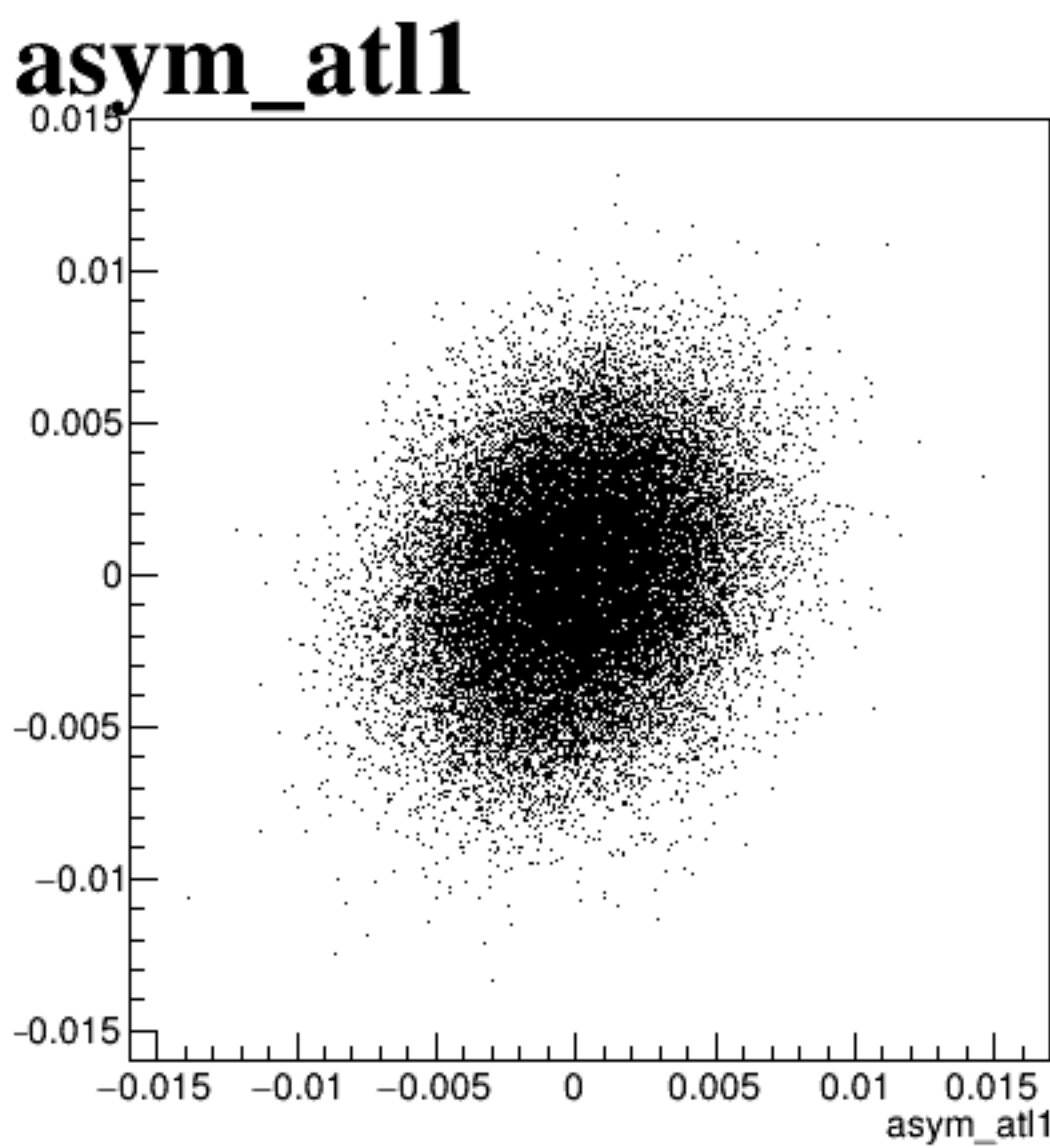
asym_atl1



asym_atl2



asym_atr1



asym_atr2

