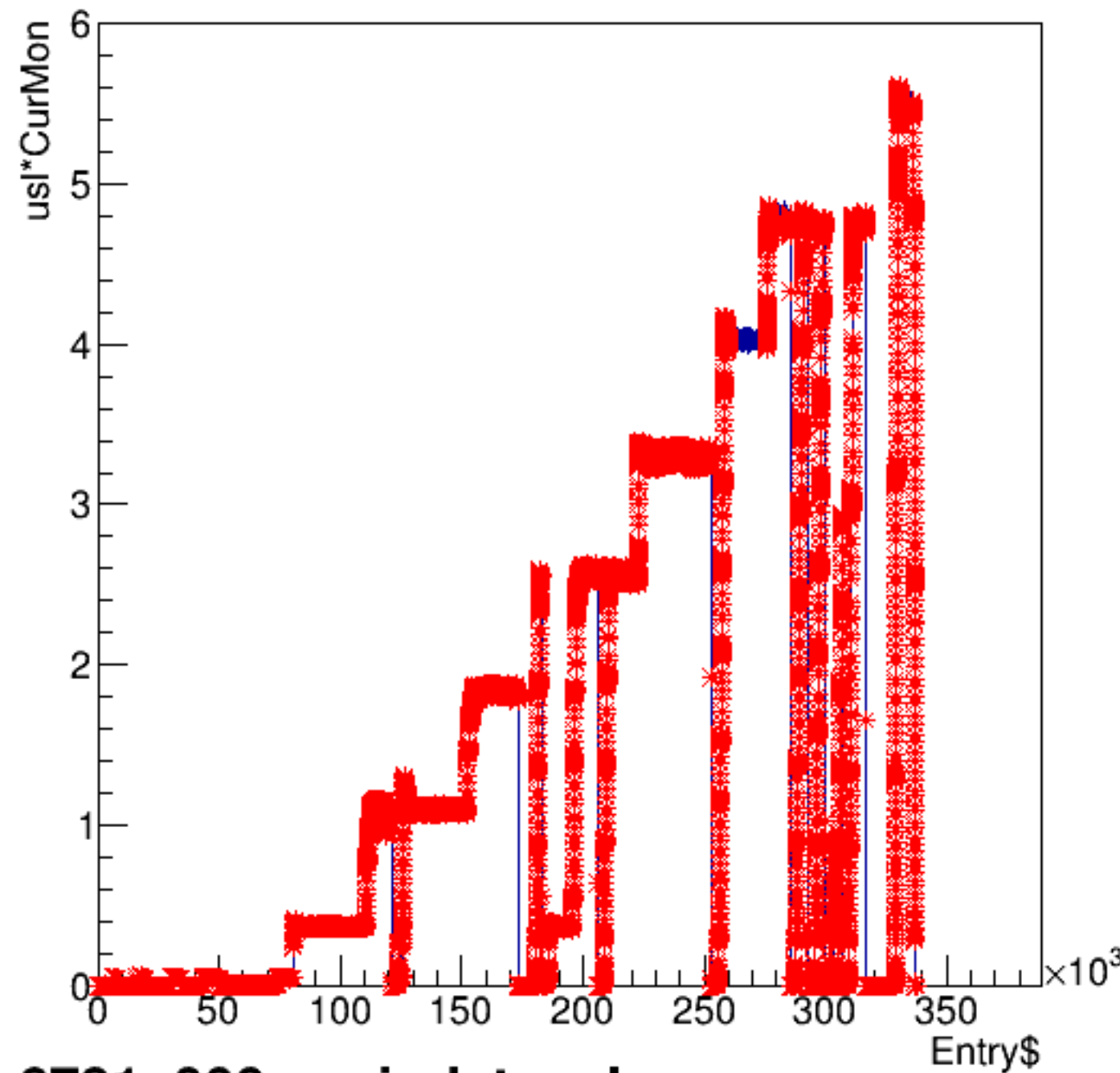
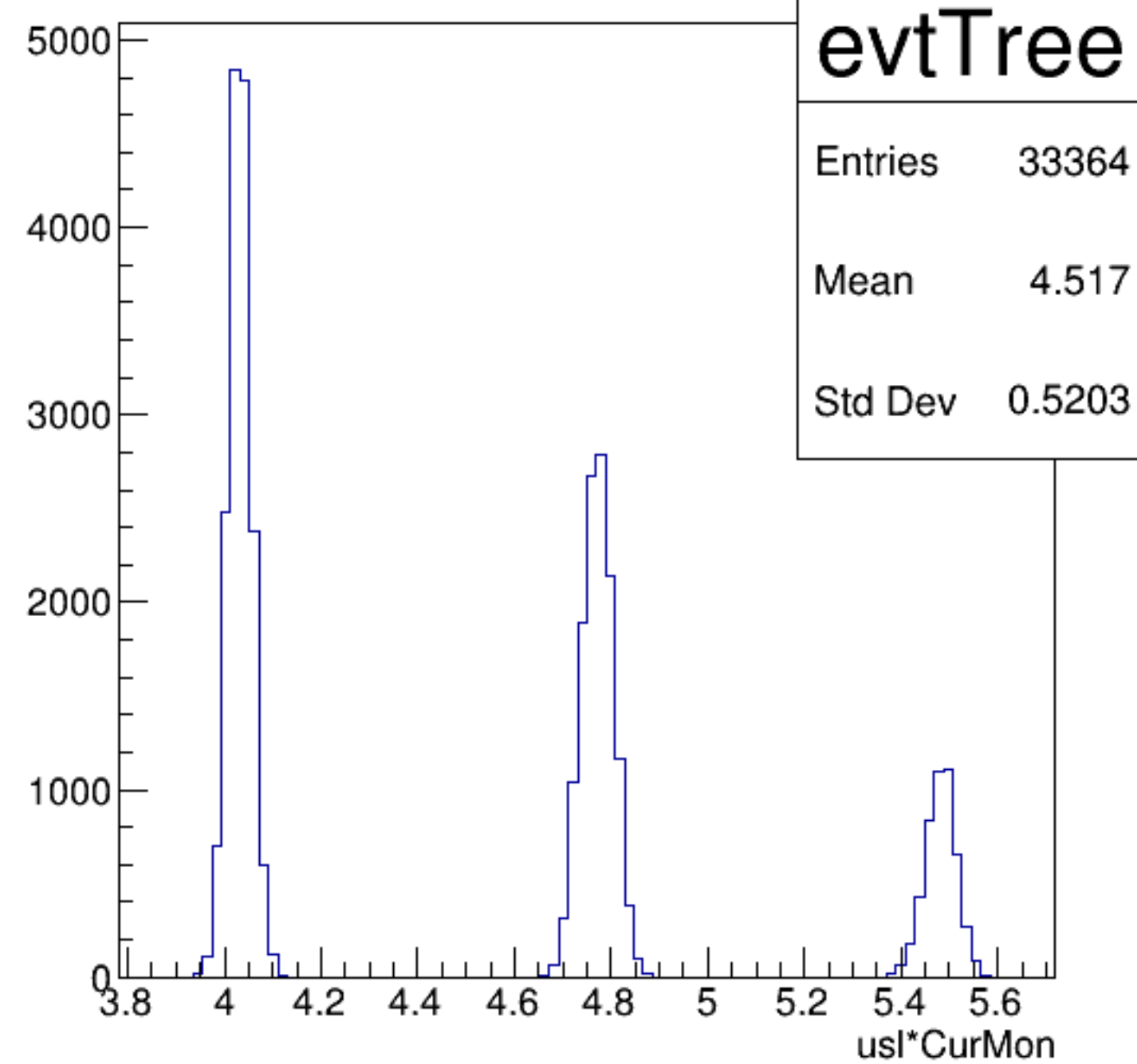


usi*CurMon:Entry\$



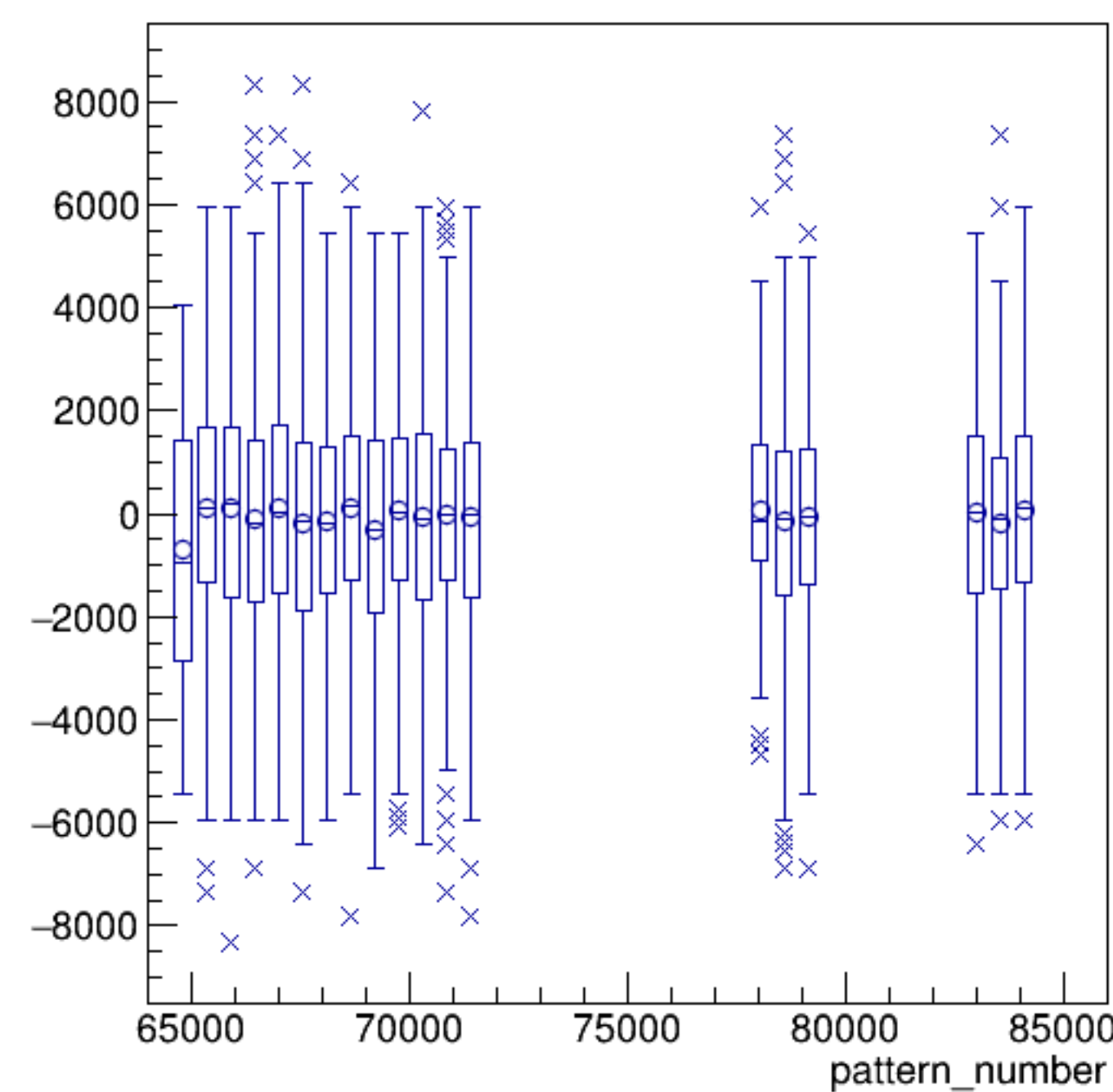
run6731_000_maintet_usl.png

usi*CurMon {ErrorFlag==0}

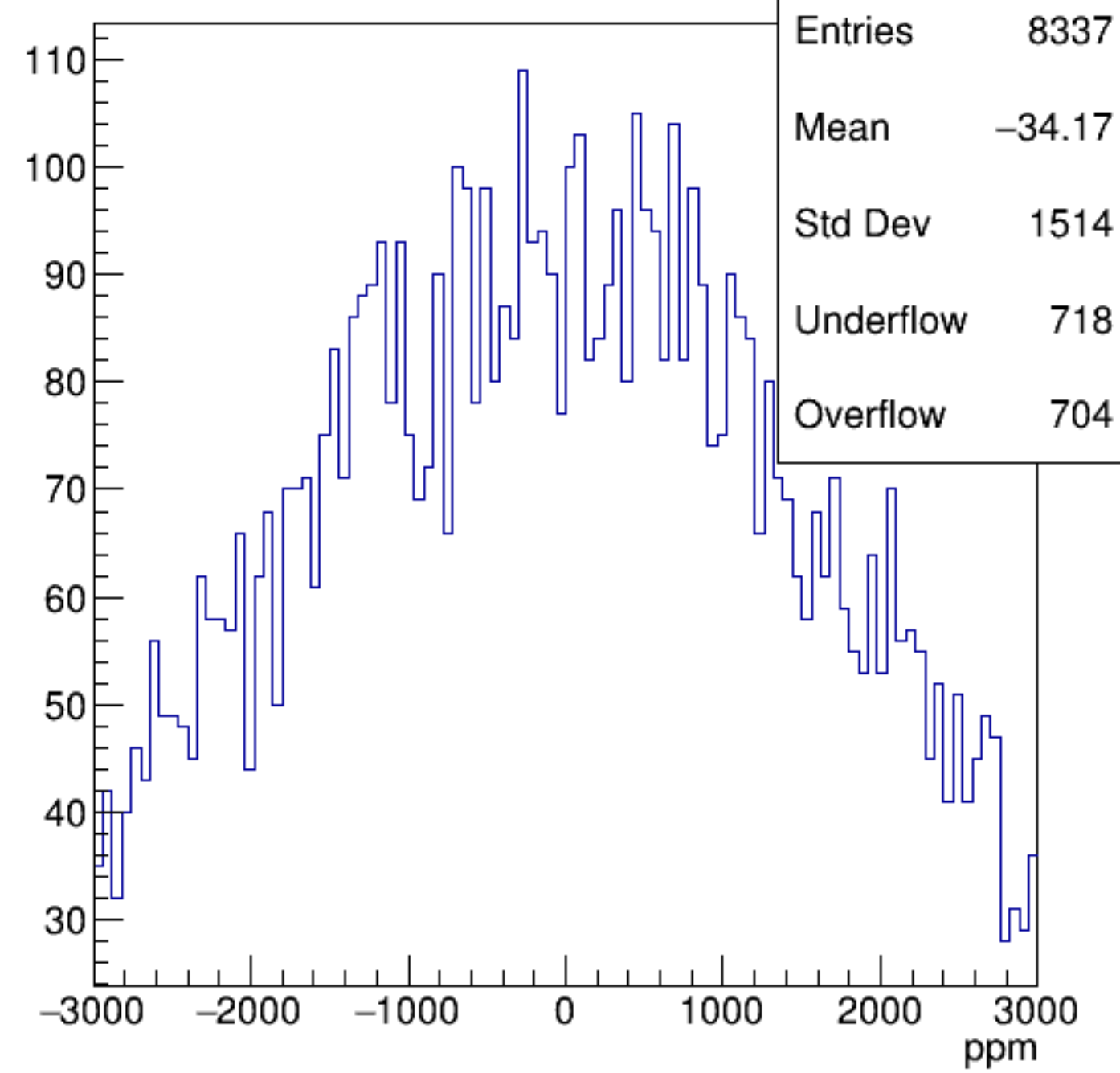


evtTree	
Entries	33364
Mean	4.517
Std Dev	0.5203

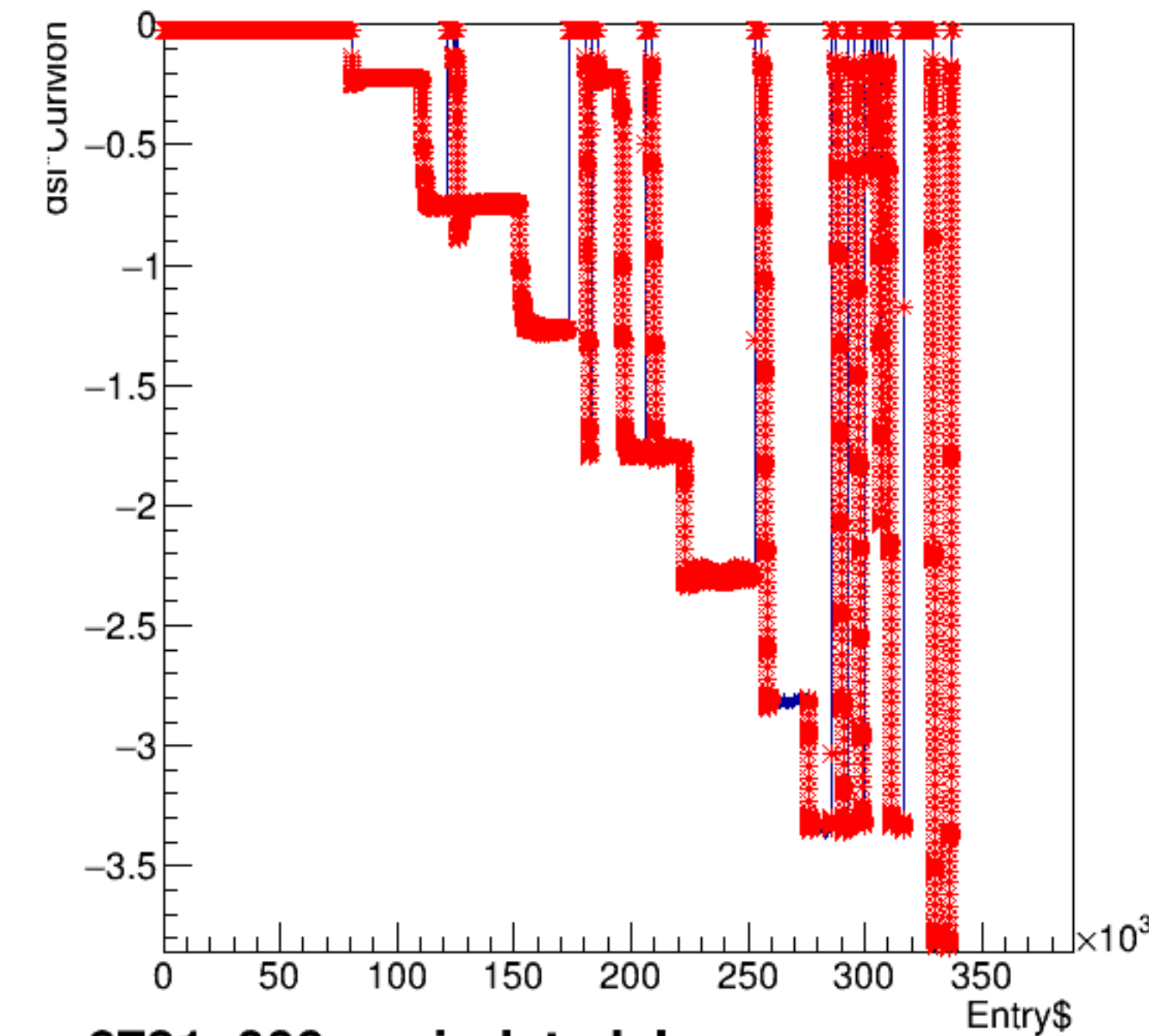
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl

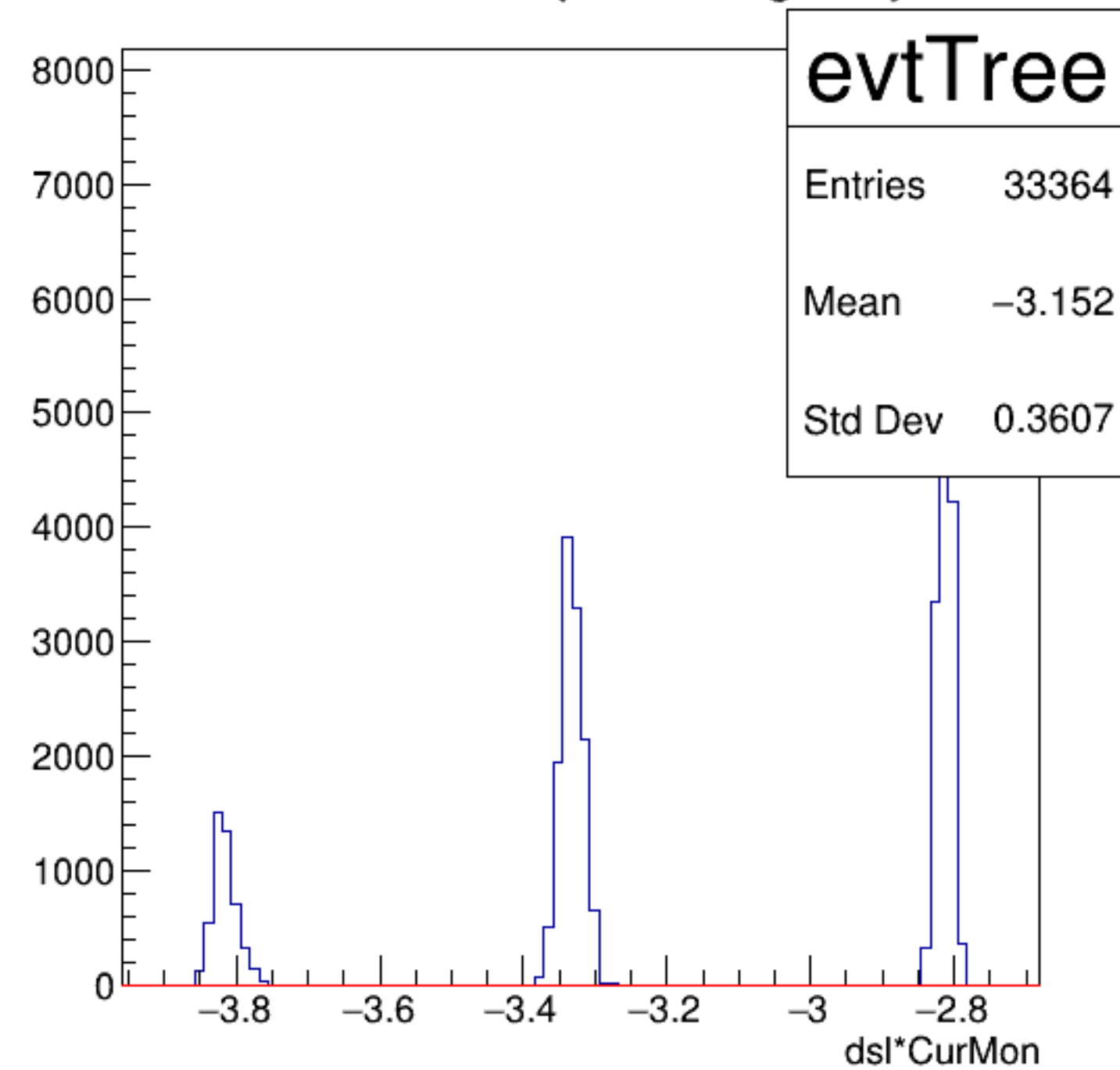


dsl*CurMon:Entry\$

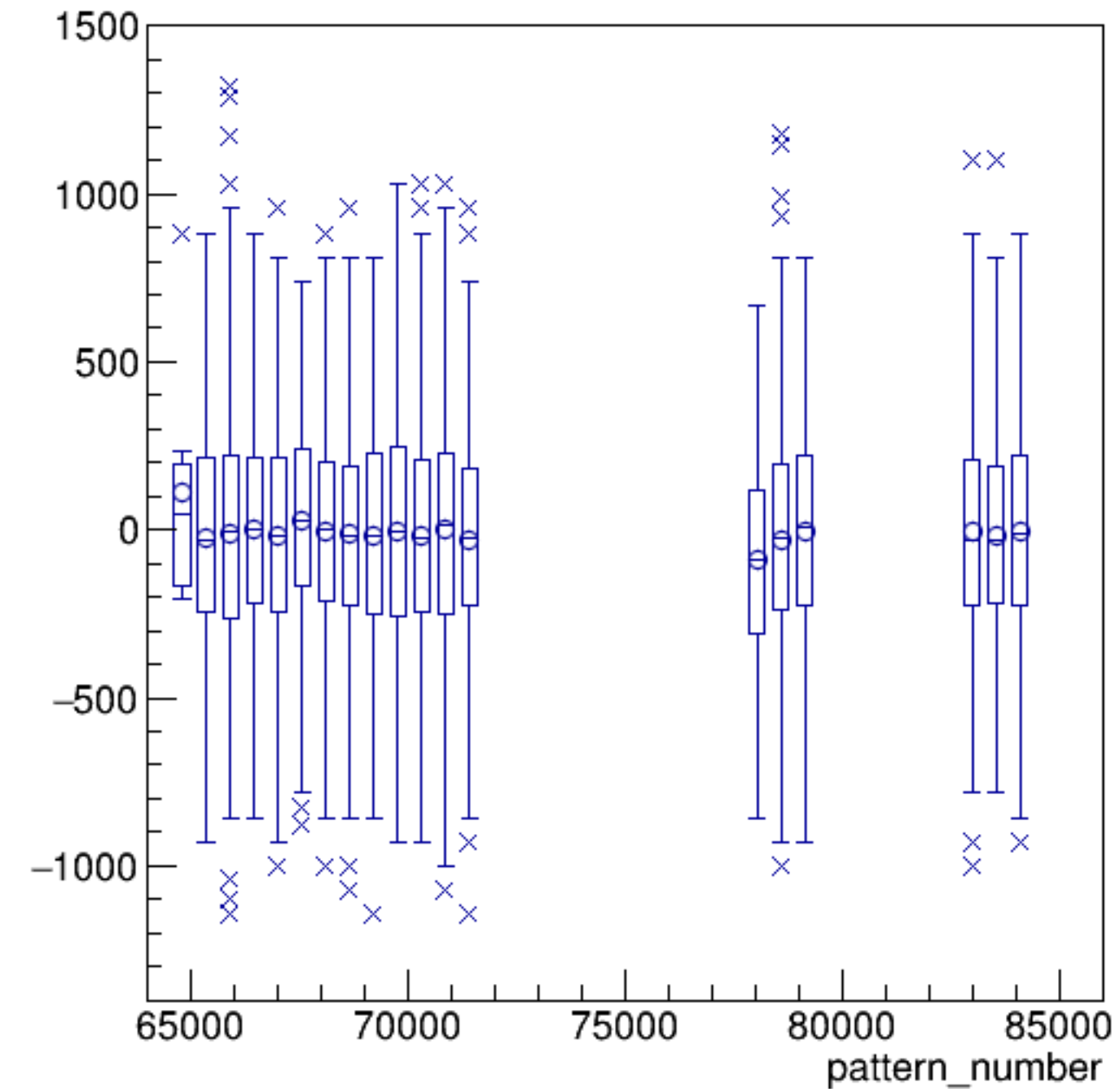


run6731_000_maindet_dsl.png

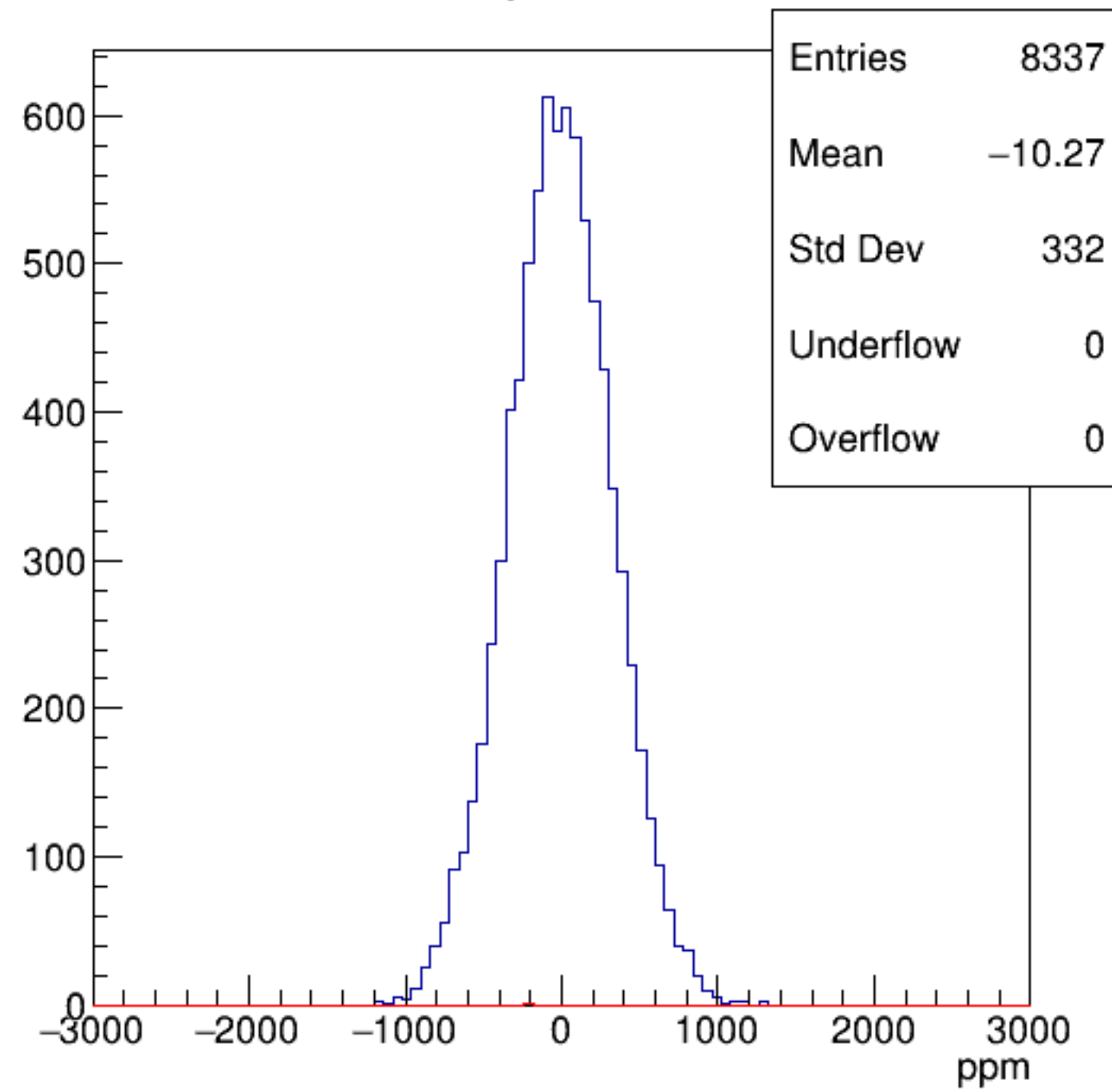
dsl*CurMon {ErrorFlag==0}



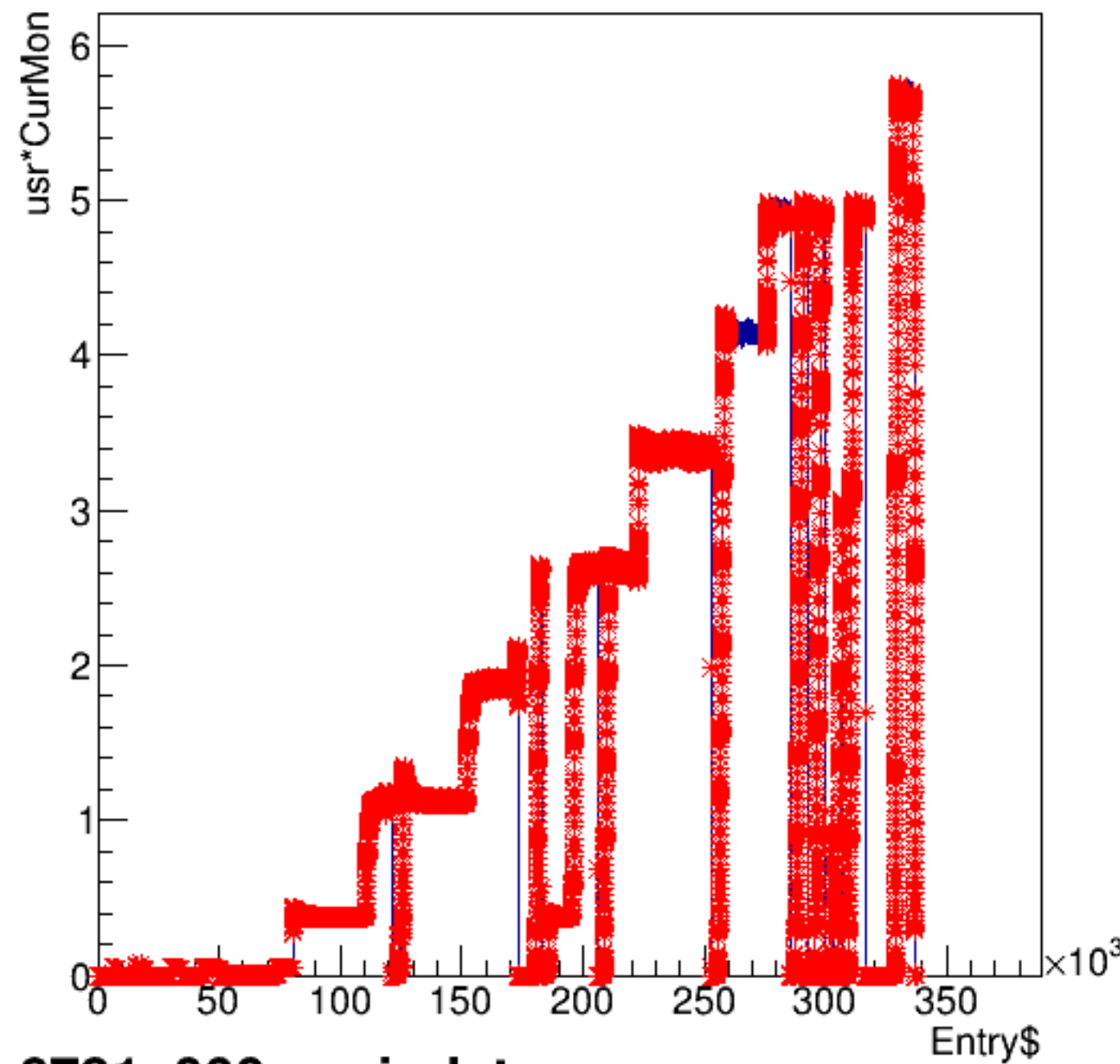
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl

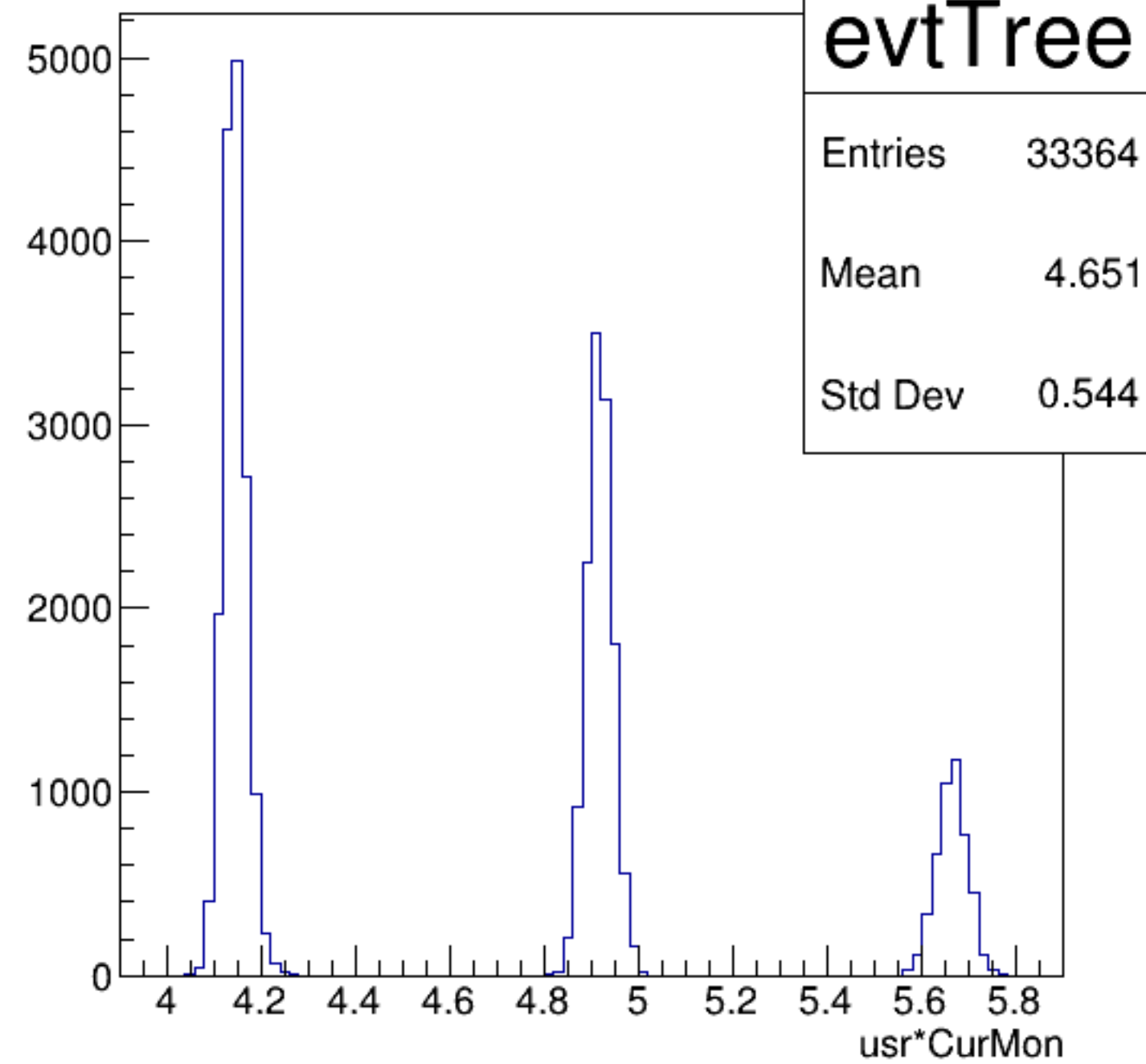


usr*CurMon:Entry\$



run6731_000_maintet_usr.png

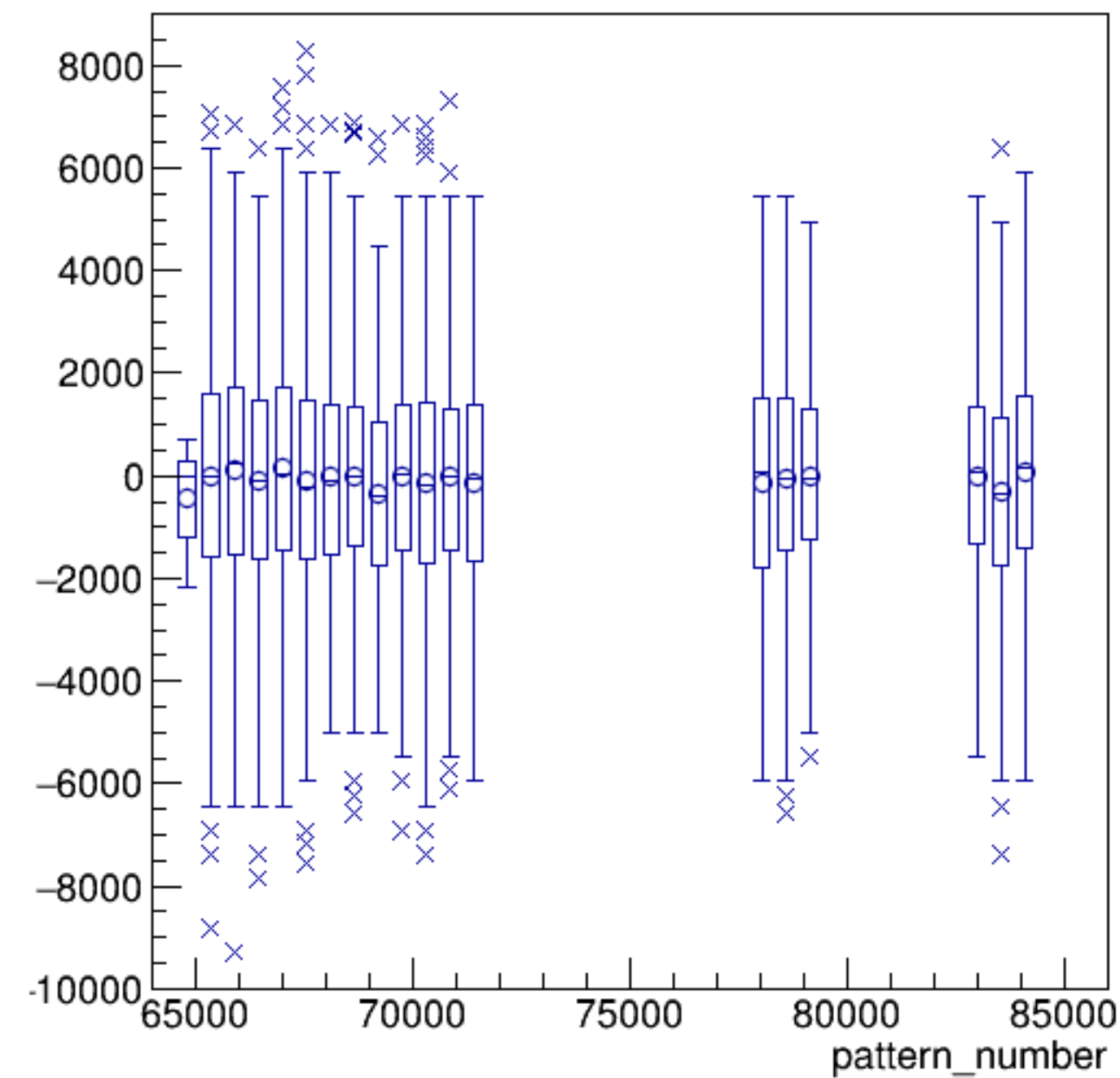
usr*CurMon {ErrorFlag==0}



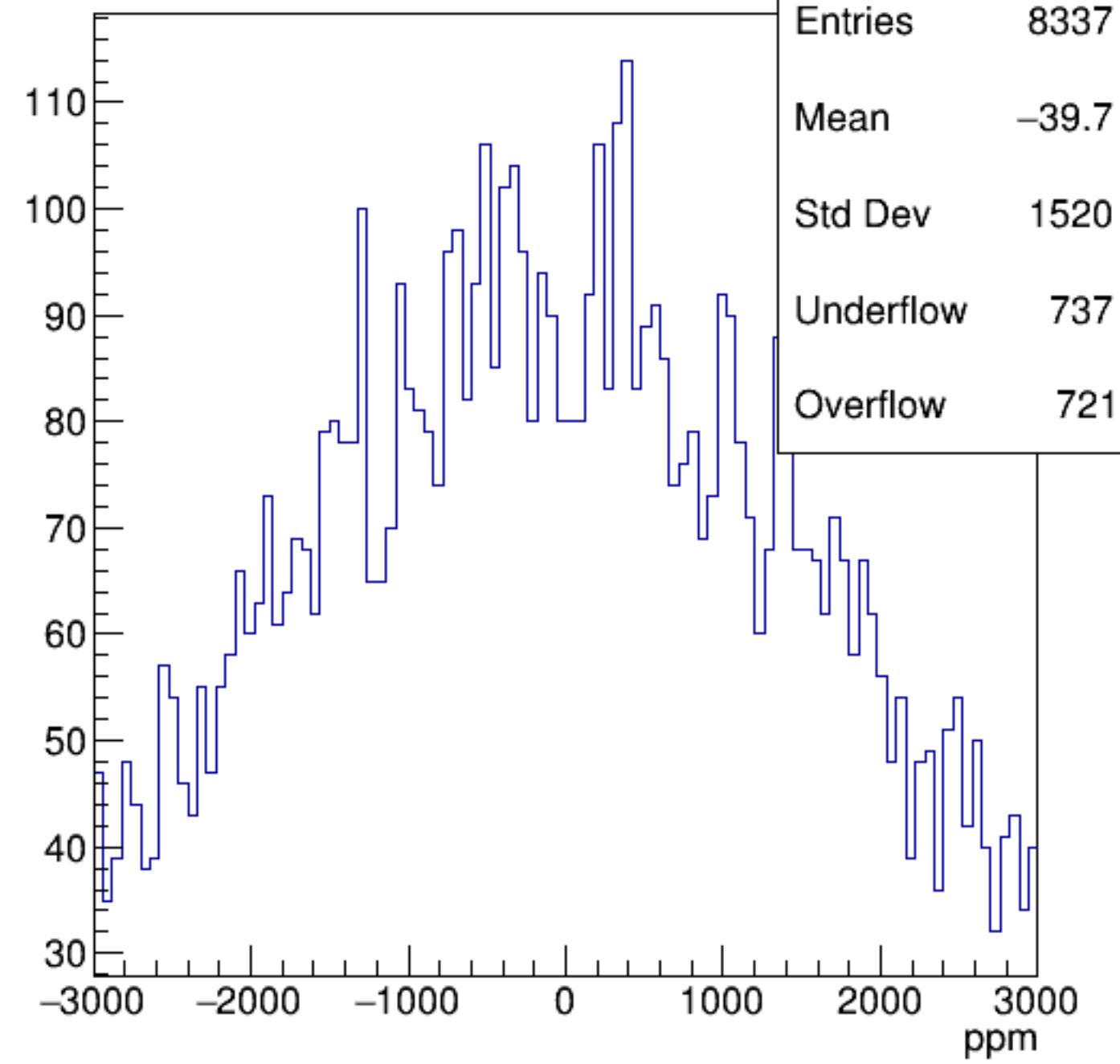
evtTree

Entries	33364
Mean	4.651
Std Dev	0.544

asym_usr/ppm:pattern_number {ErrorFlag==0}

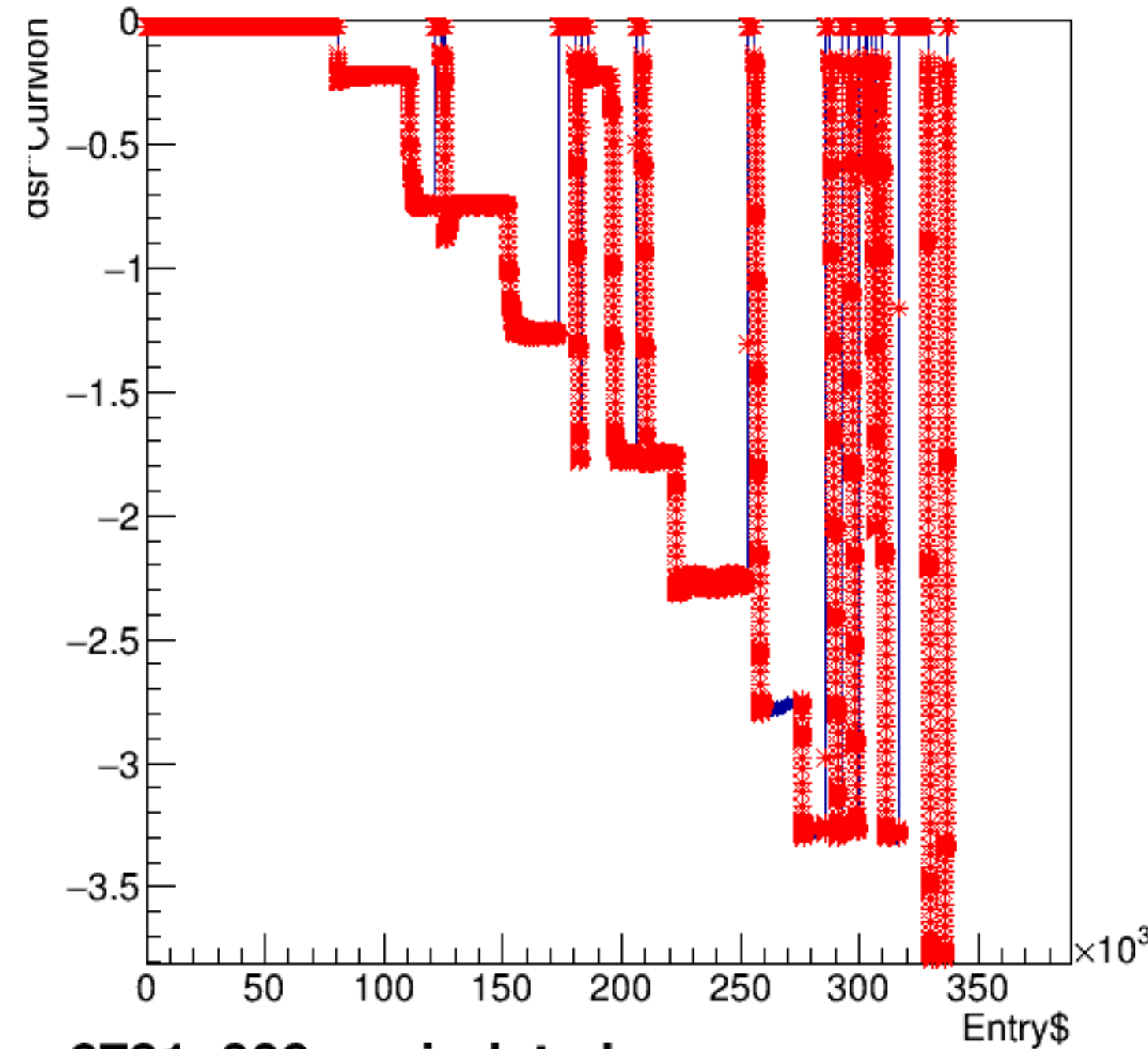


asym_usr

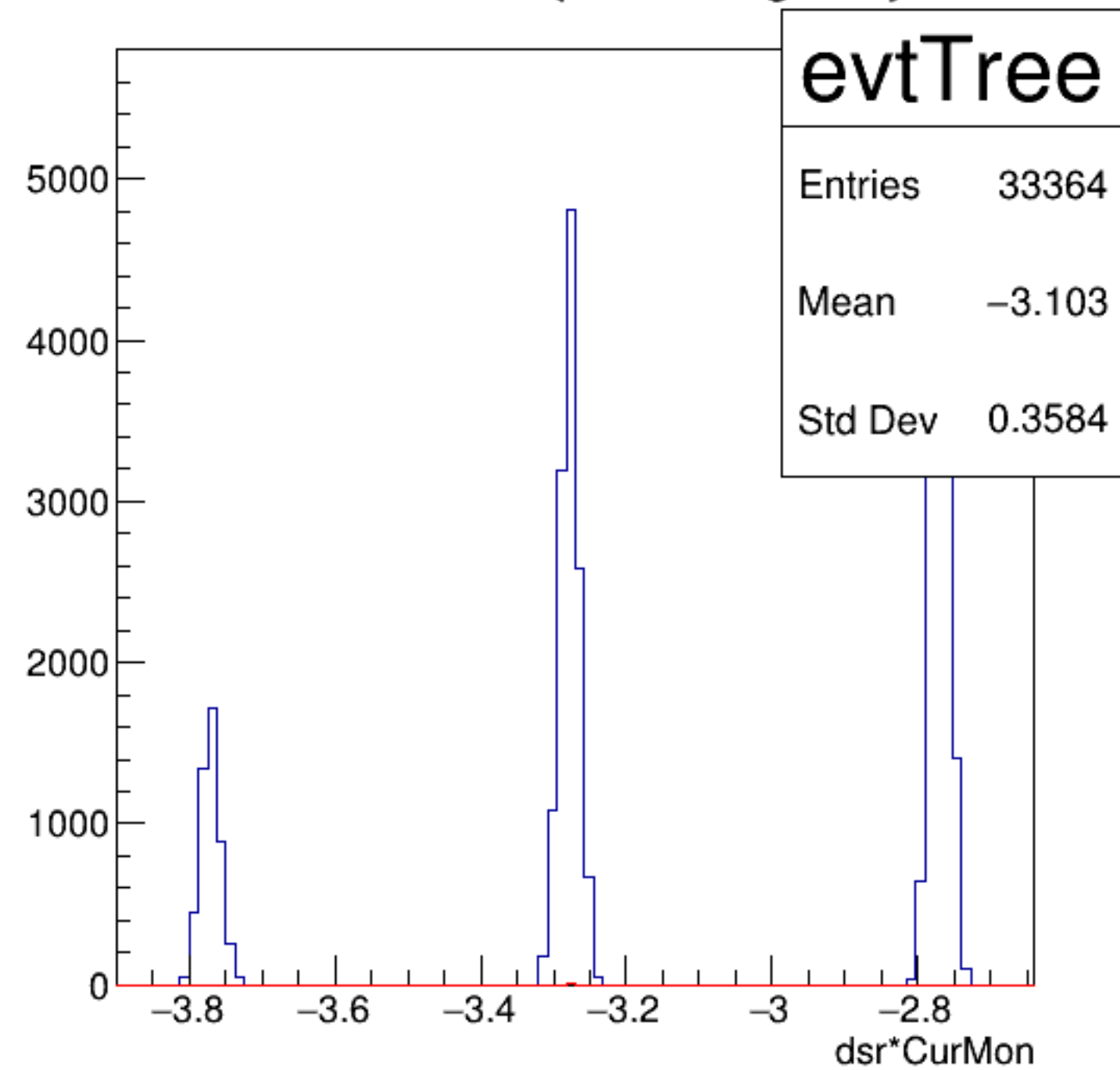


Entries	8337
Mean	-39.7
Std Dev	1520
Underflow	737
Overflow	721

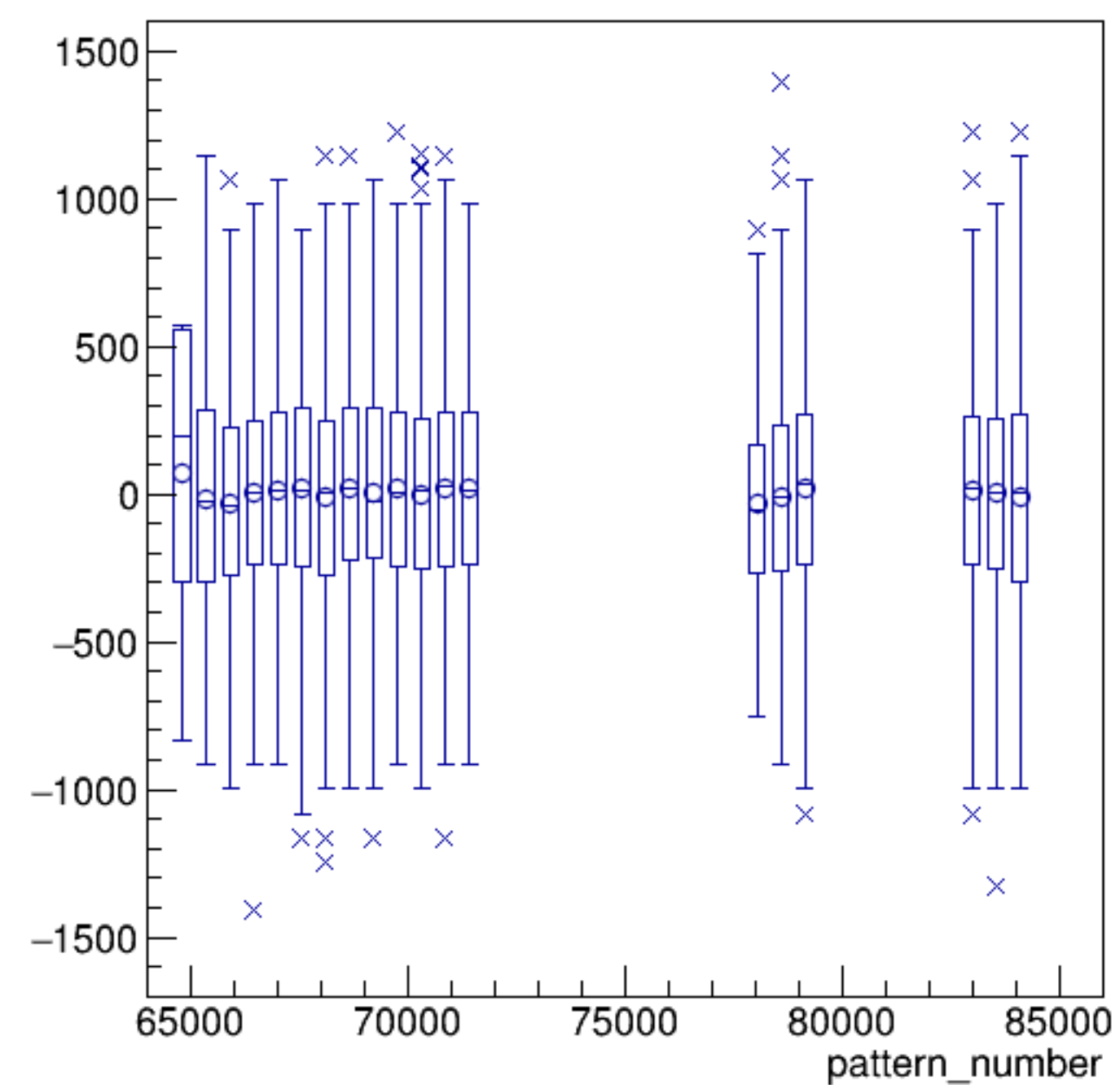
dsr*CurMon:Entry\$



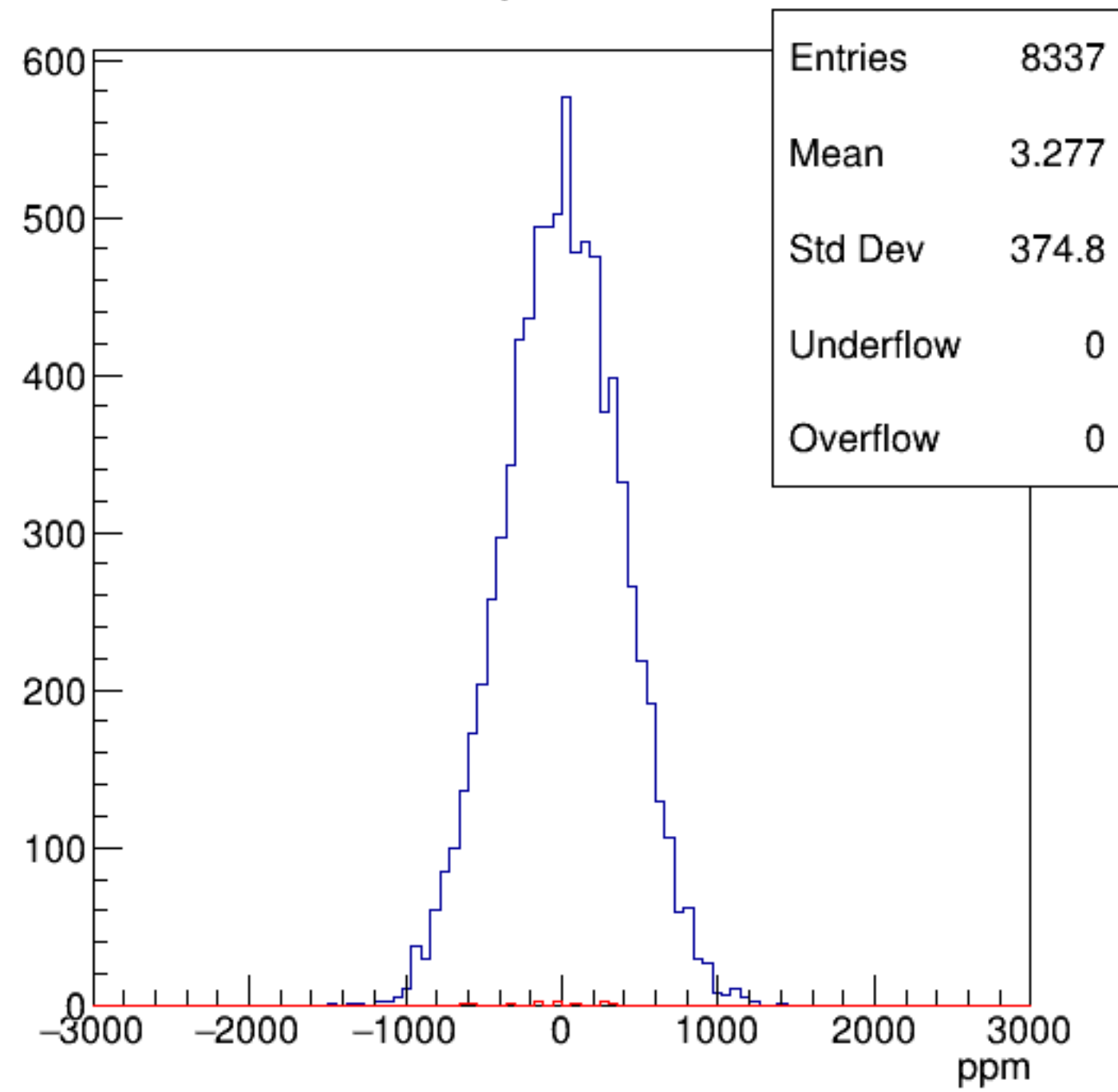
dsr*CurMon {ErrorFlag==0}



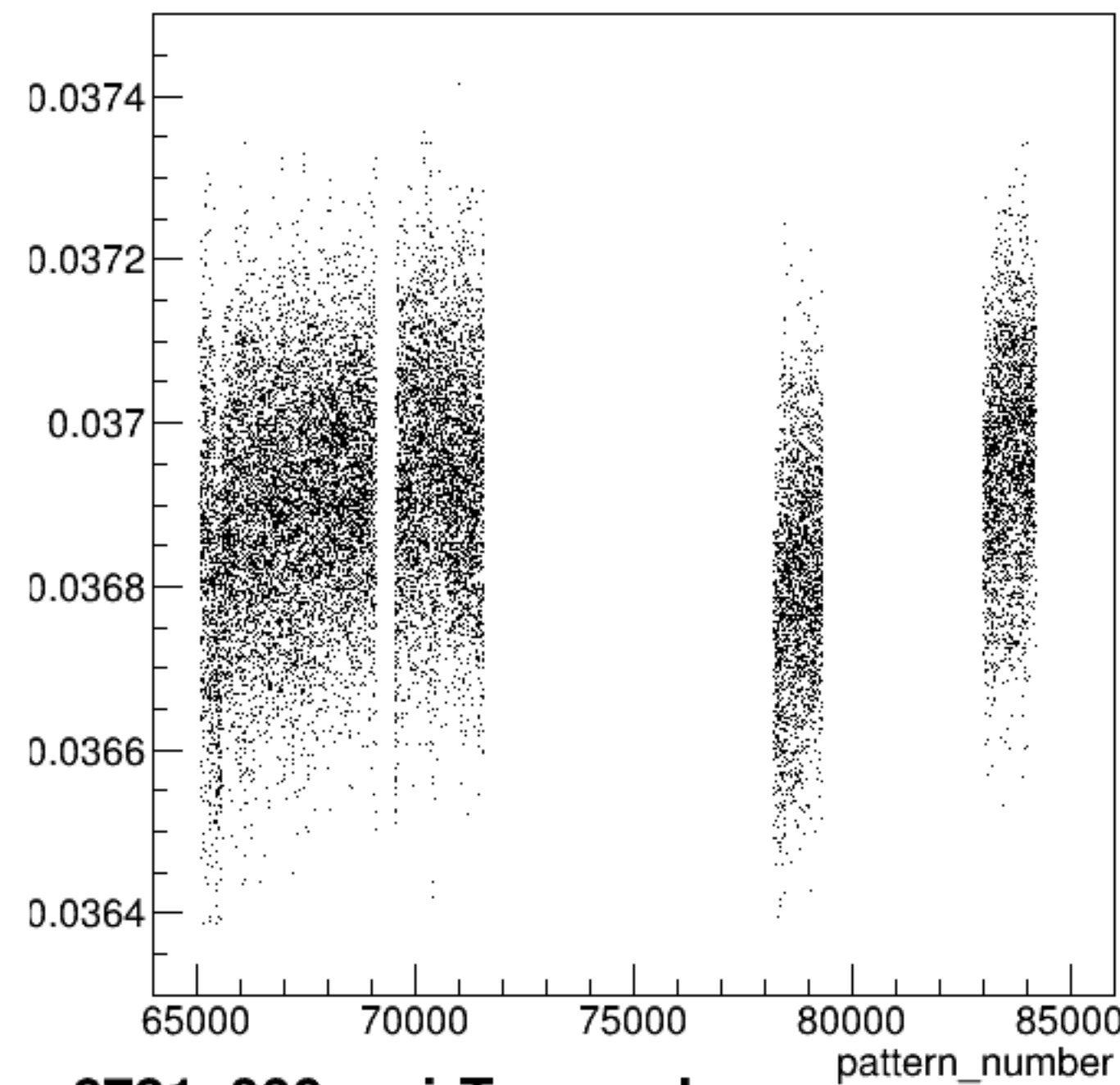
asym_dsr/ppm:pattern_number {ErrorFlag==0}



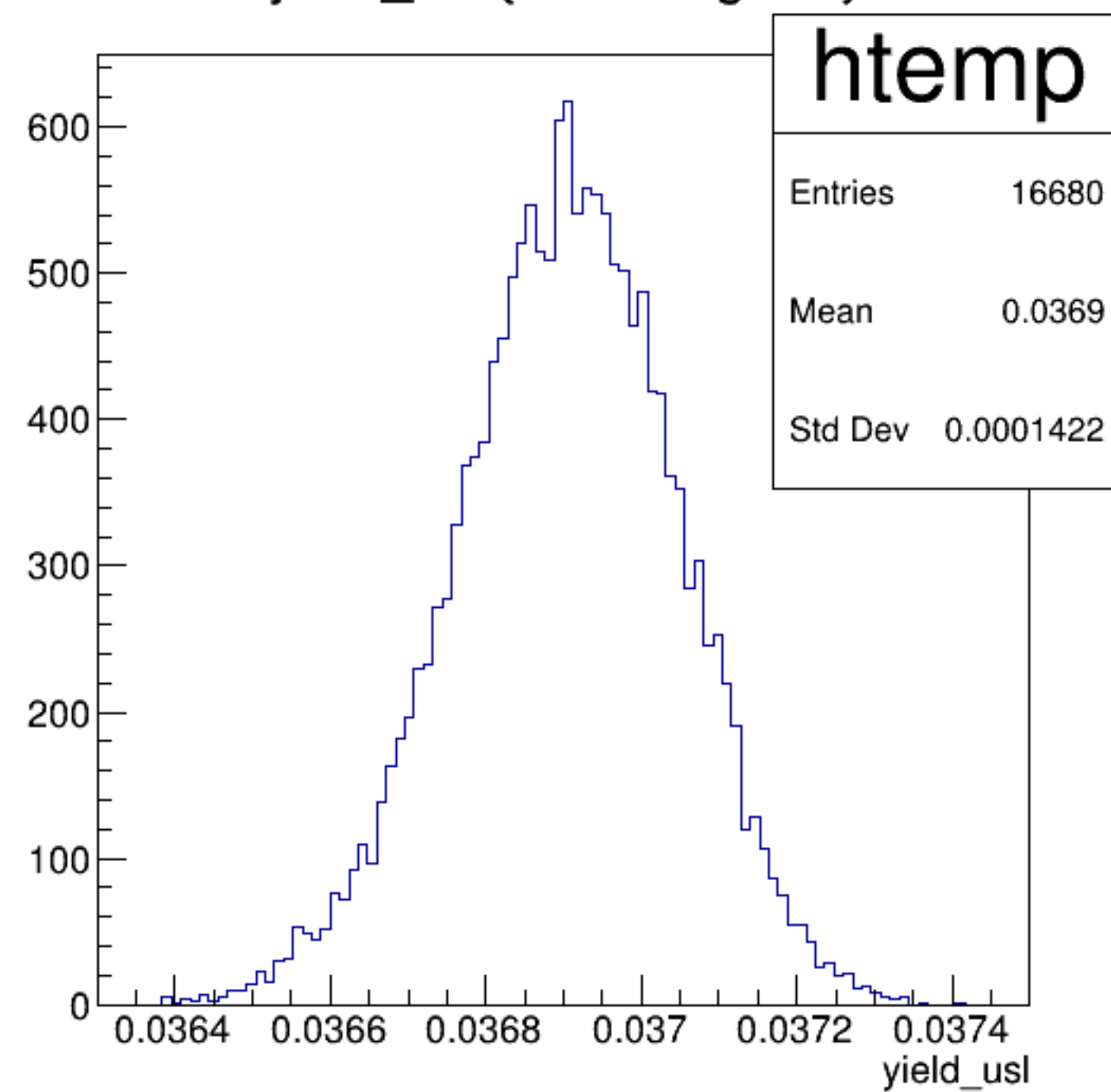
asym_dsr



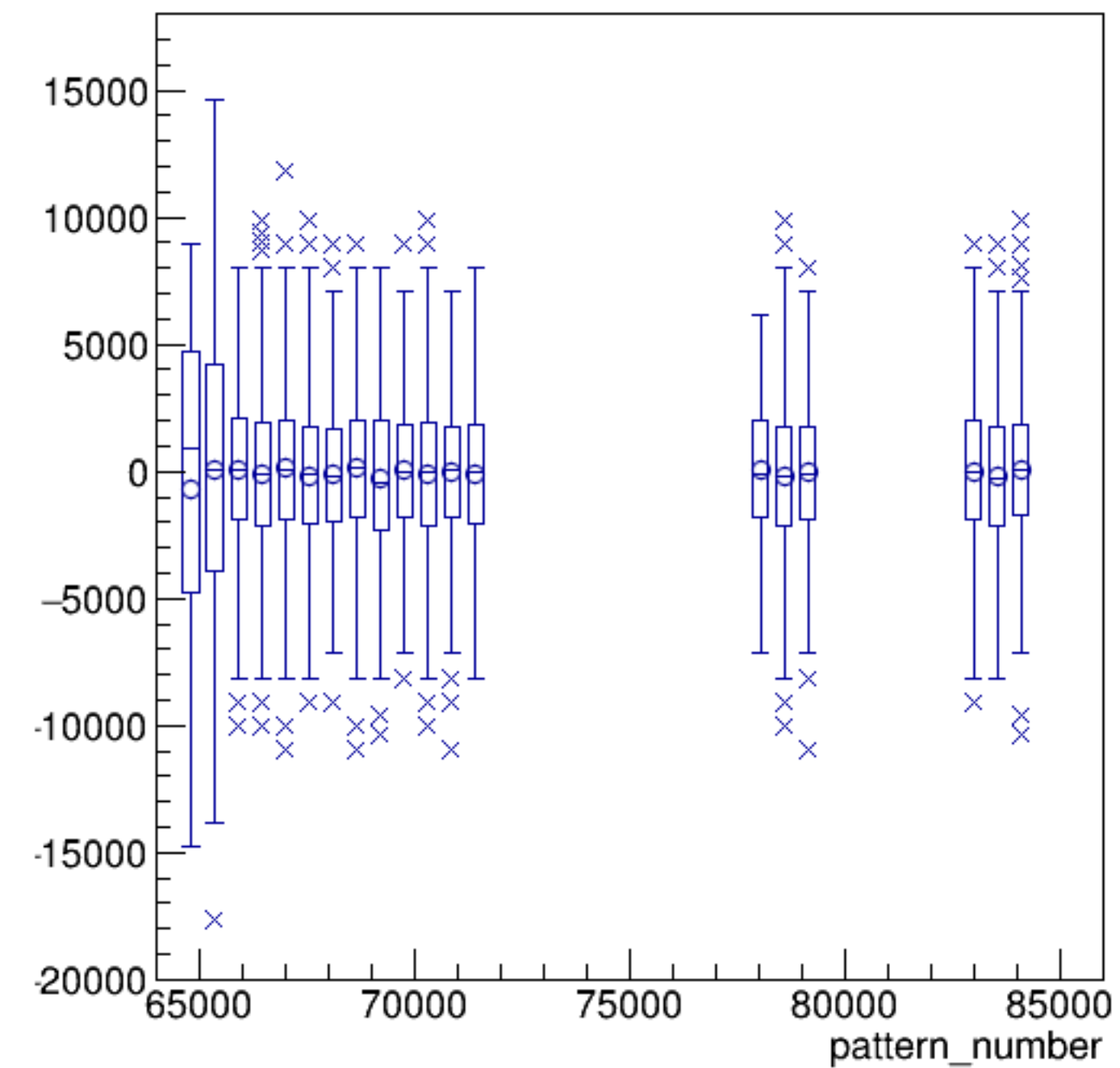
yield_usl:pattern_number {ErrorFlag==0}



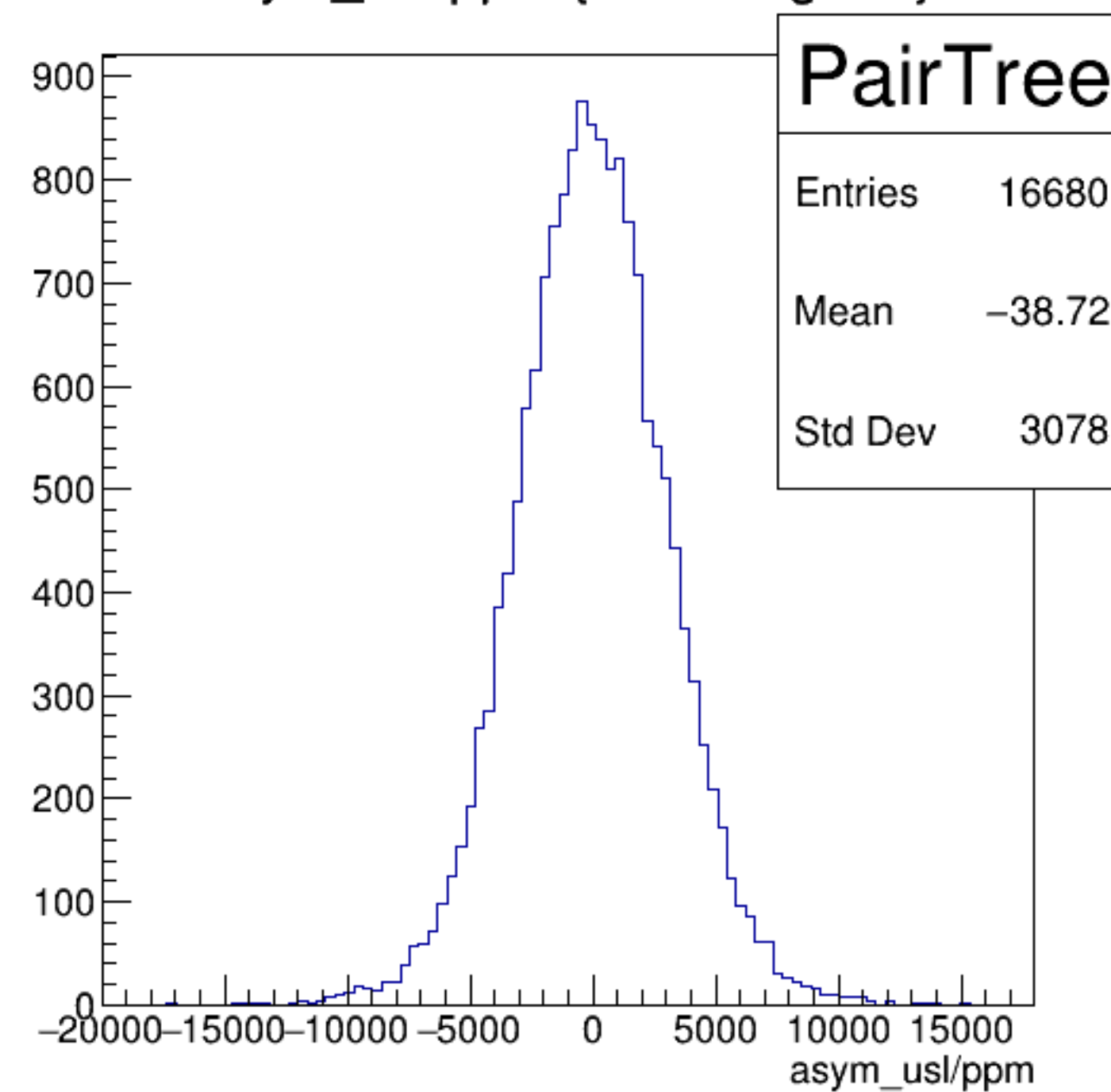
yield_usl {ErrorFlag==0}



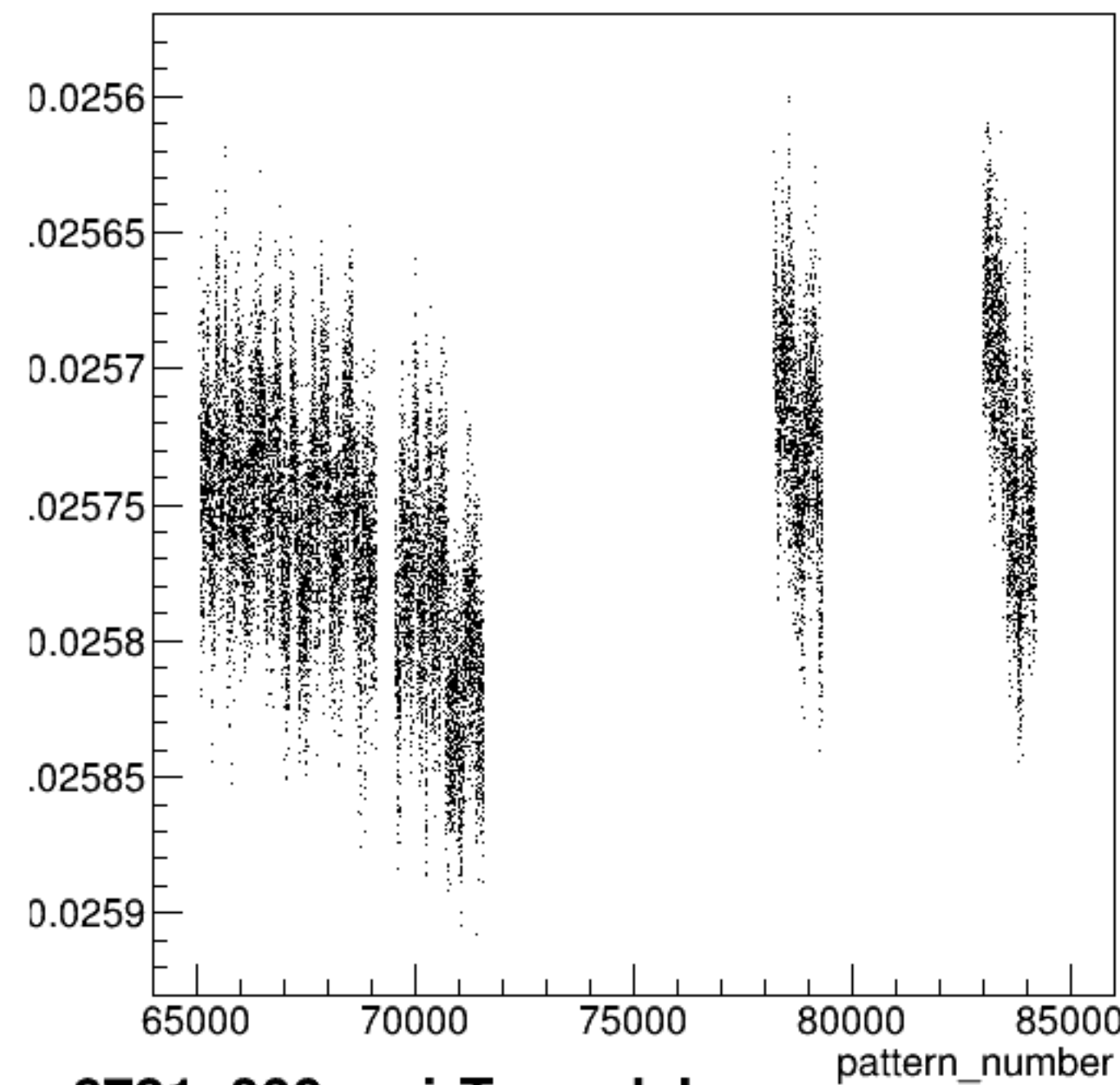
asym_usl/ppm:pattern_number {ErrorFlag==0}



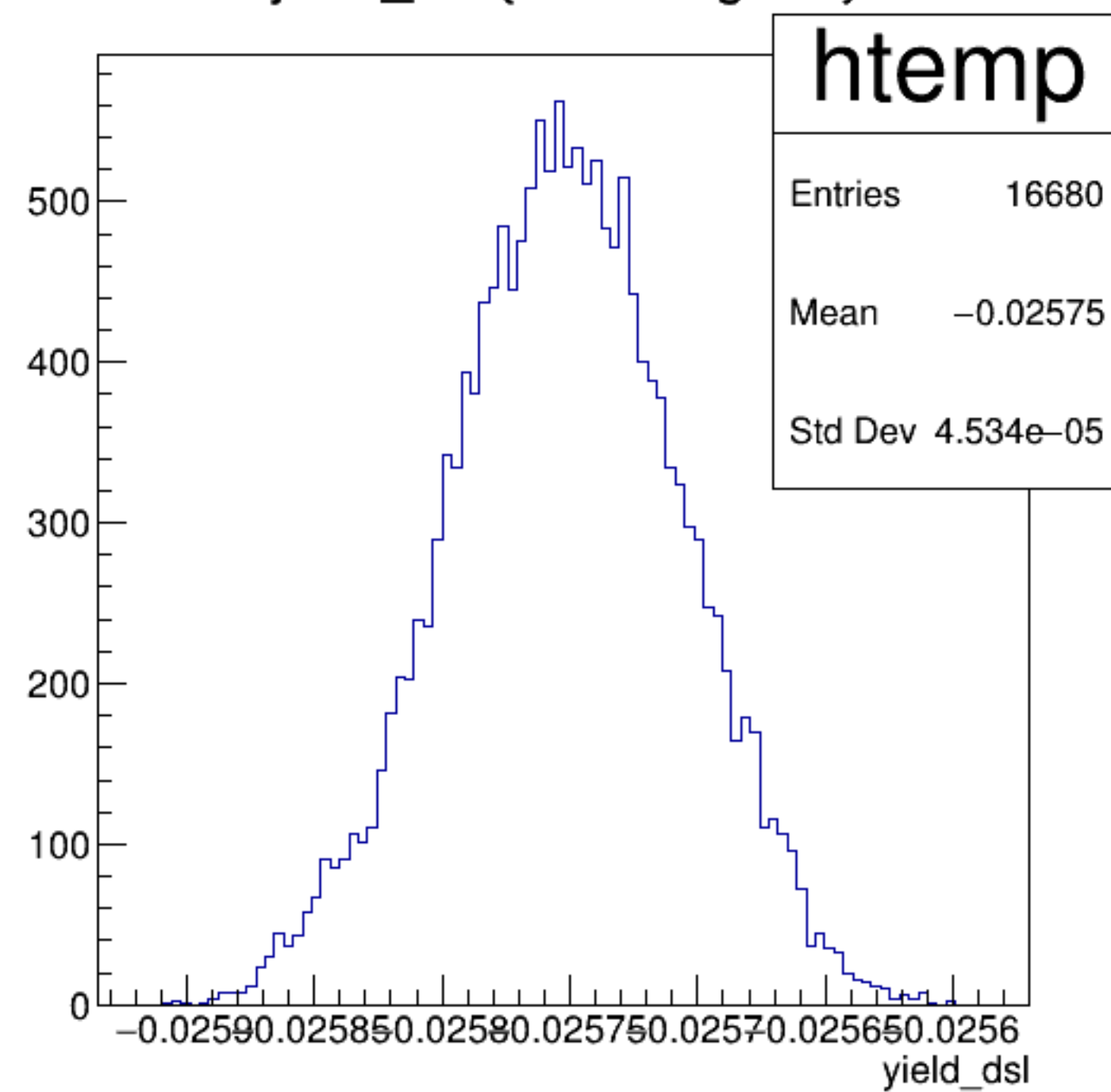
asym_usl/ppm {ErrorFlag==0}



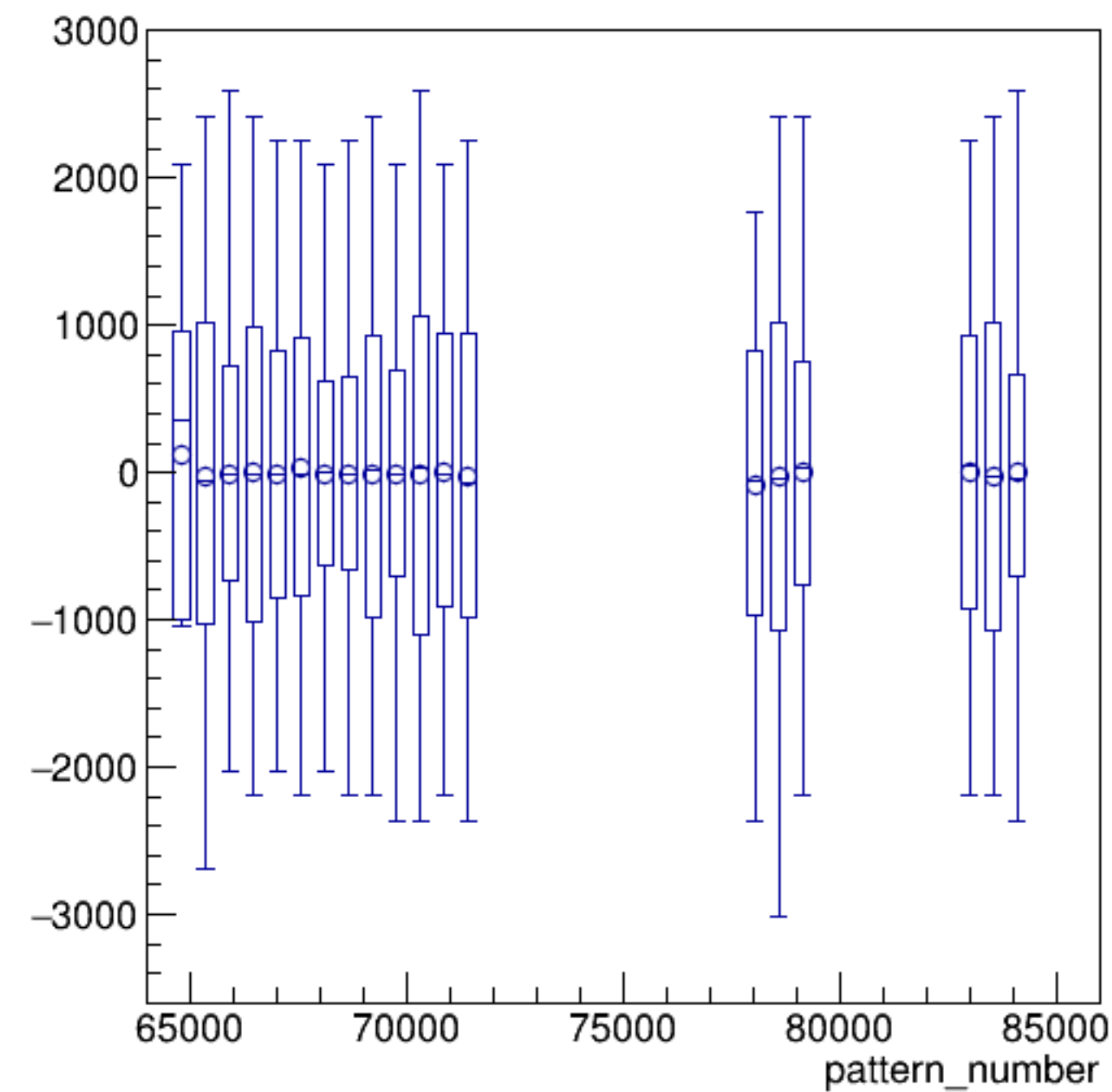
yield_dsl:pattern_number {ErrorFlag==0}



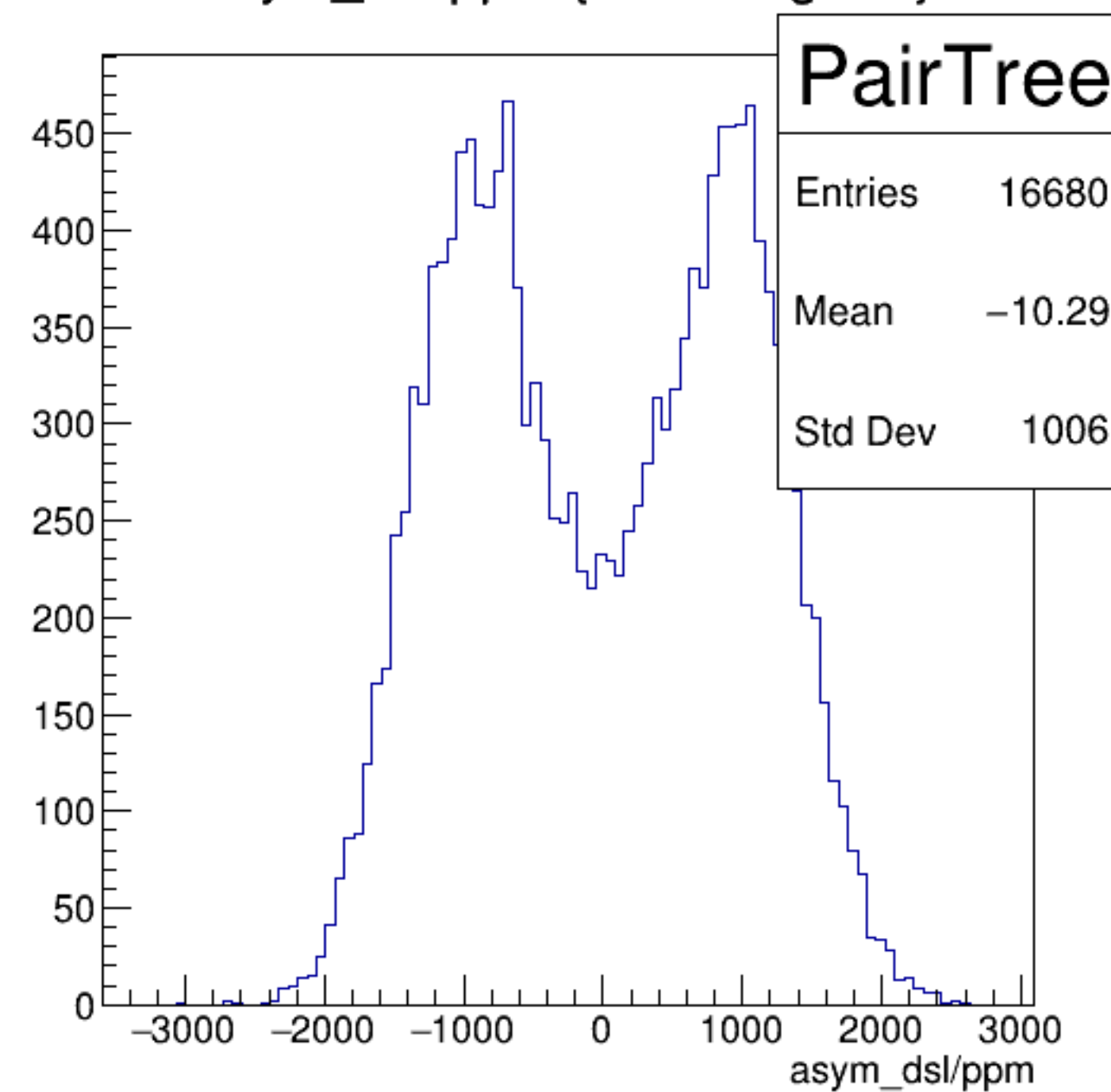
yield_dsl {ErrorFlag==0}



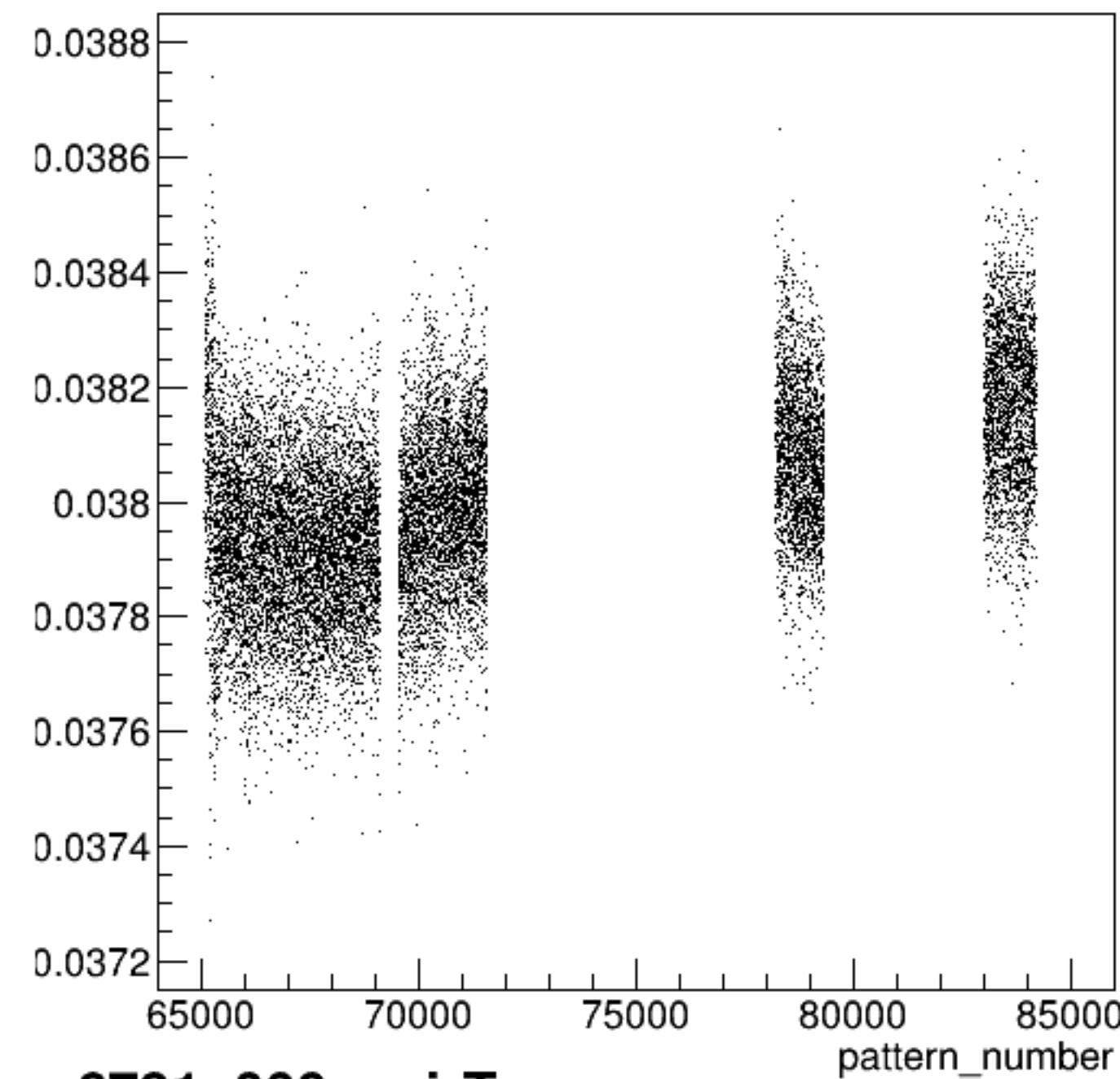
asym_dsl/ppm:pattern_number {ErrorFlag==0}



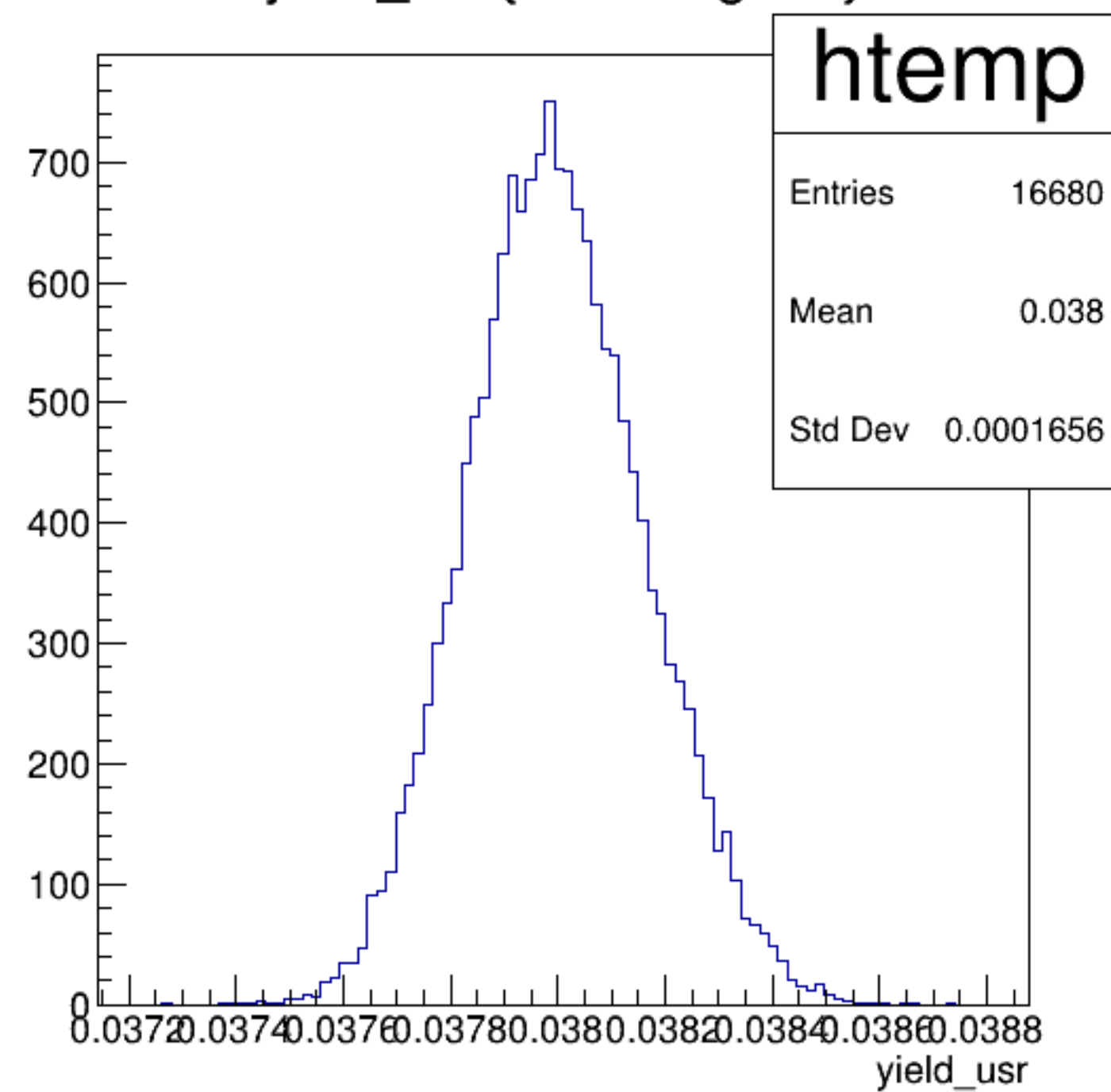
asym_dsl/ppm {ErrorFlag==0}



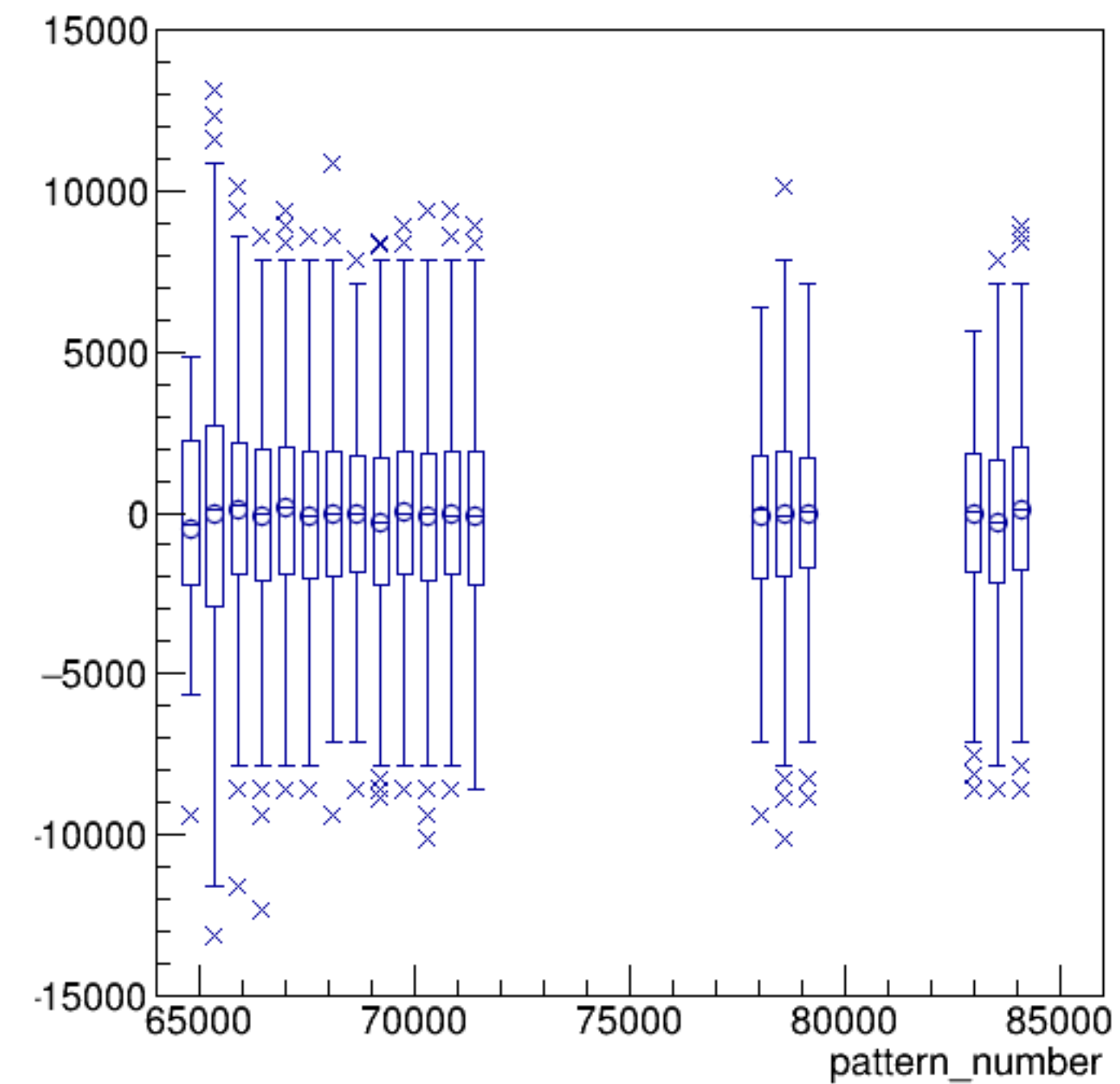
yield_usr:pattern_number {ErrorFlag==0}



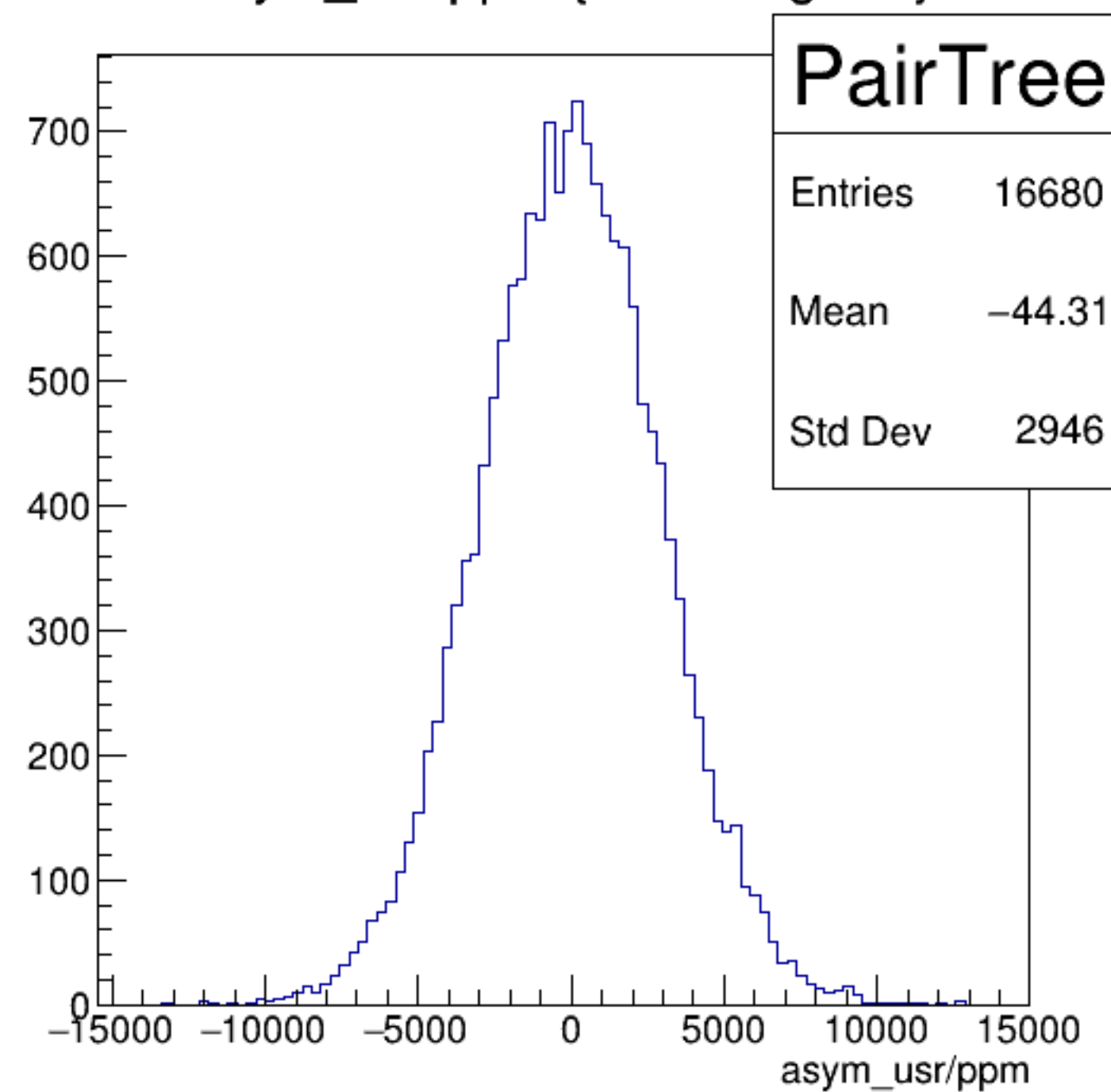
yield_usr {ErrorFlag==0}



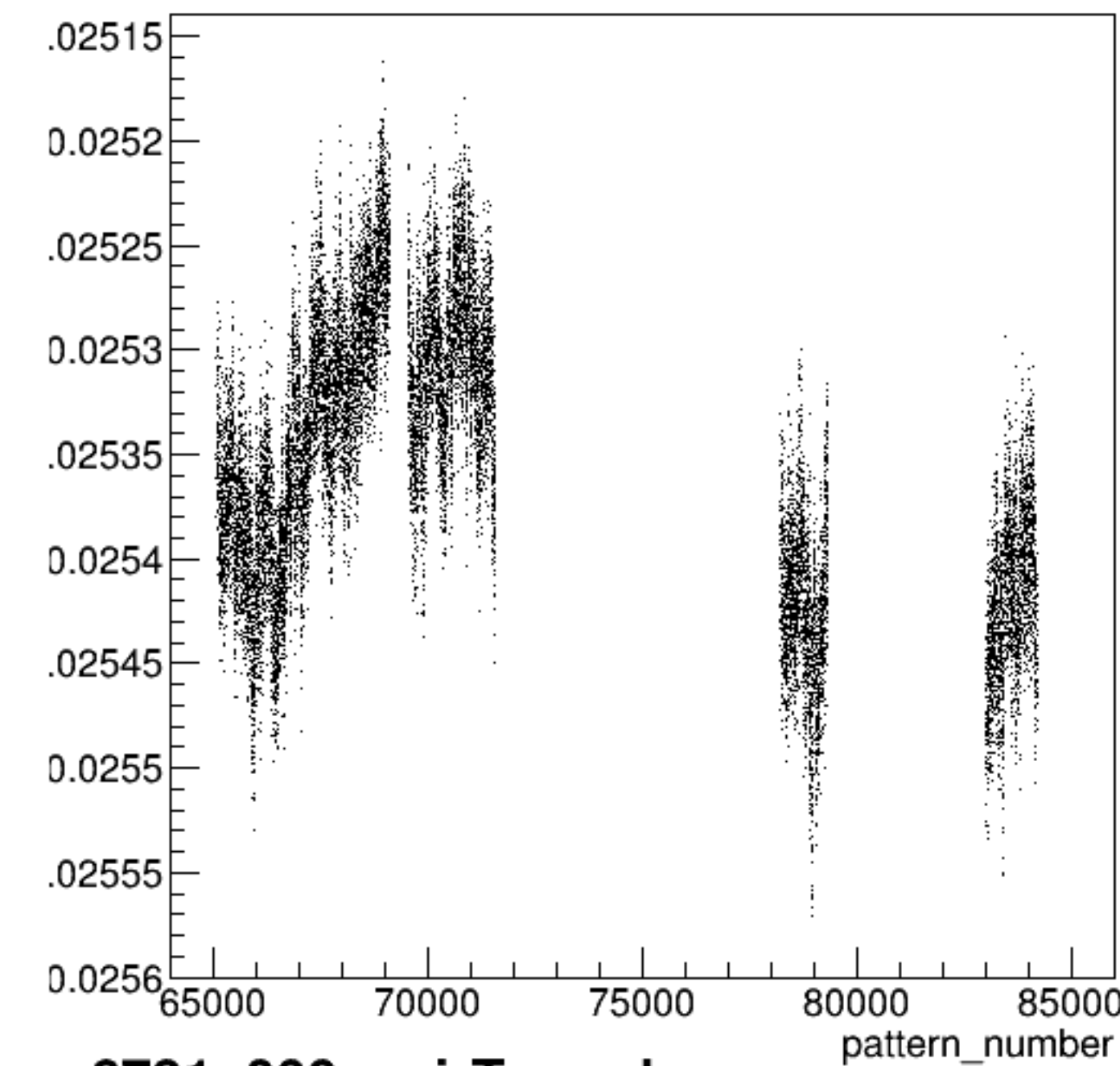
asym_usr/ppm:pattern_number {ErrorFlag==0}



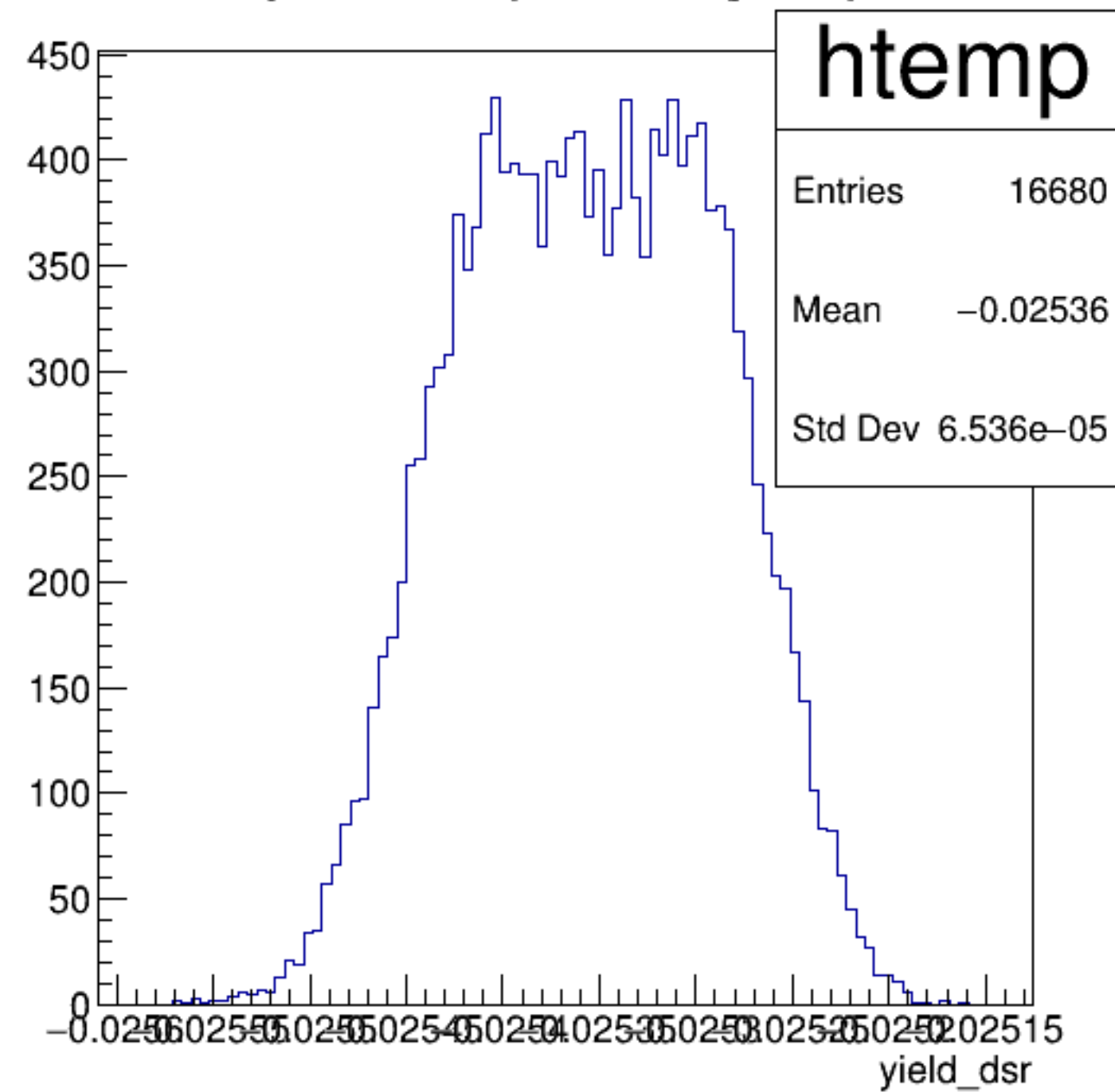
asym_usr/ppm {ErrorFlag==0}



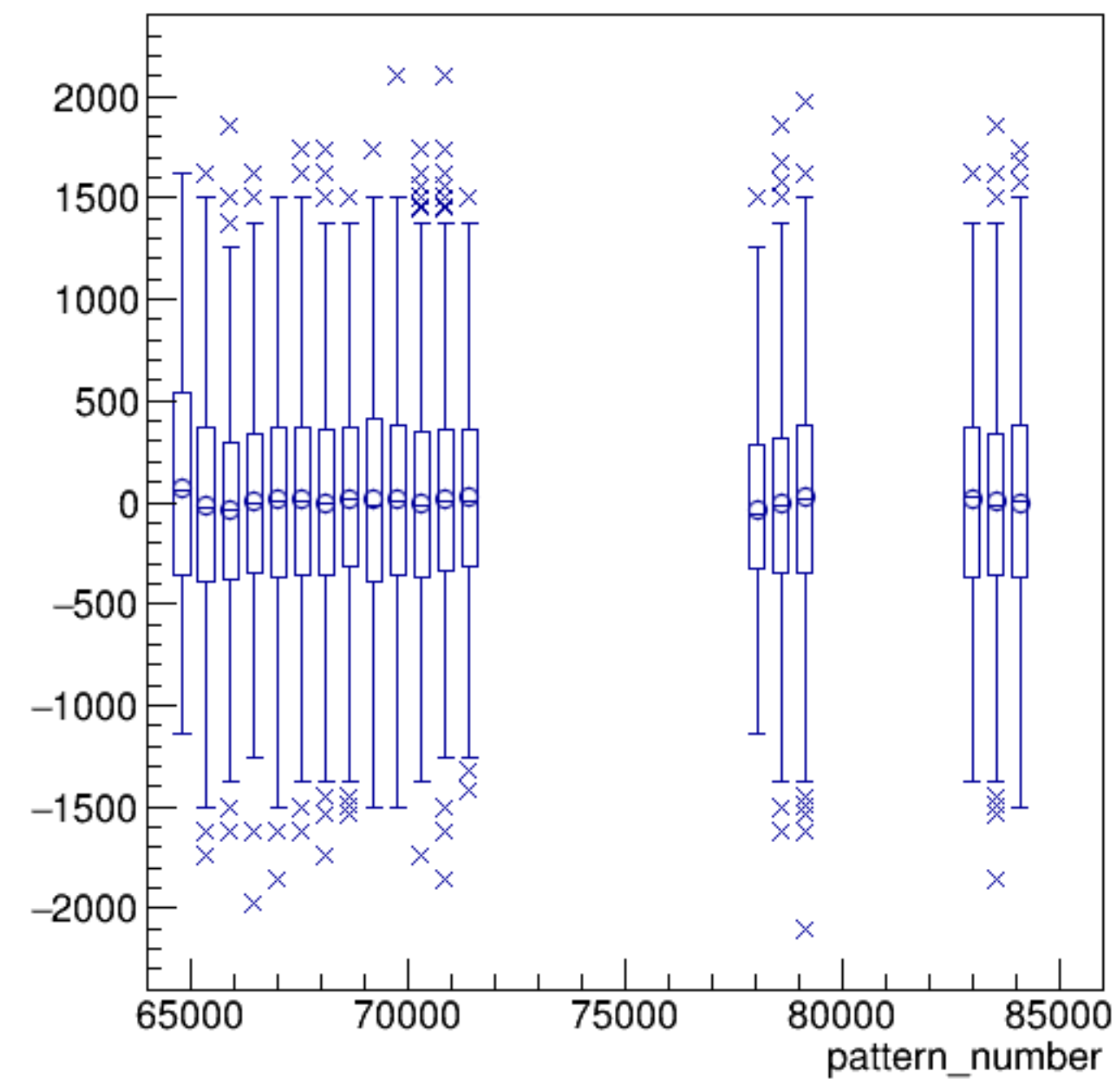
yield_dsr:pattern_number {ErrorFlag==0}



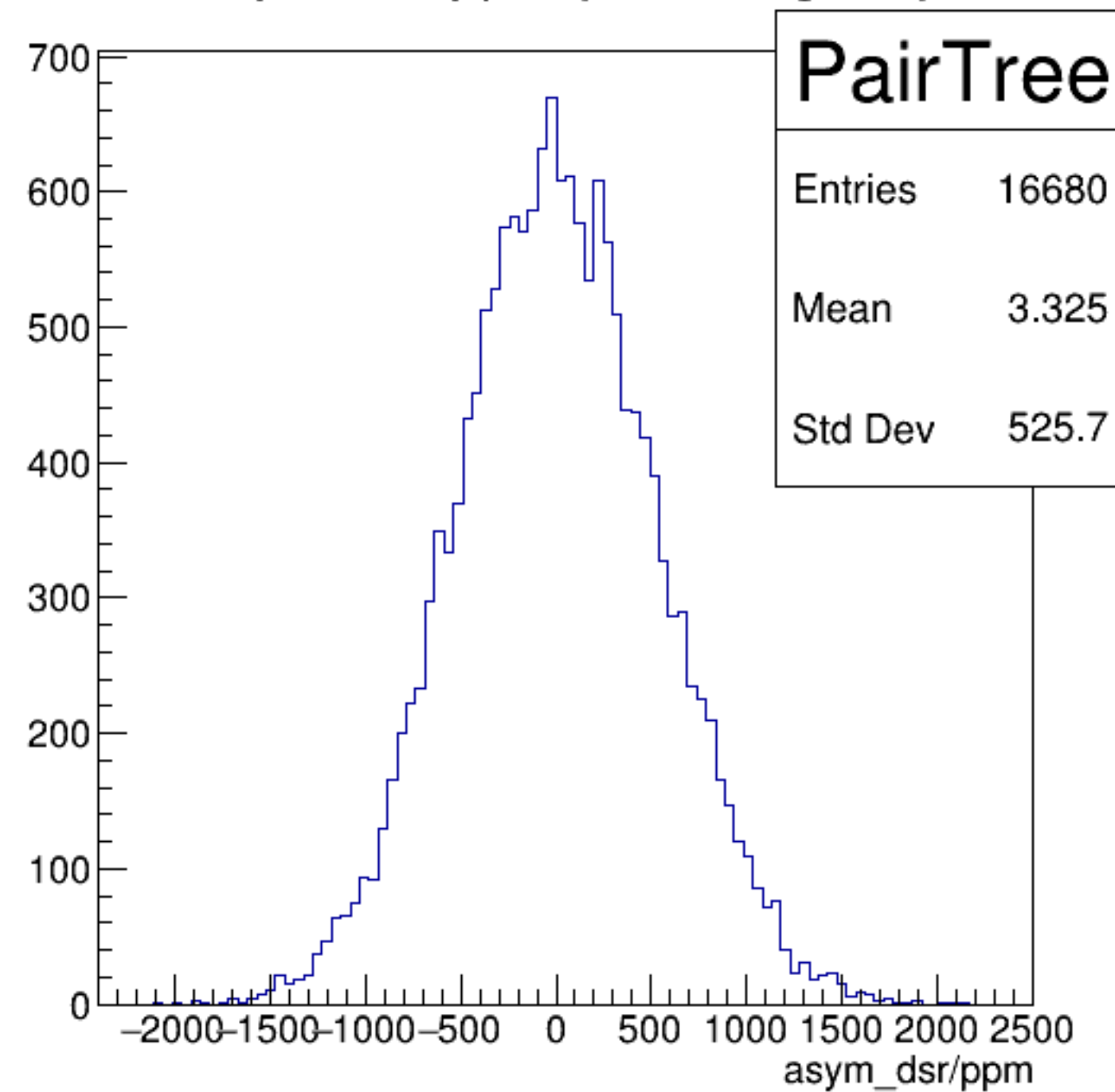
yield_dsr {ErrorFlag==0}



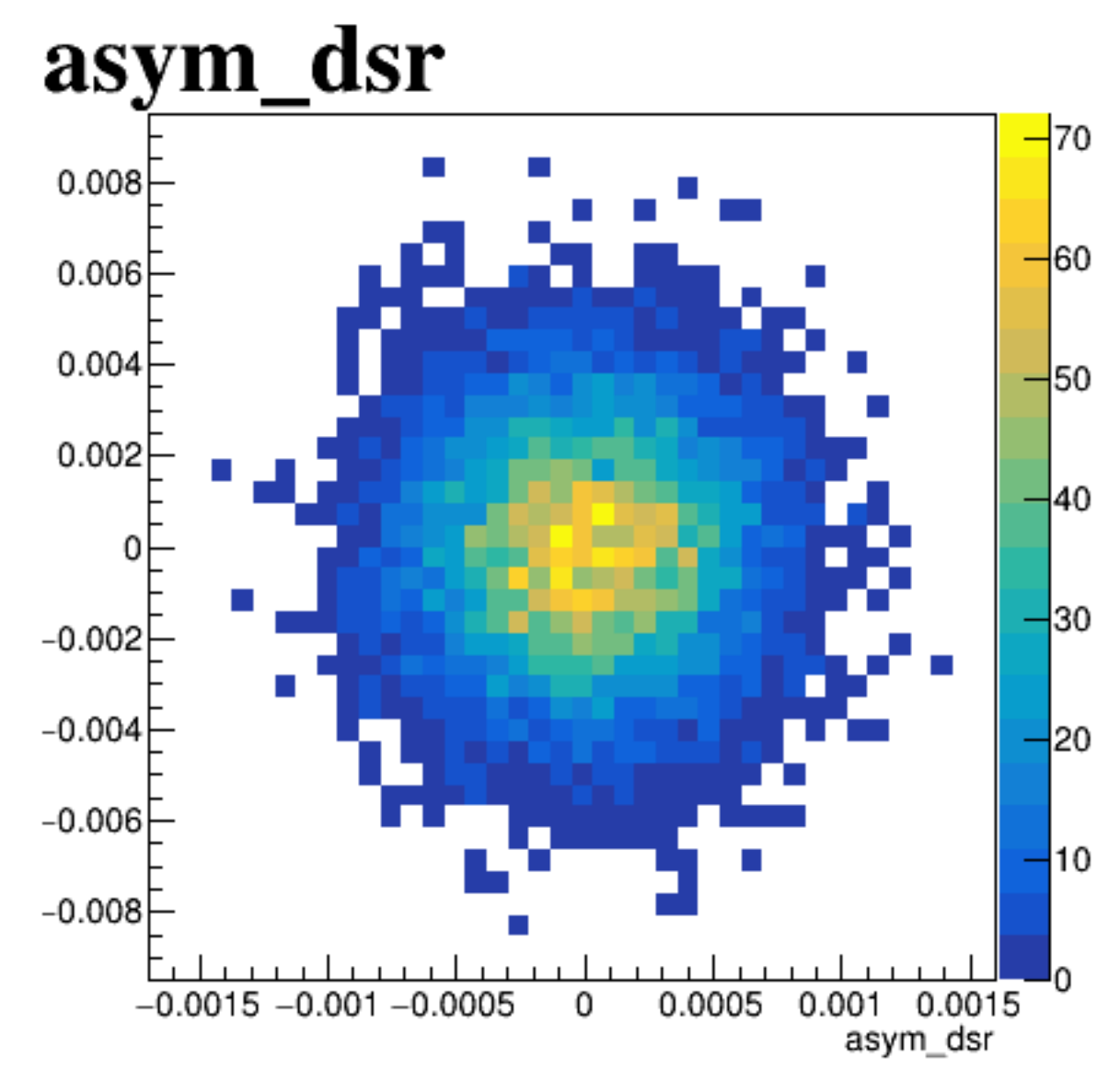
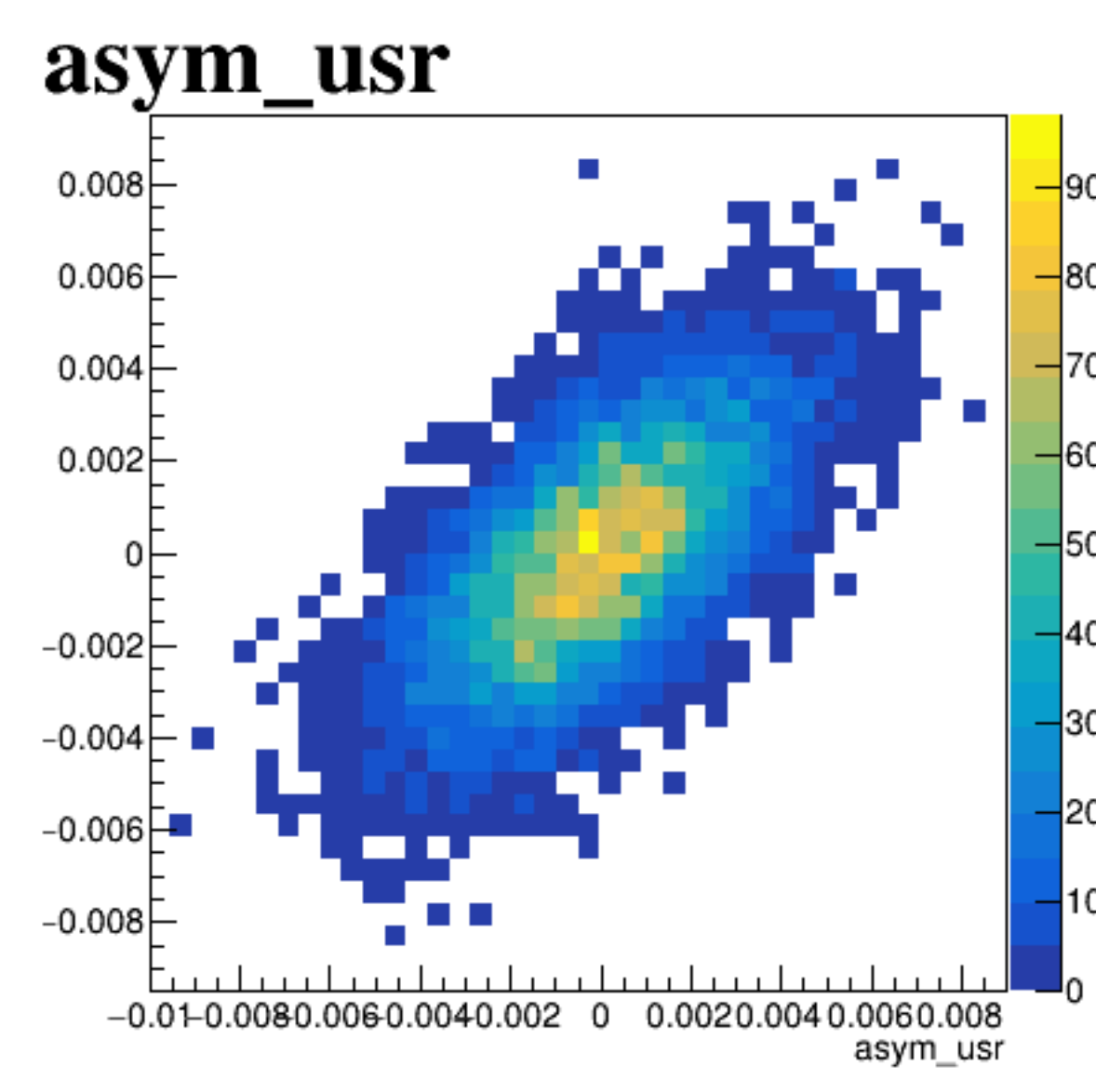
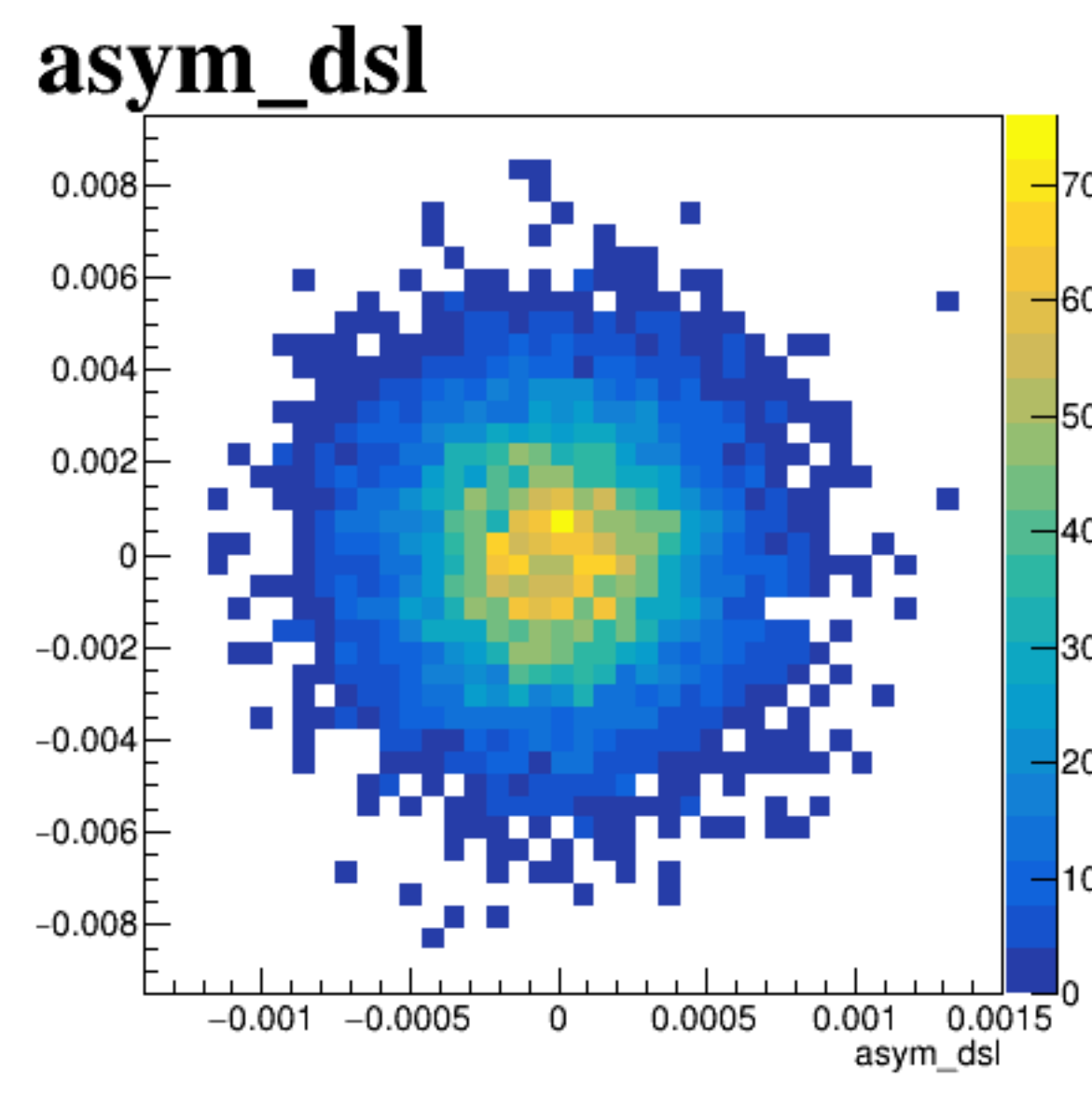
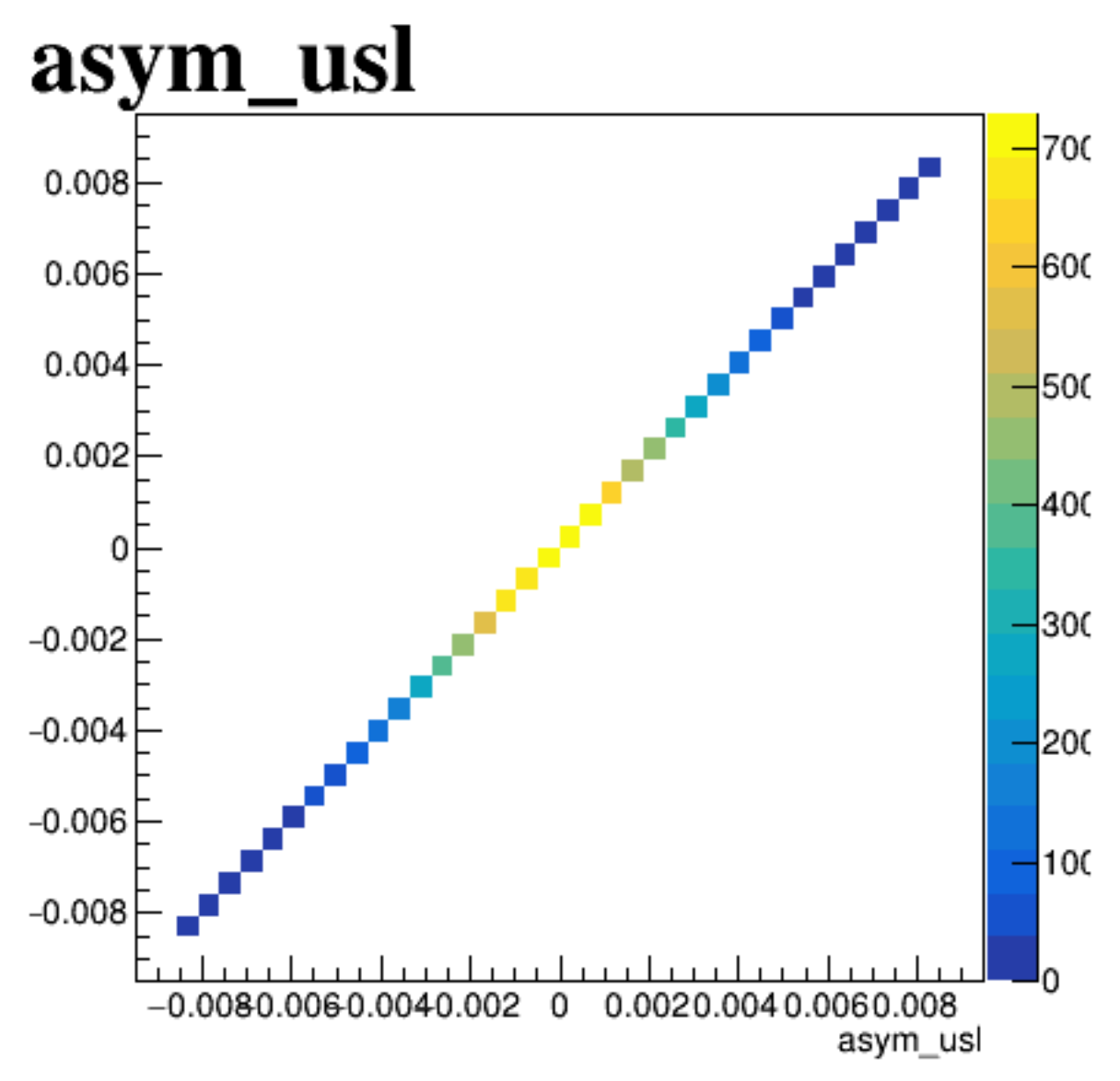
asym_dsr/ppm:pattern_number {ErrorFlag==0}



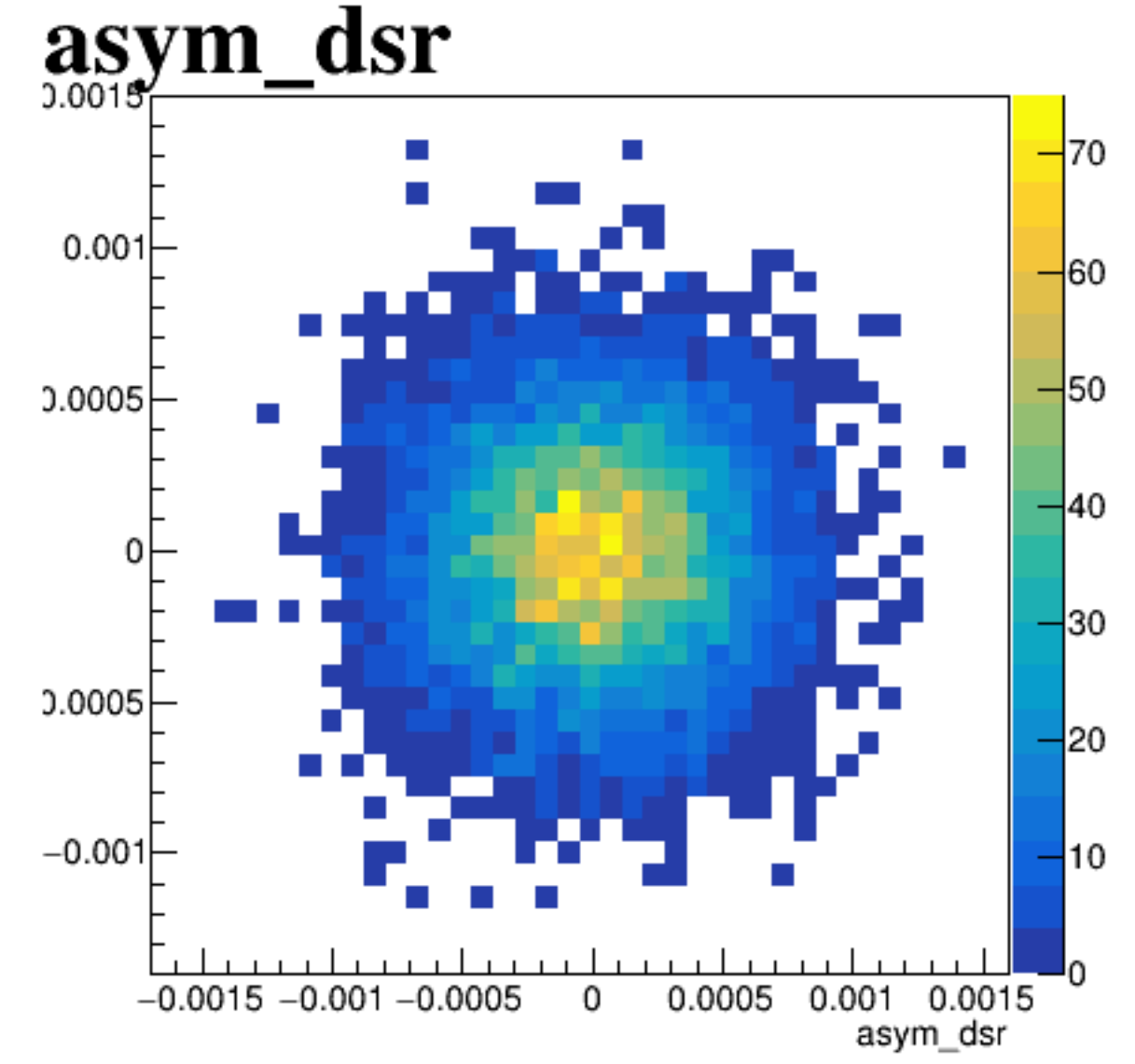
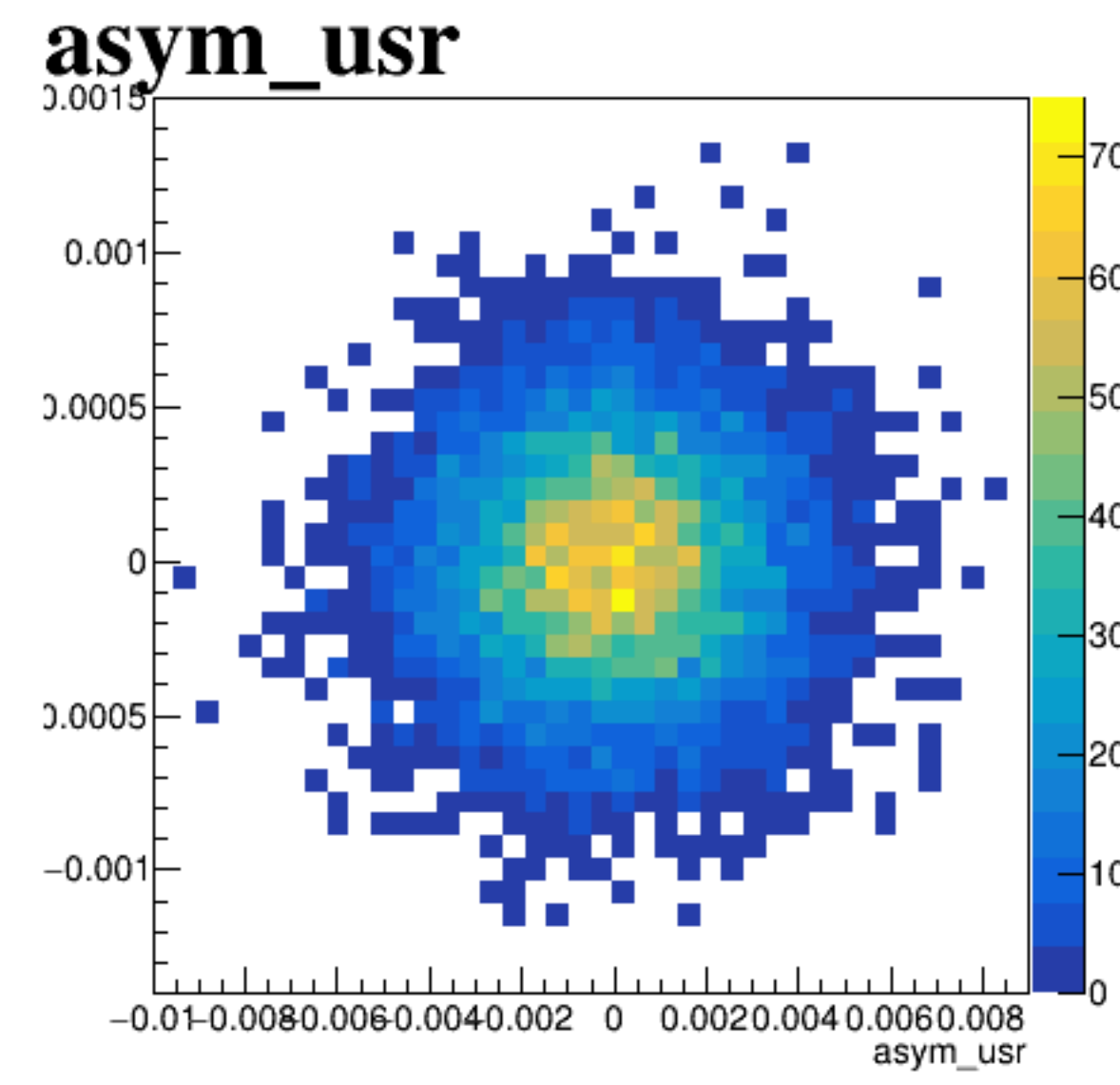
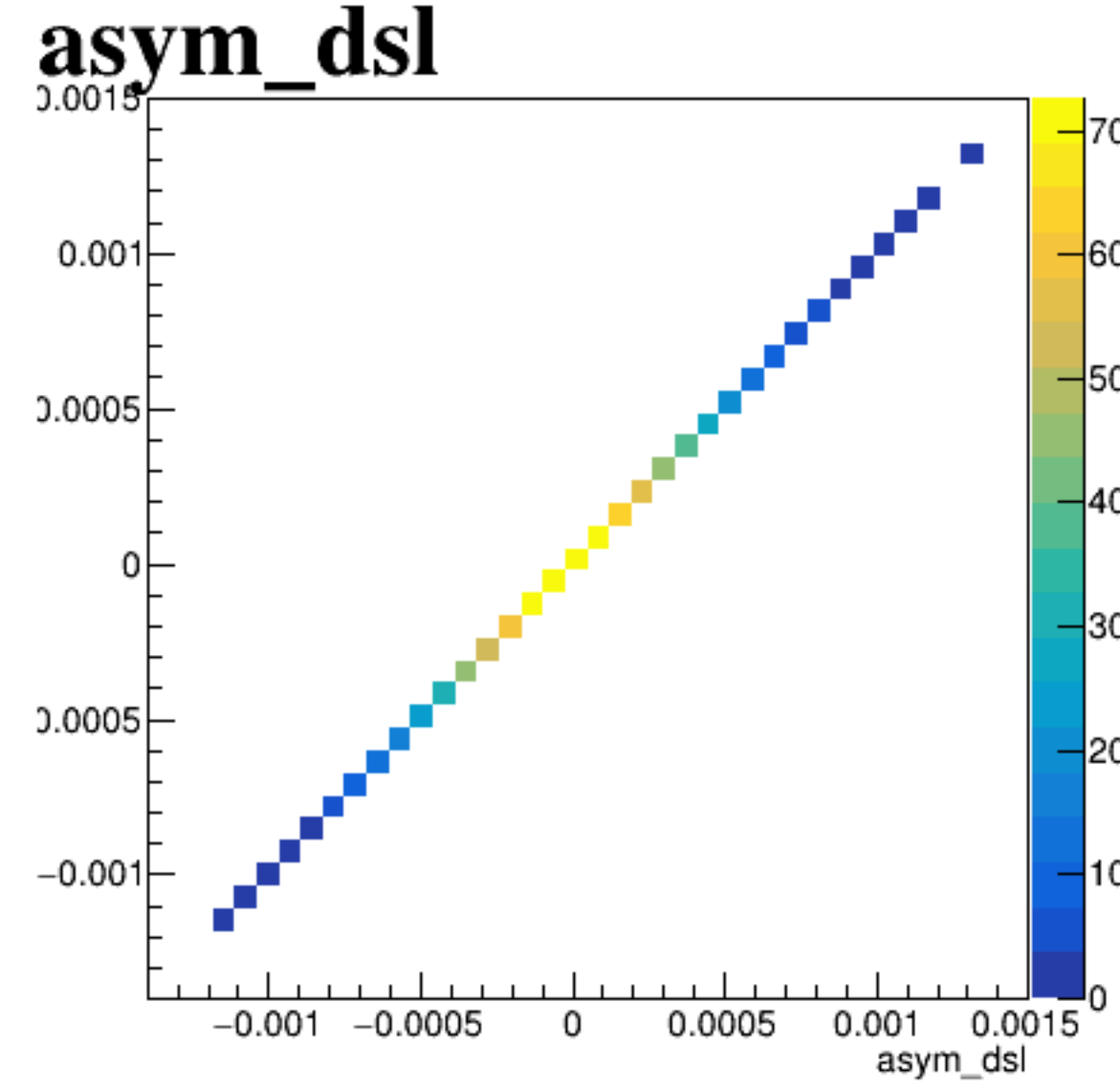
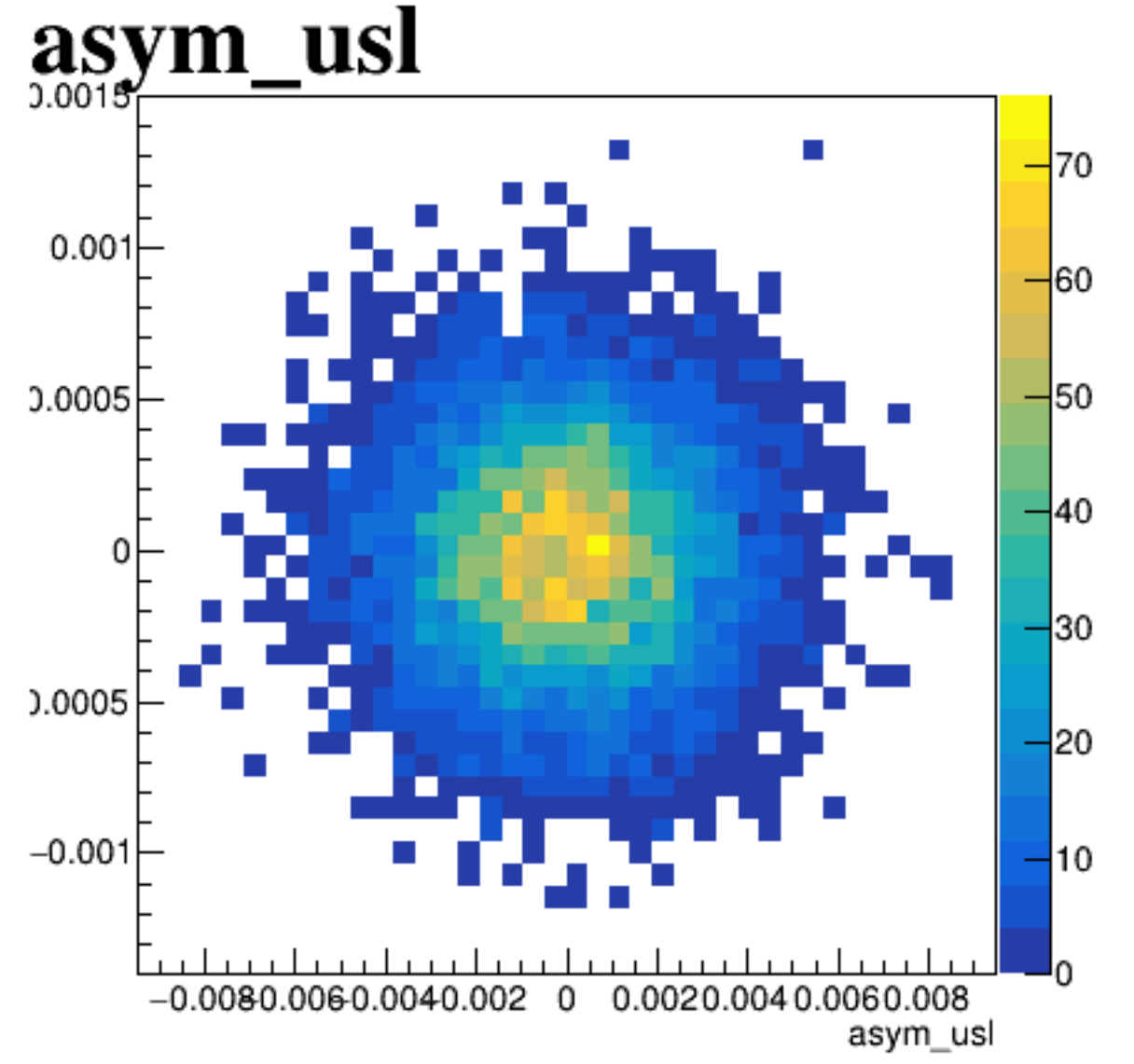
asym_dsr/ppm {ErrorFlag==0}



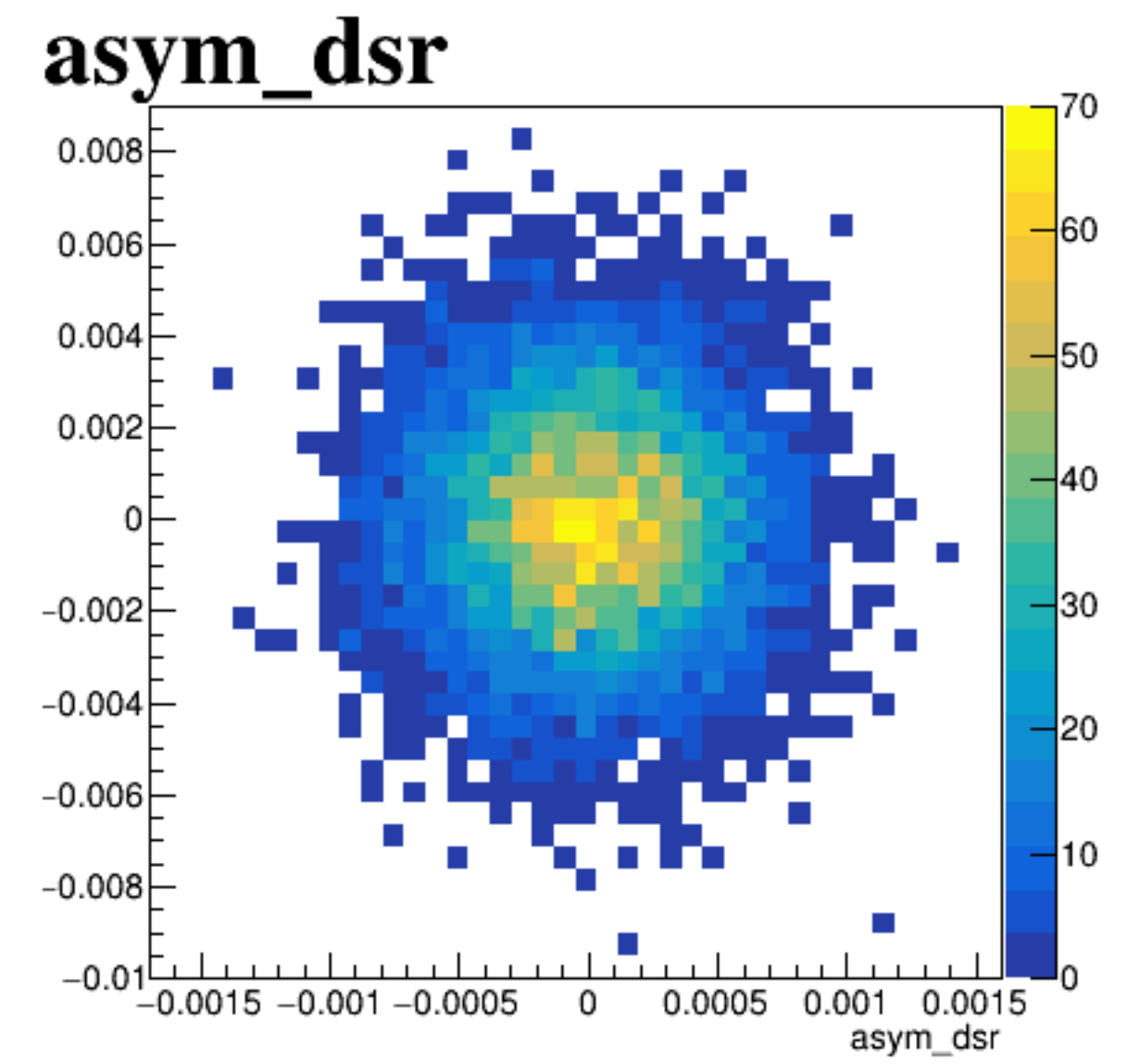
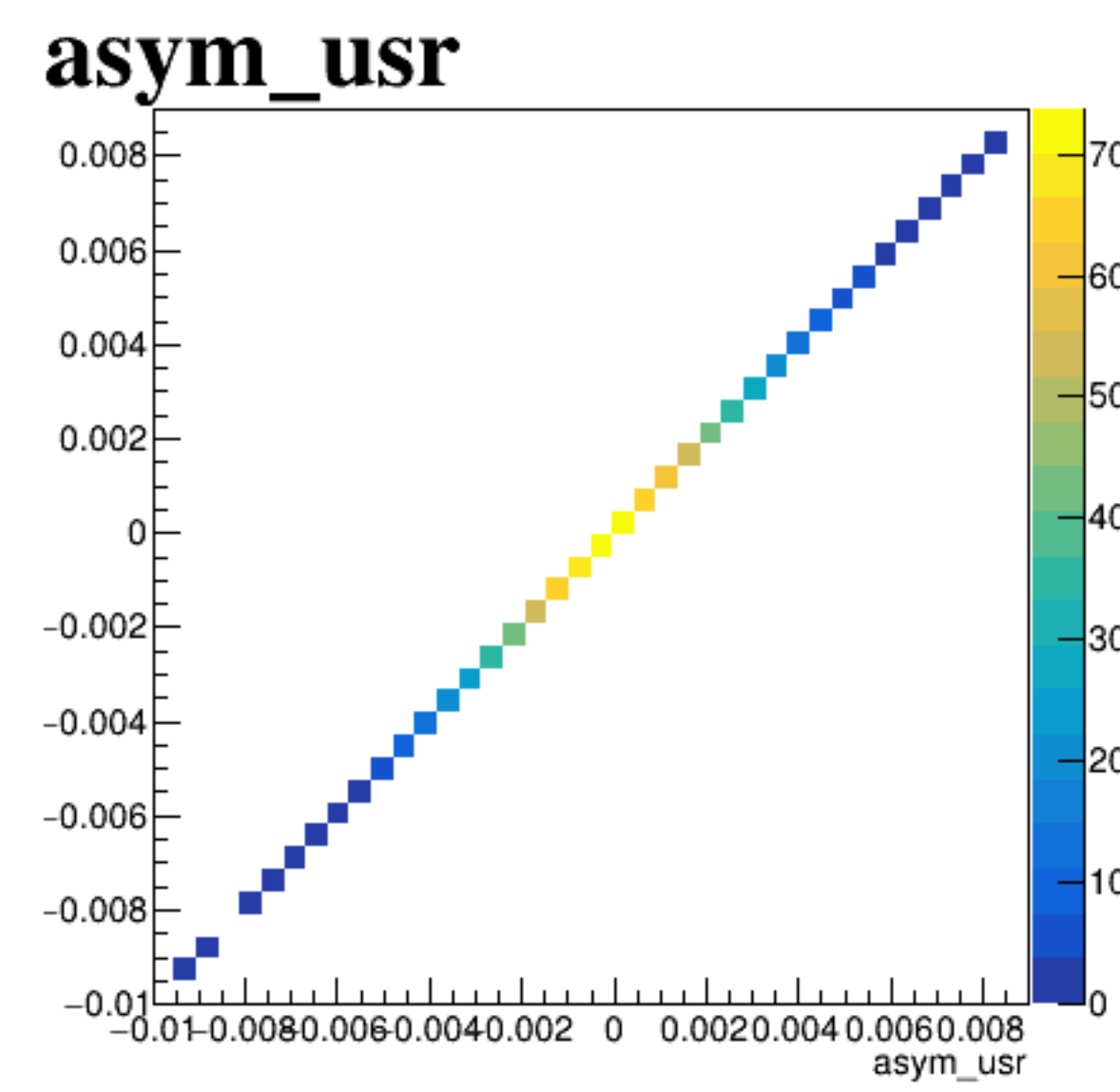
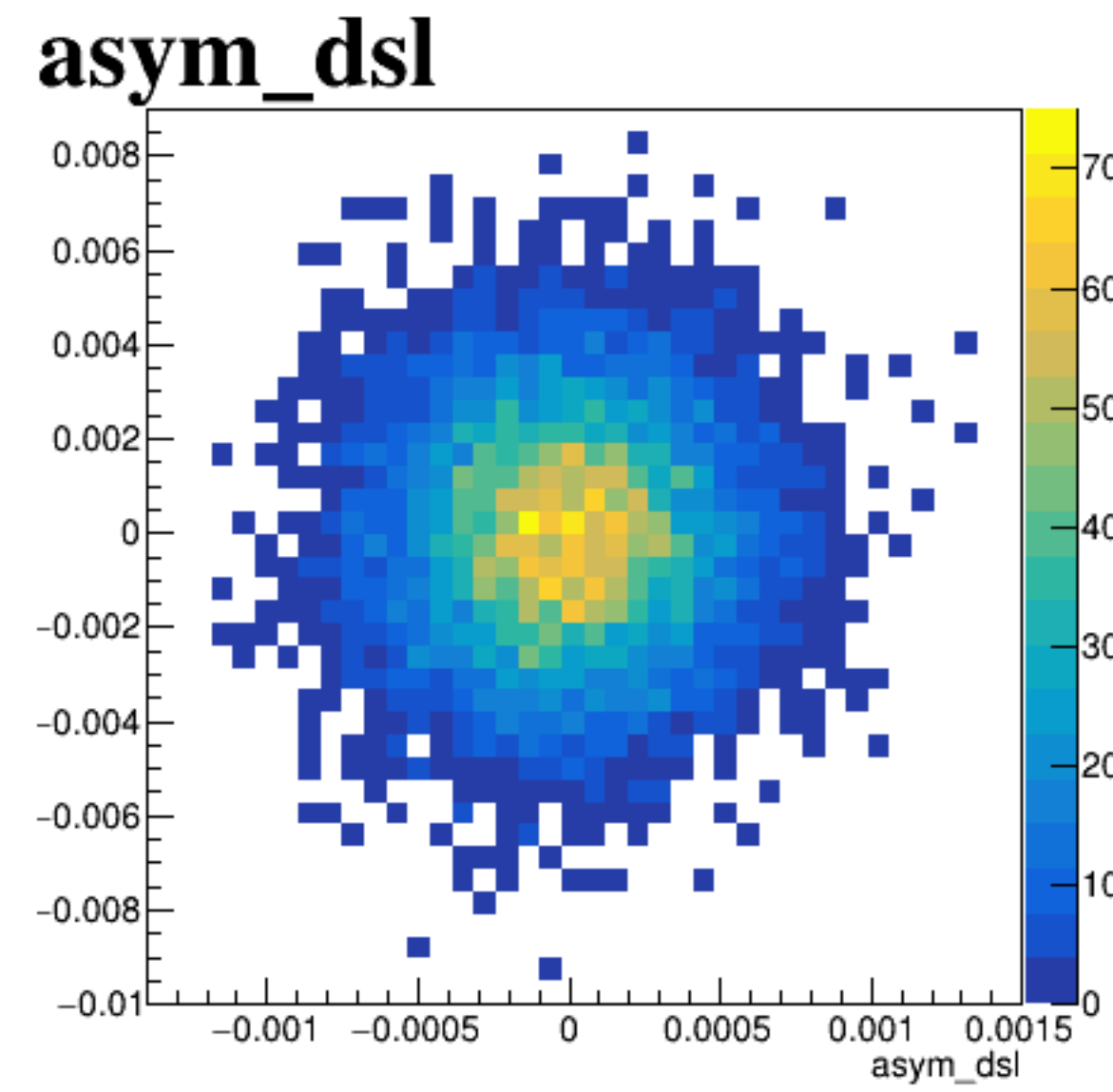
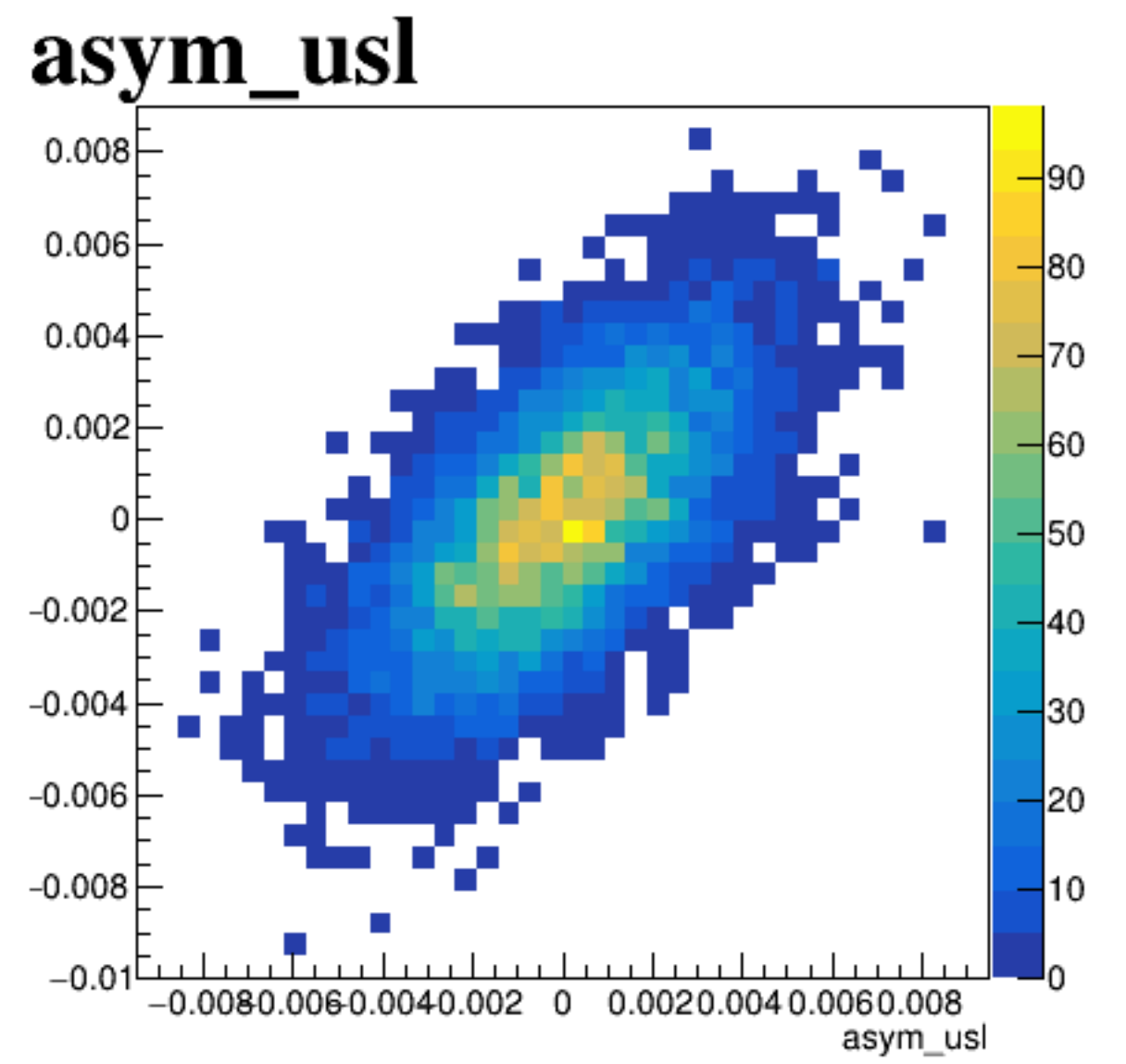
asym_usl



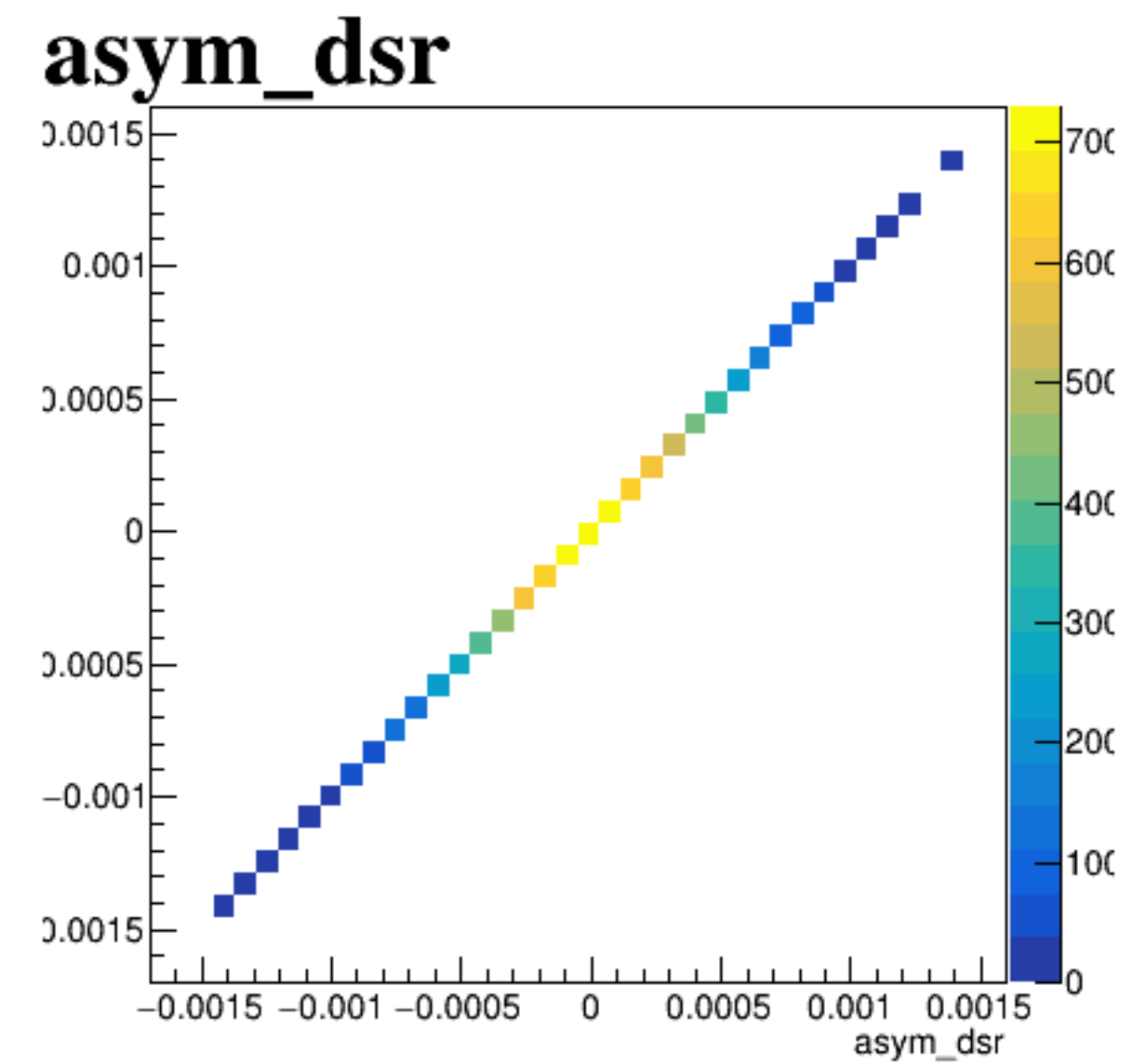
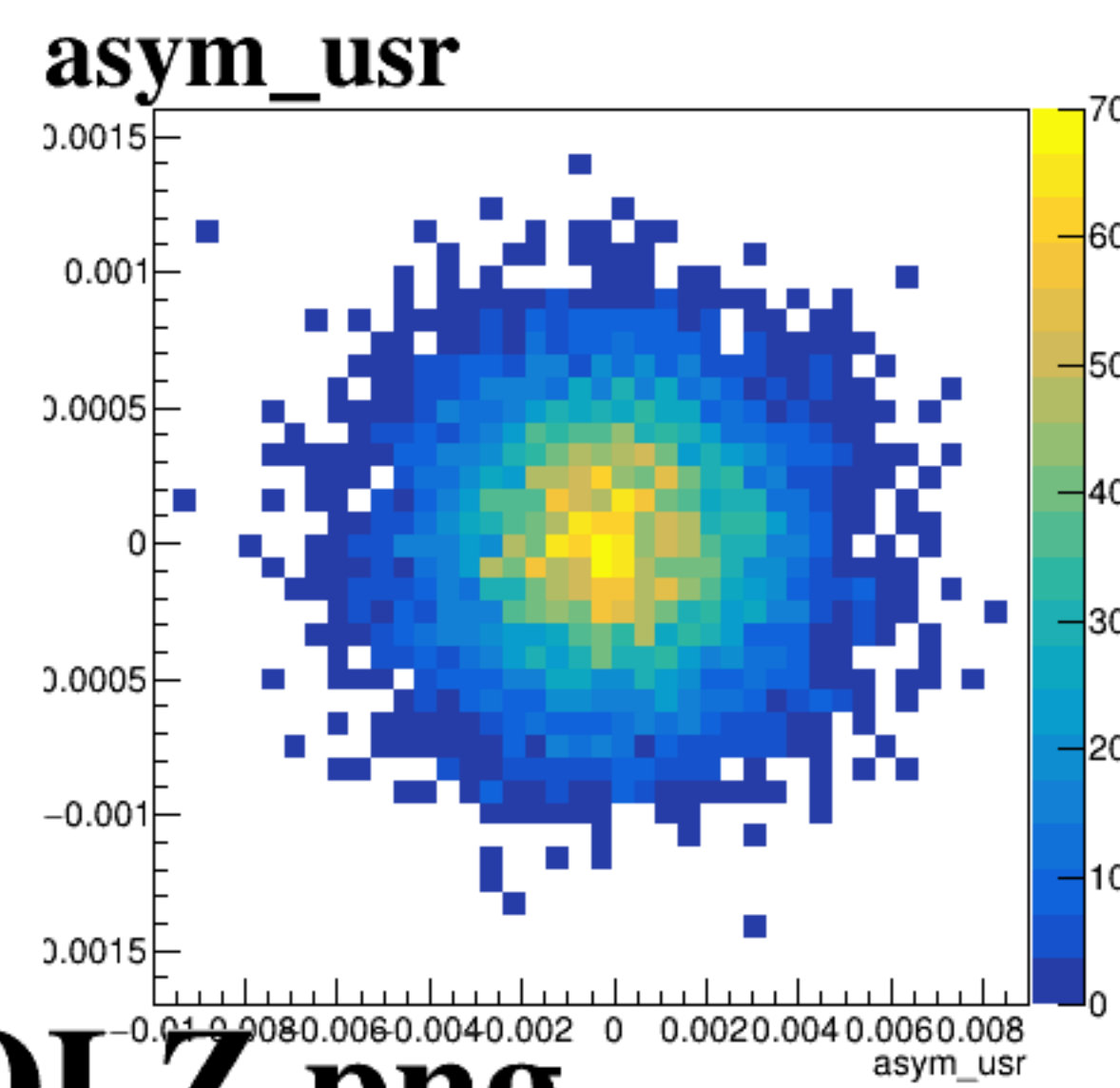
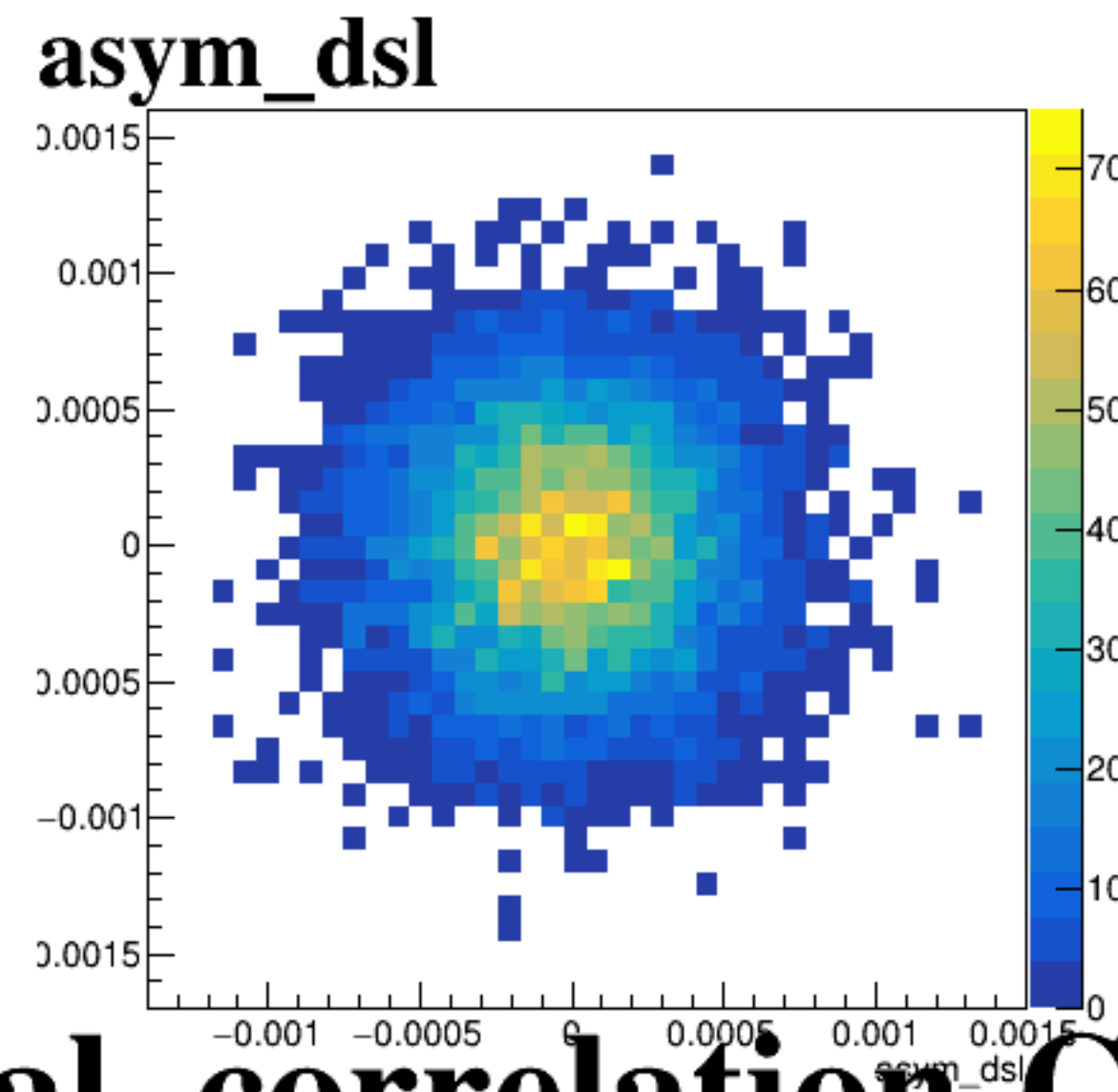
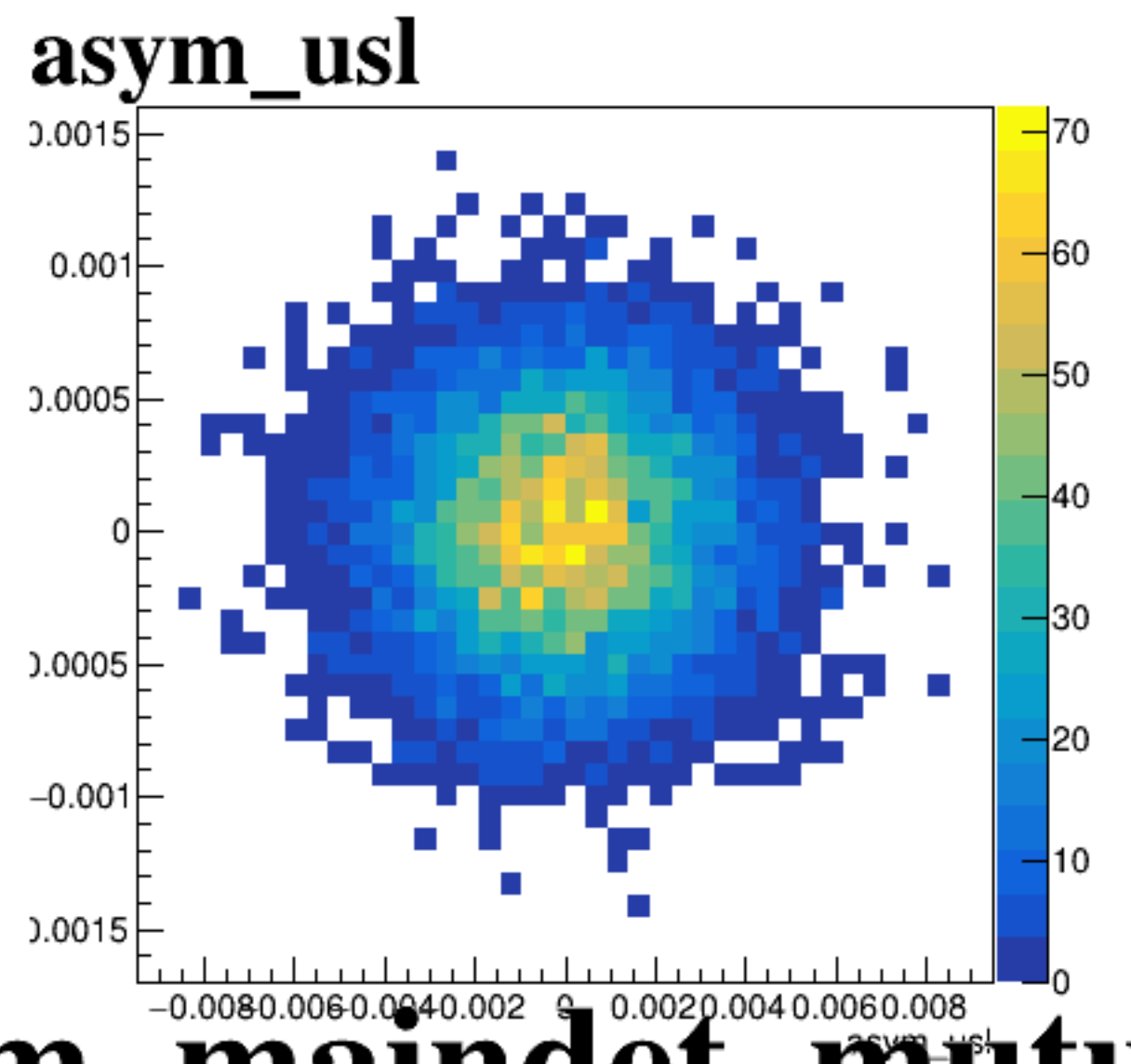
asym_dsl



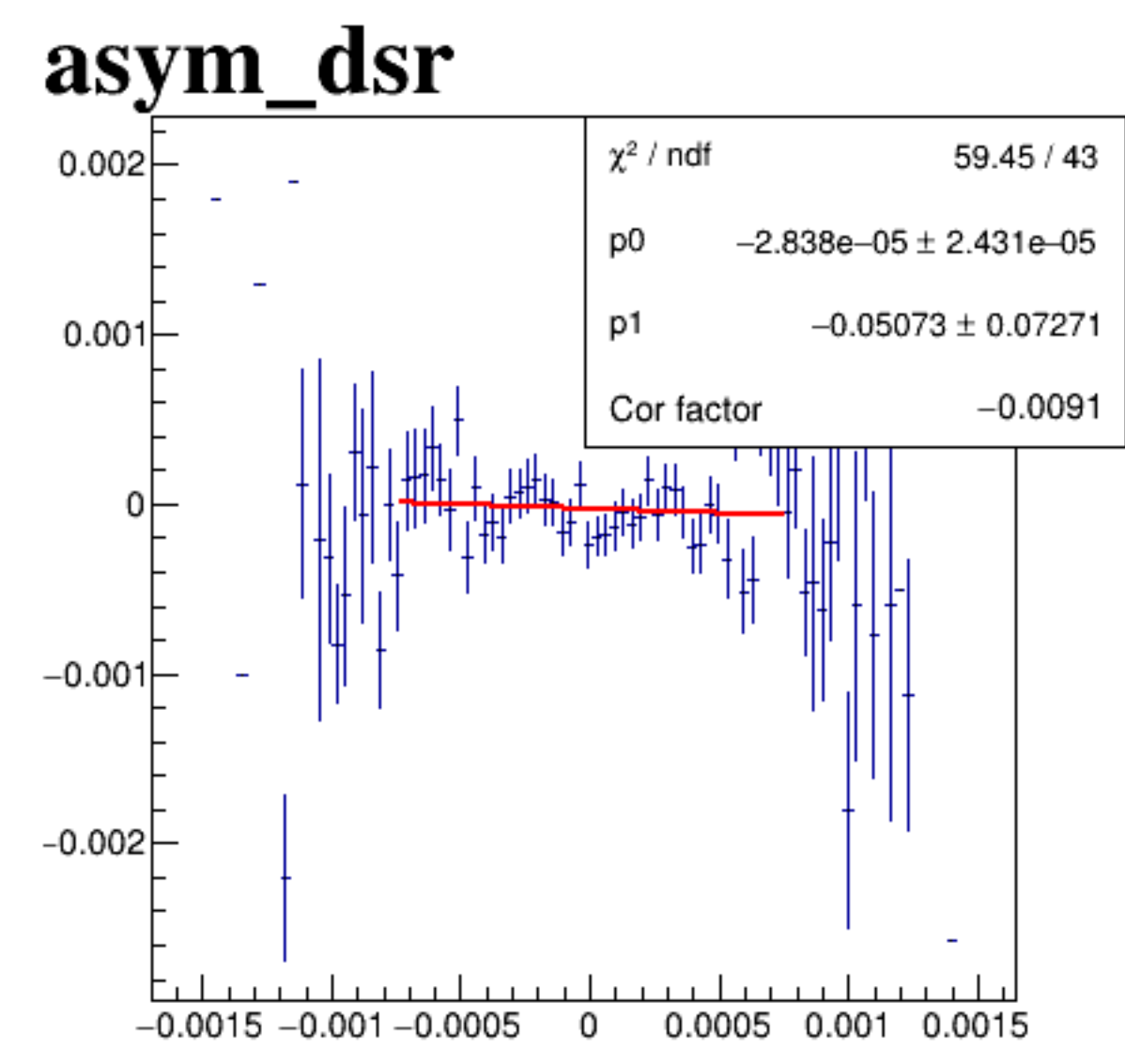
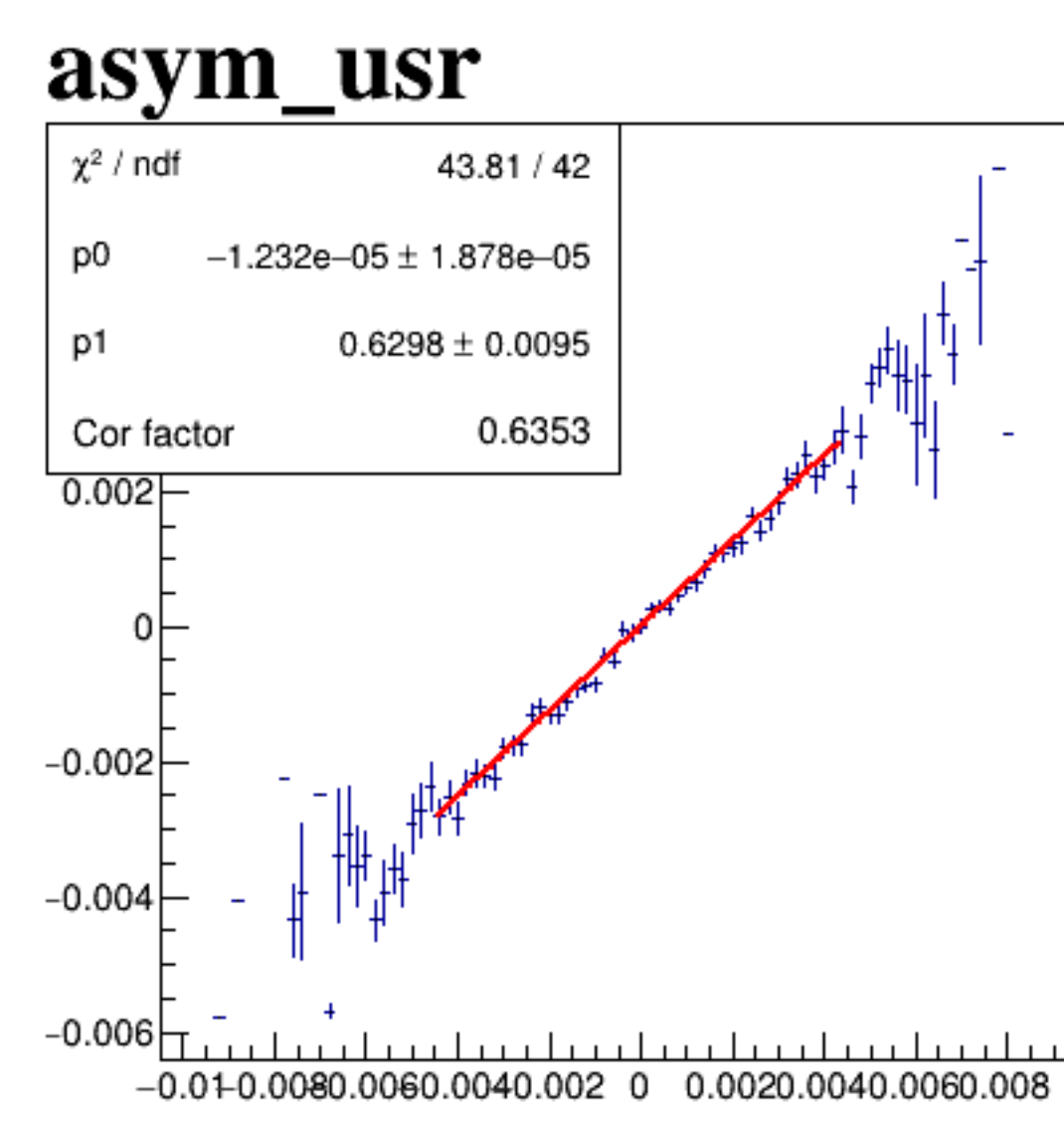
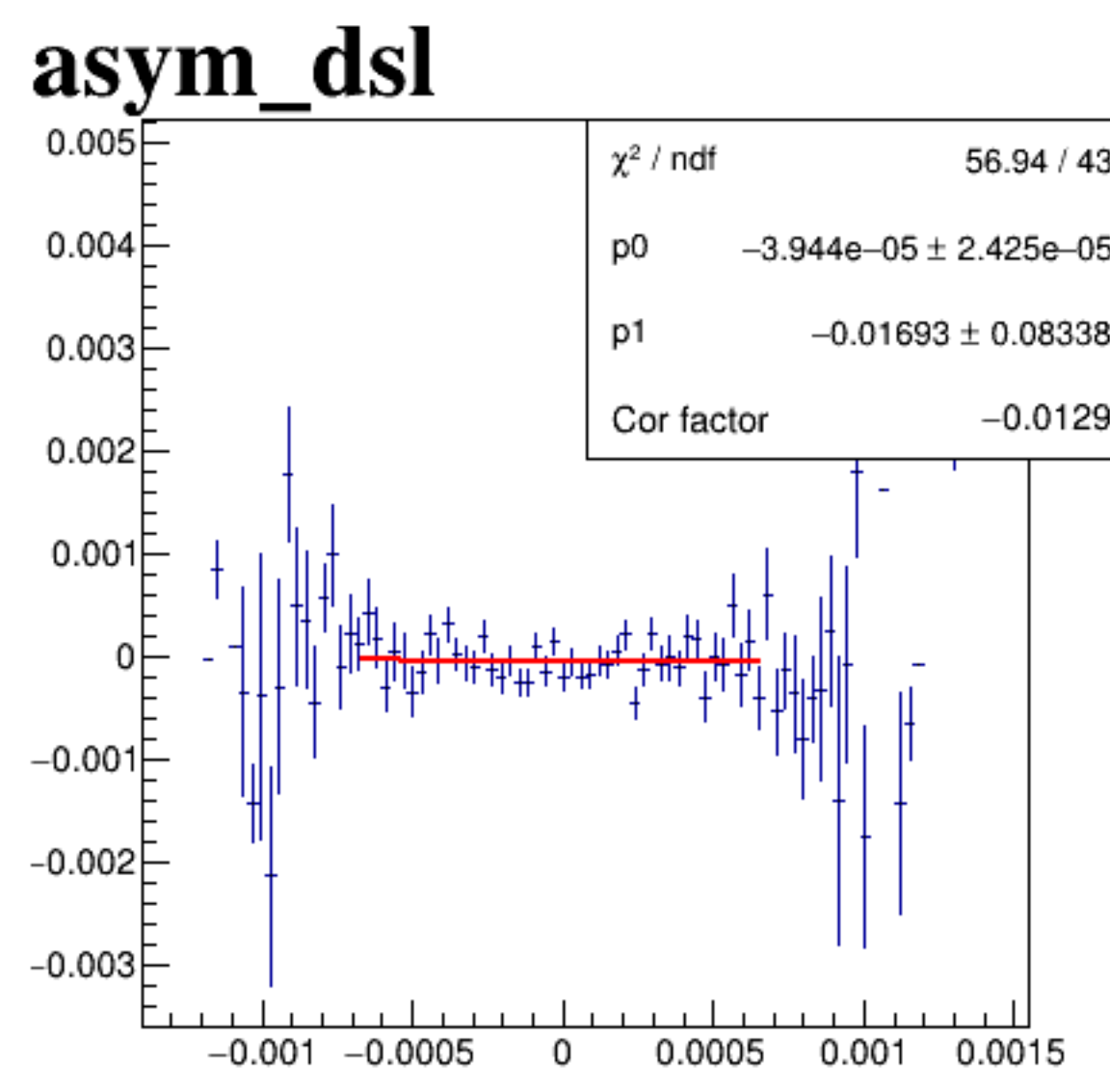
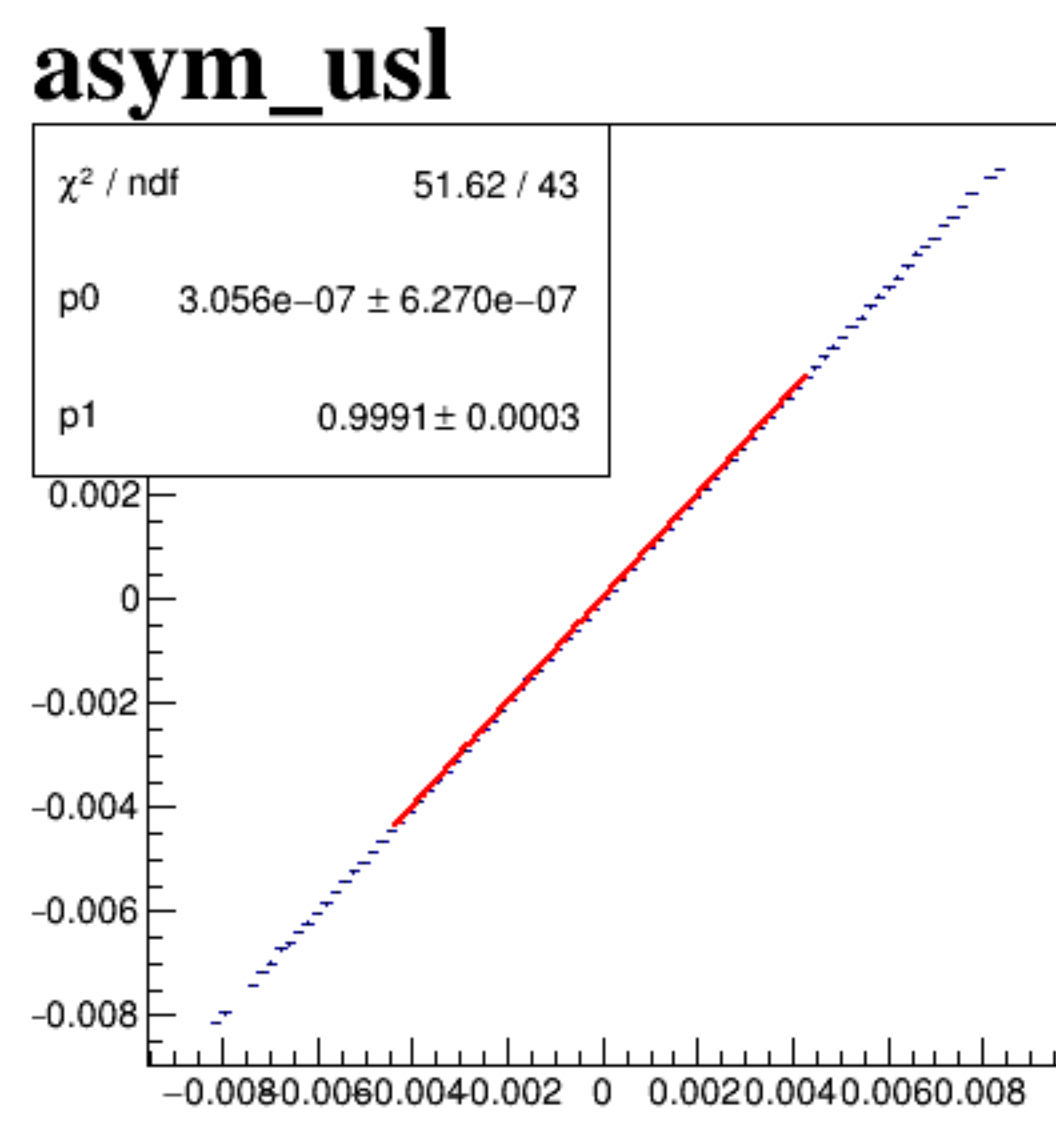
asym_usr



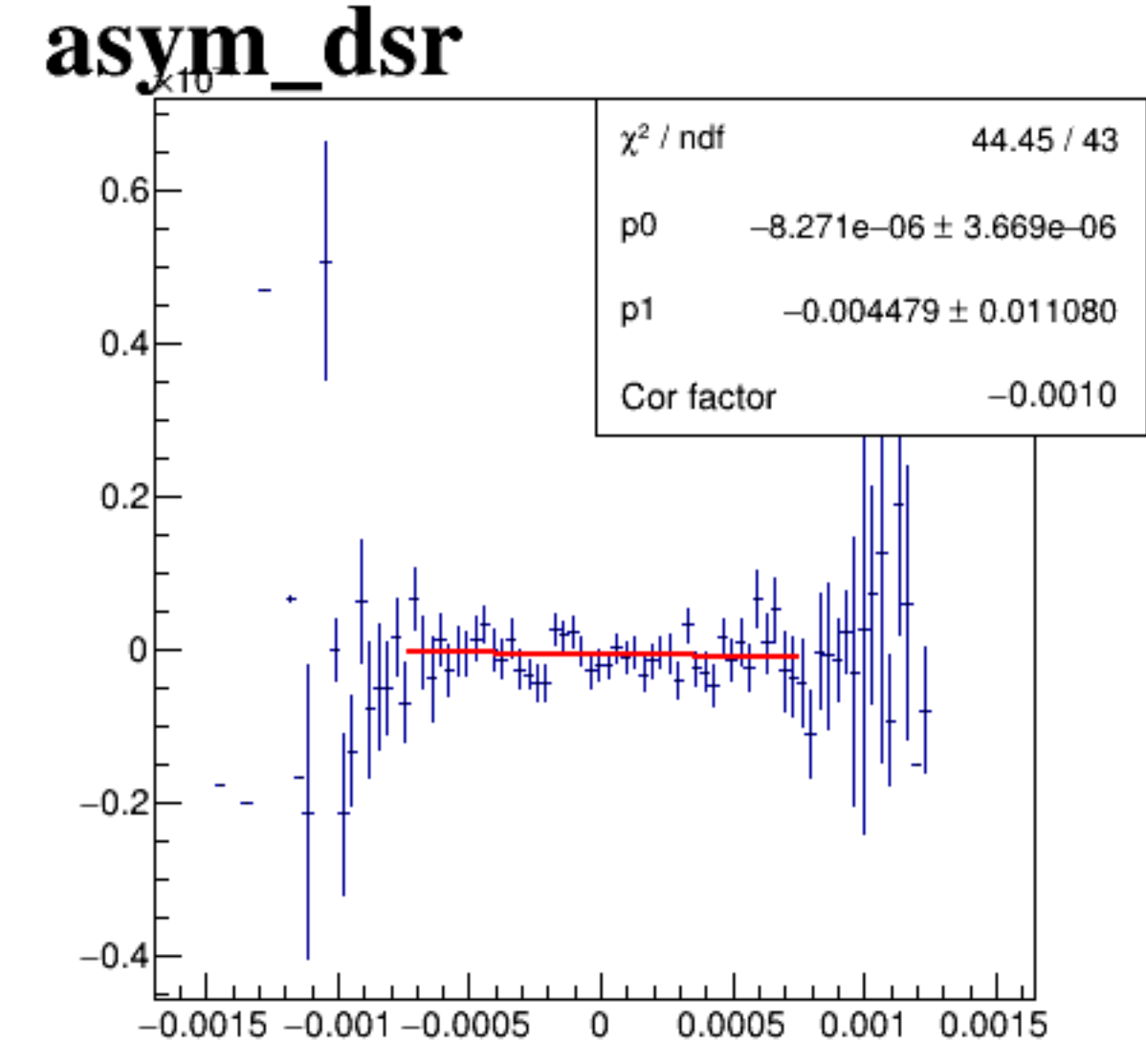
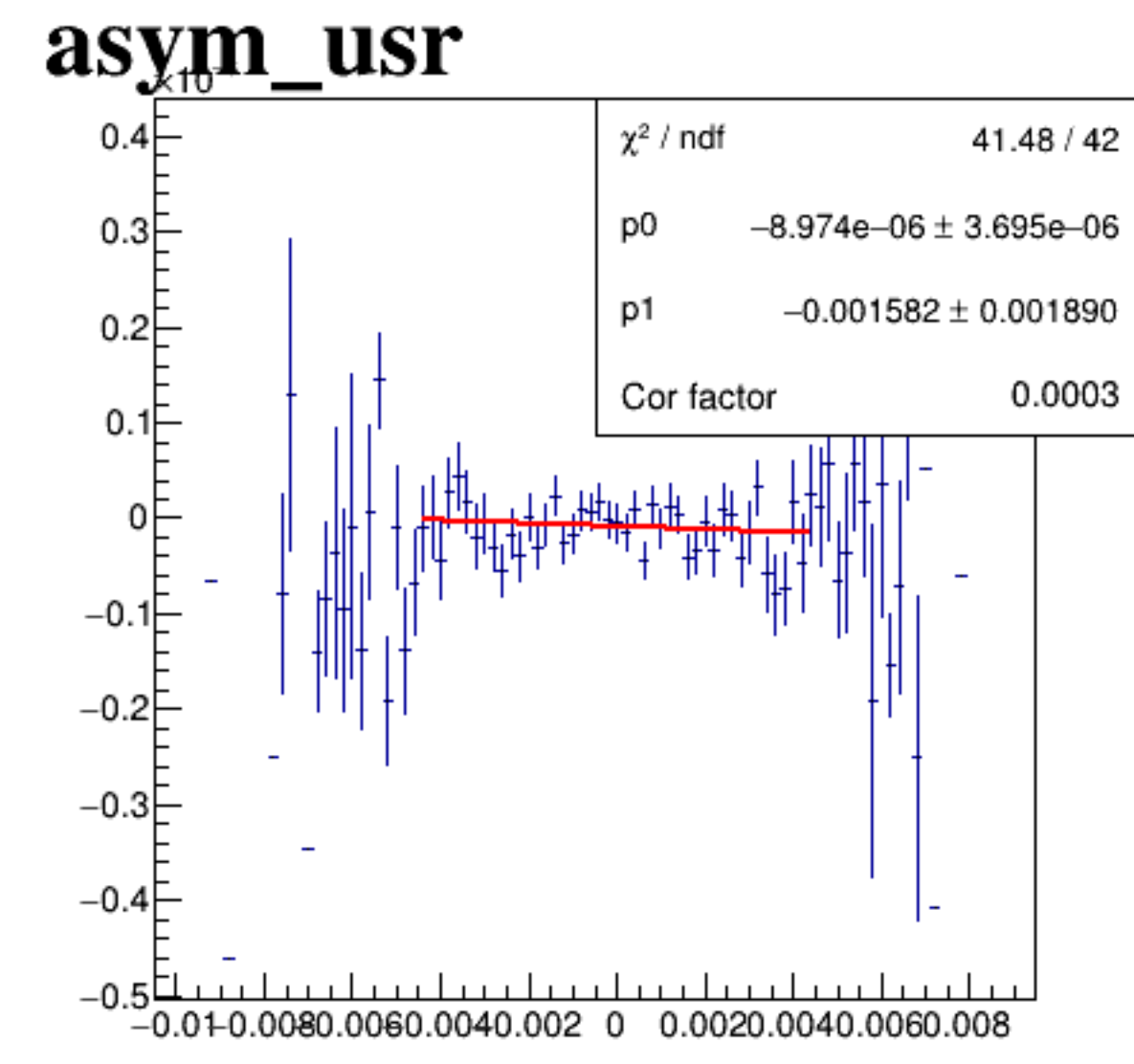
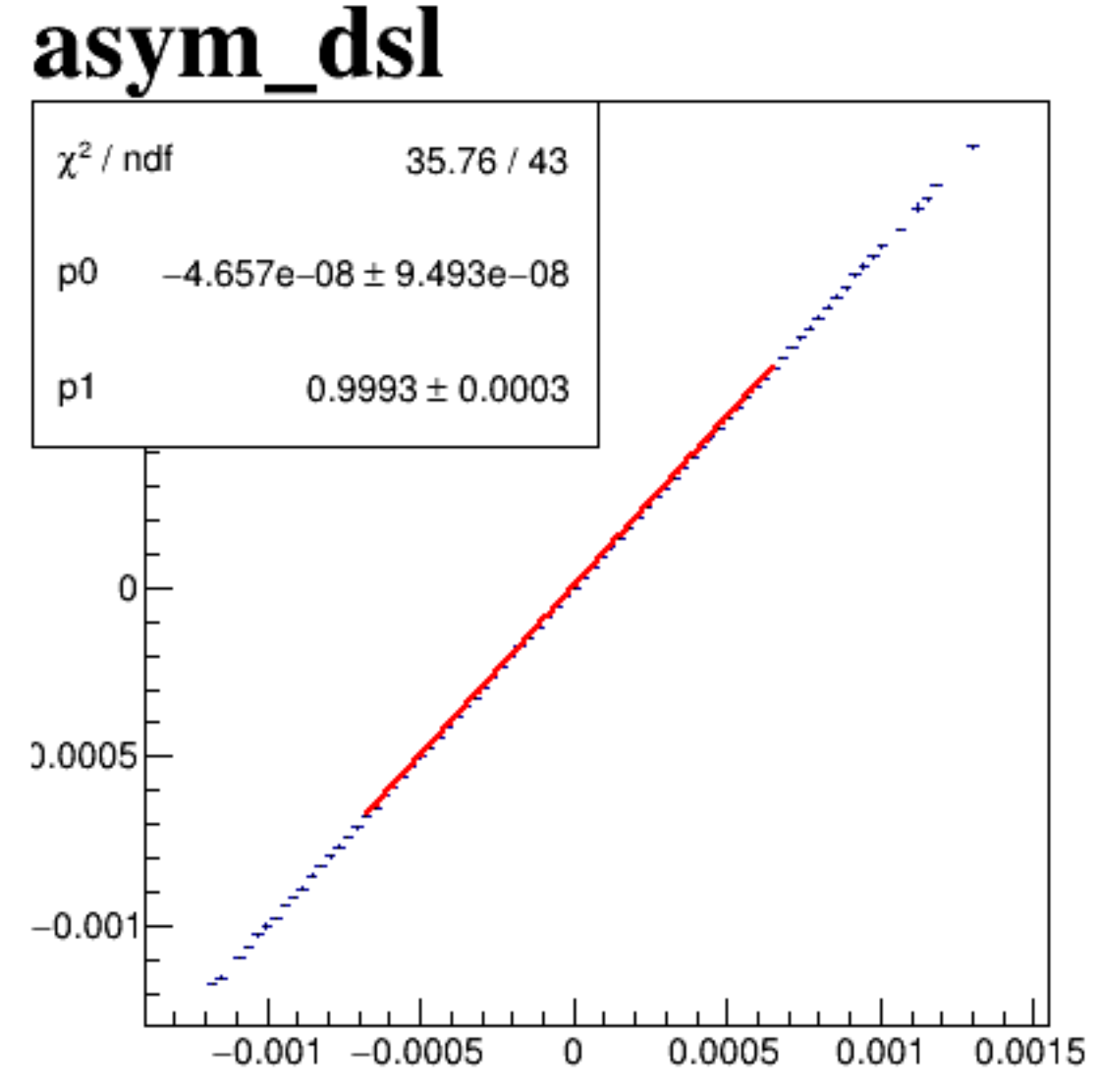
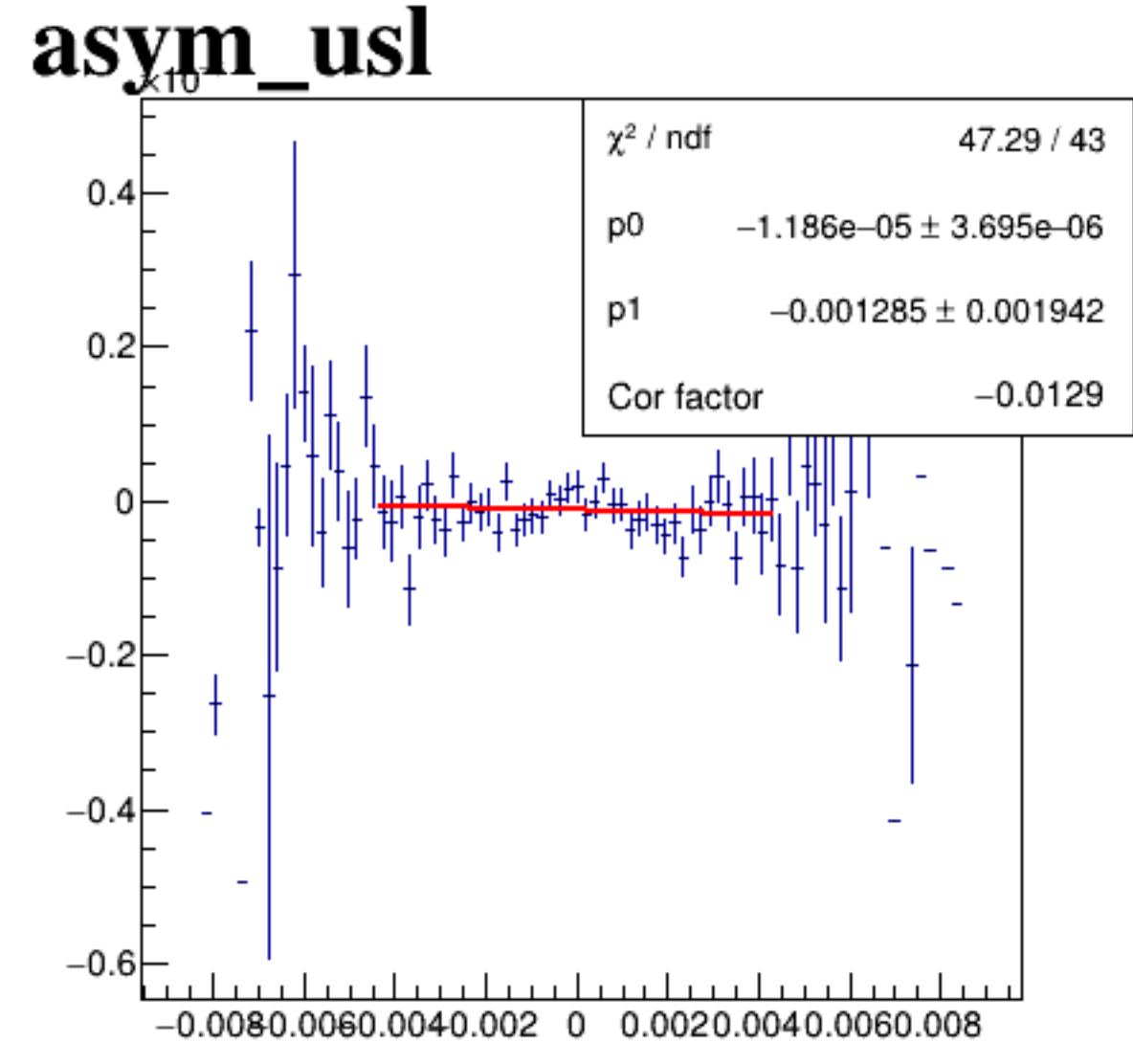
asym_dsr



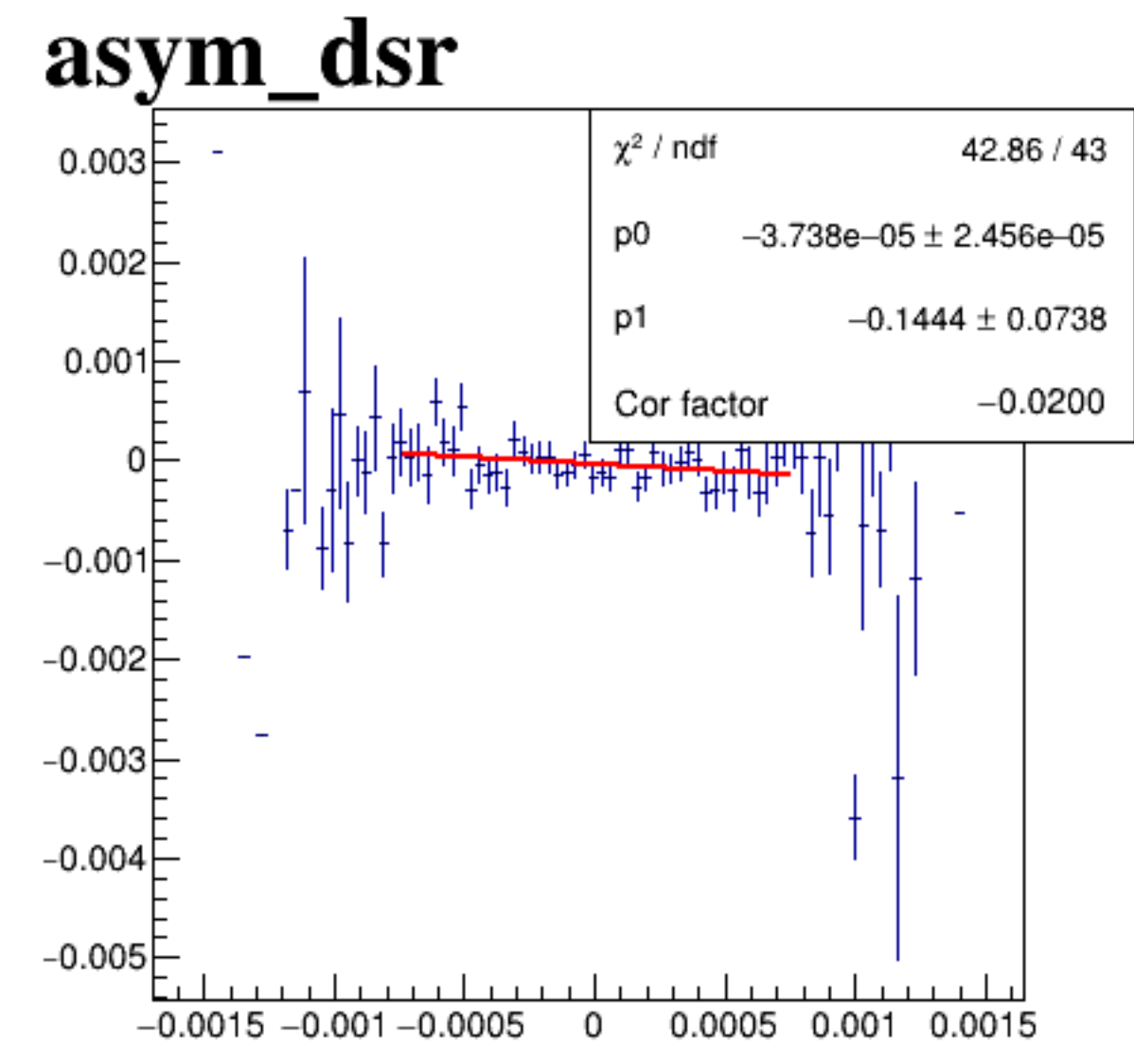
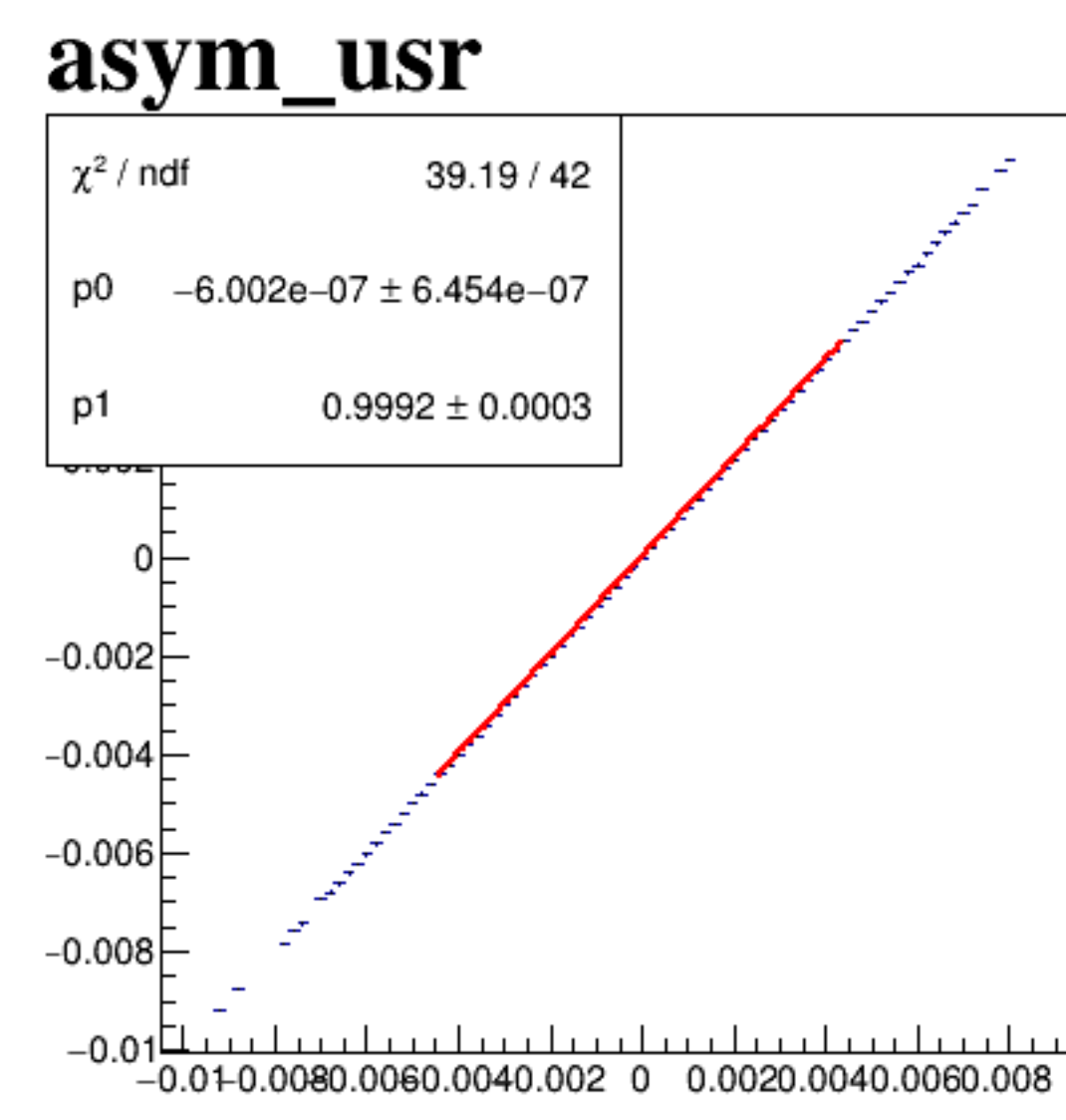
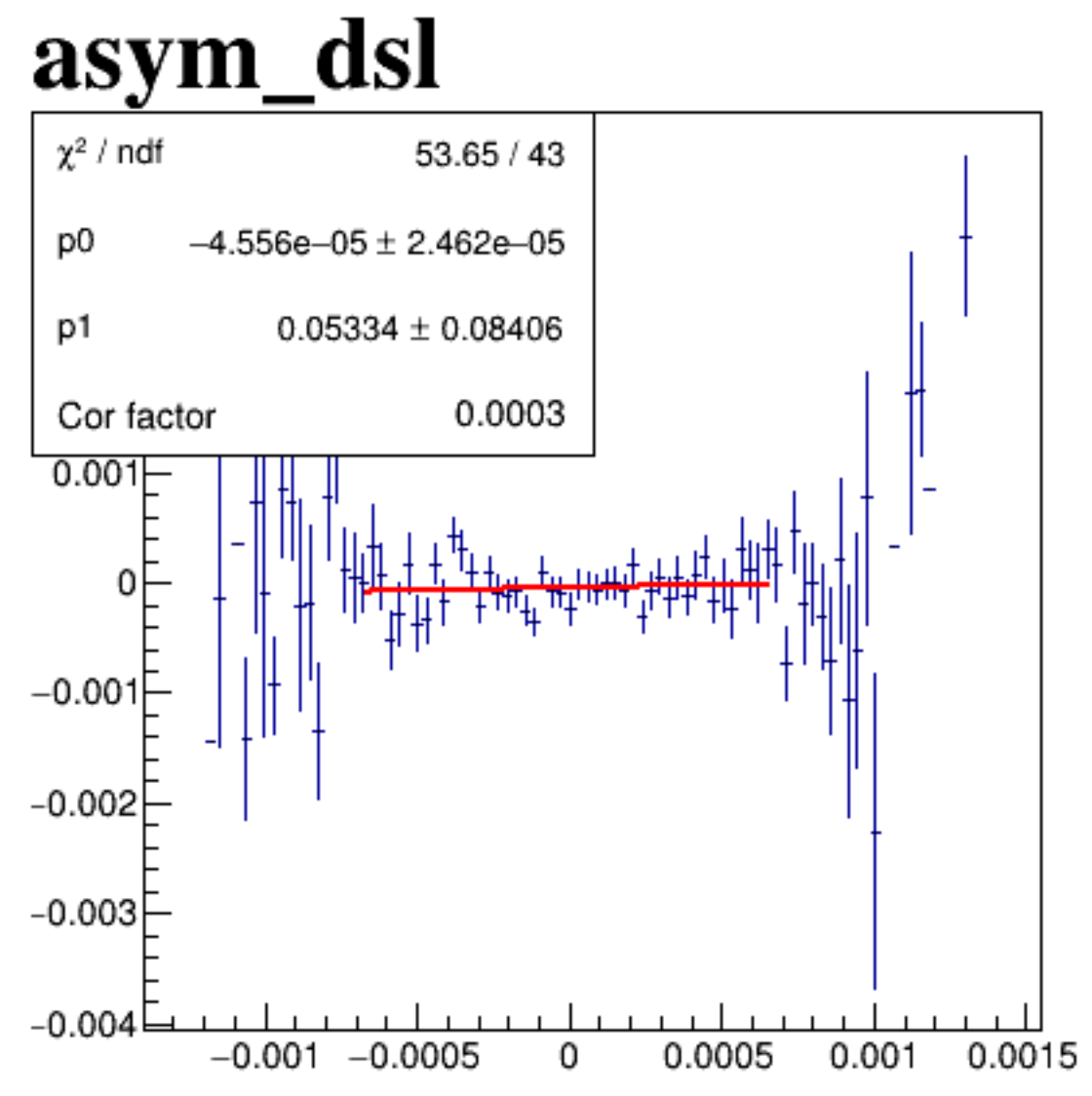
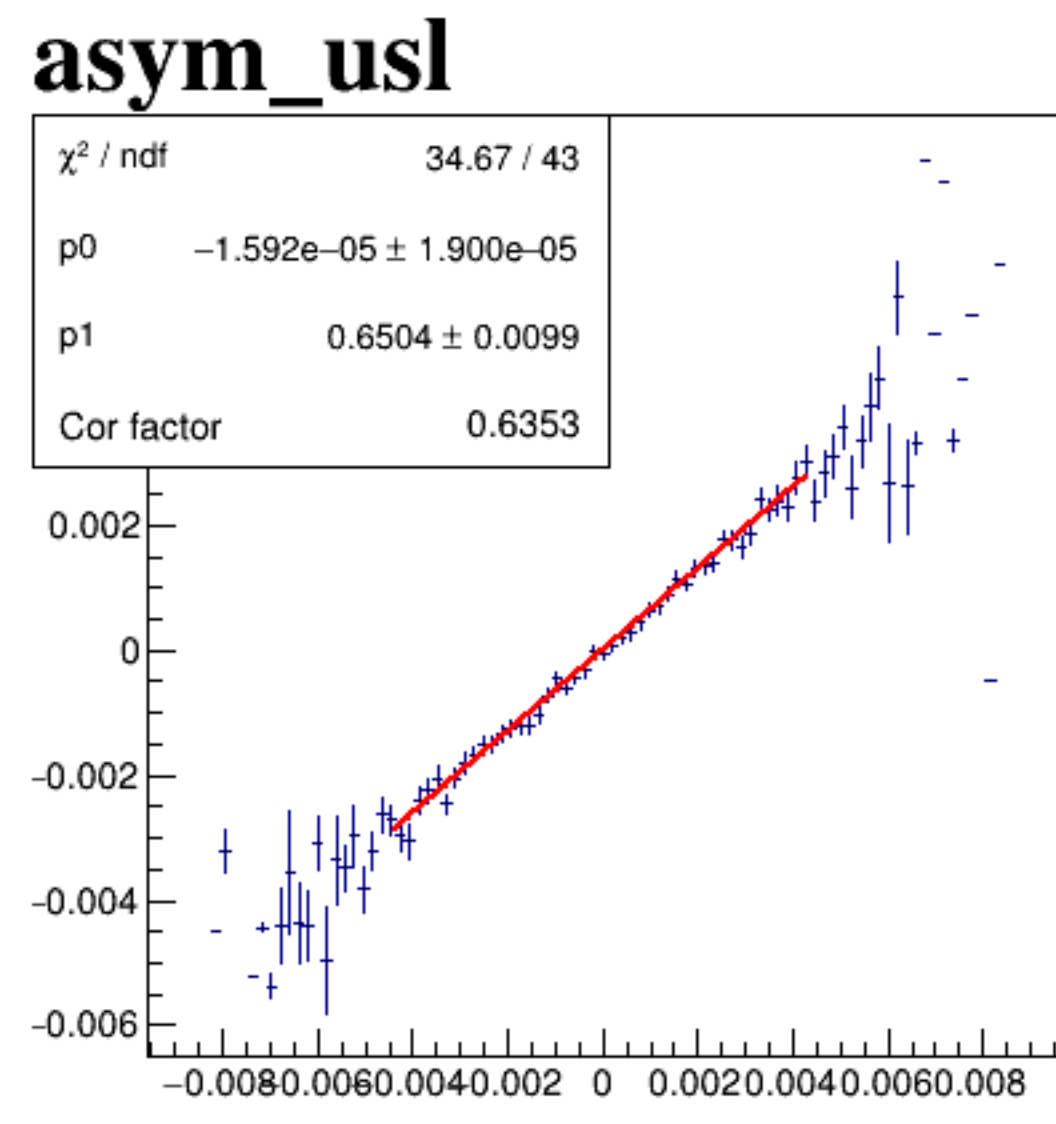
asym_usl



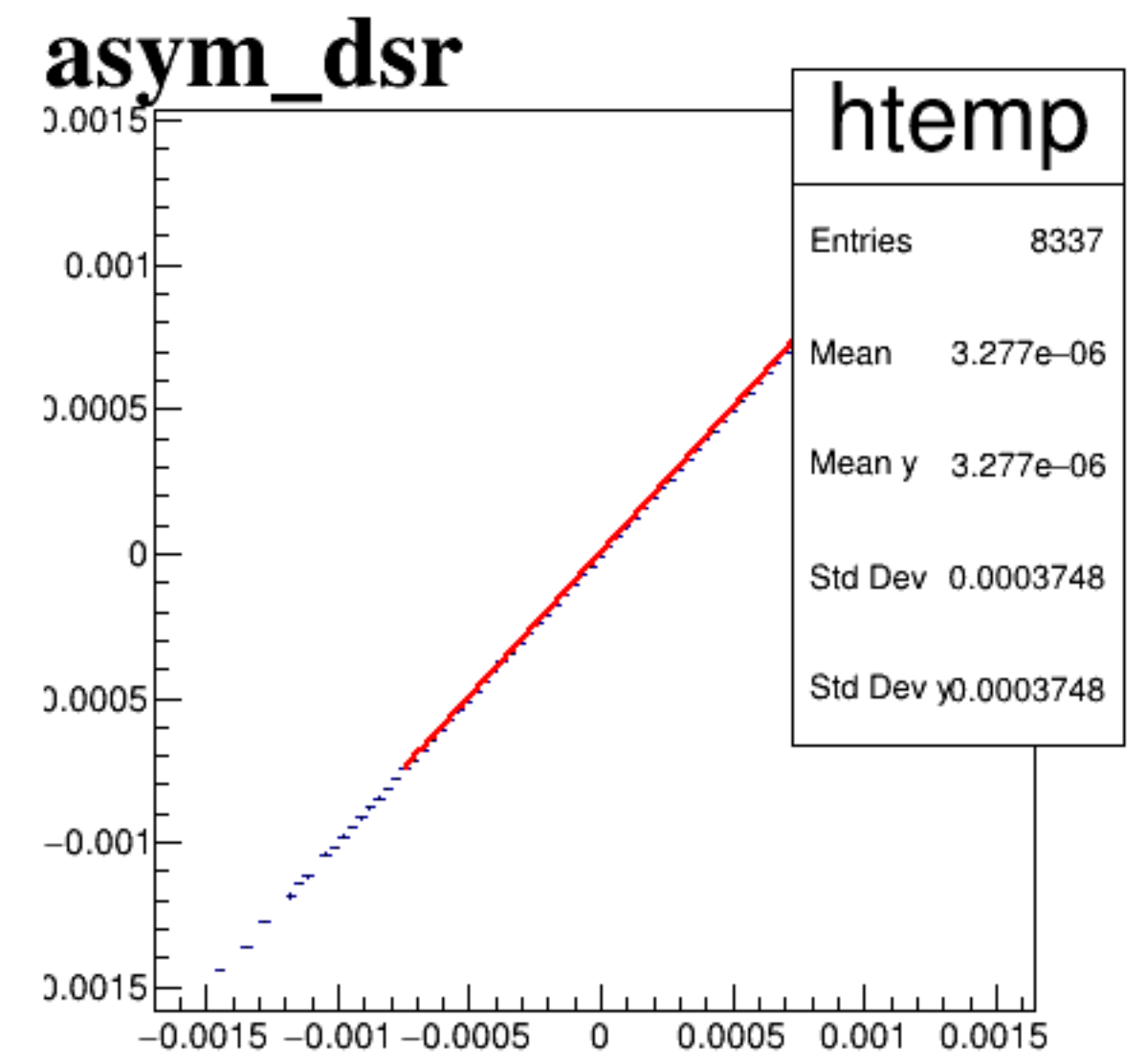
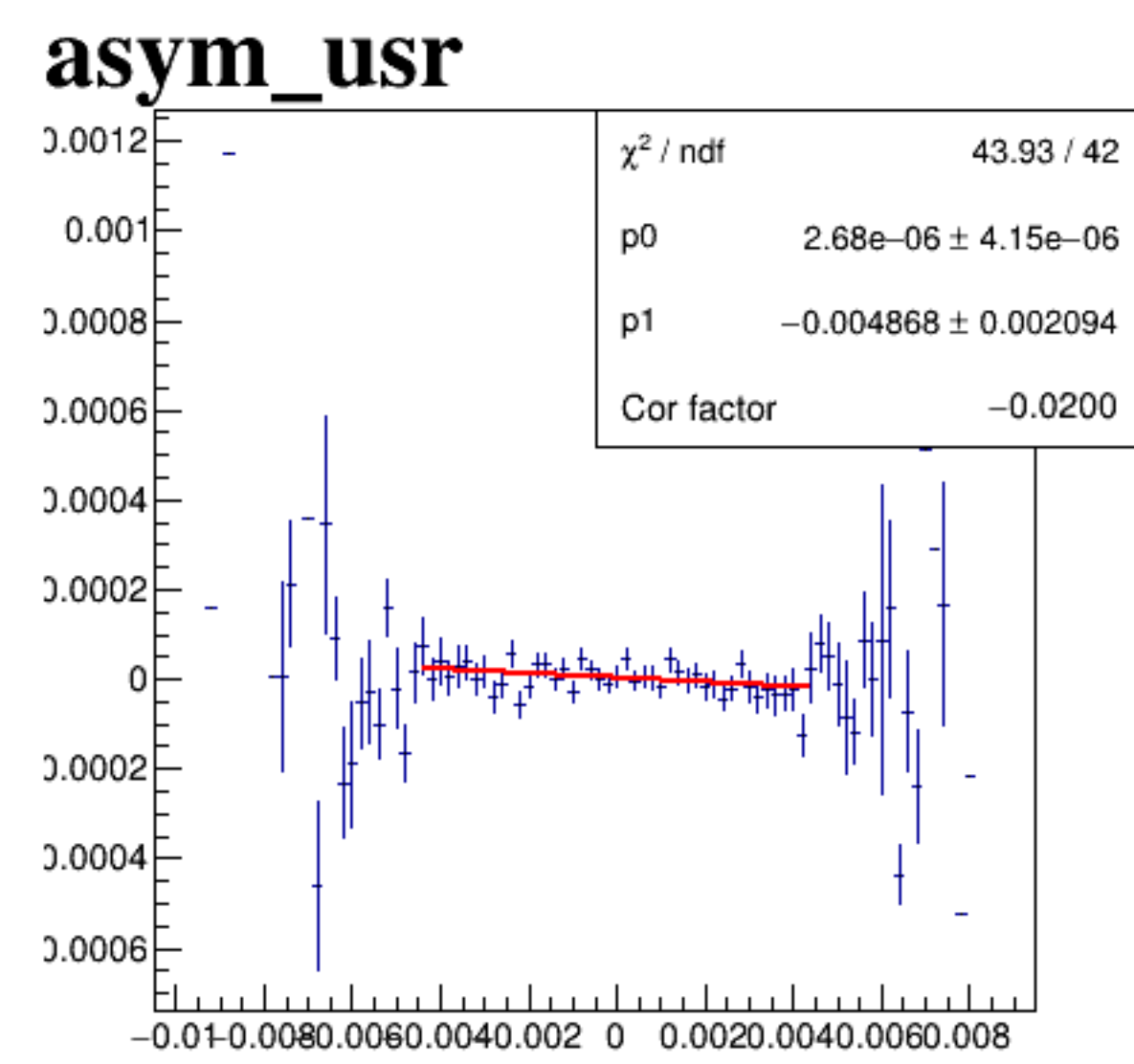
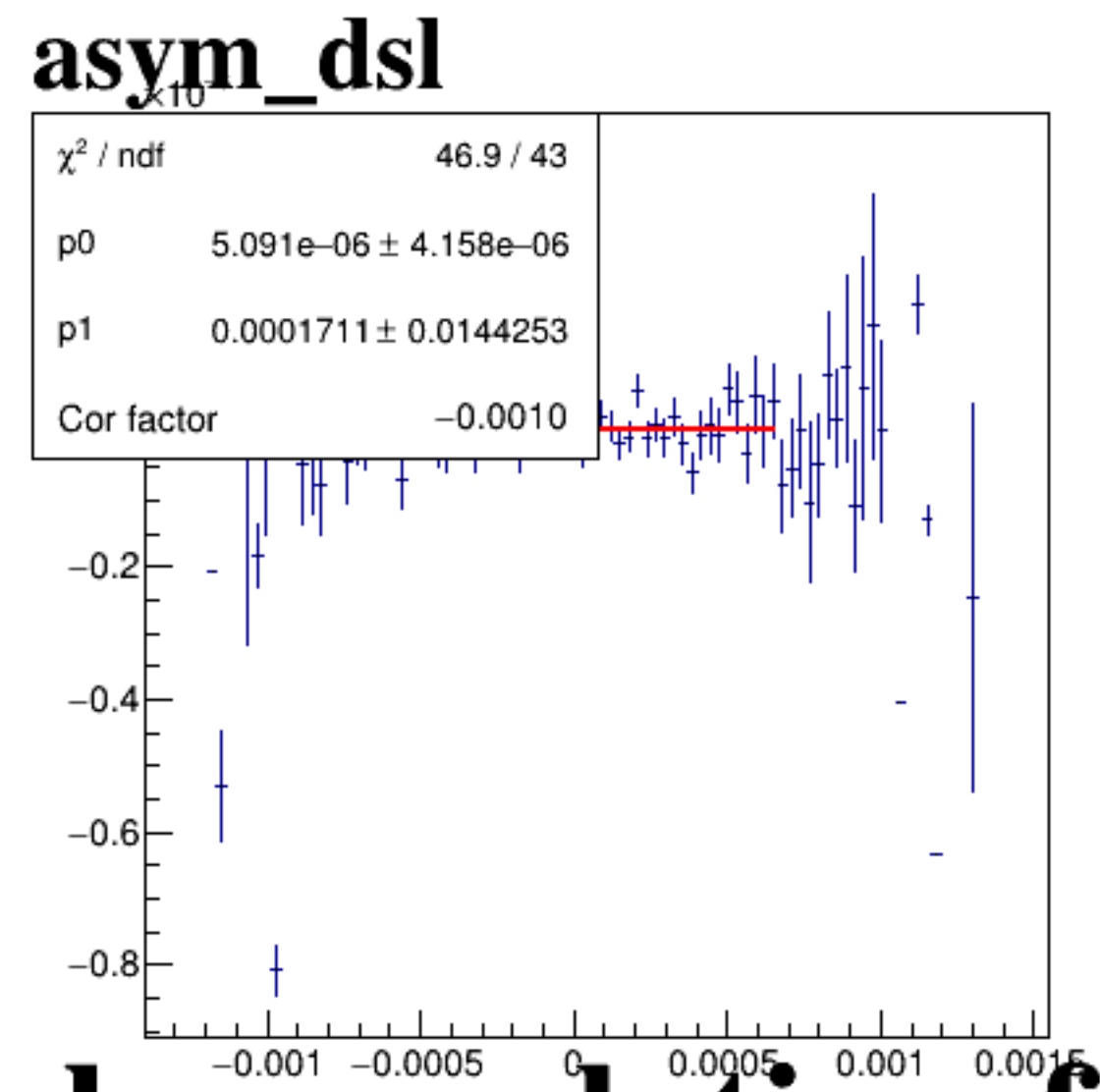
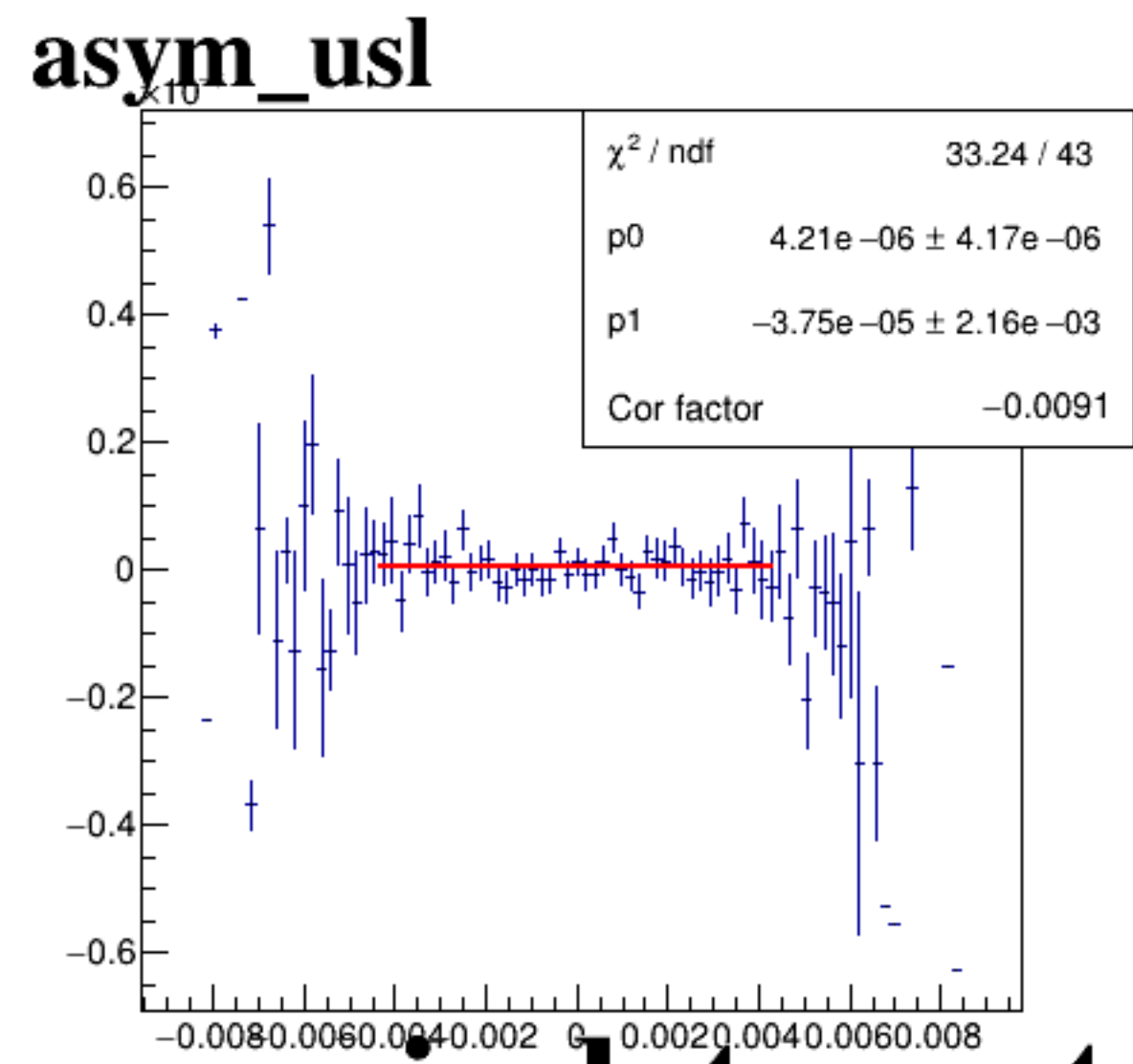
asym_dsl



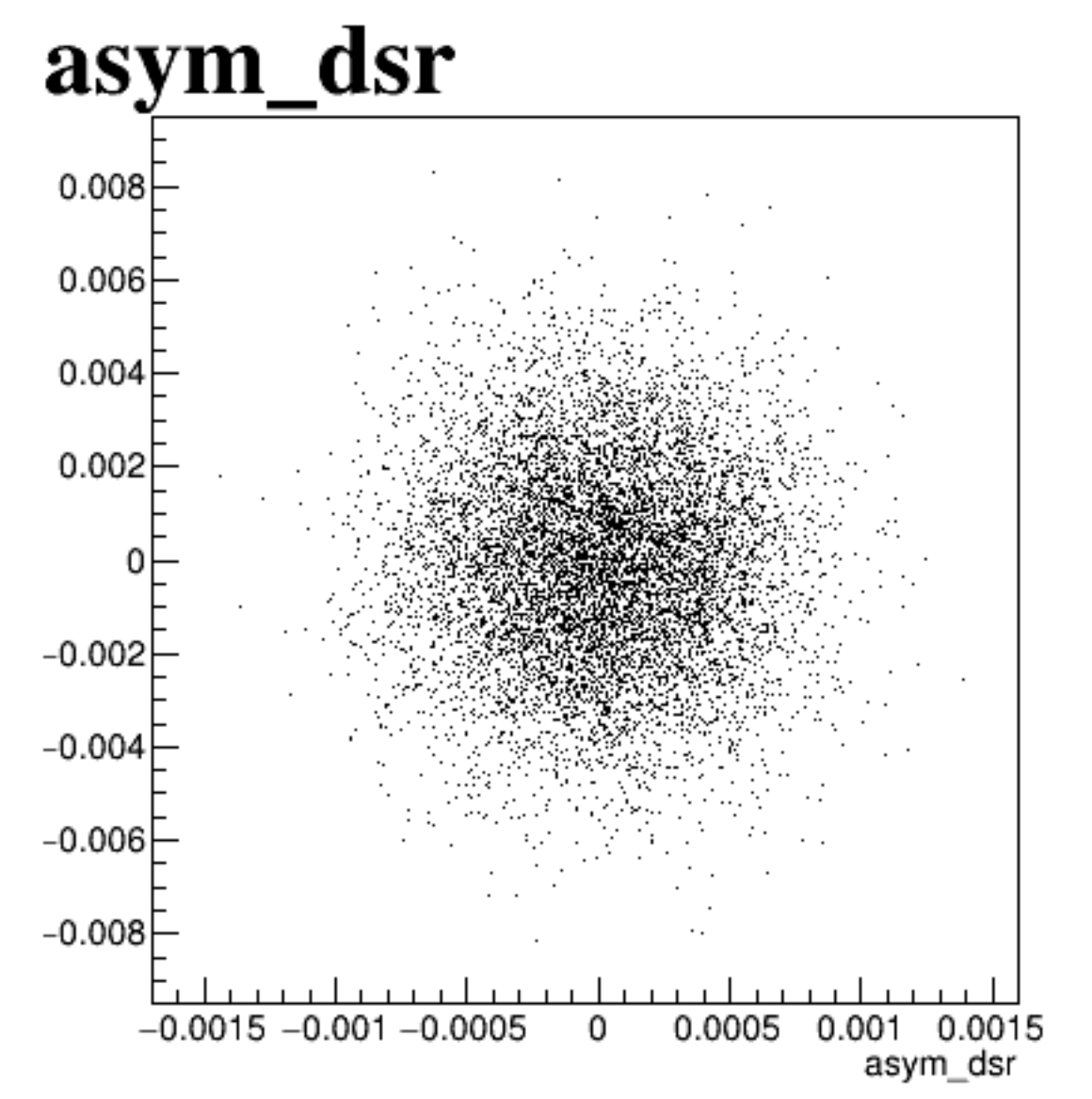
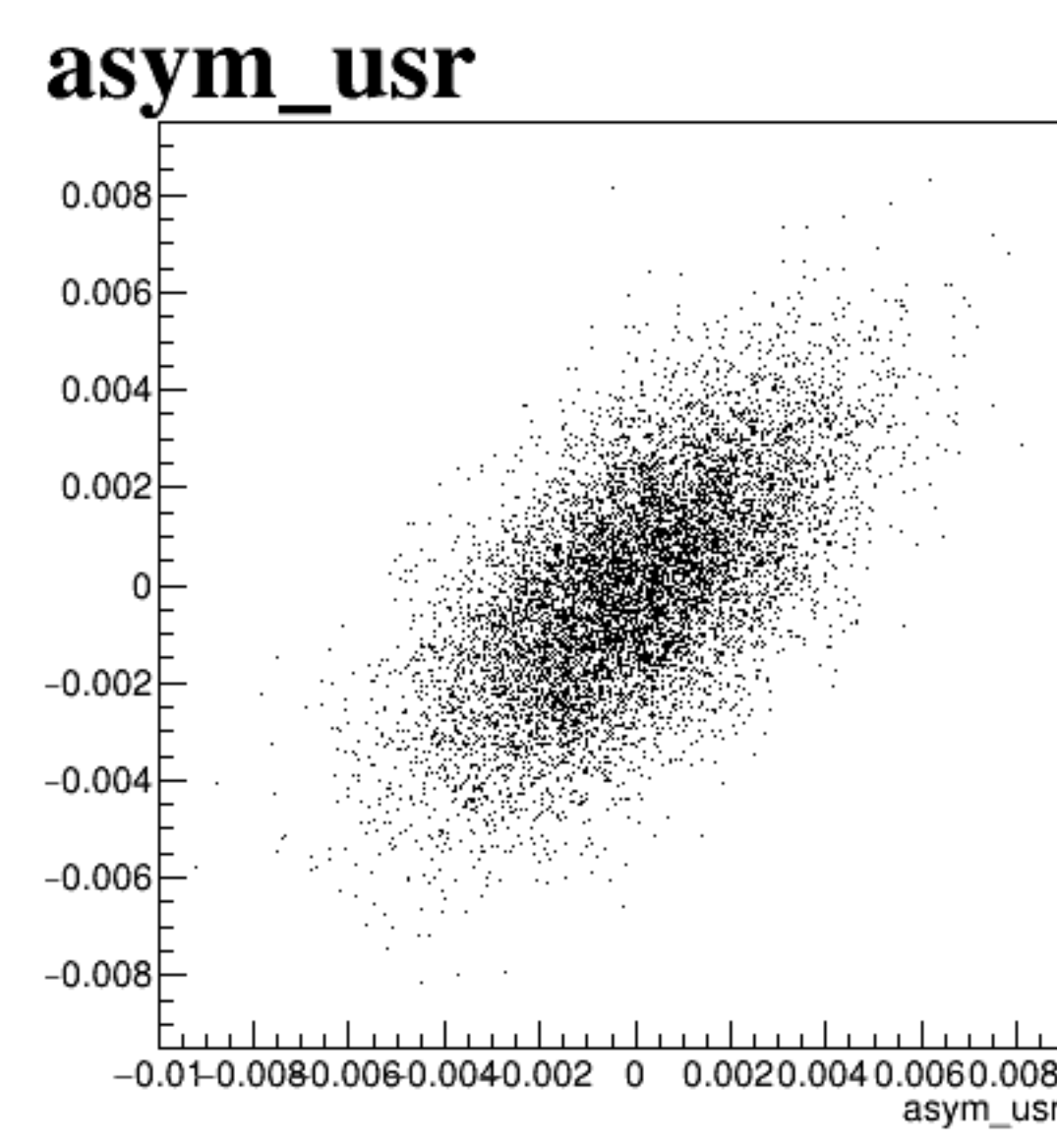
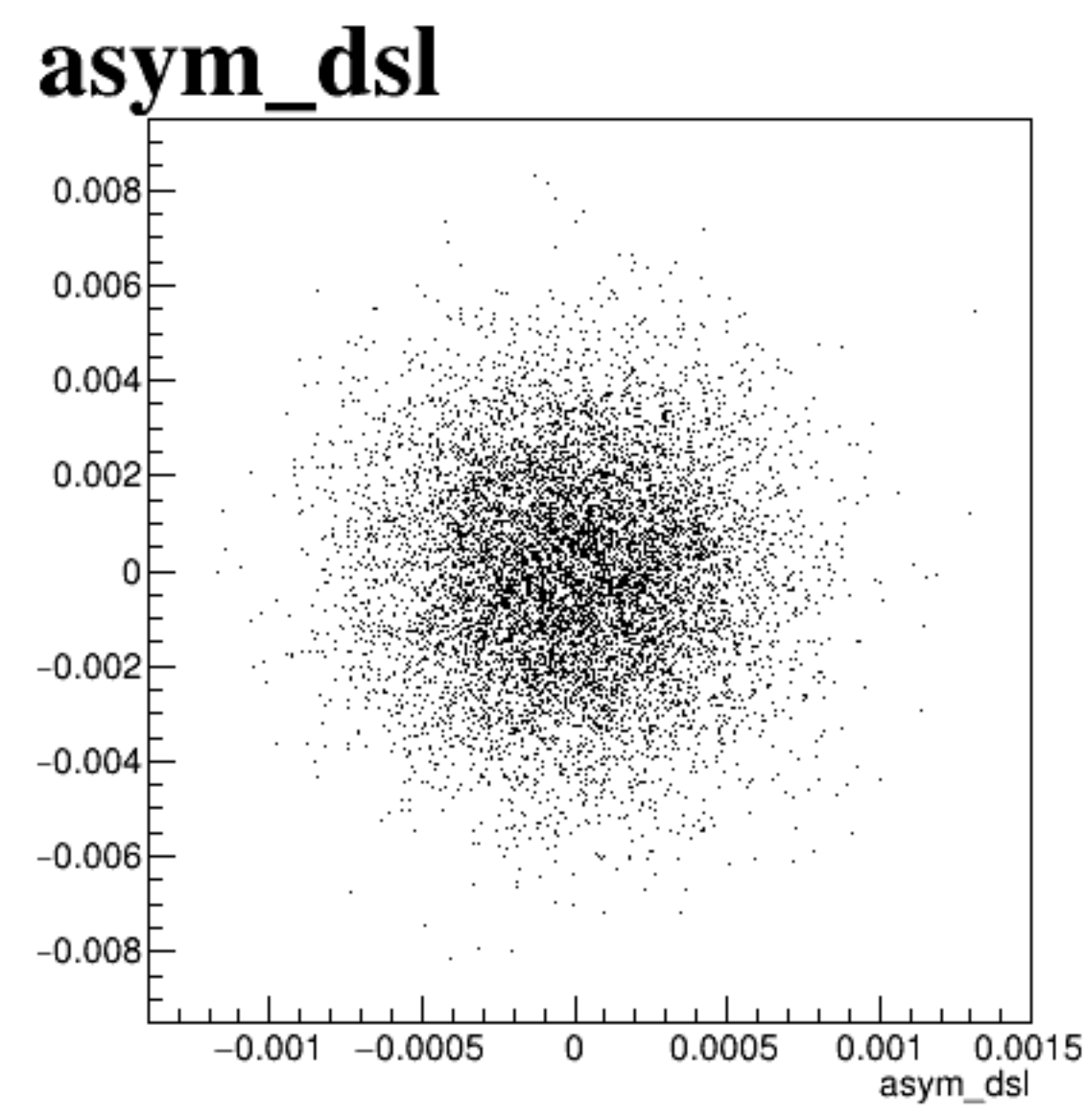
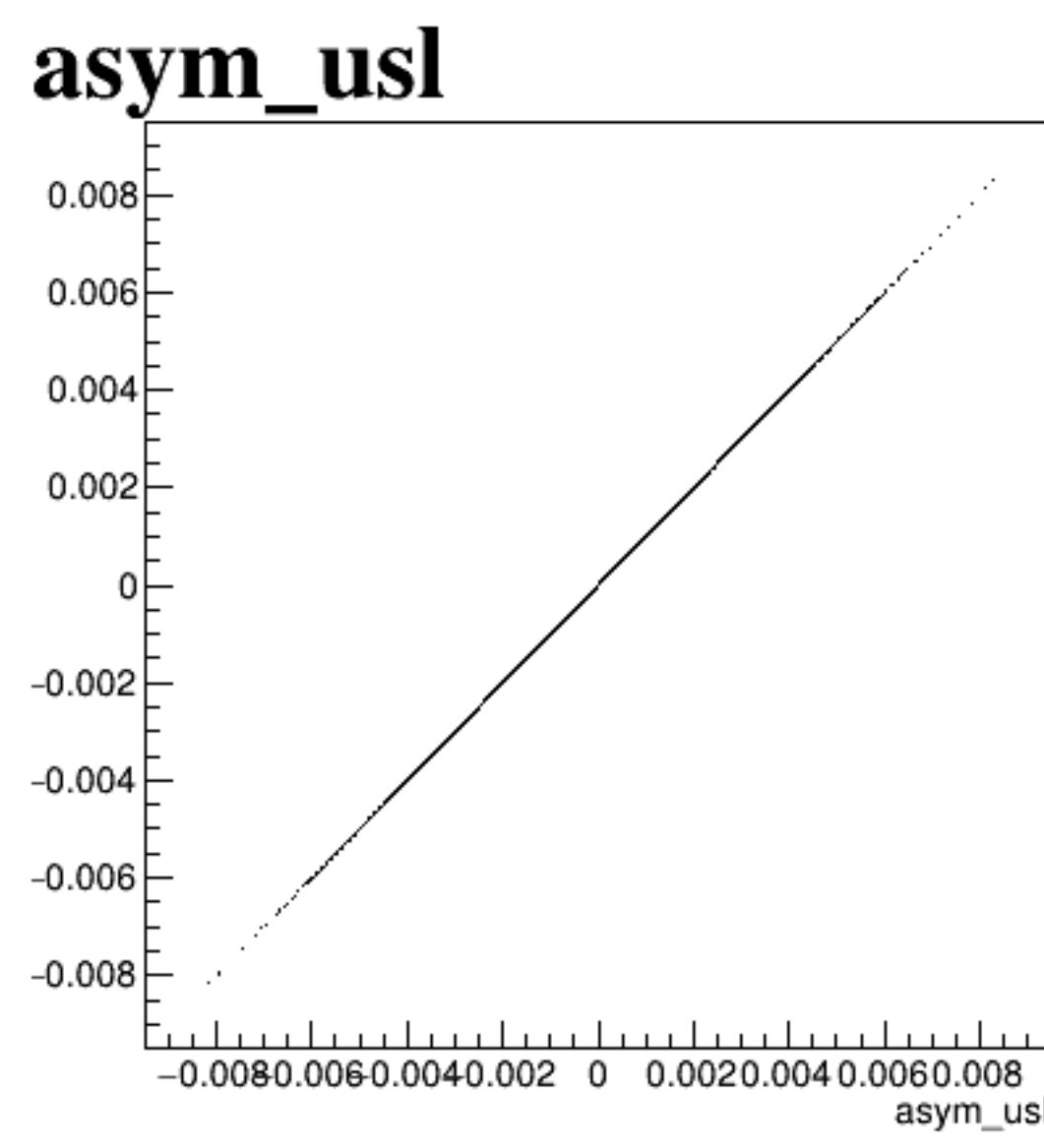
asym_usr



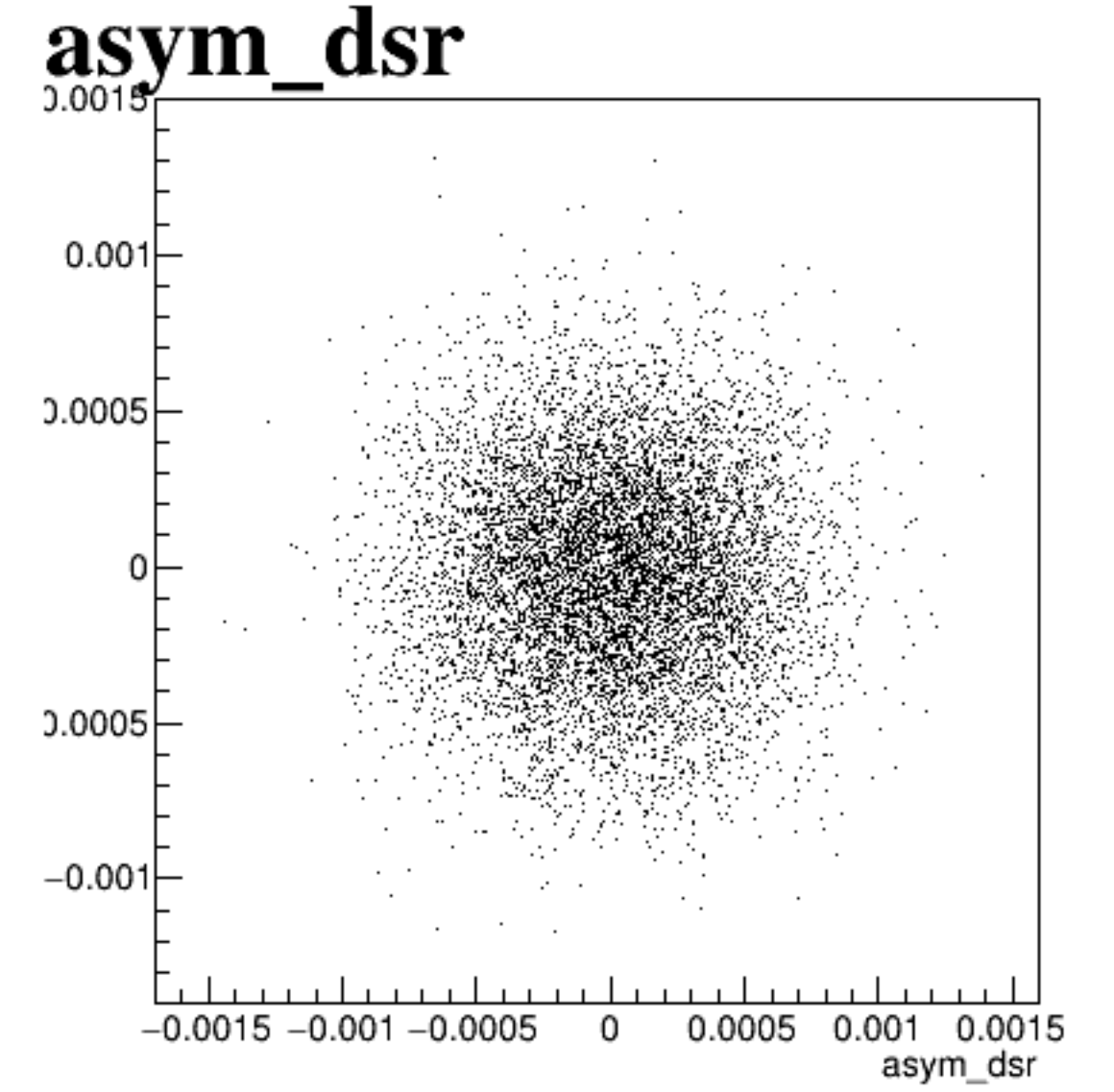
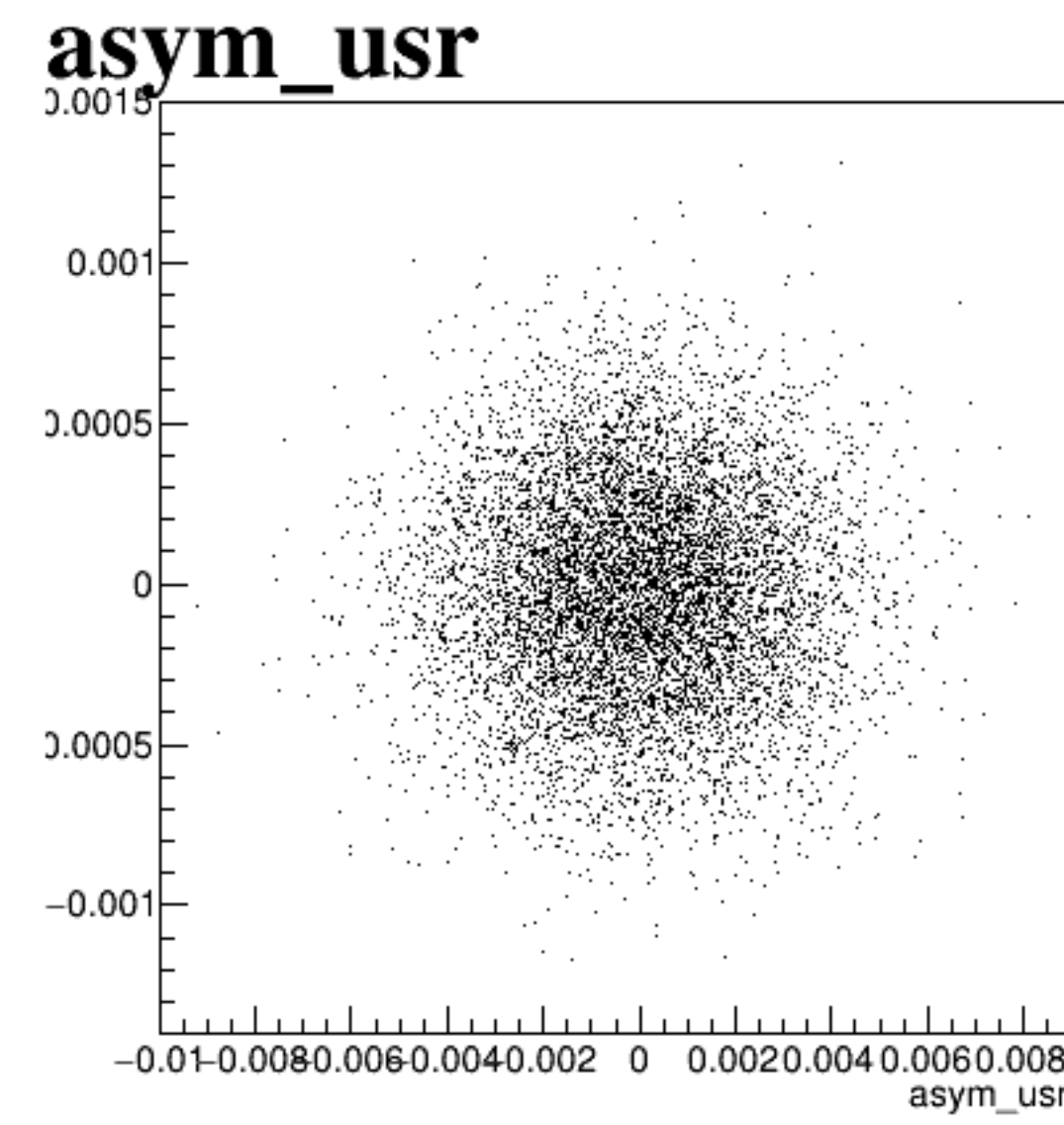
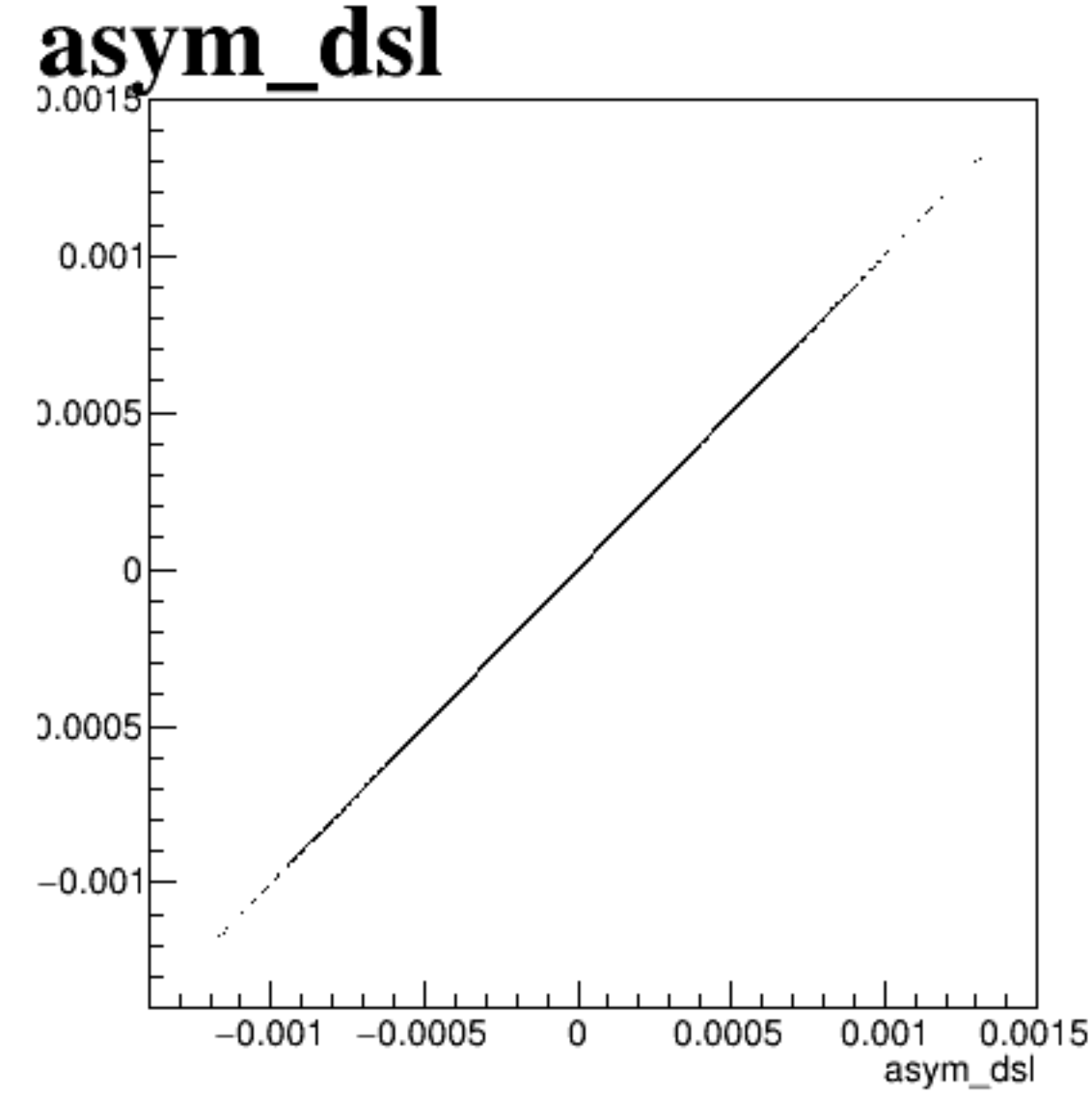
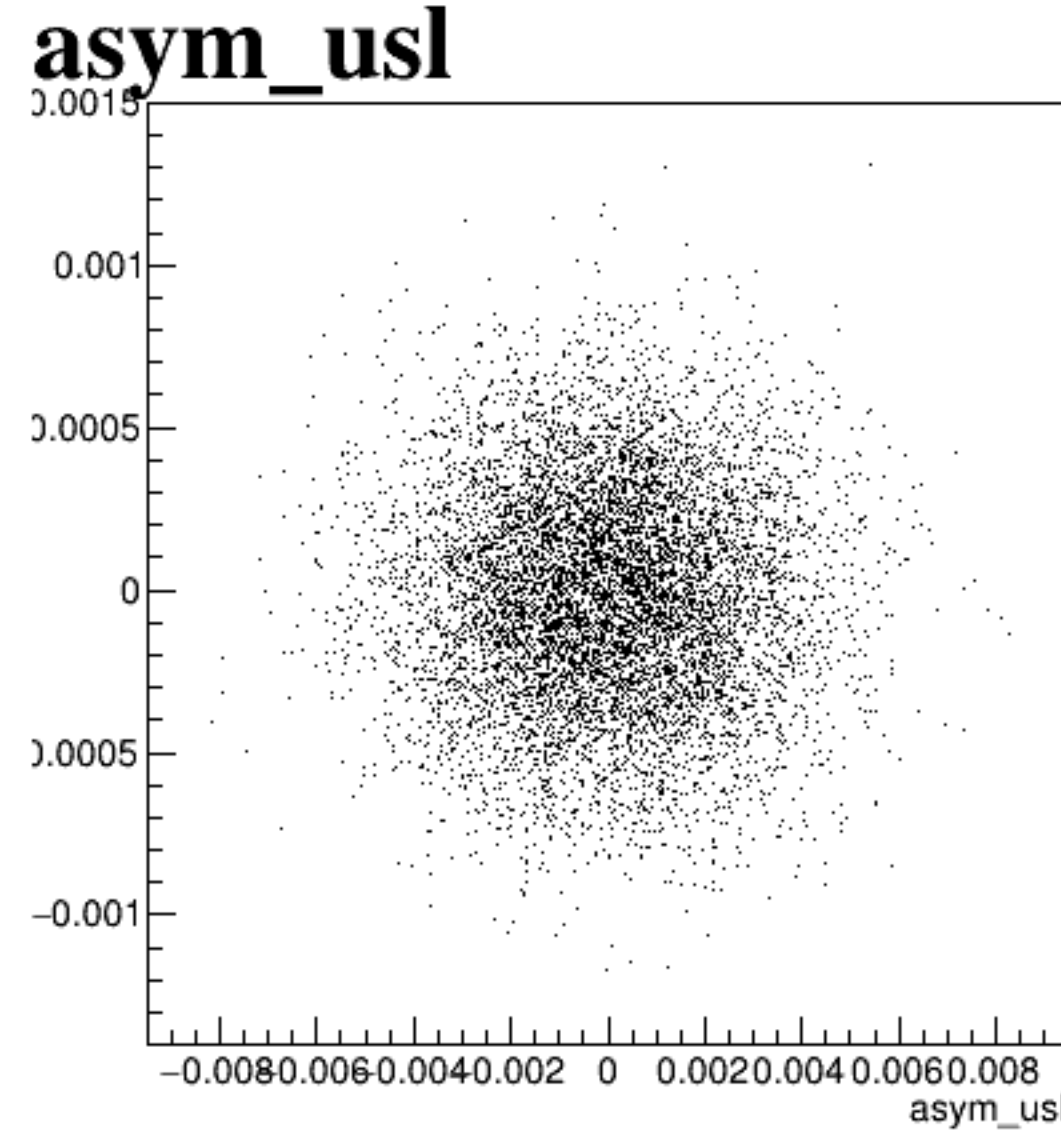
asym_dsr



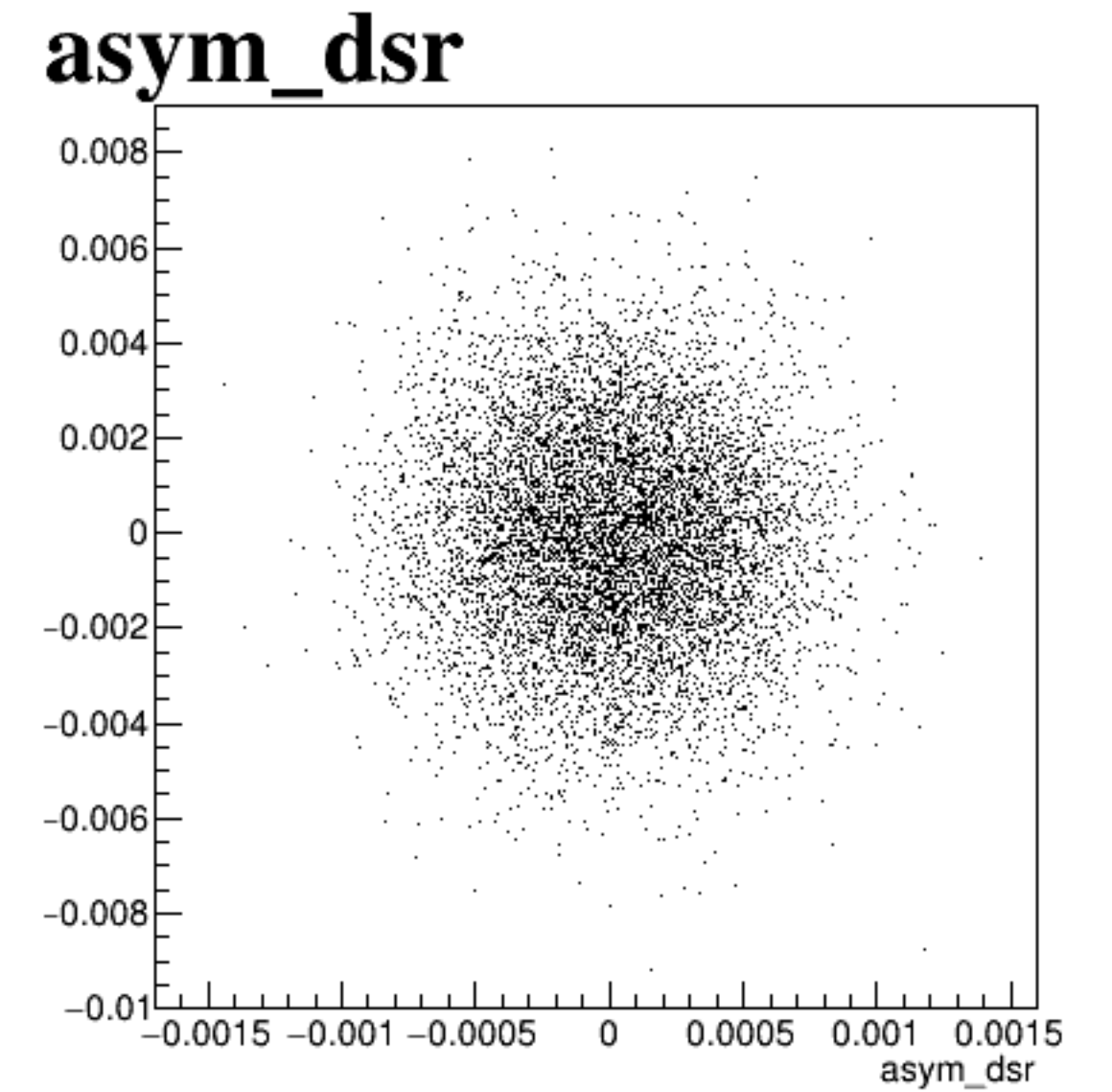
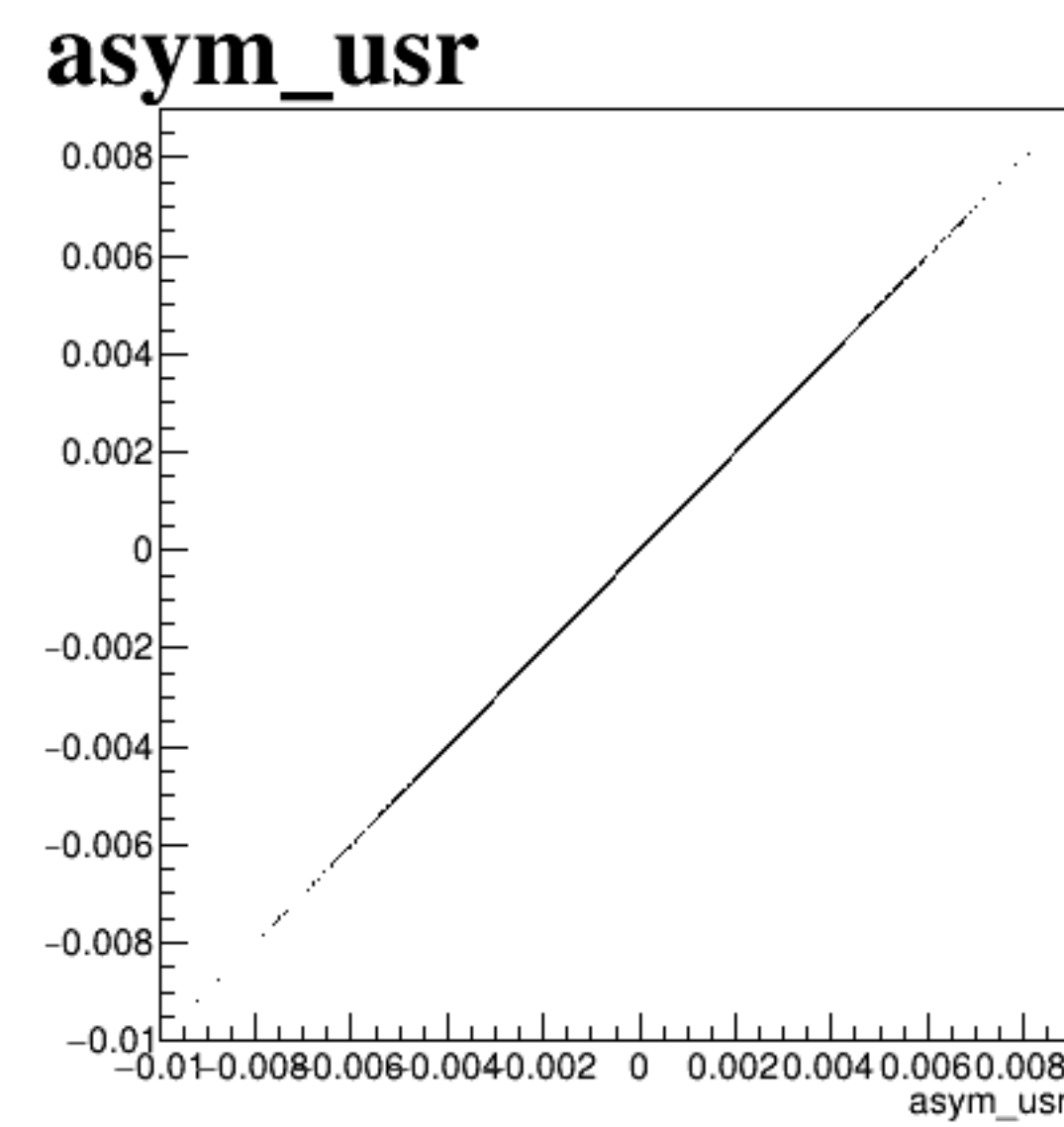
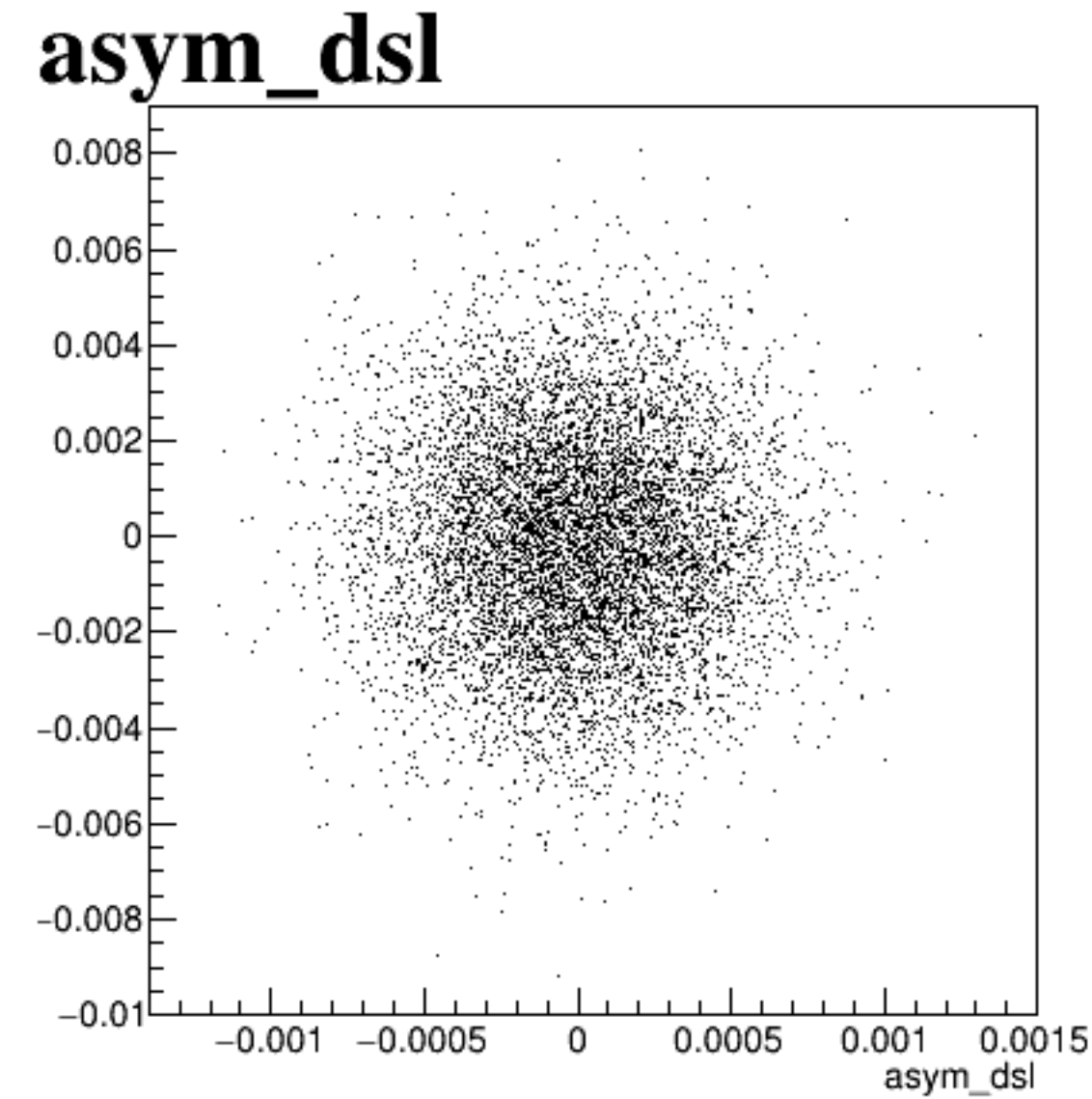
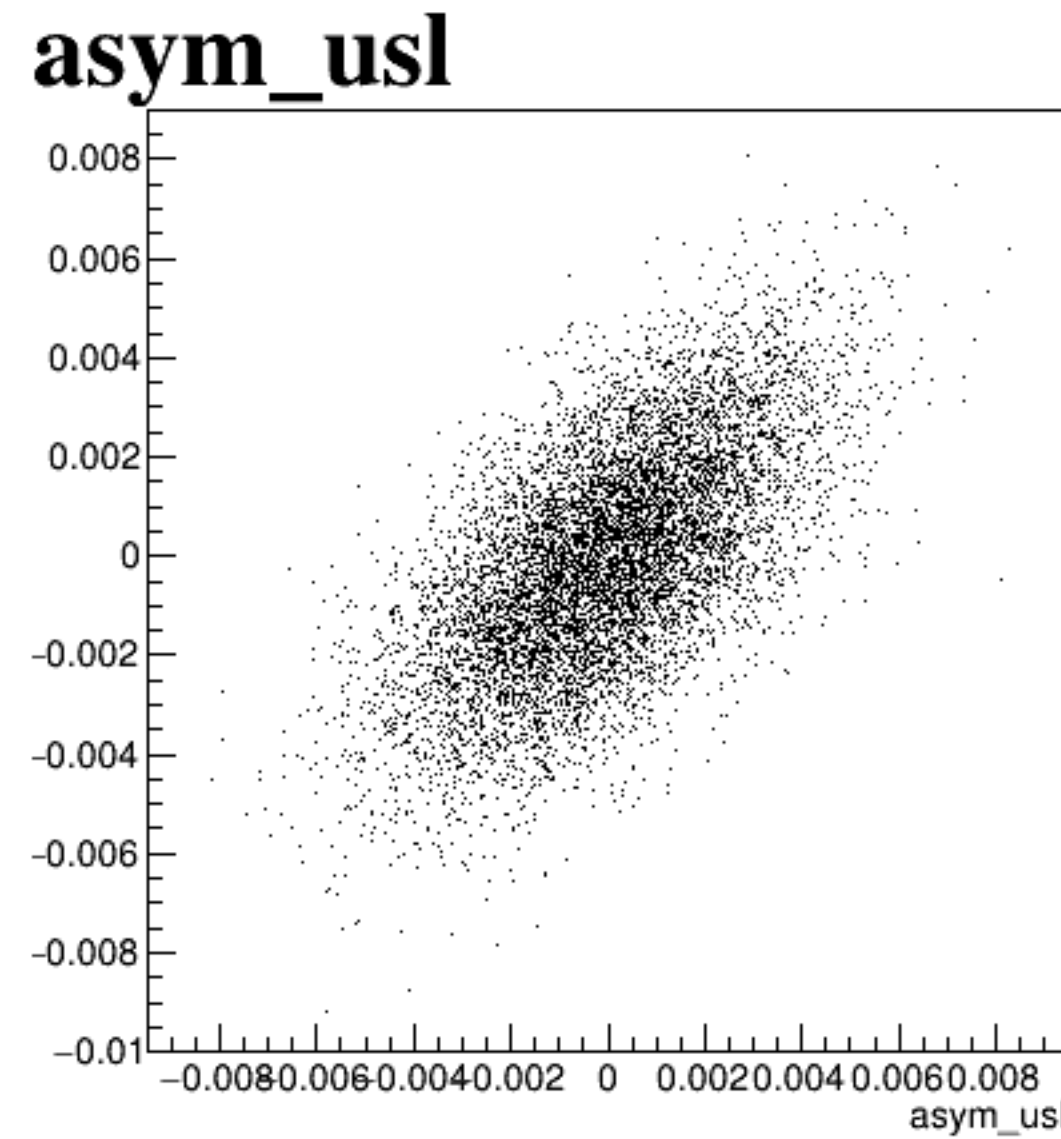
asym_usl



asym_dsl



asym_usr



asym_dsr

