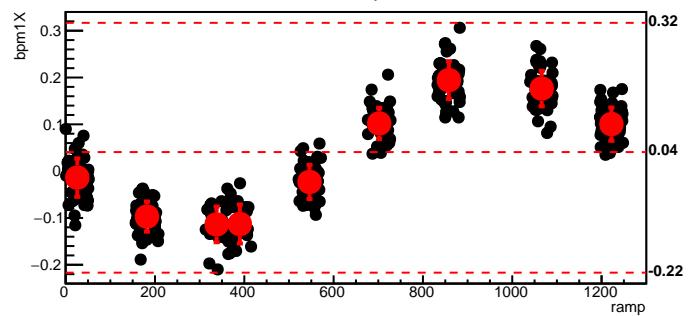
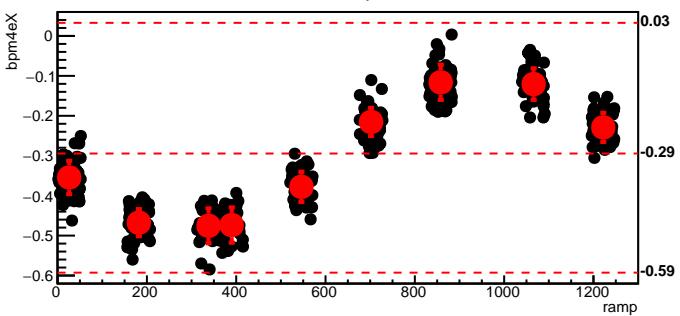


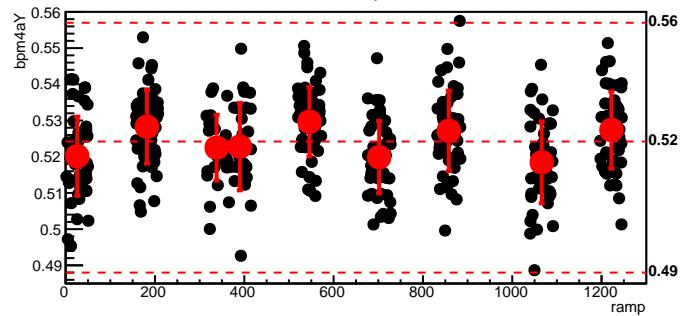
bmod Trim1,cyc=7008



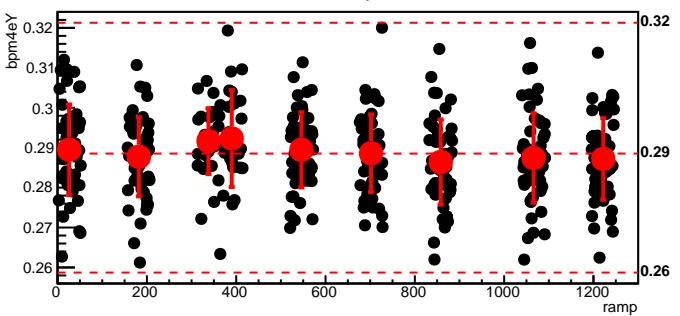
bmod Trim1,cyc=7008



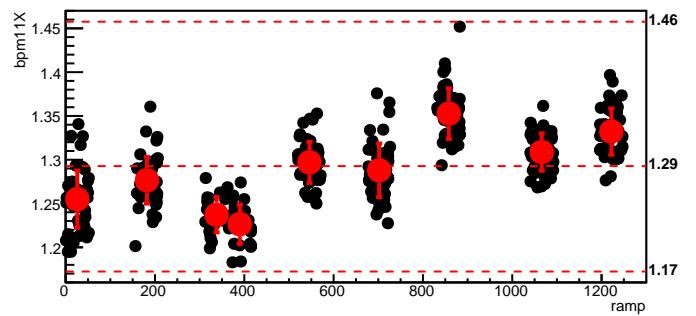
bmod Trim1,cyc=7008



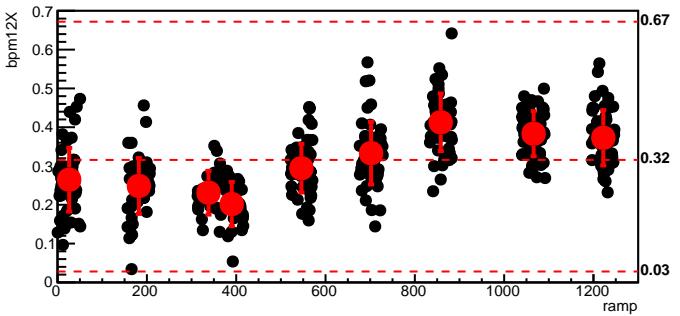
bmod Trim1,cyc=7008



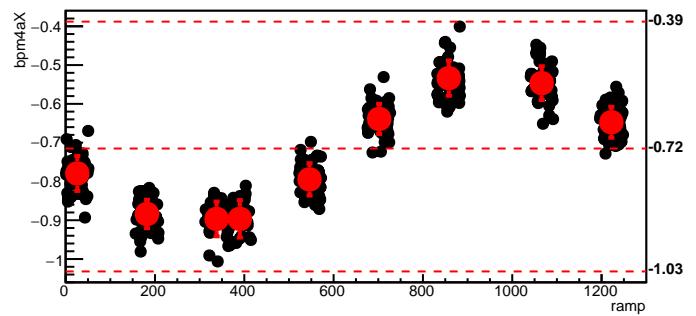
bmod Trim1,cyc=7008



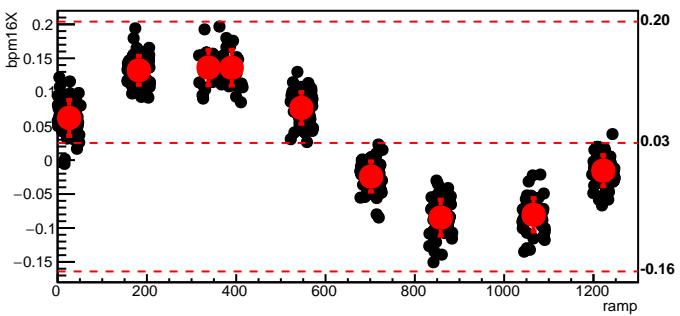
bmod Trim1,cyc=7008



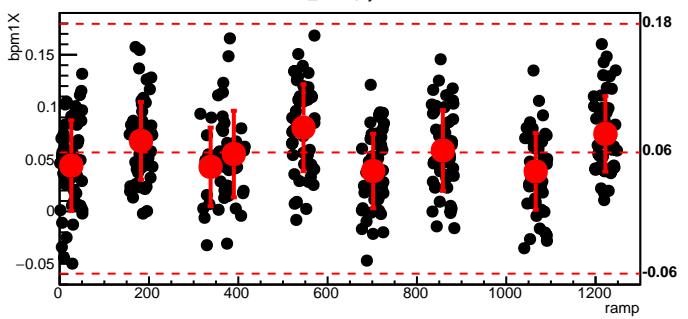
bmod Trim1,cyc=7008



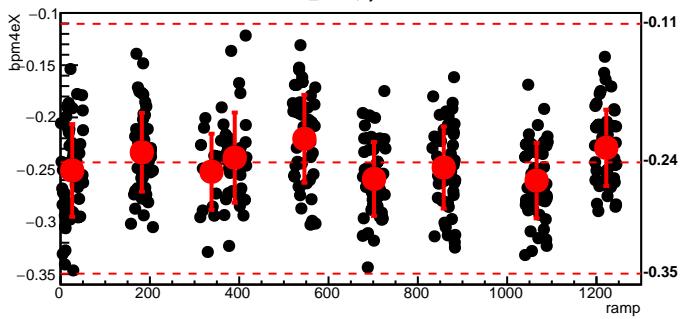
bmod Trim1,cyc=7008



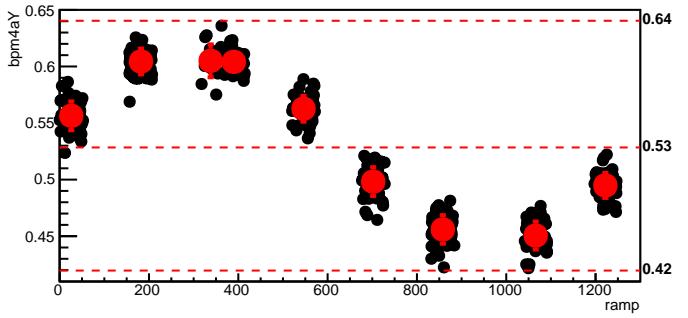
bmod Trim2,cyc=7008



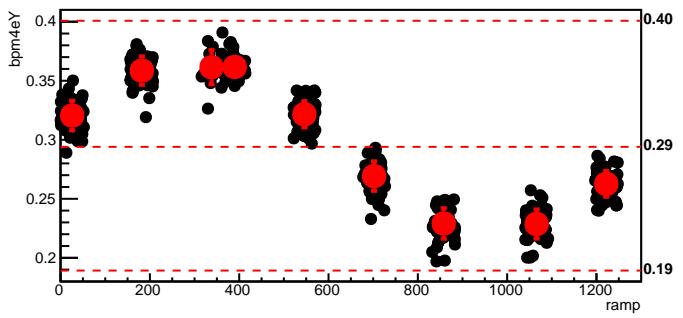
bmod Trim2,cyc=7008



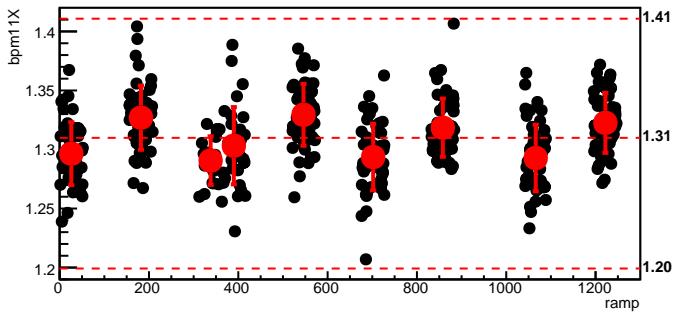
bmod Trim2,cyc=7008



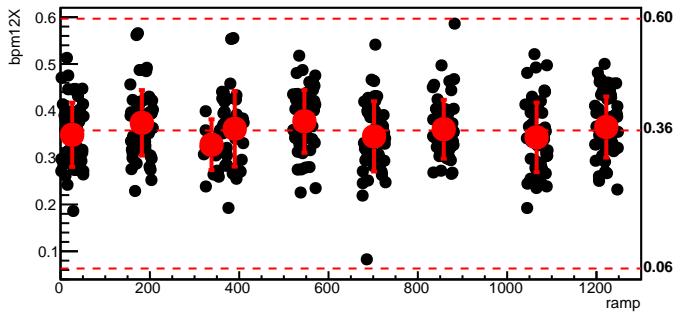
bmod Trim2,cyc=7008



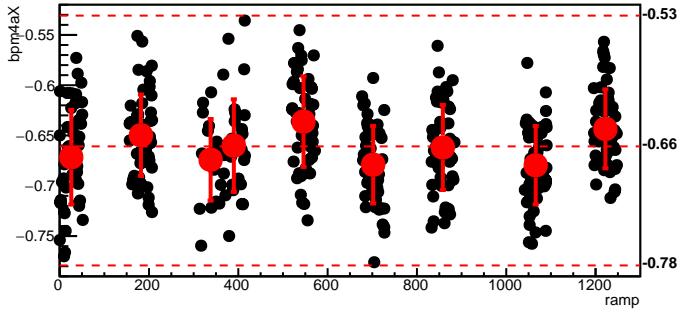
bmod Trim2,cyc=7008



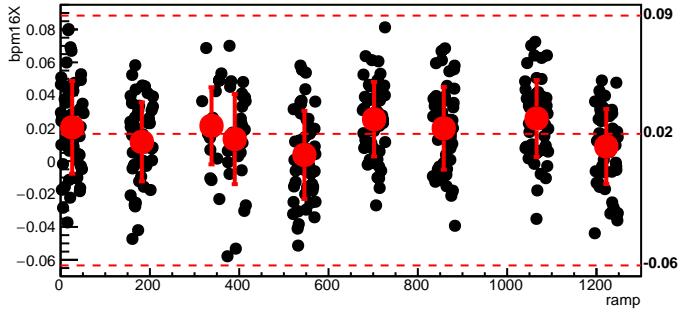
bmod Trim2,cyc=7008



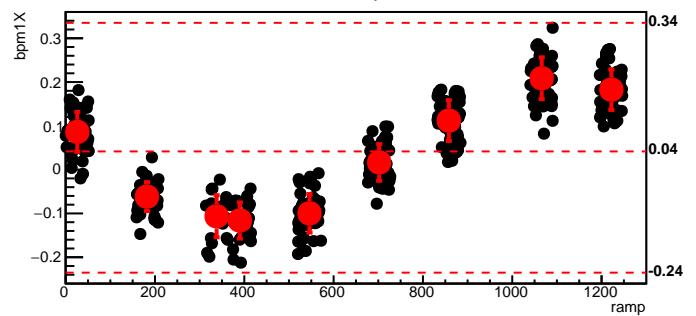
bmod Trim2,cyc=7008



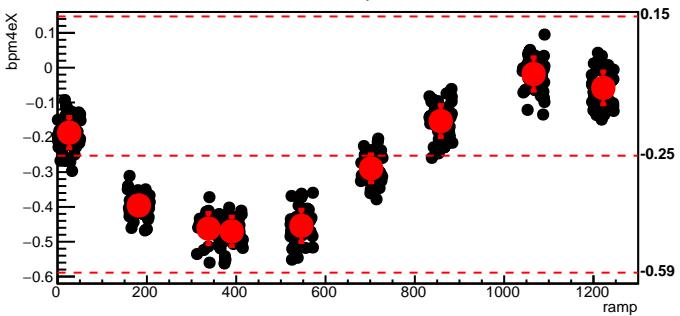
bmod Trim2,cyc=7008



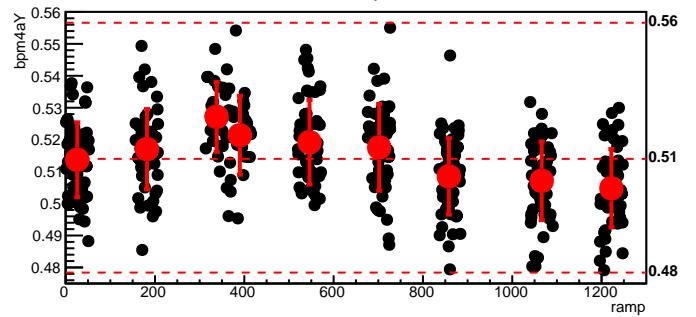
bmod Trim3,cyc=7008



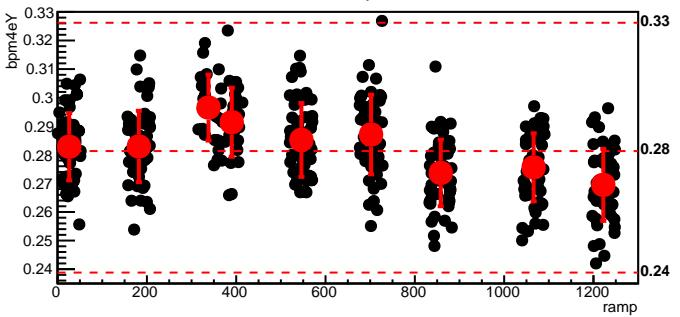
bmod Trim3,cyc=7008



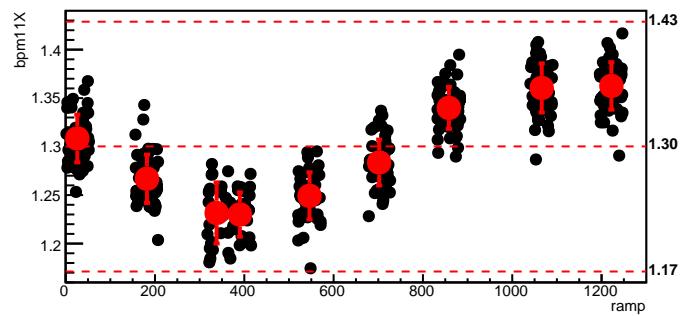
bmod Trim3,cyc=7008



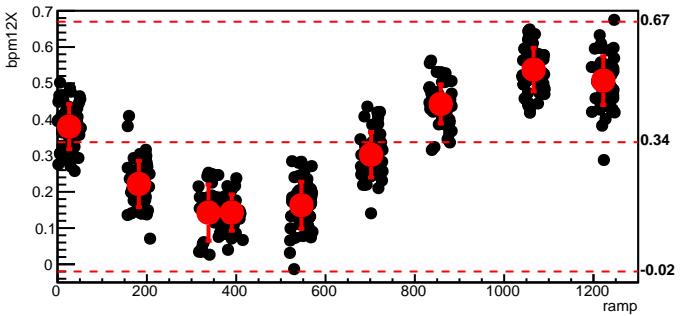
bmod Trim3,cyc=7008



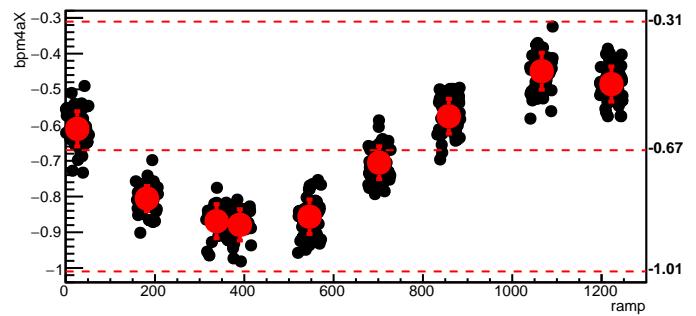
bmod Trim3,cyc=7008



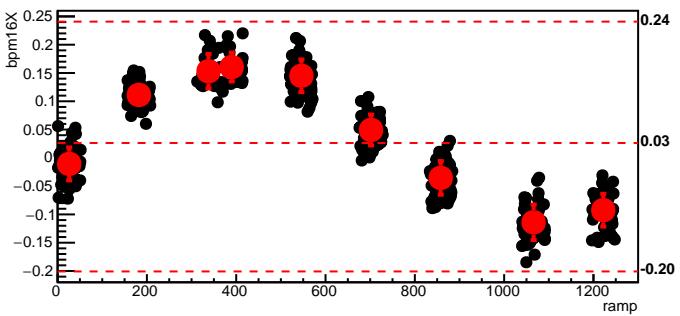
bmod Trim3,cyc=7008



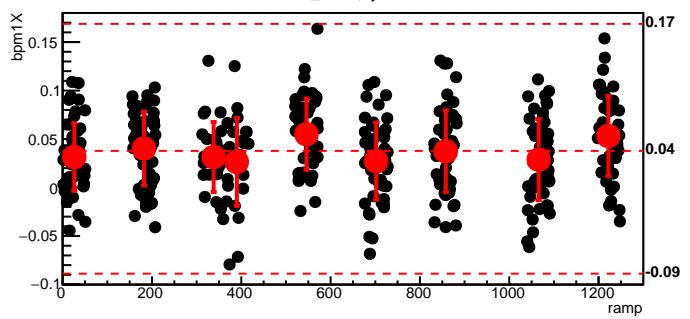
bmod Trim3,cyc=7008



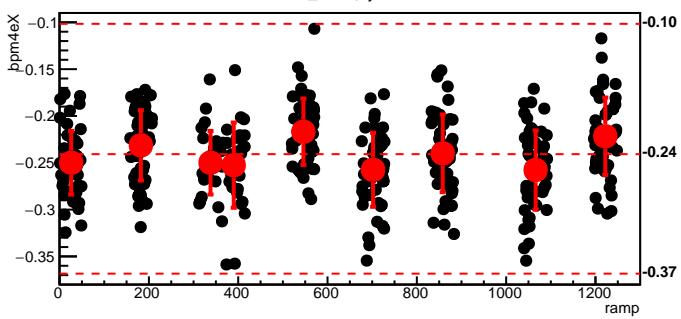
bmod Trim3,cyc=7008



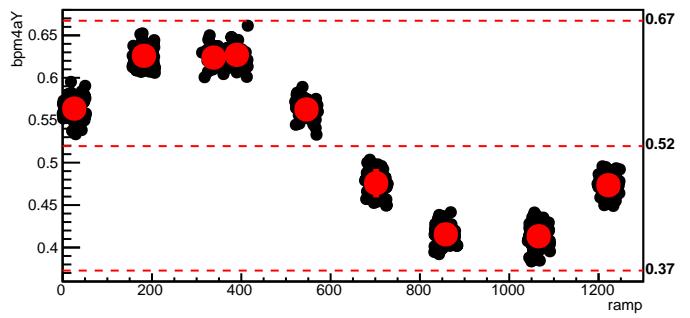
bmod Trim4,cyc=7008



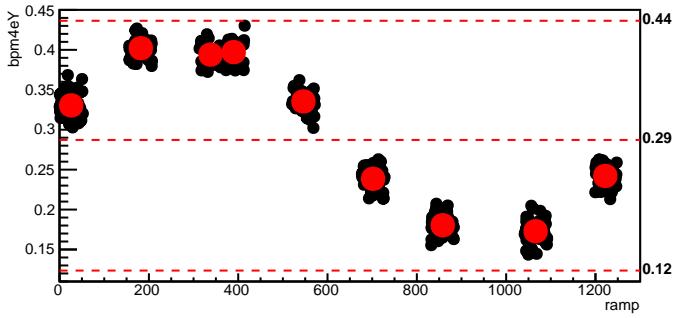
bmod Trim4,cyc=7008



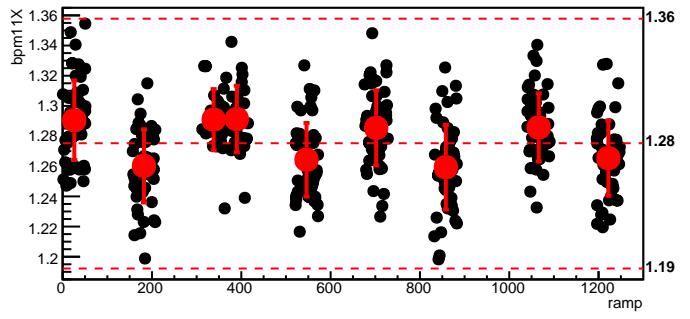
bmod Trim4,cyc=7008



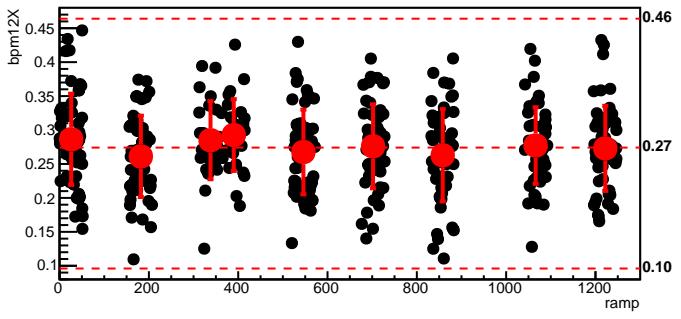
bmod Trim4,cyc=7008



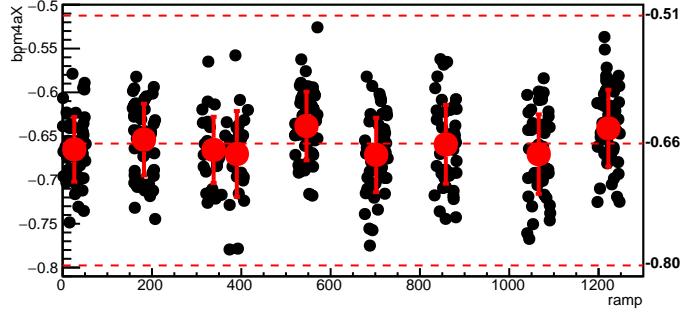
bmod Trim4,cyc=7008



bmod Trim4,cyc=7008



bmod Trim4,cyc=7008



bmod Trim4,cyc=7008

