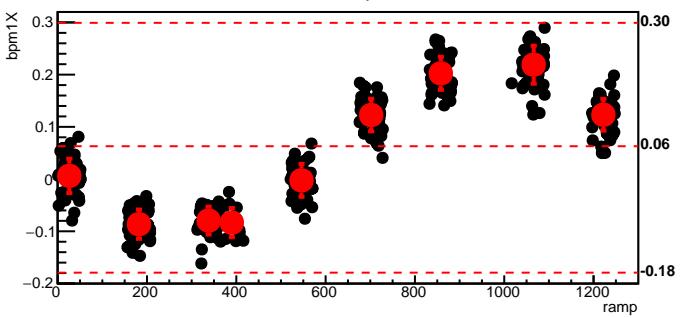
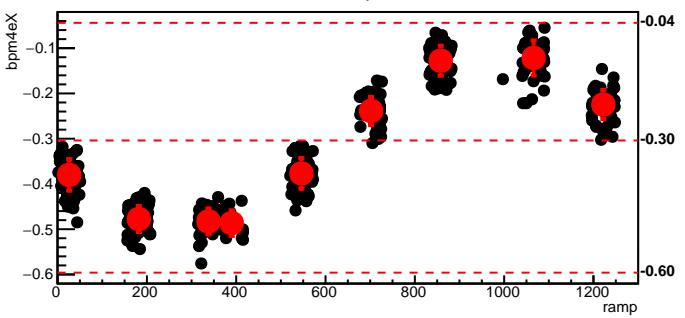


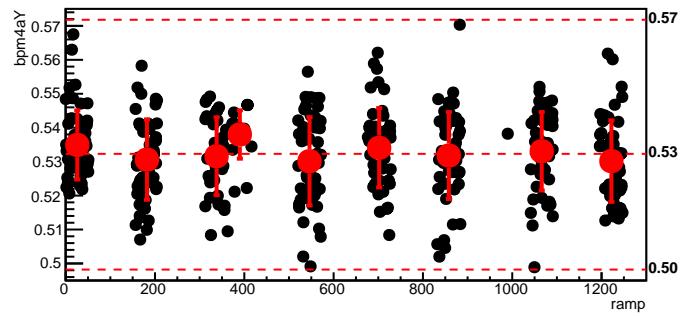
bmod Trim1,cyc=7106



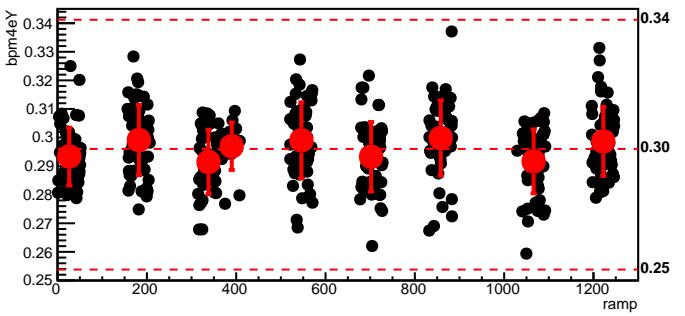
bmod Trim1,cyc=7106



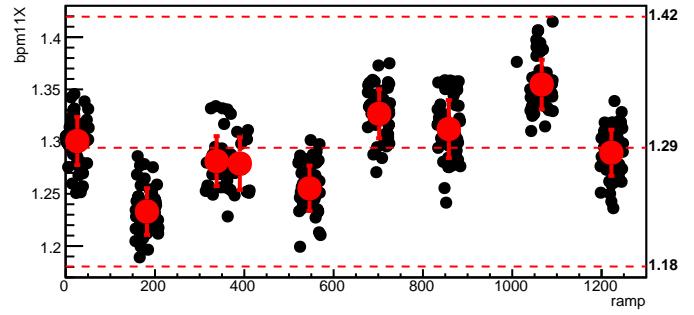
bmod Trim1,cyc=7106



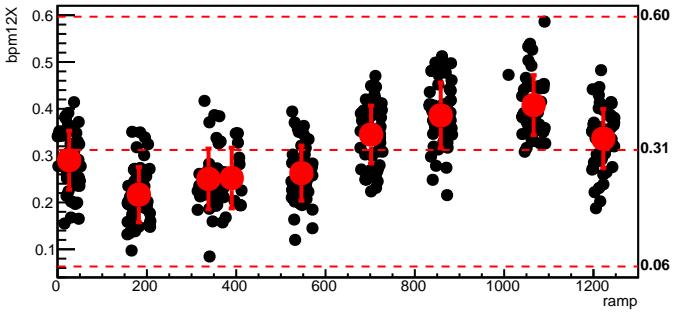
bmod Trim1,cyc=7106



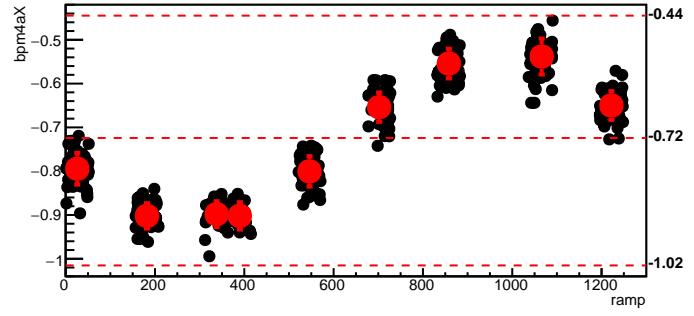
bmod Trim1,cyc=7106



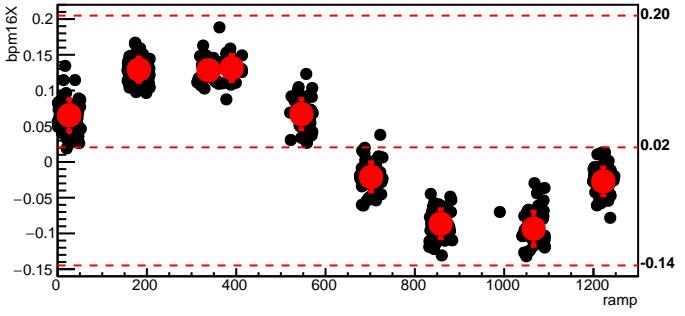
bmod Trim1,cyc=7106



bmod Trim1,cyc=7106



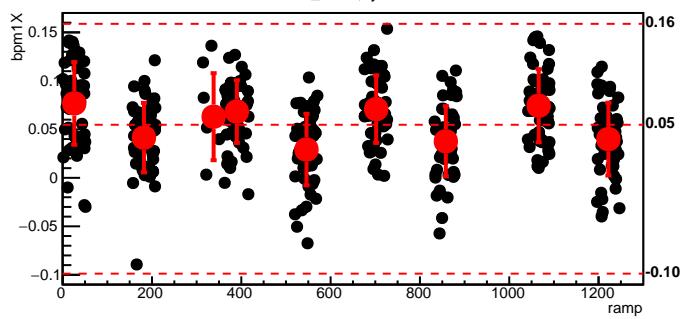
bmod Trim1,cyc=7106



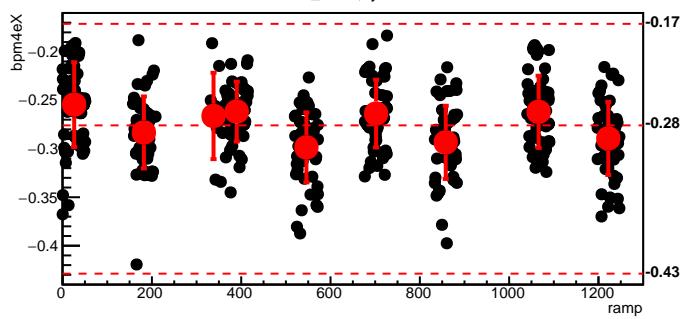




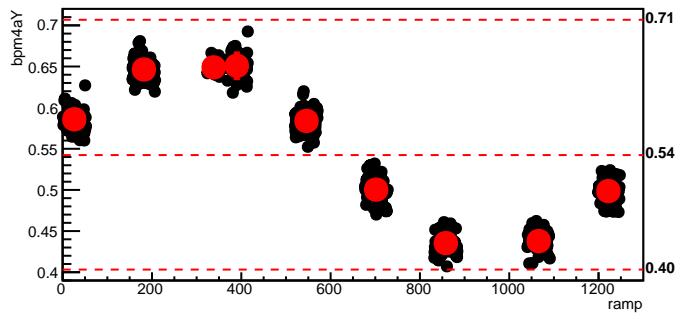
bmod Trim4,cyc=7106



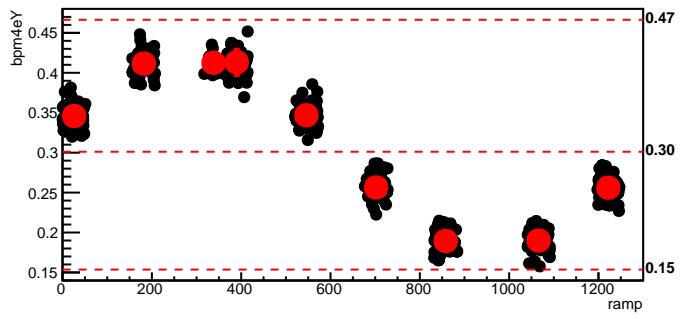
bmod Trim4,cyc=7106



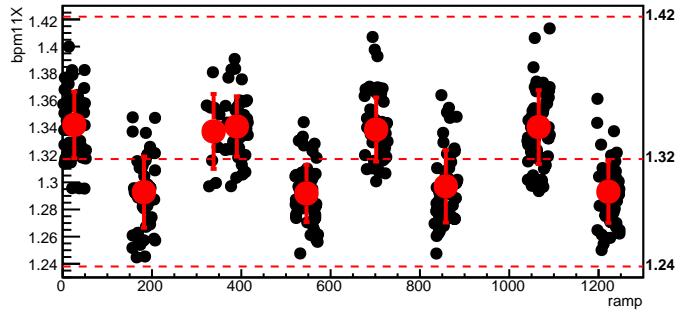
bmod Trim4,cyc=7106



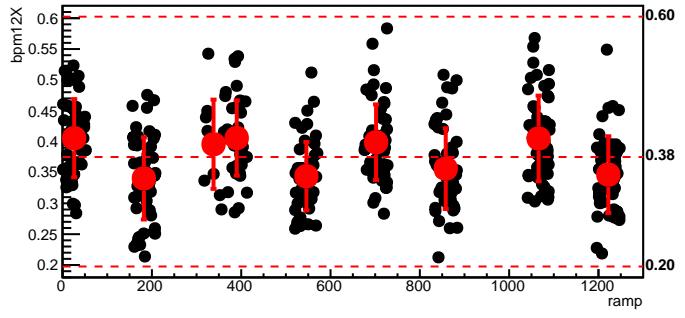
bmod Trim4,cyc=7106



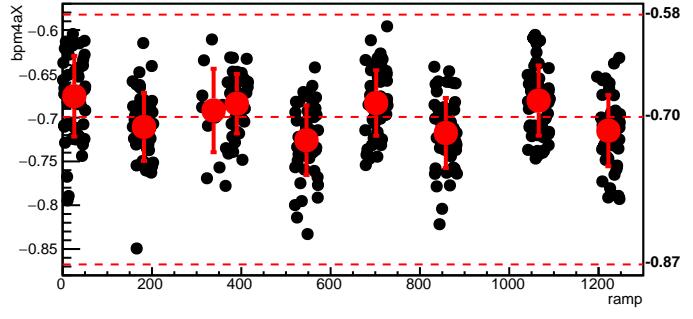
bmod Trim4,cyc=7106



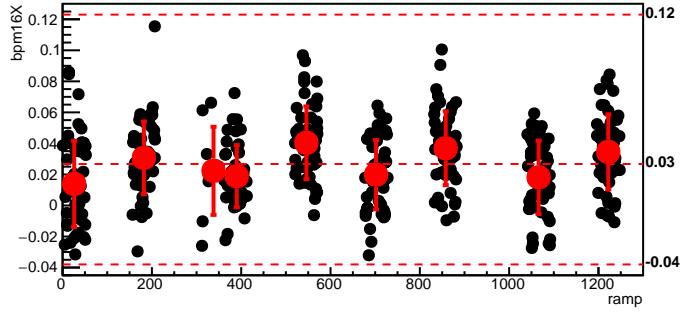
bmod Trim4,cyc=7106



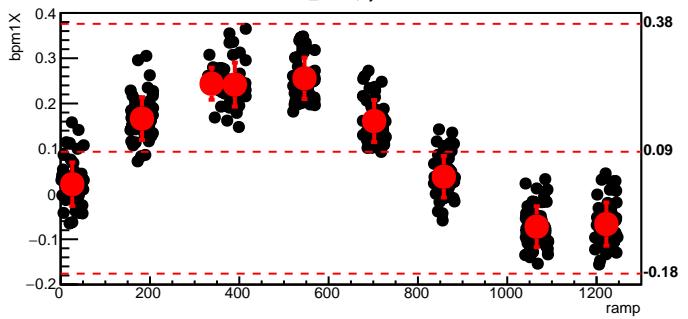
bmod Trim4,cyc=7106



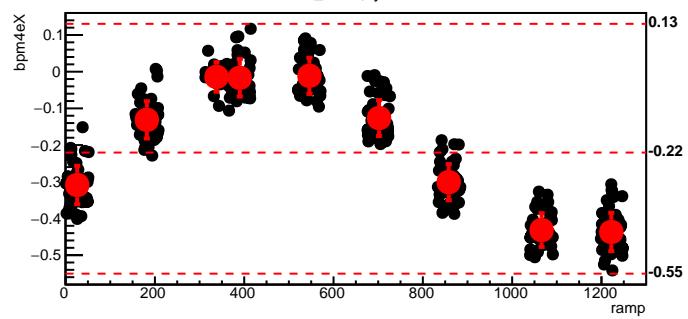
bmod Trim4,cyc=7106



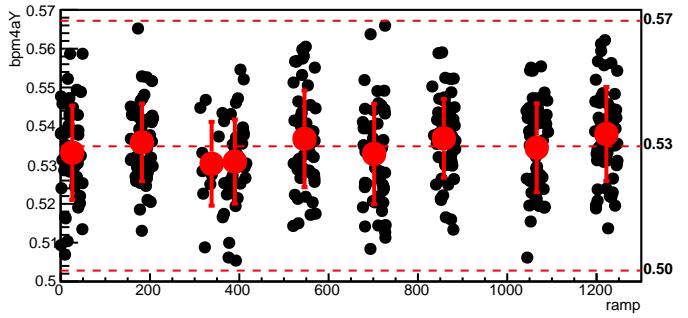
bmod Trim5,cyc=7106



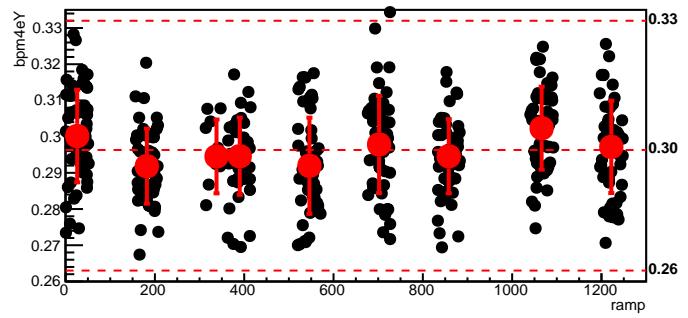
bmod Trim5,cyc=7106



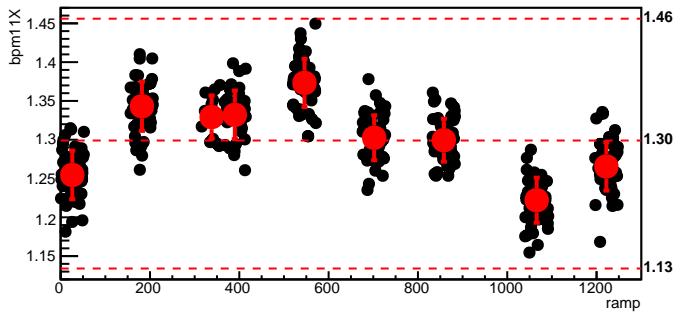
bmod Trim5,cyc=7106



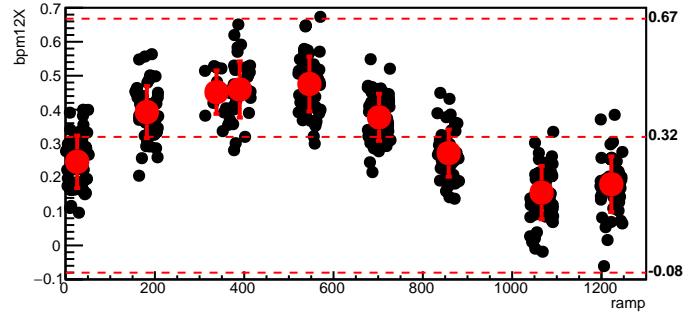
bmod Trim5,cyc=7106



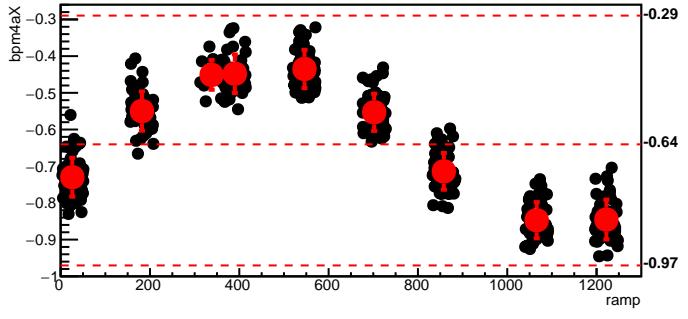
bmod Trim5,cyc=7106



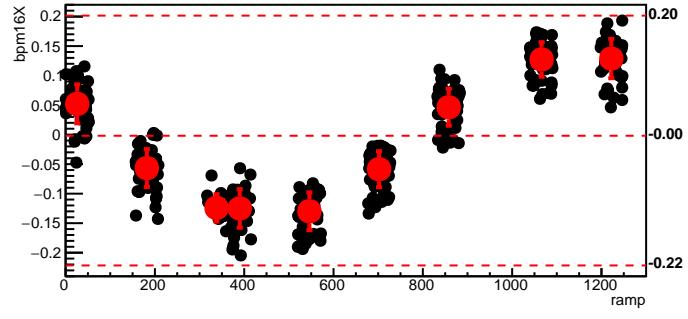
bmod Trim5,cyc=7106



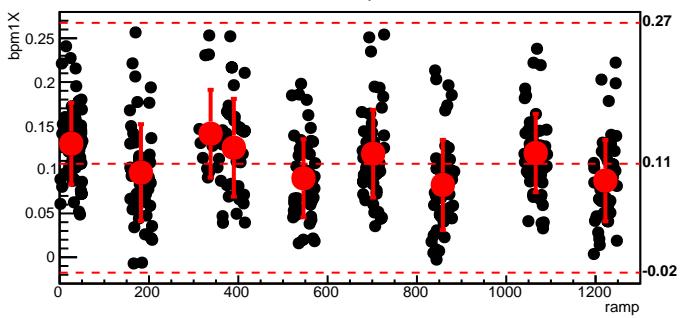
bmod Trim5,cyc=7106



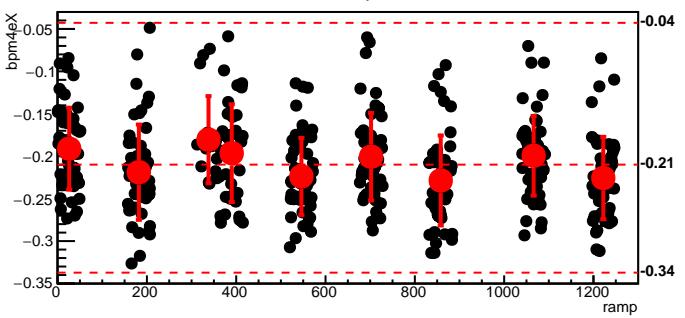
bmod Trim5,cyc=7106



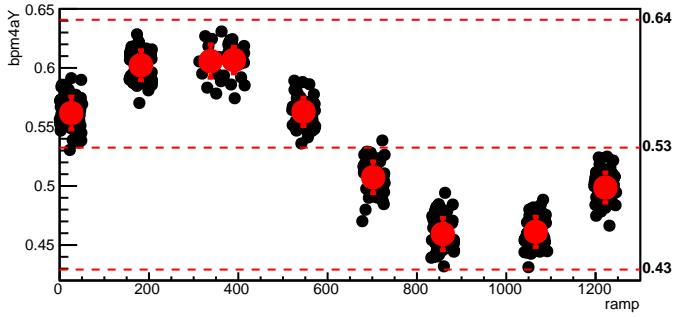
bmod Trim6,cyc=7106



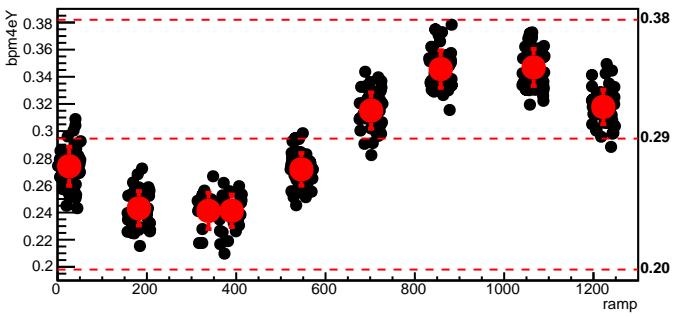
bmod Trim6,cyc=7106



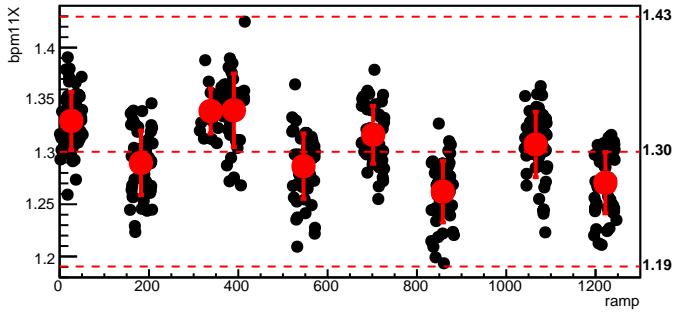
bmod Trim6,cyc=7106



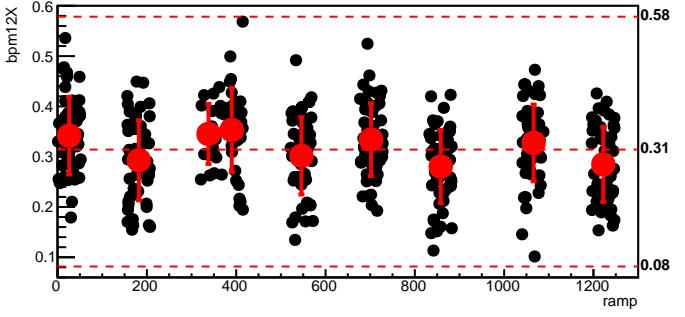
bmod Trim6,cyc=7106



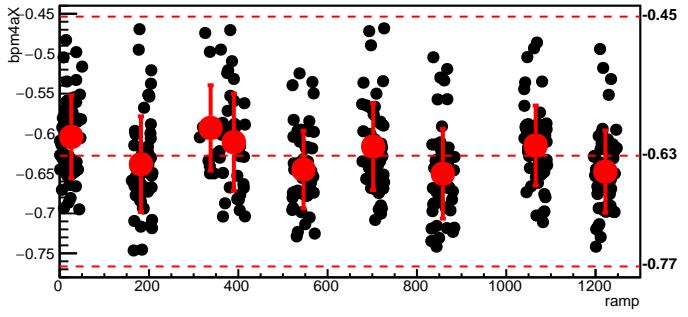
bmod Trim6,cyc=7106



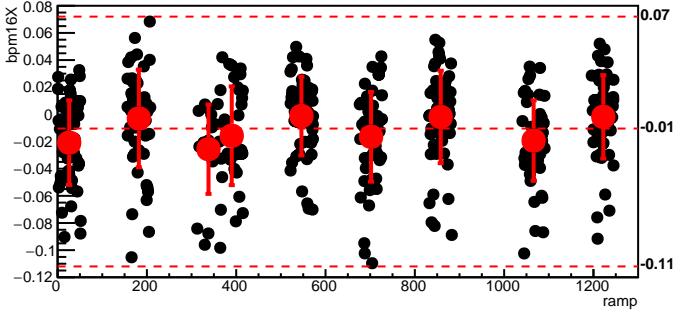
bmod Trim6,cyc=7106



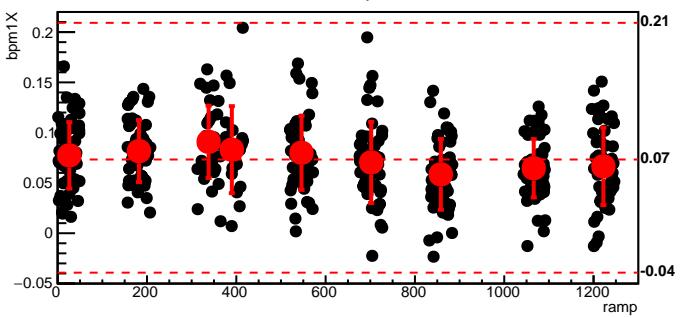
bmod Trim6,cyc=7106



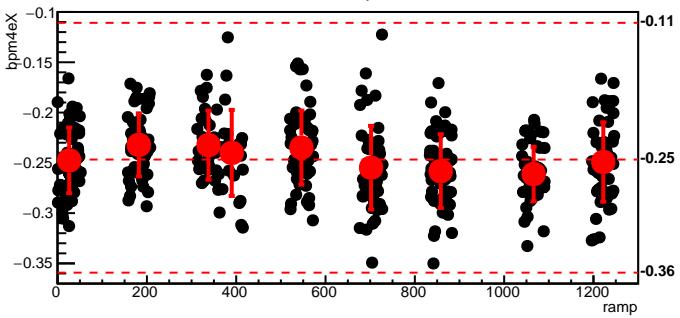
bmod Trim6,cyc=7106



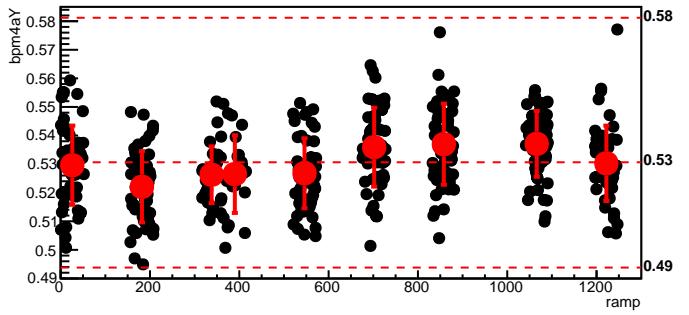
bmod Trim7,cyc=7106



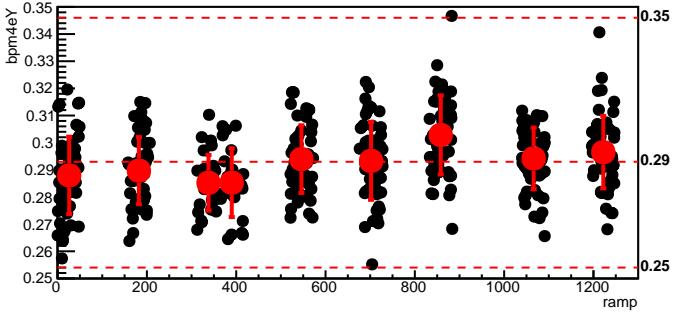
bmod Trim7,cyc=7106



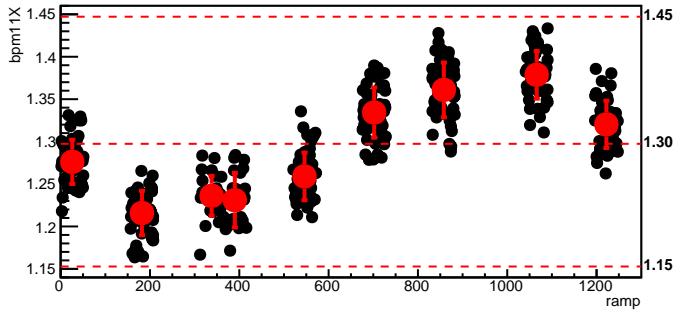
bmod Trim7,cyc=7106



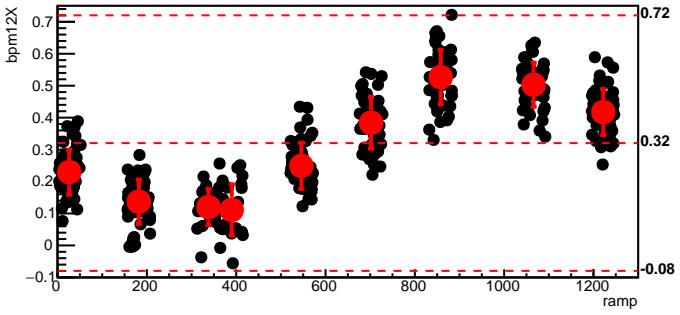
bmod Trim7,cyc=7106



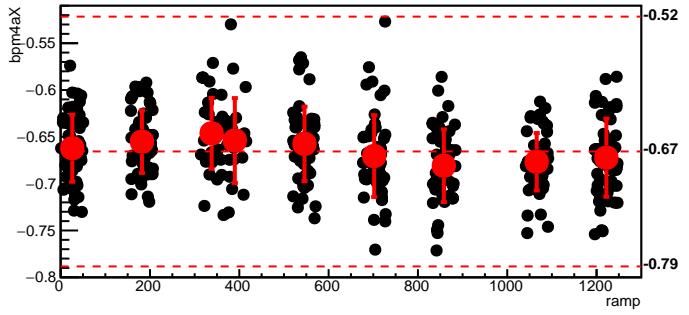
bmod Trim7,cyc=7106



bmod Trim7,cyc=7106



bmod Trim7,cyc=7106



bmod Trim7,cyc=7106

