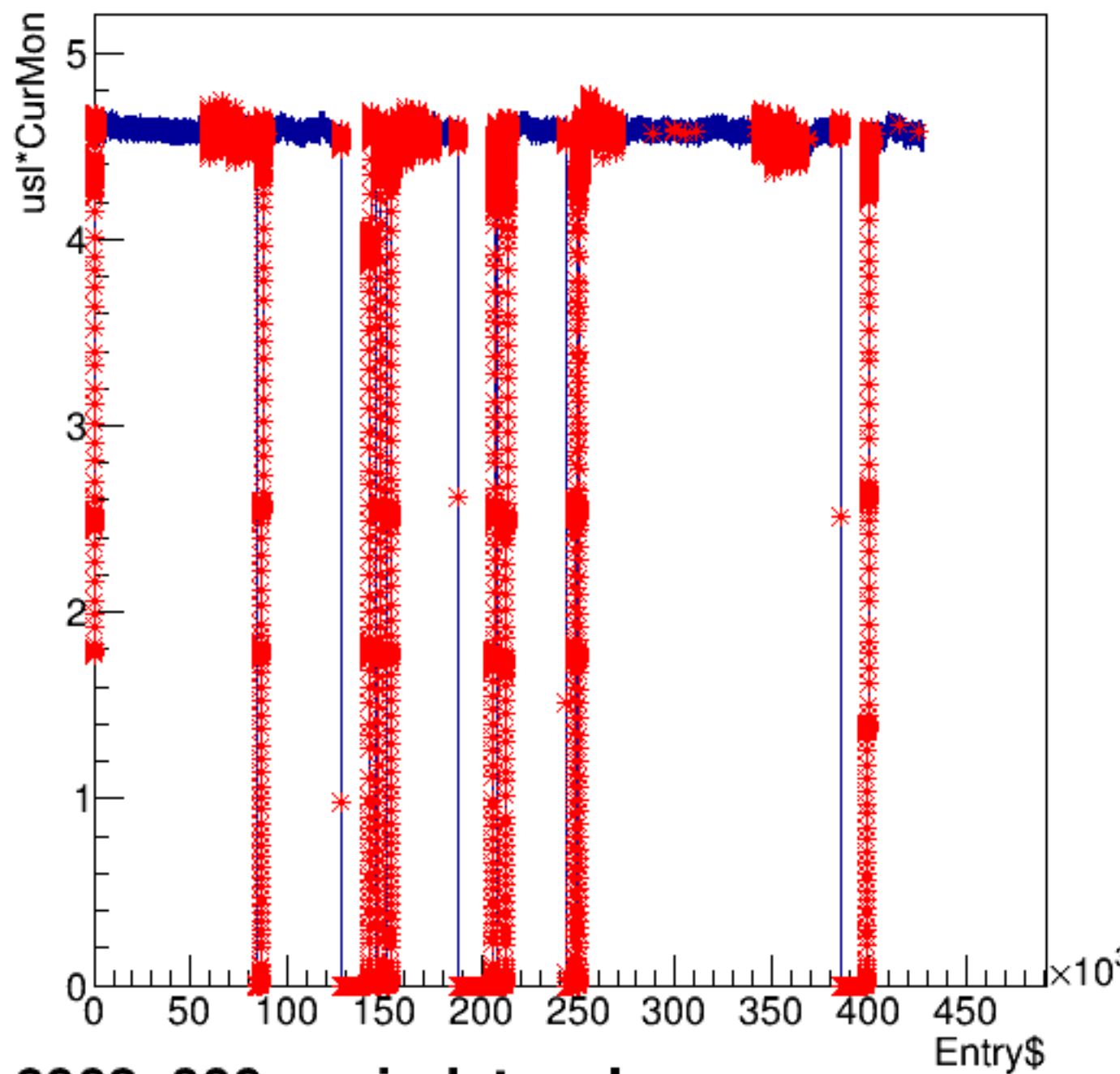
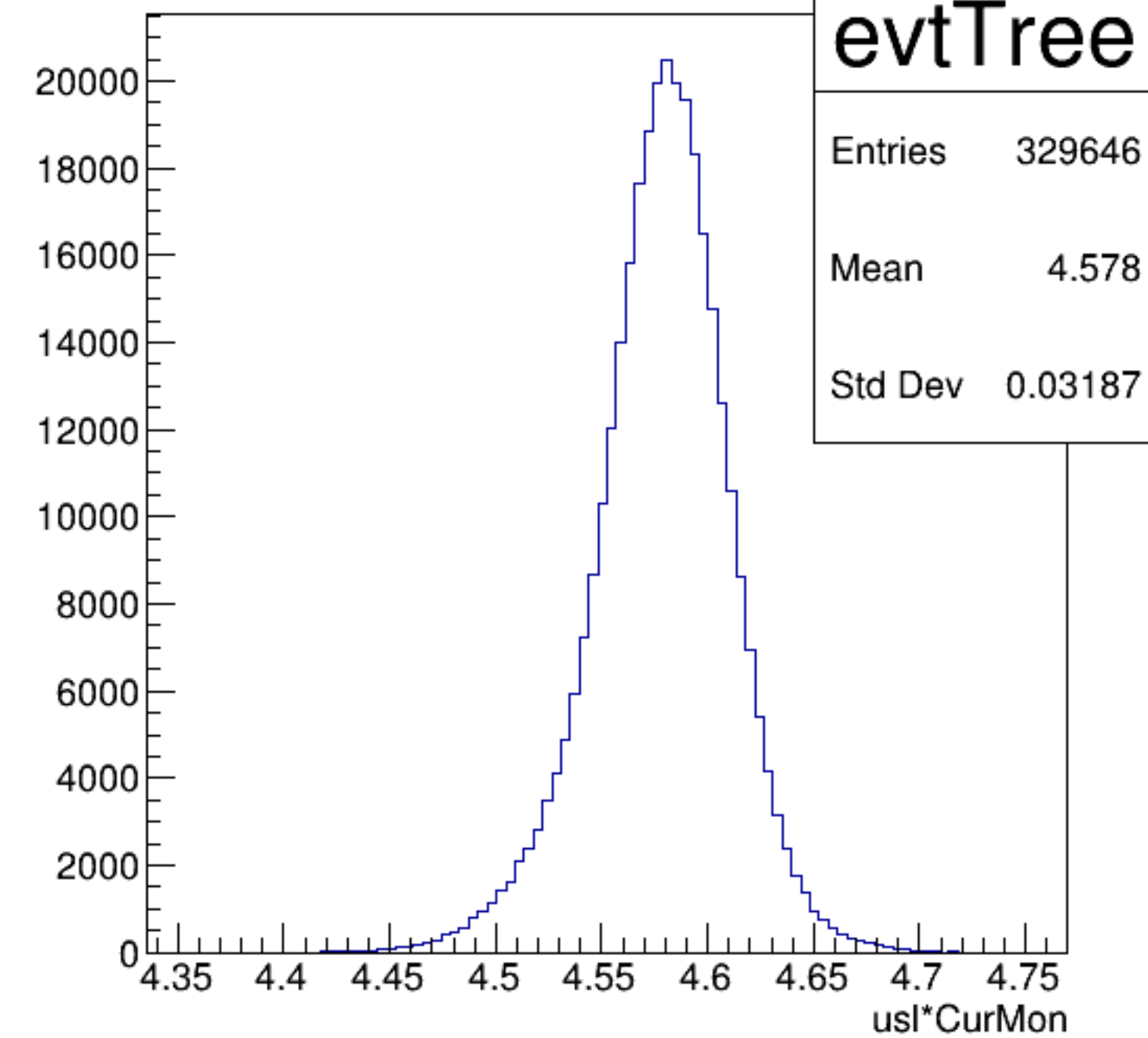


usl*CurMon:Entry\$

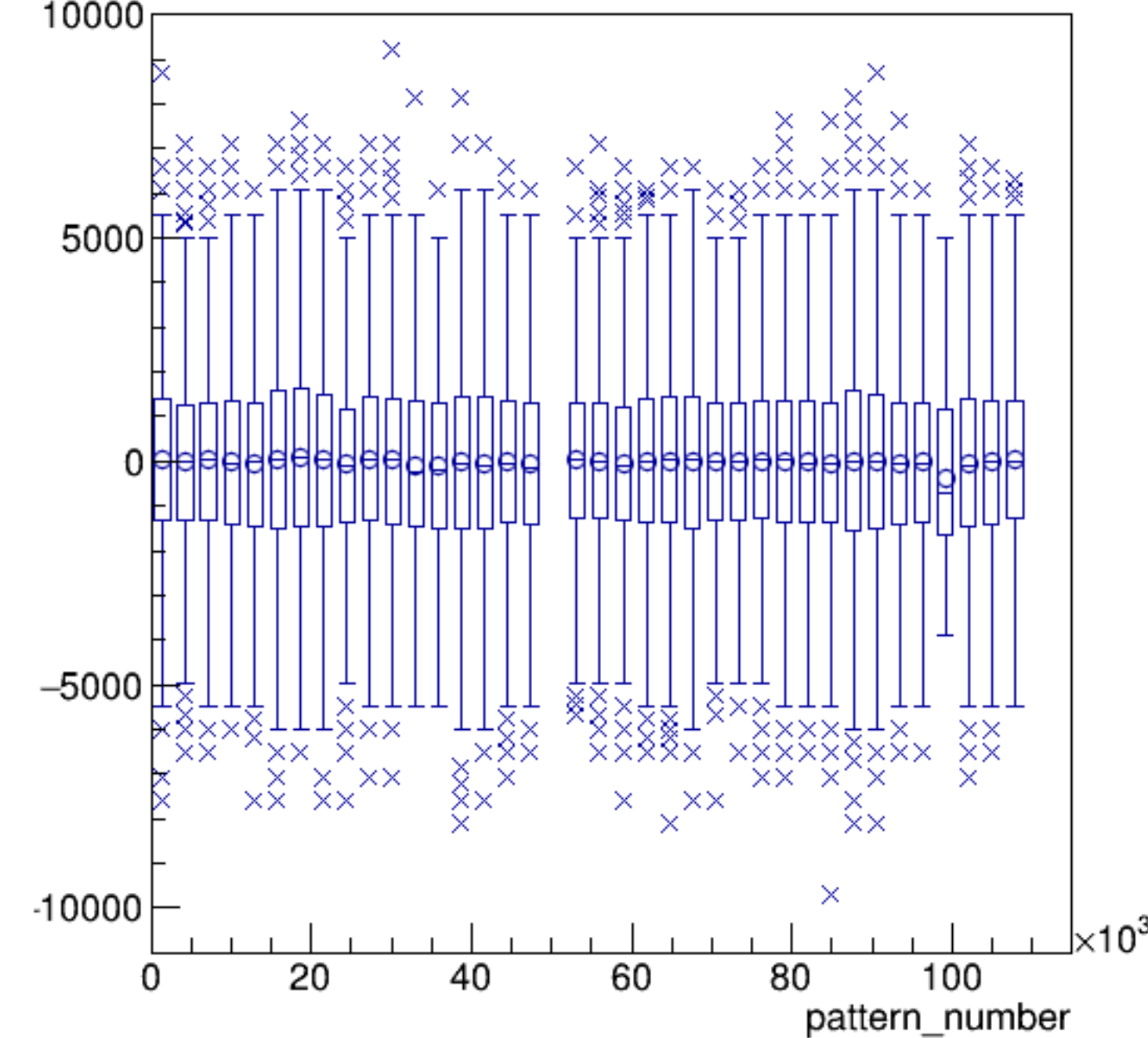


run6962_000_maindet_usl.png

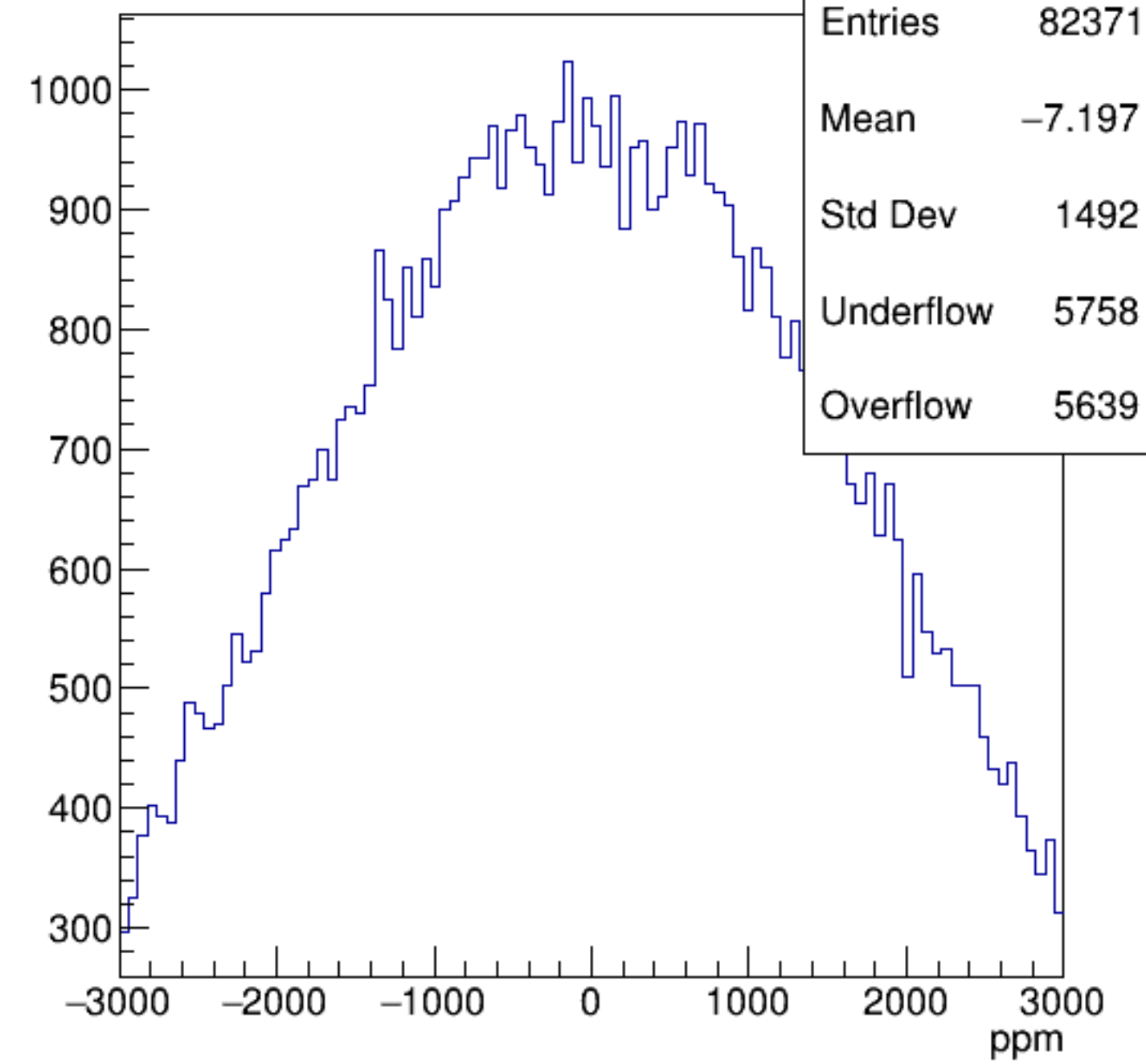
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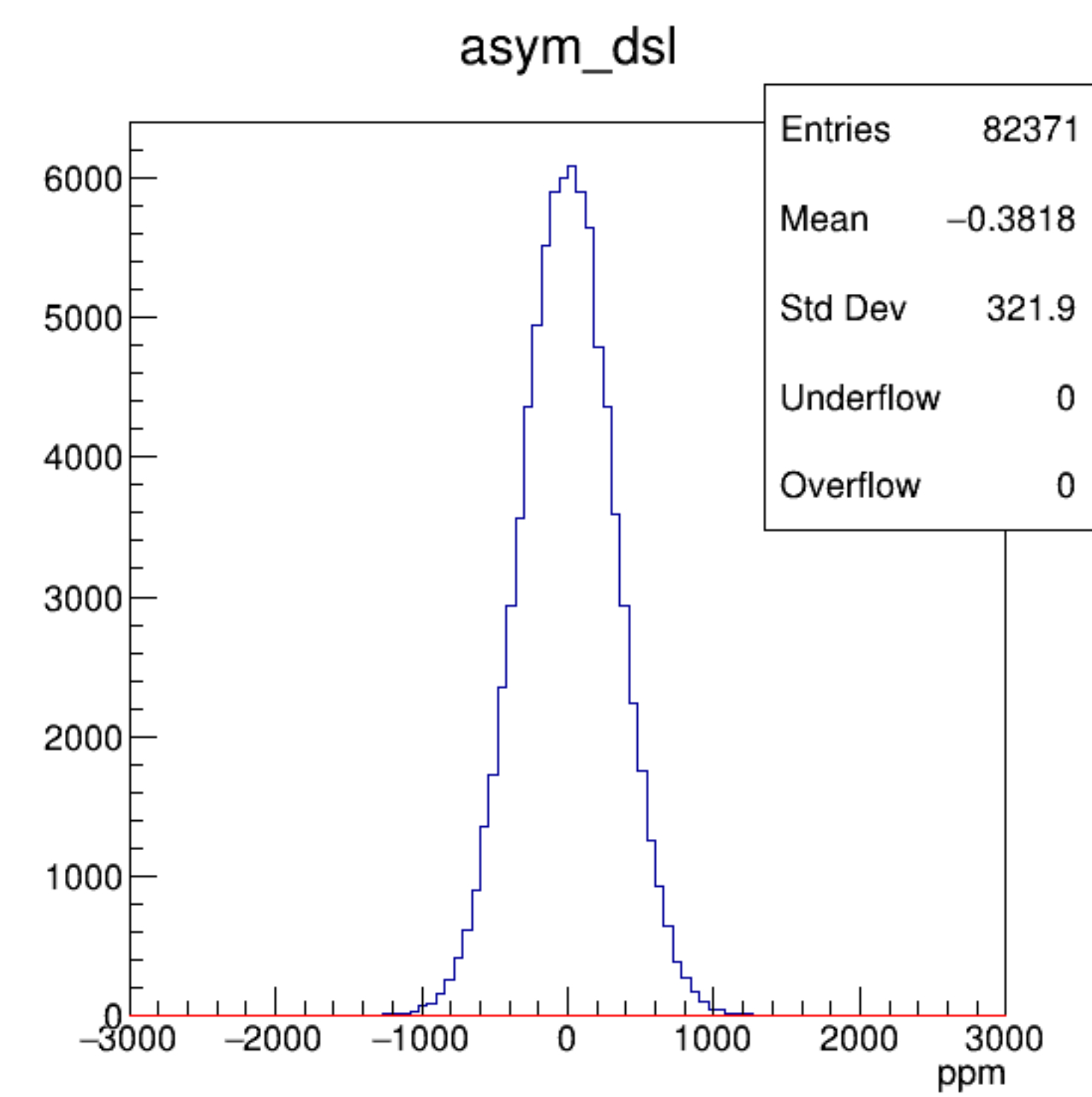
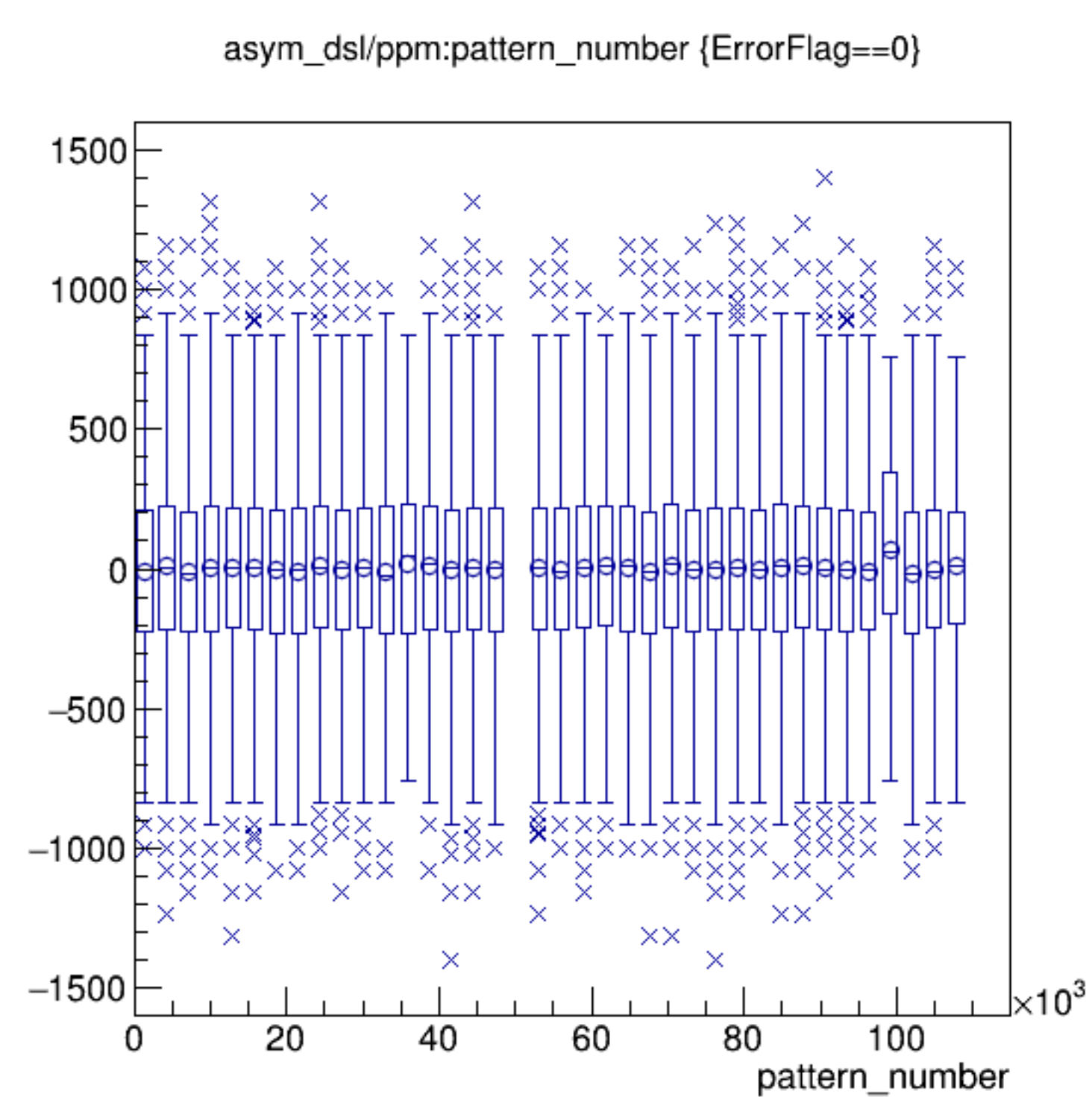
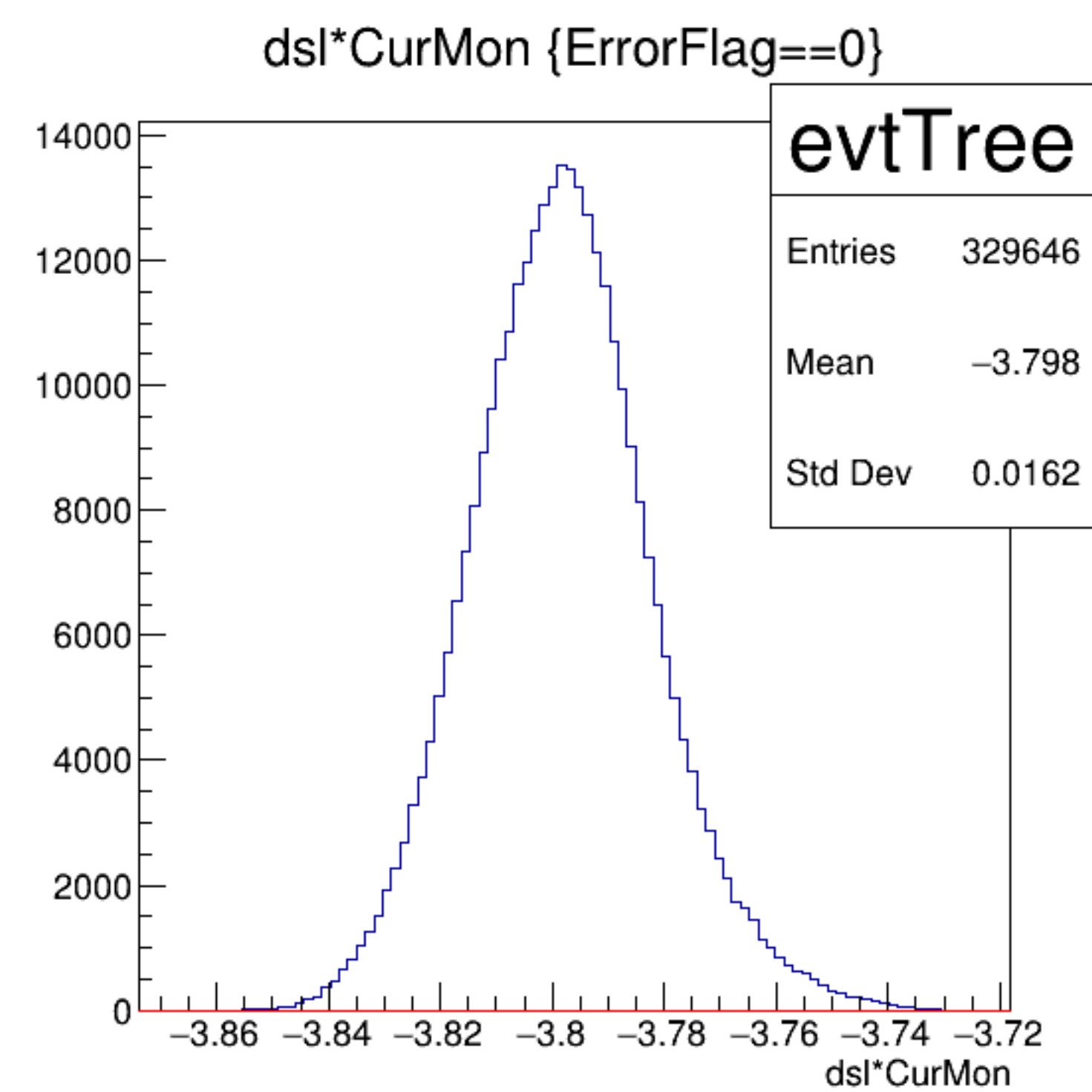
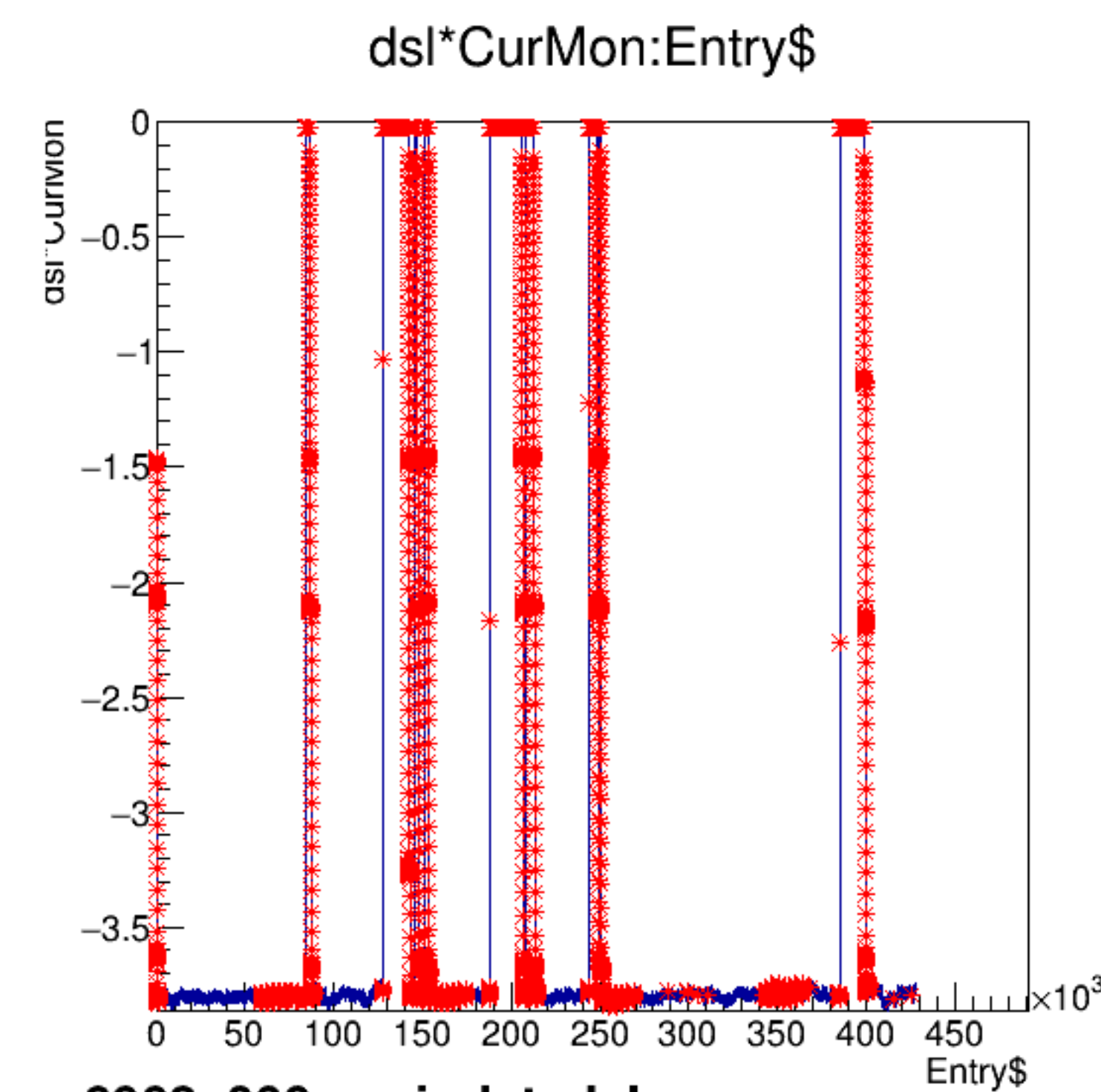


asym_usl/ppm:pattern_number {ErrorFlag==0}

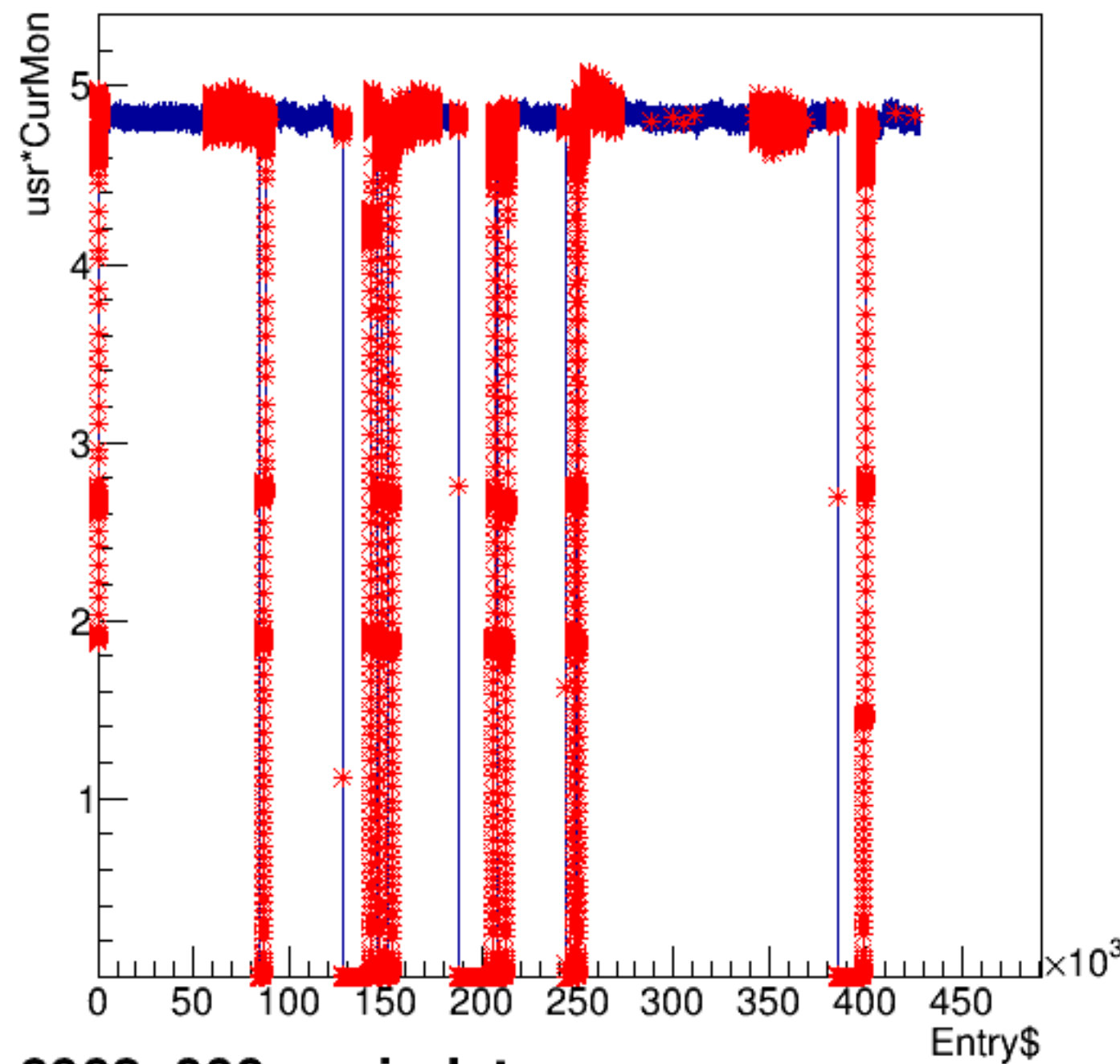


asym_usl



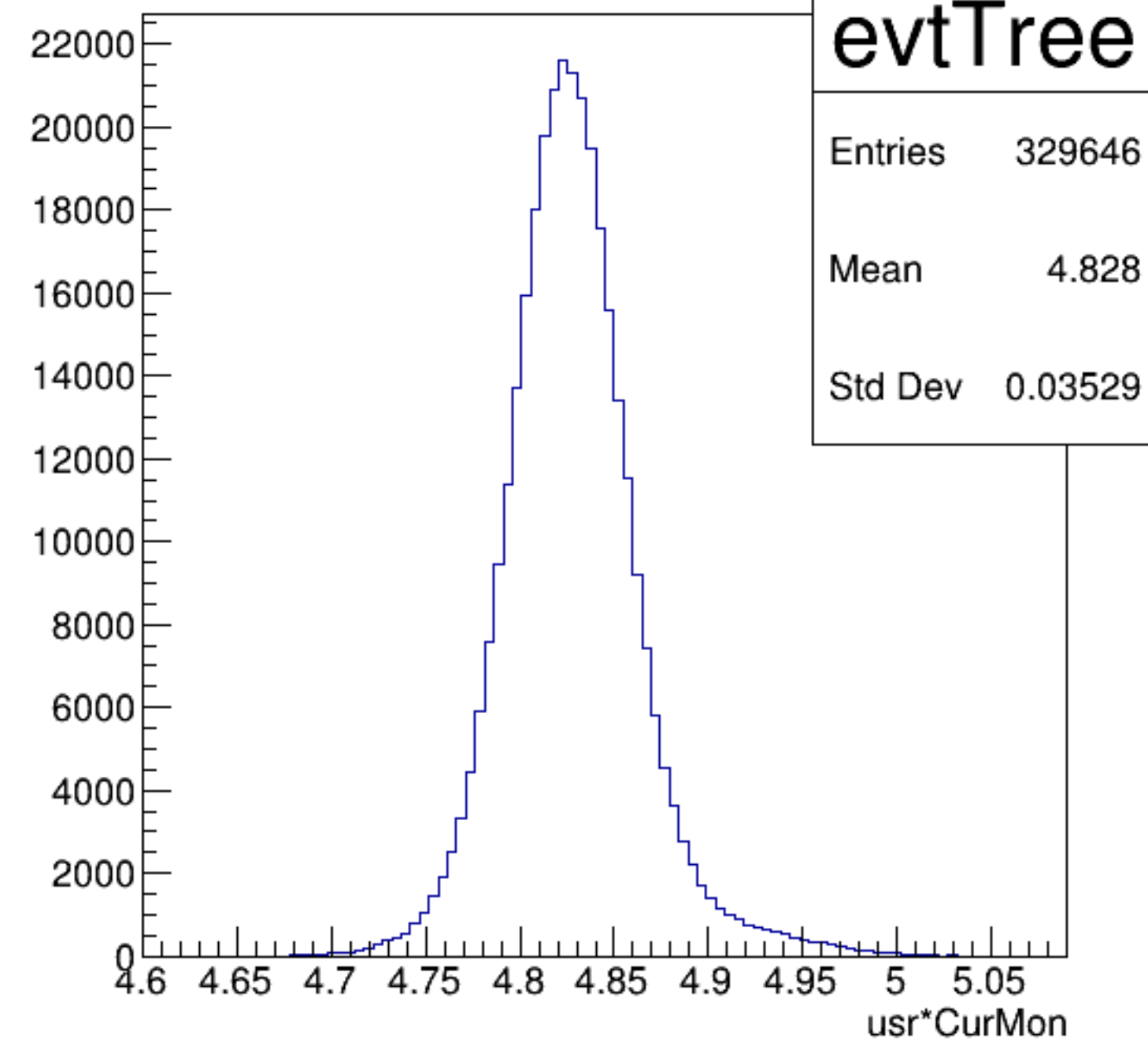


usr*CurMon:Entry\$

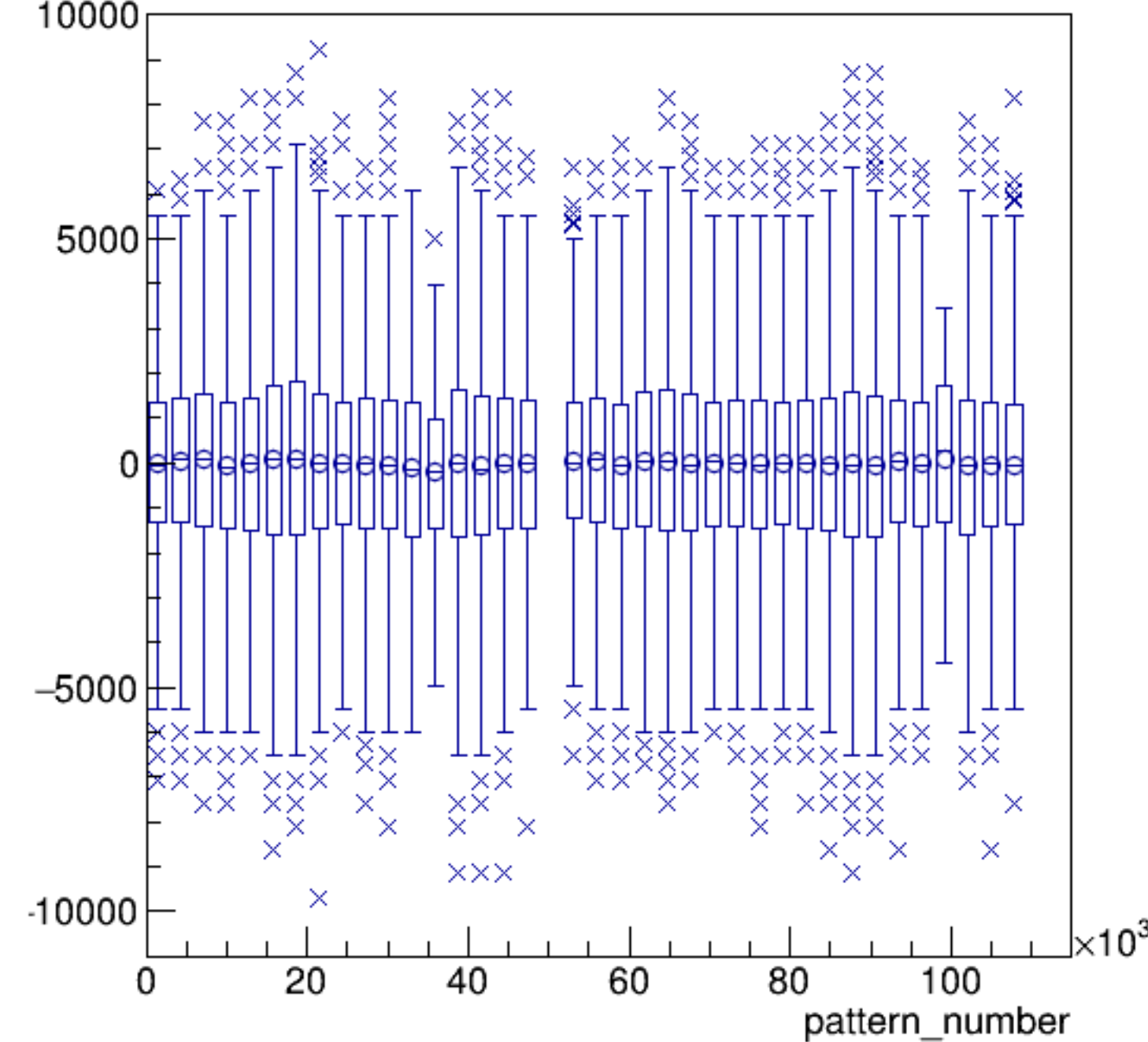


run6962_000_maindet_usr.png

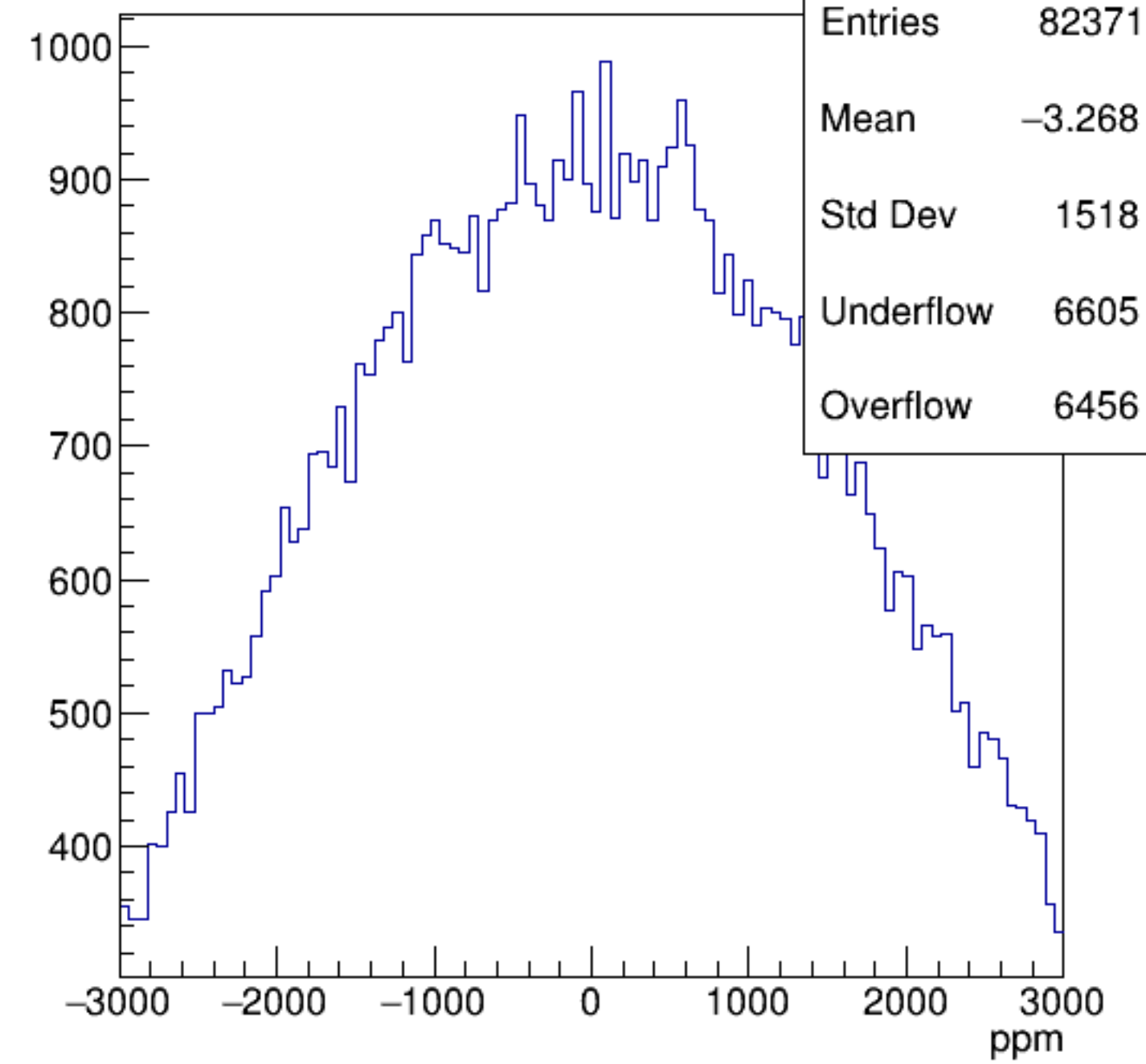
usr*CurMon {ErrorFlag==0}

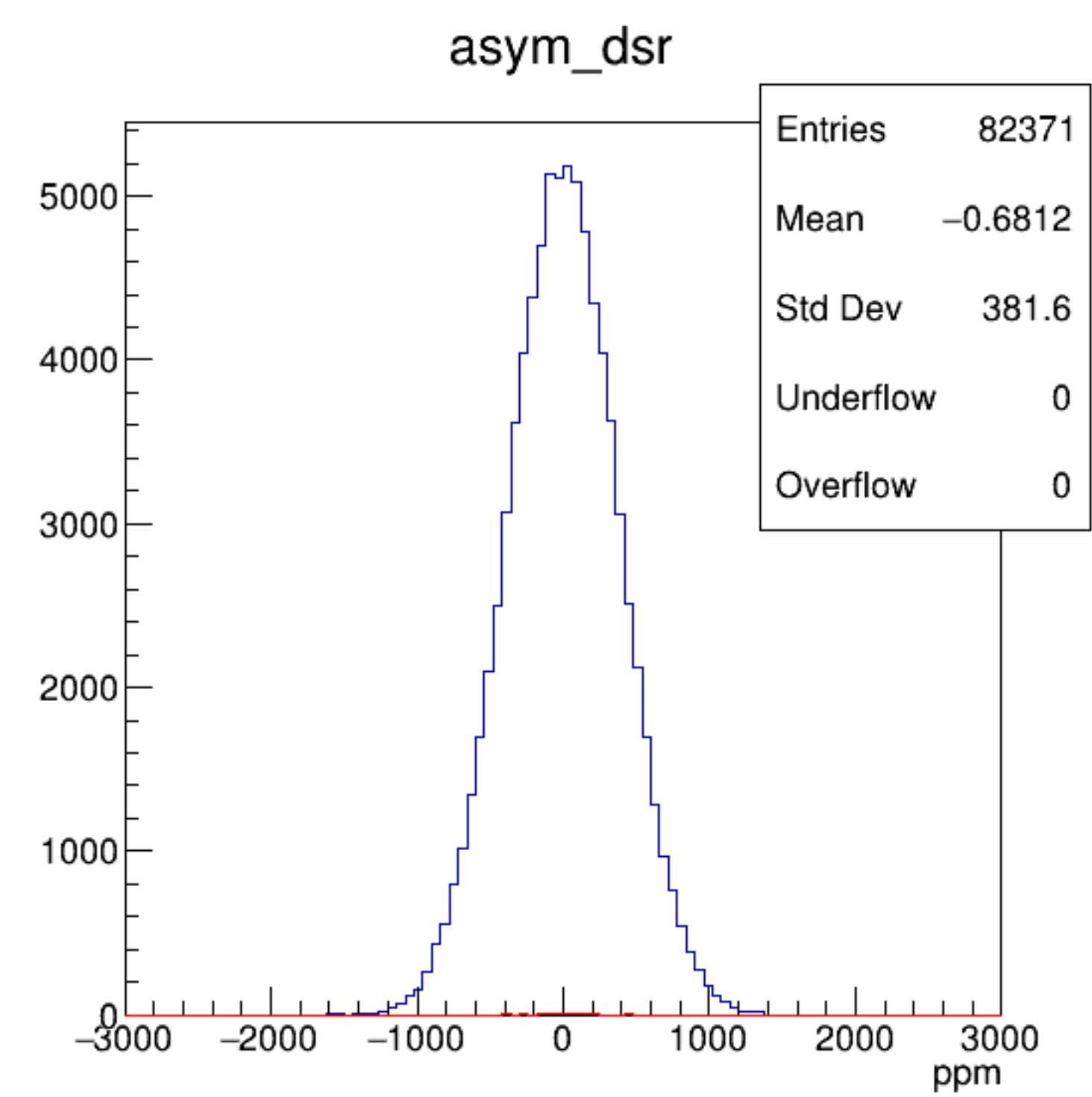
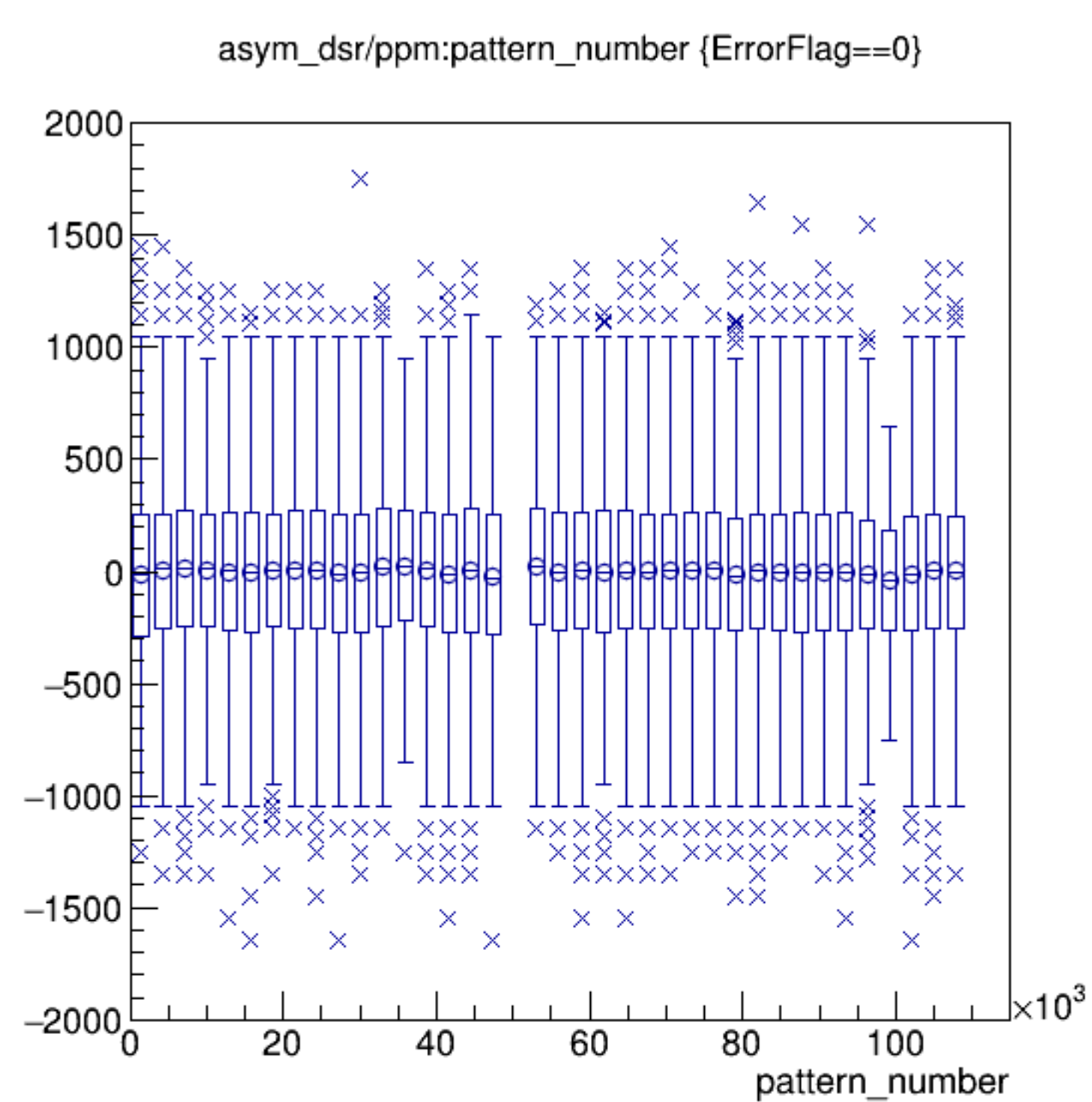
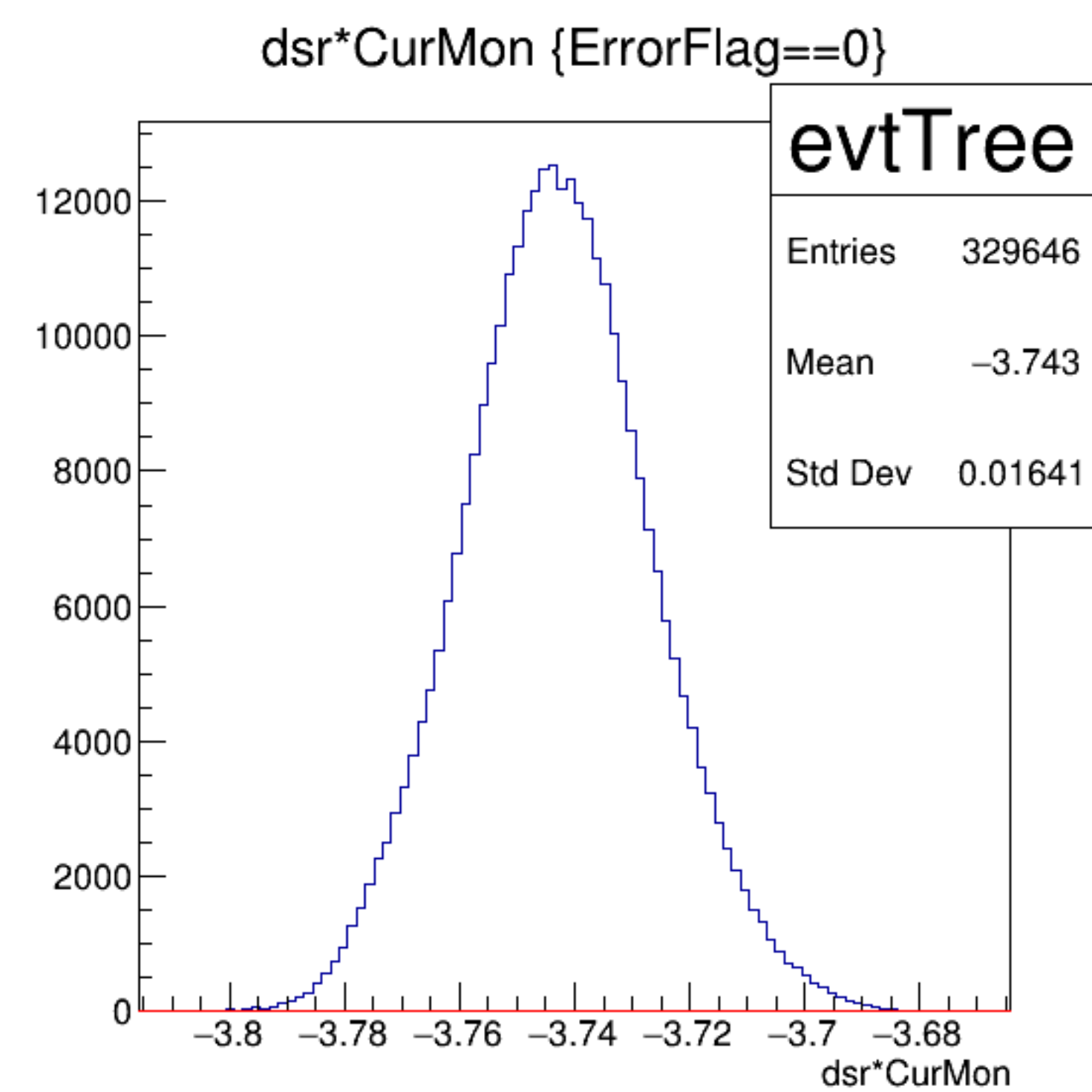
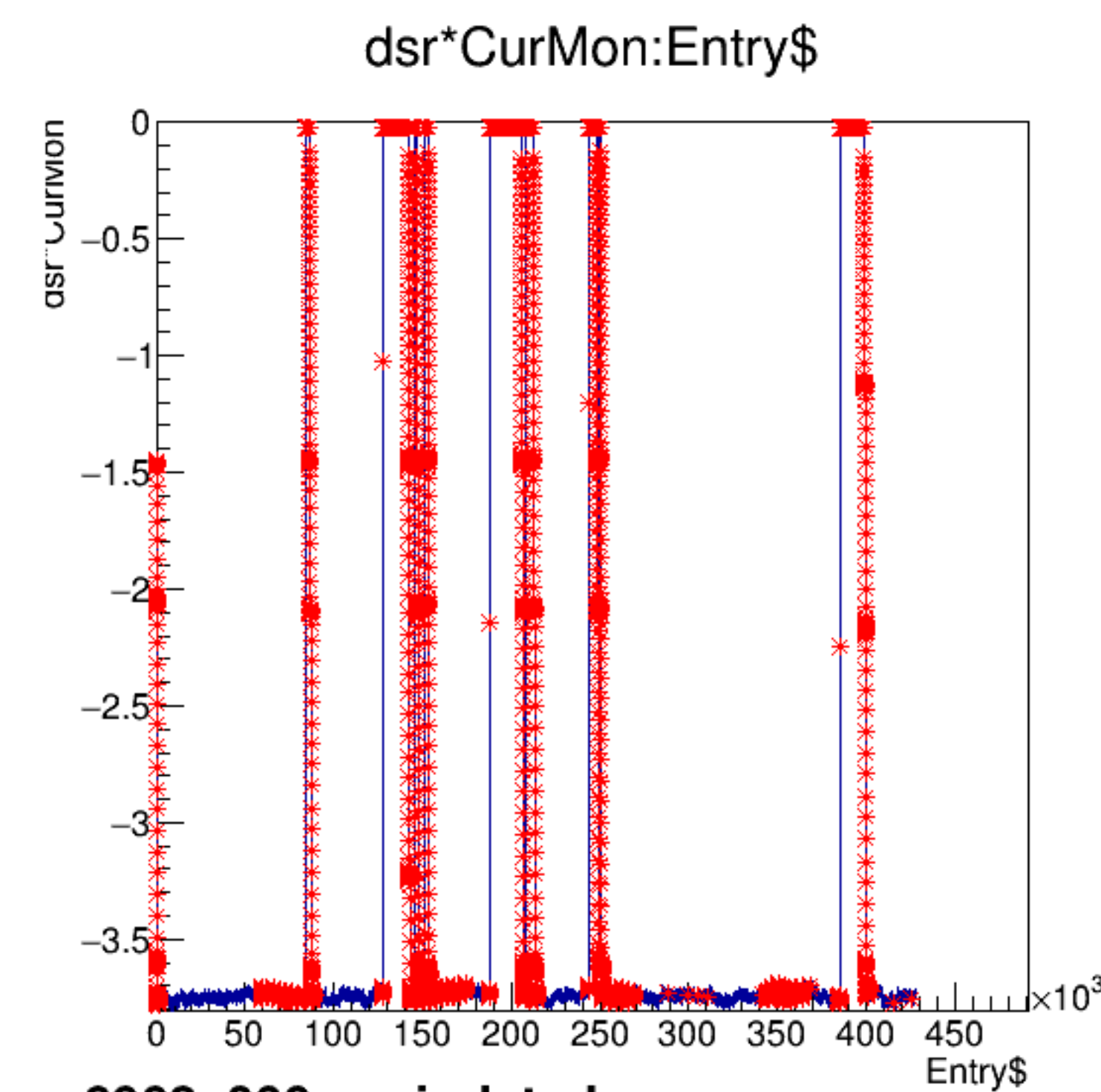


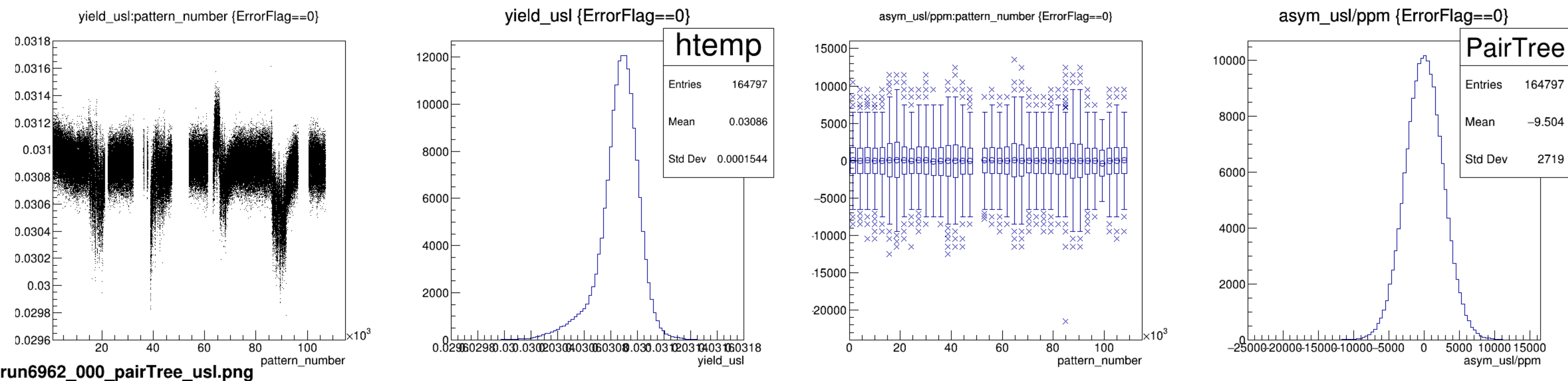
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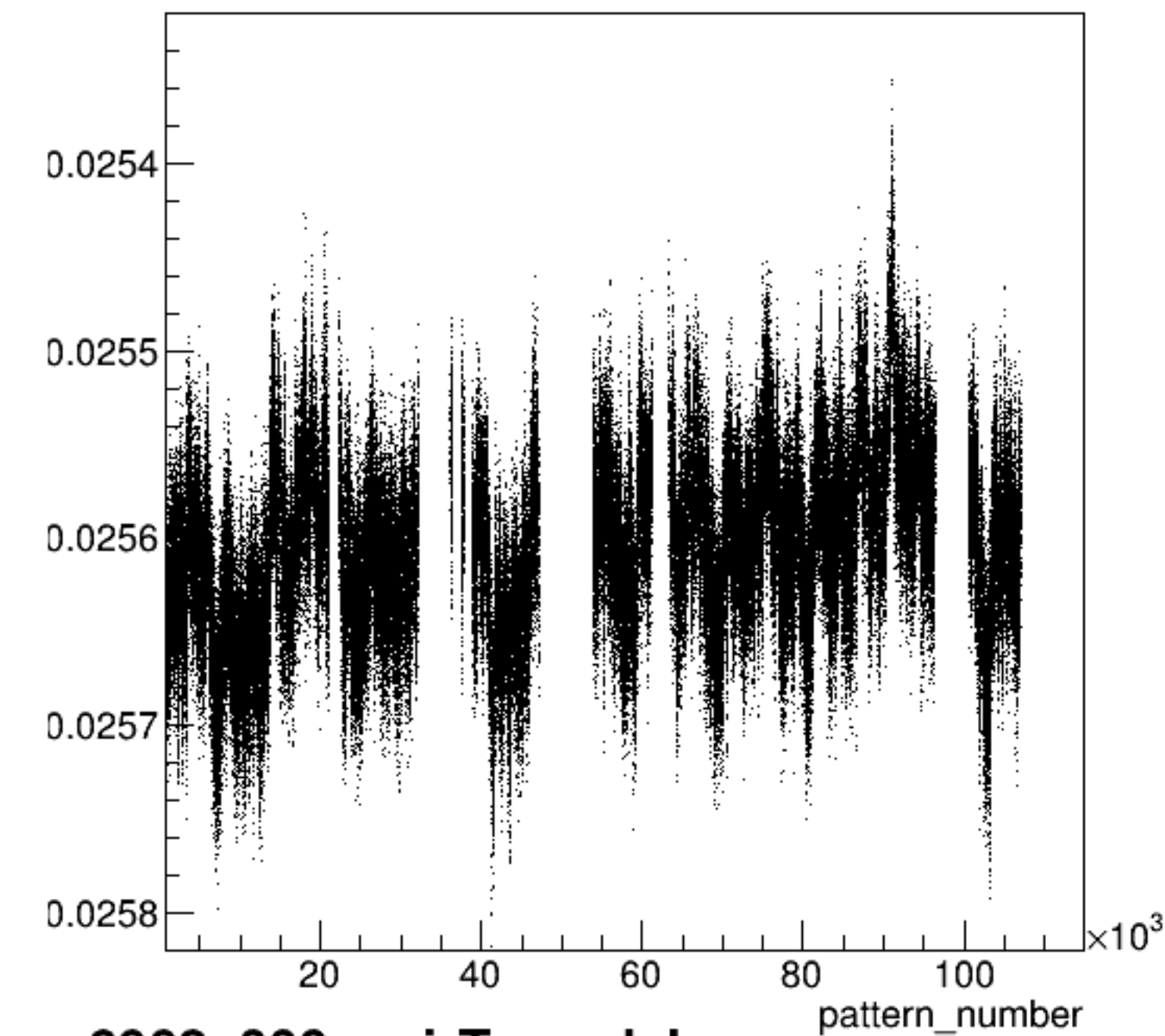
asym_usr





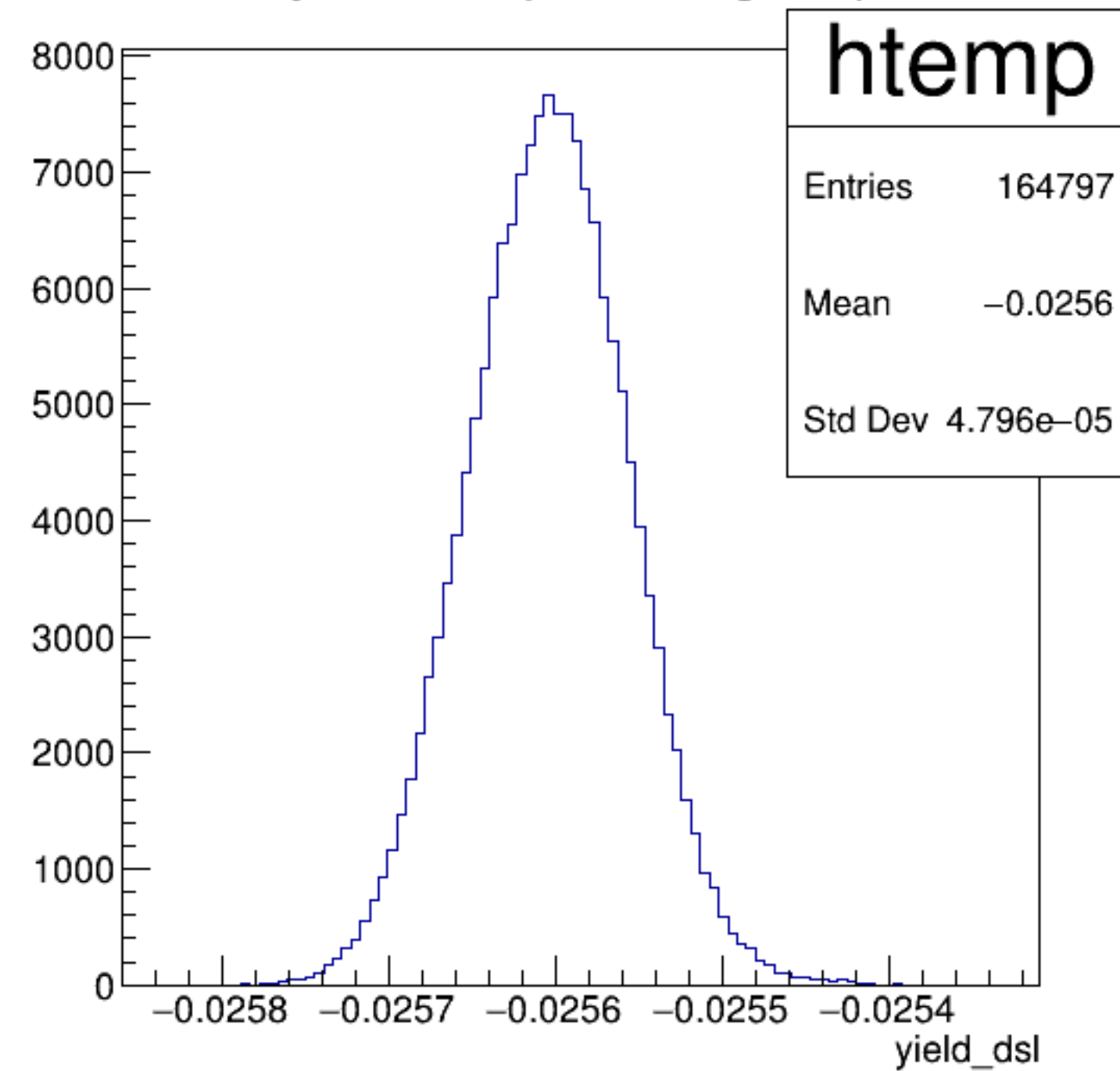


yield_dsl:pattern_number {ErrorFlag==0}

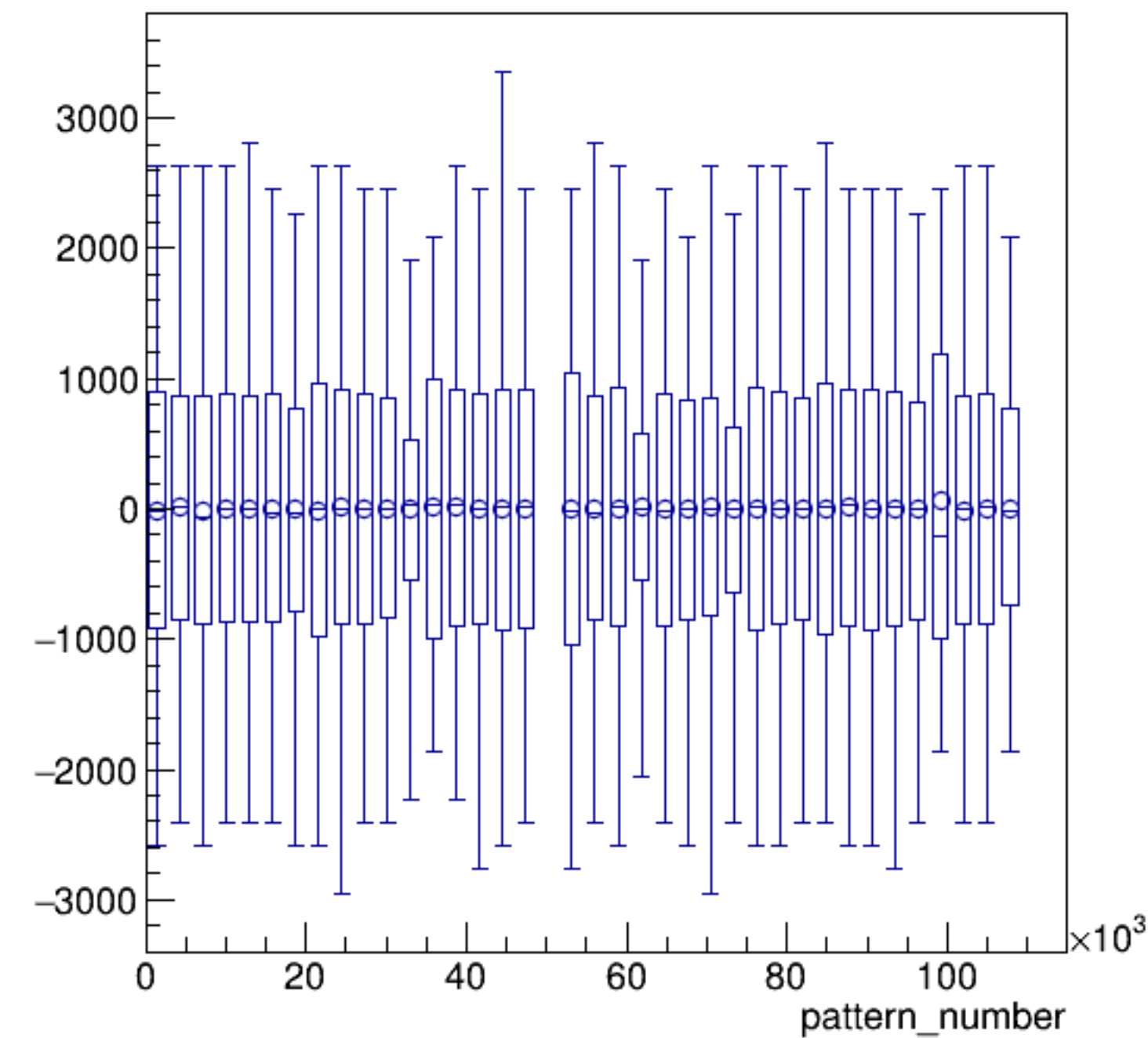


run6962_000_pairTree_dsl.png

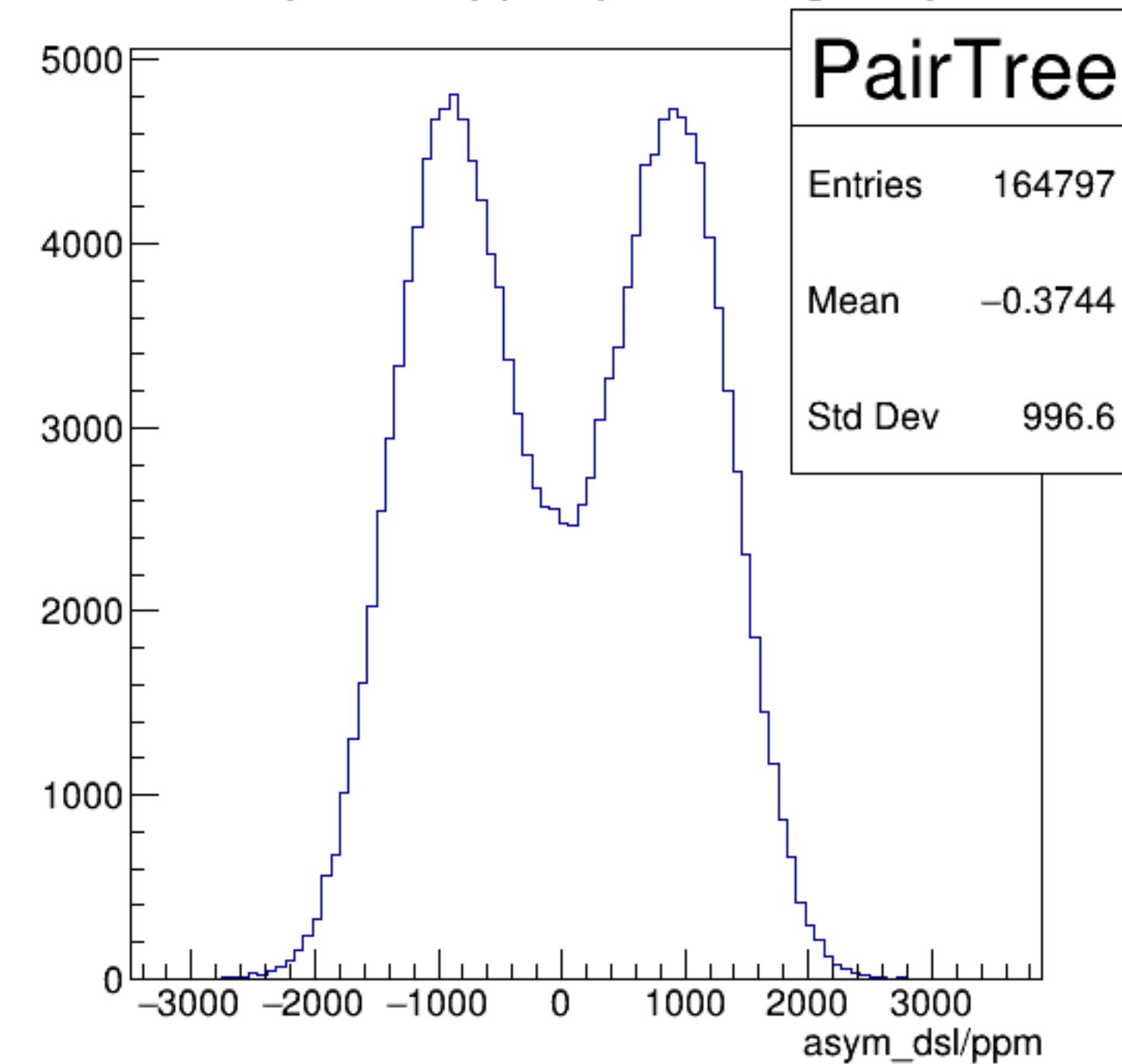
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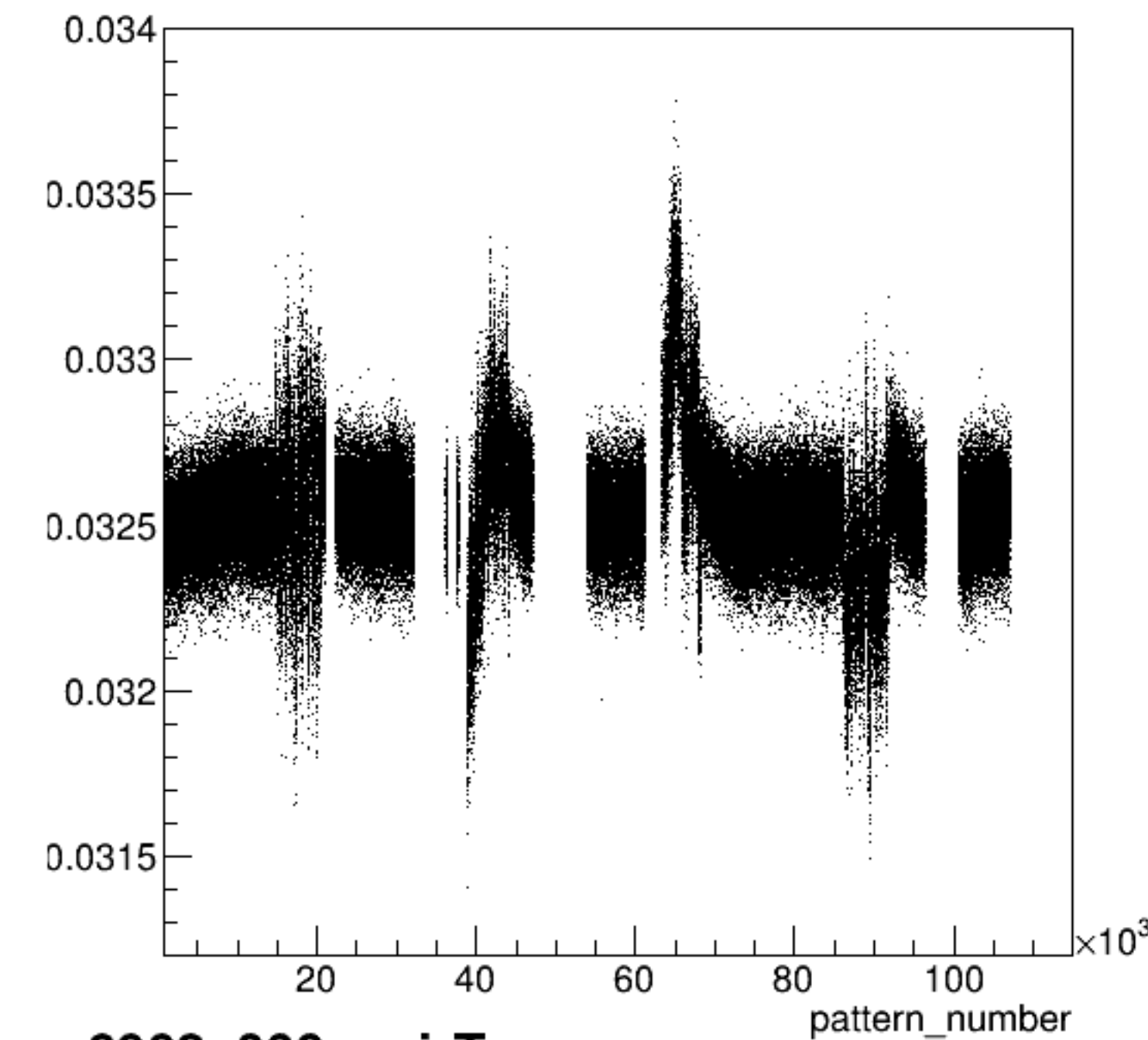
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asym_dsl/ppm {ErrorFlag==0}

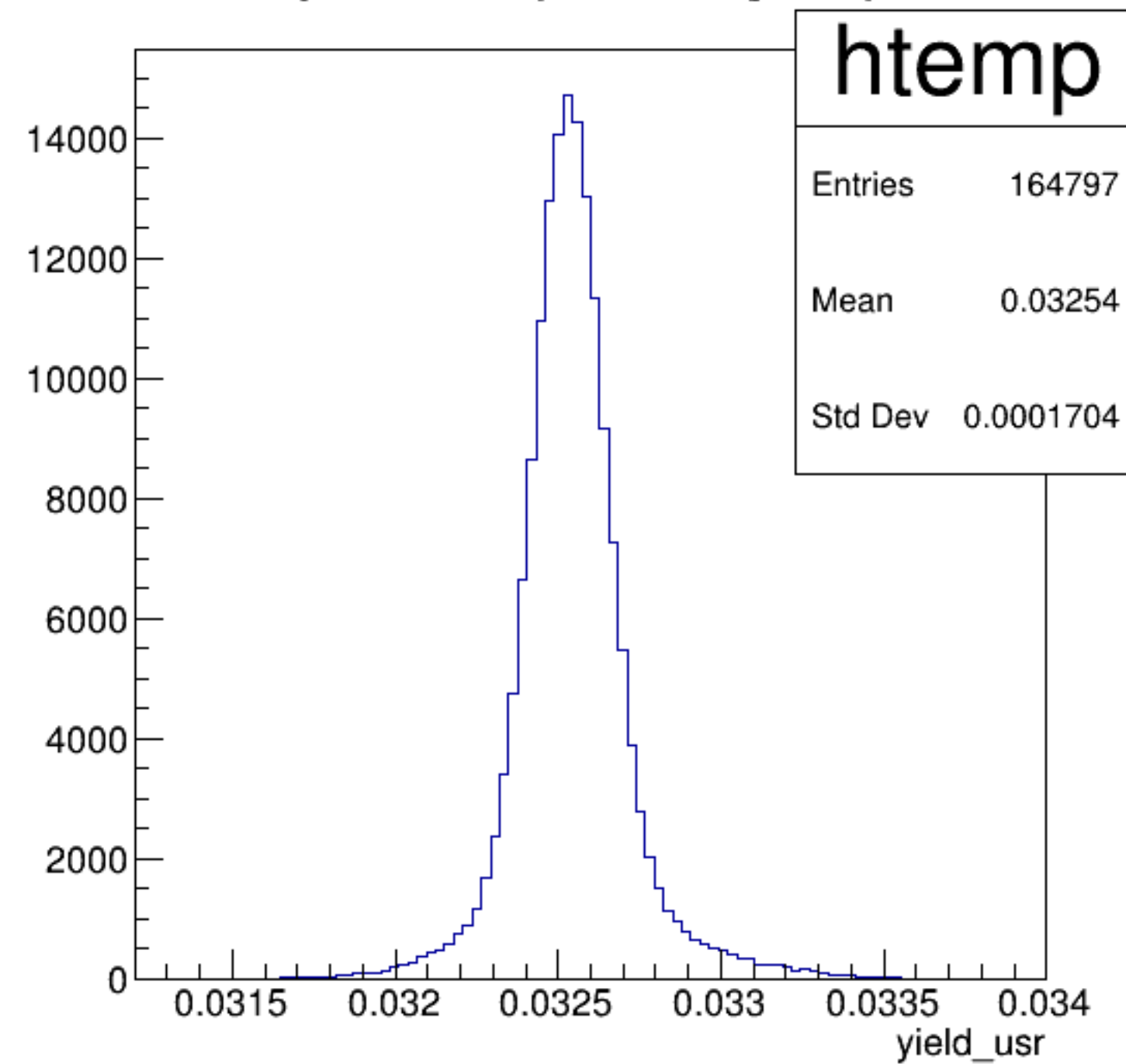


yield_usr:pattern_number {ErrorFlag==0}

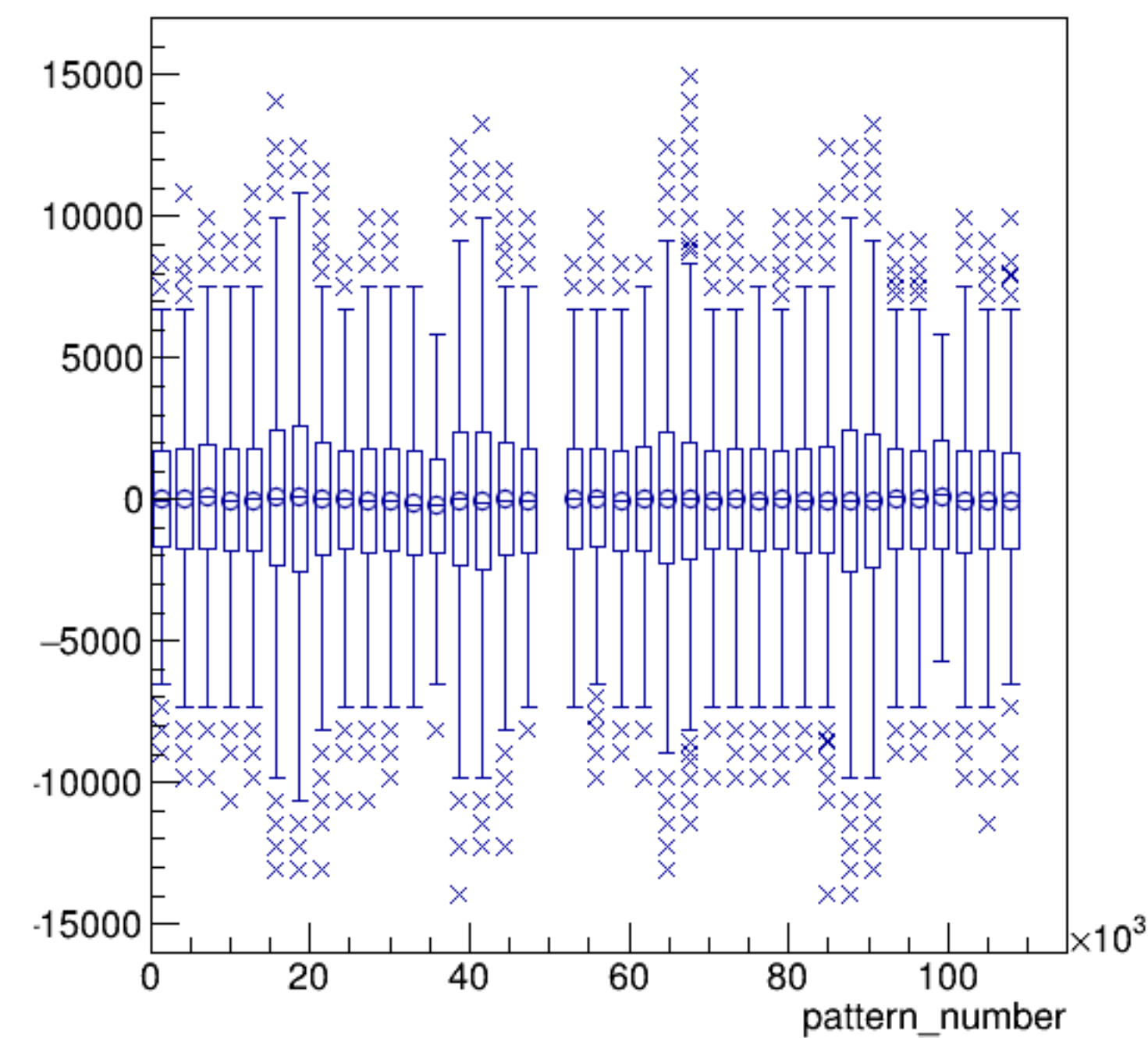


run6962_000_pairTree_usr.png

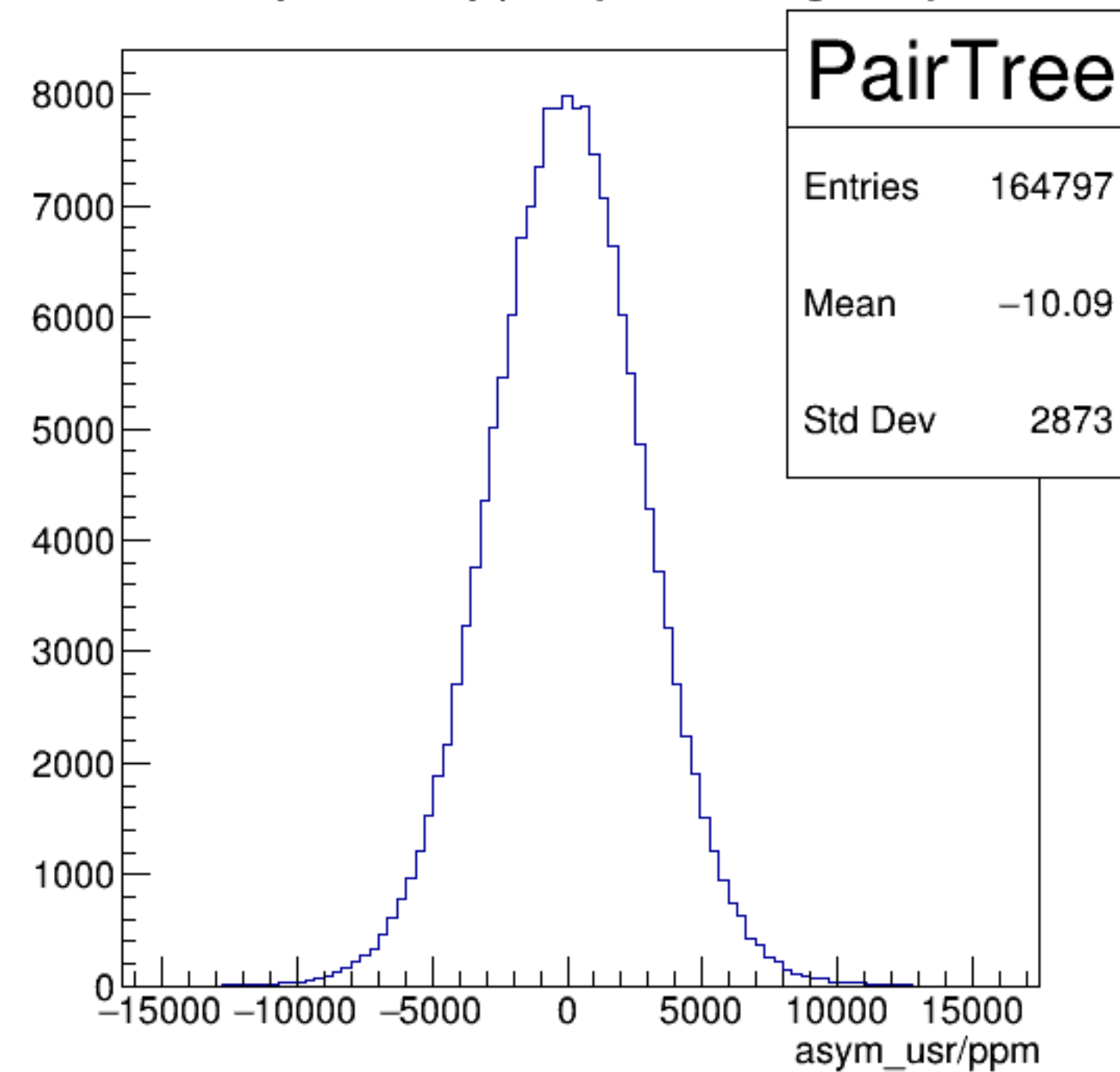
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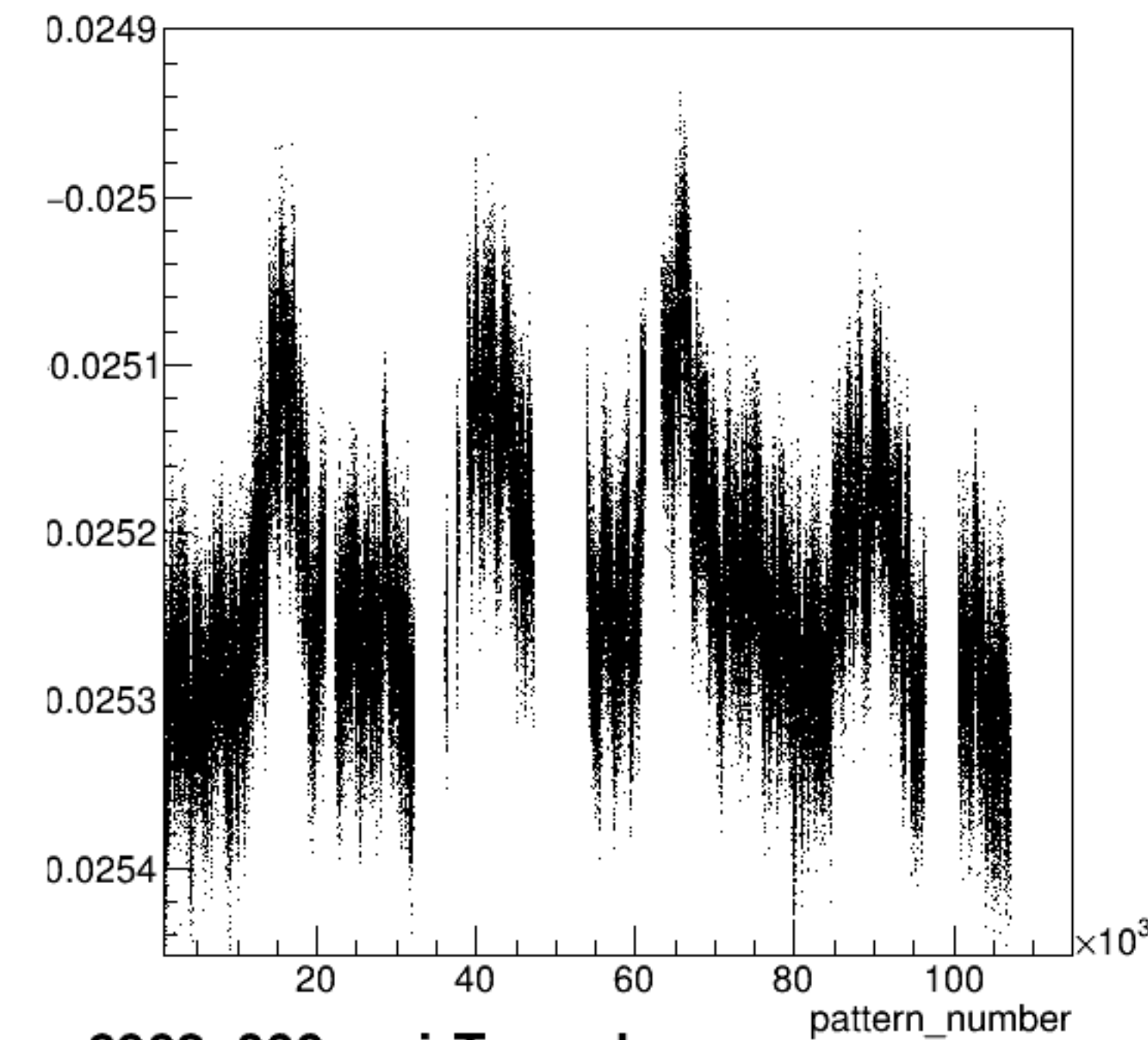
asym_usr/ppm:pattern_number {ErrorFlag==0}



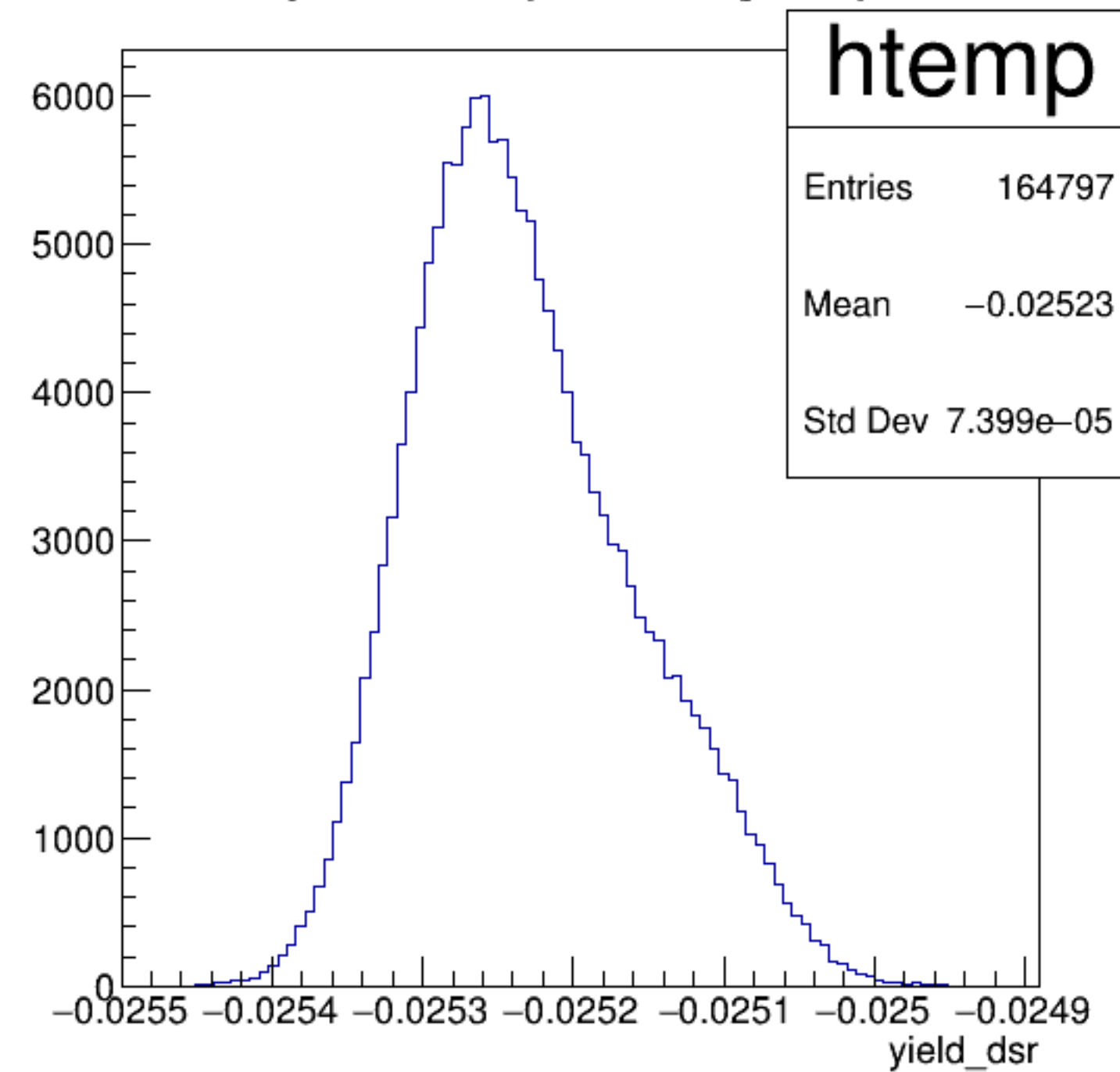
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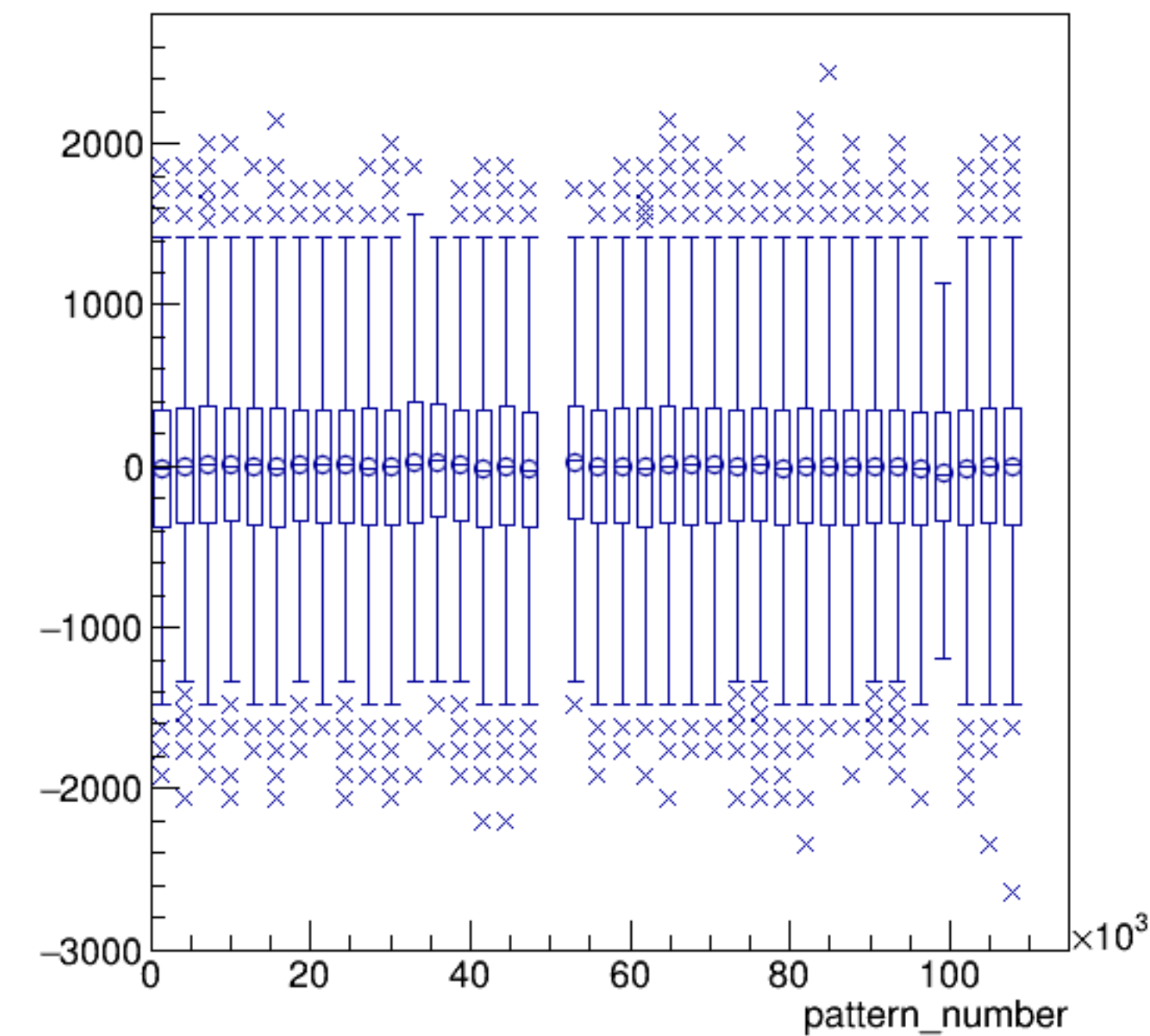
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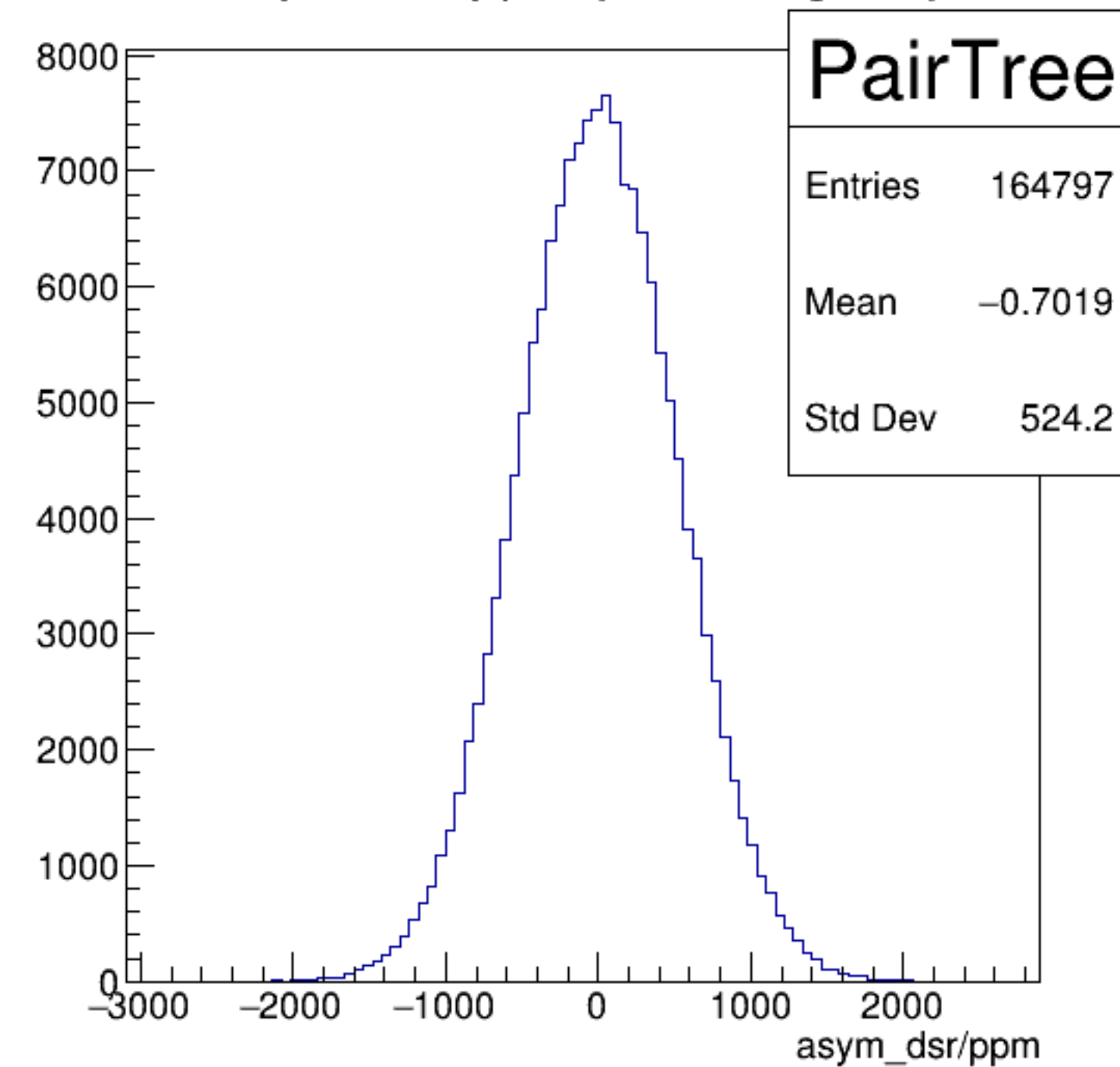
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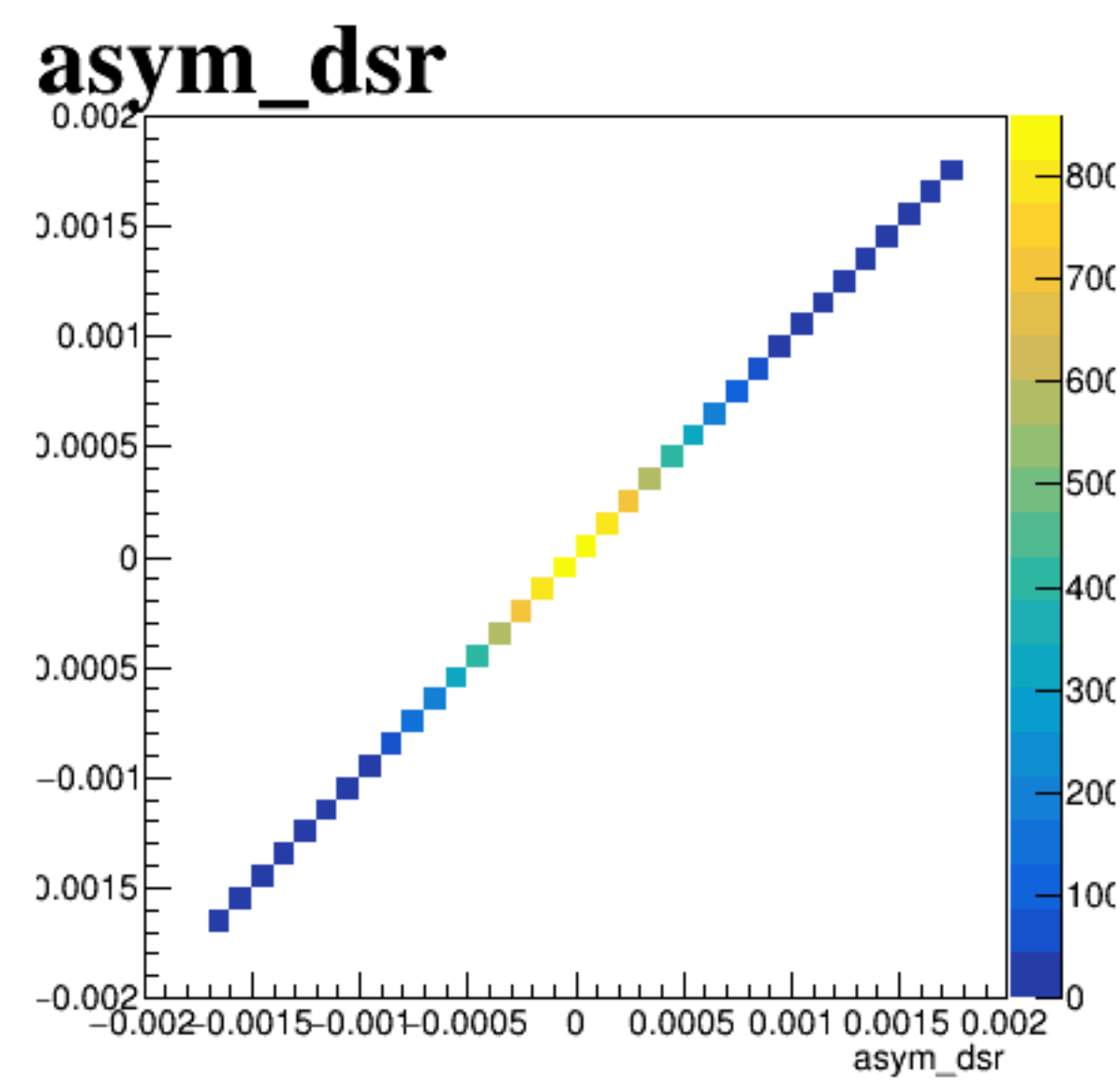
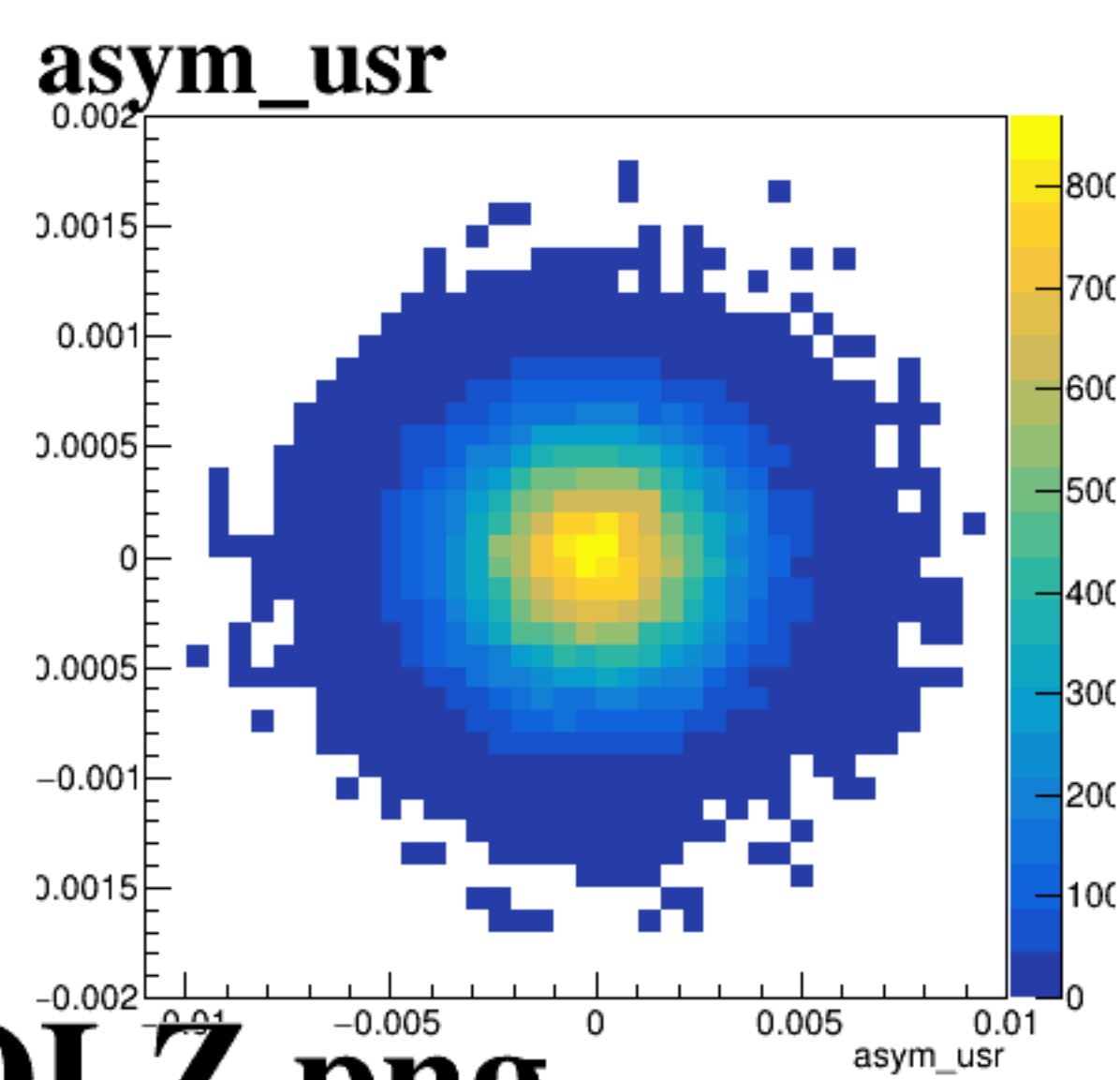
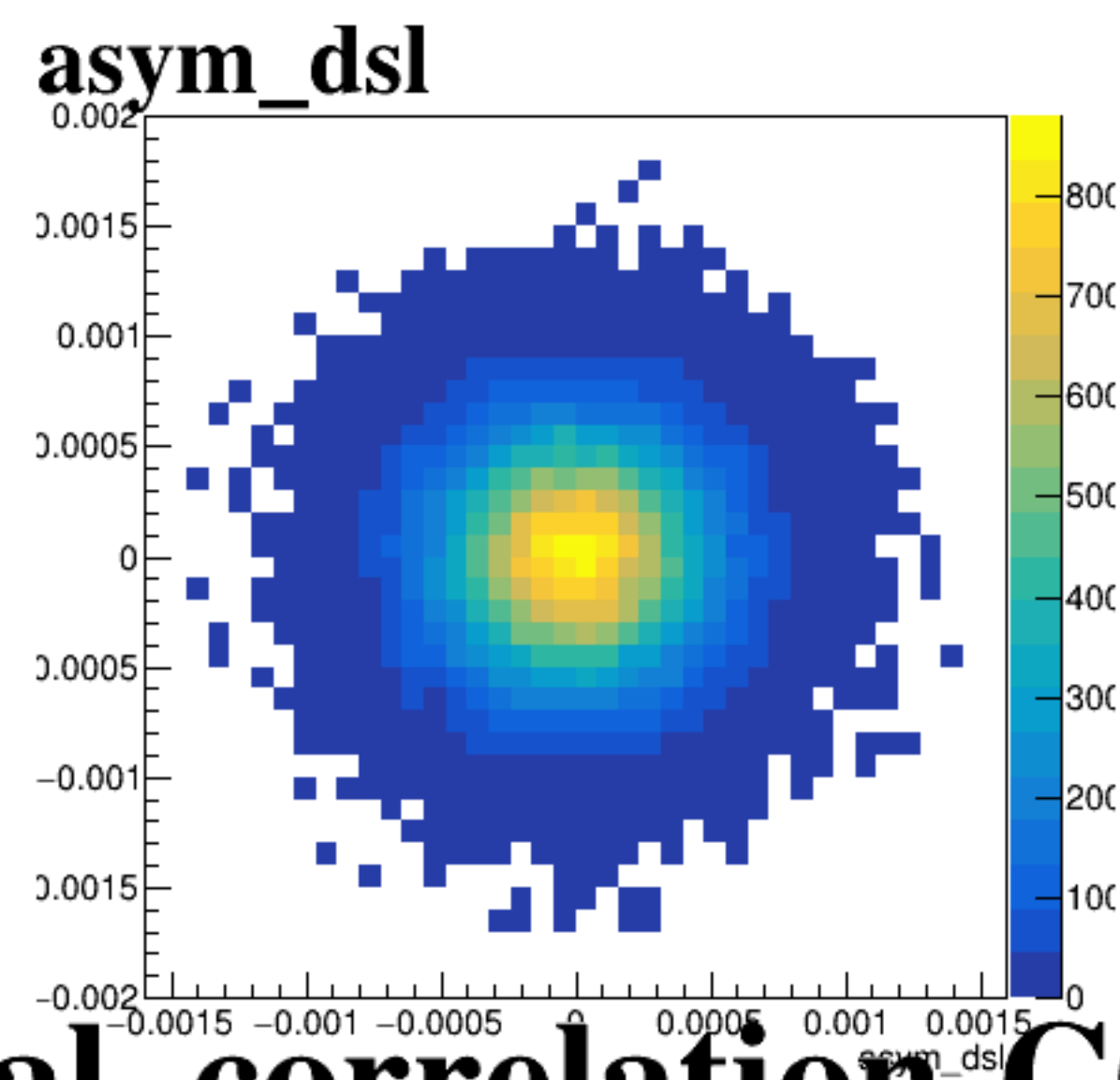
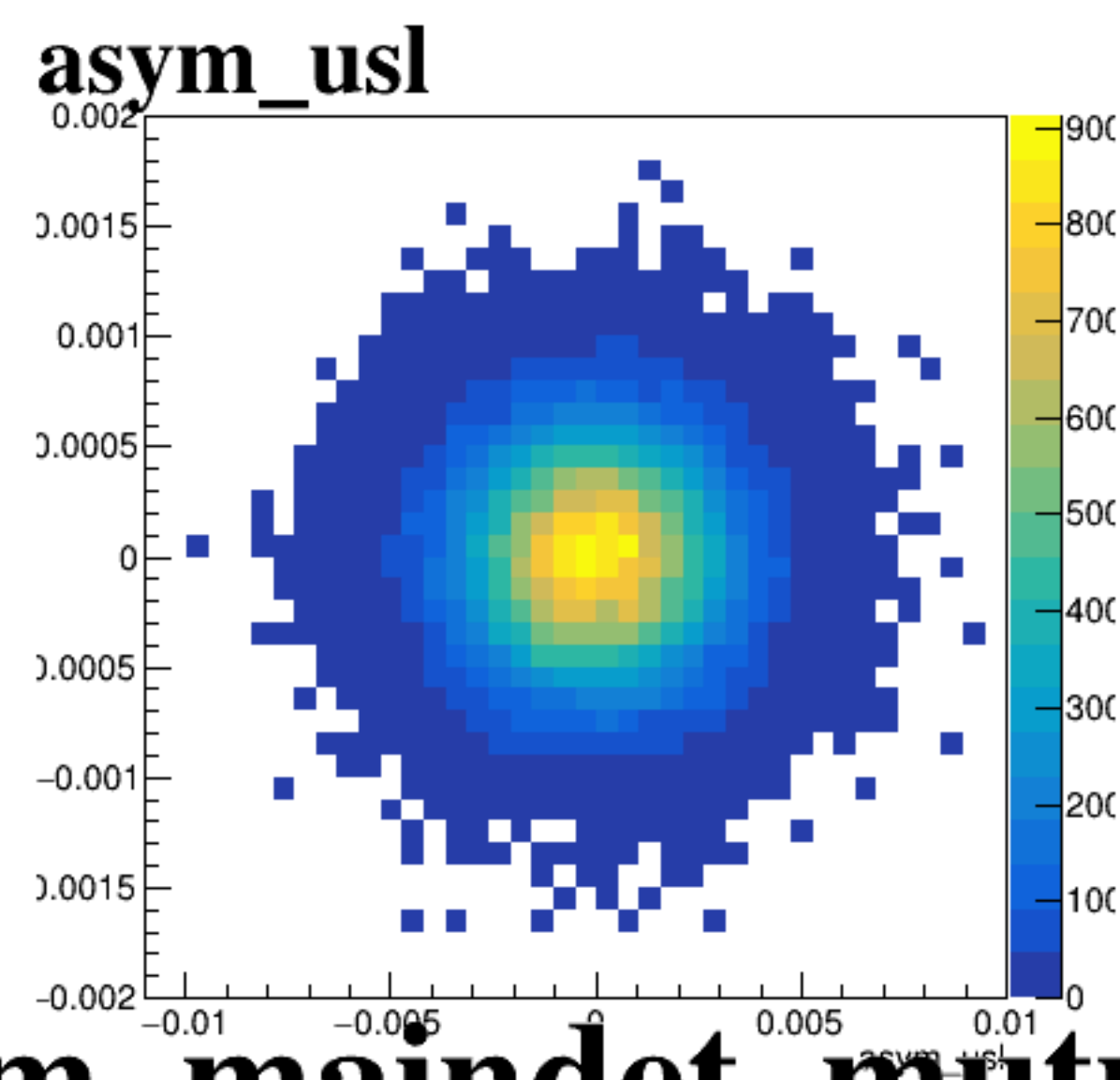
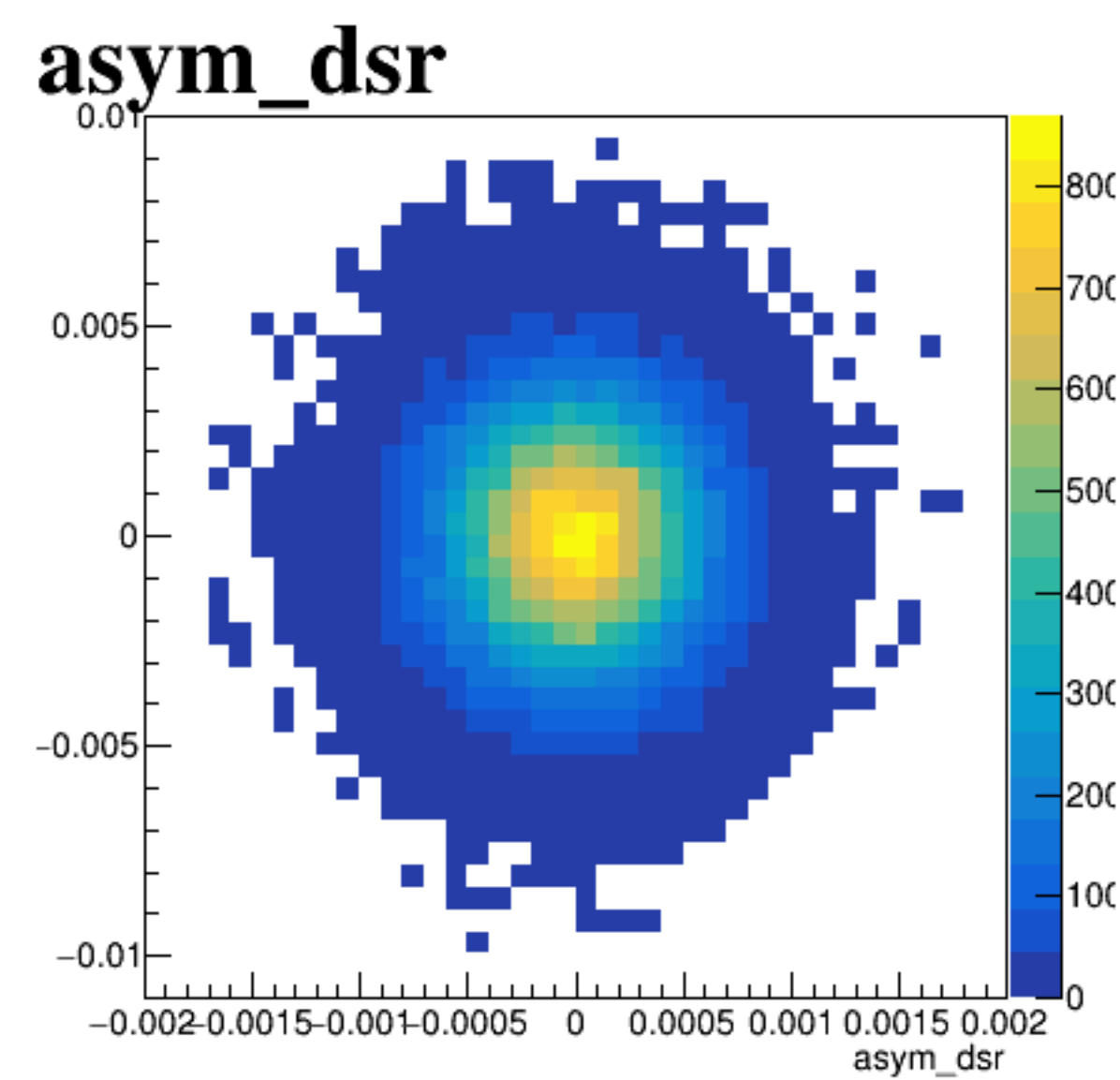
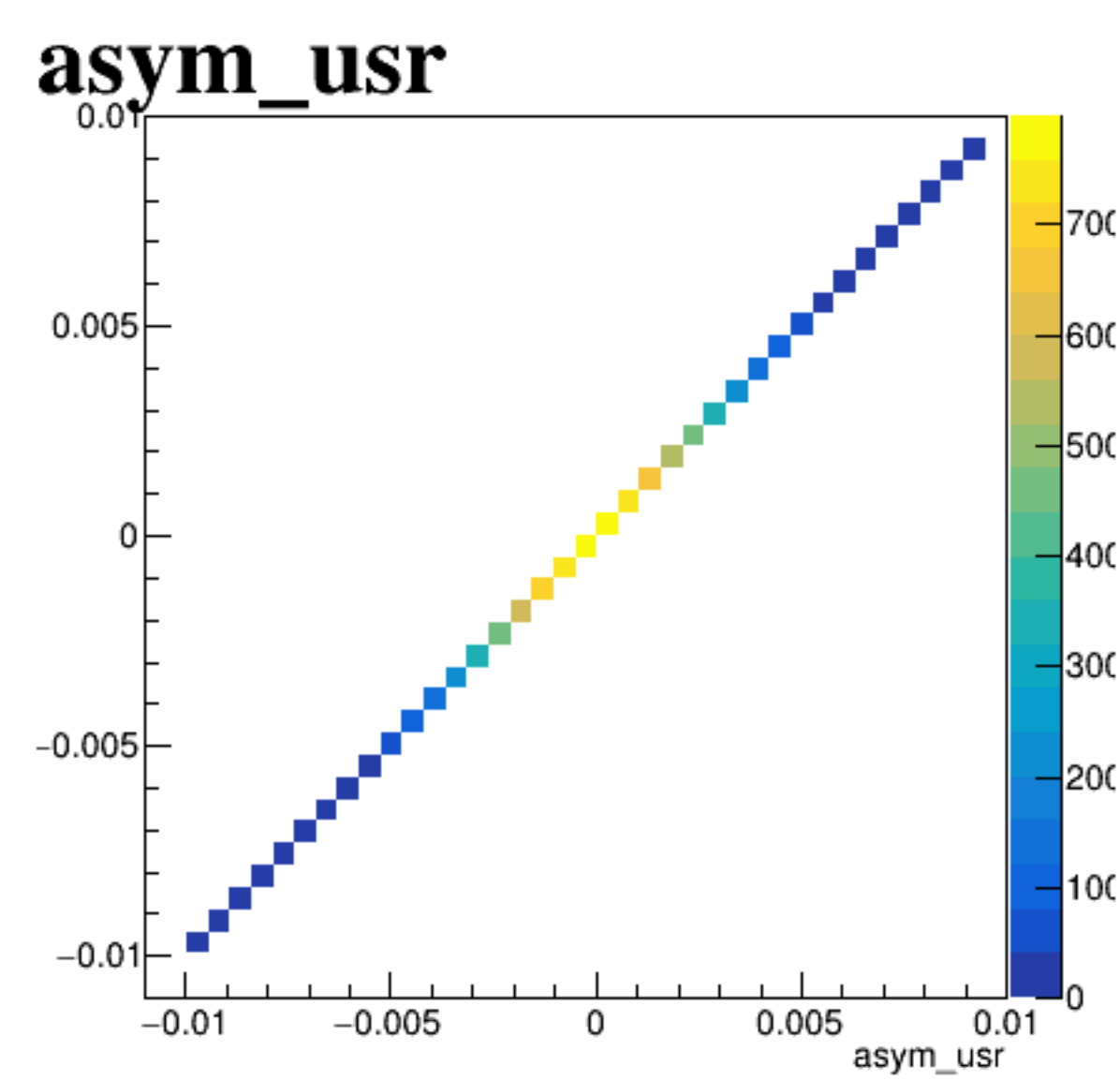
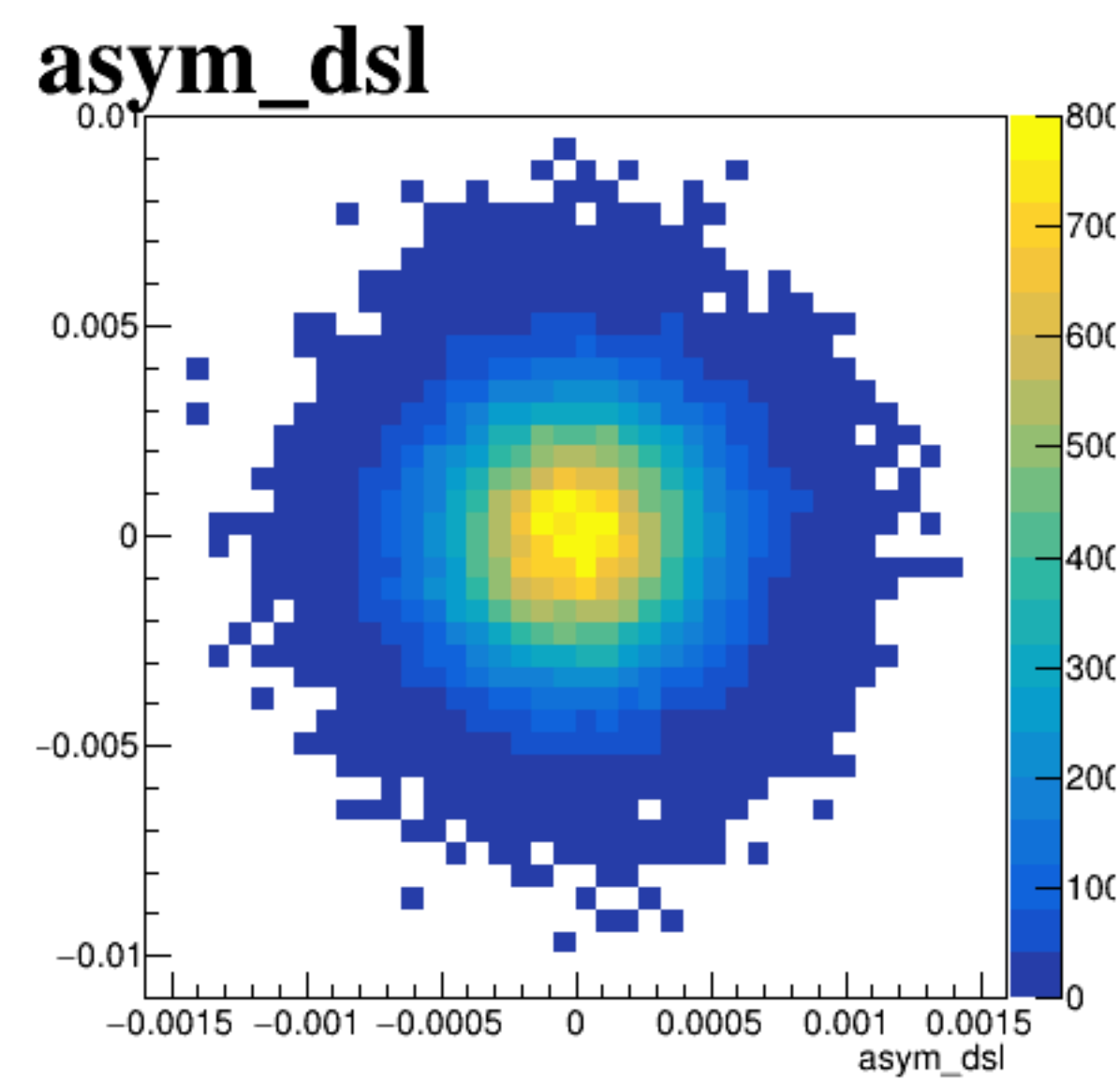
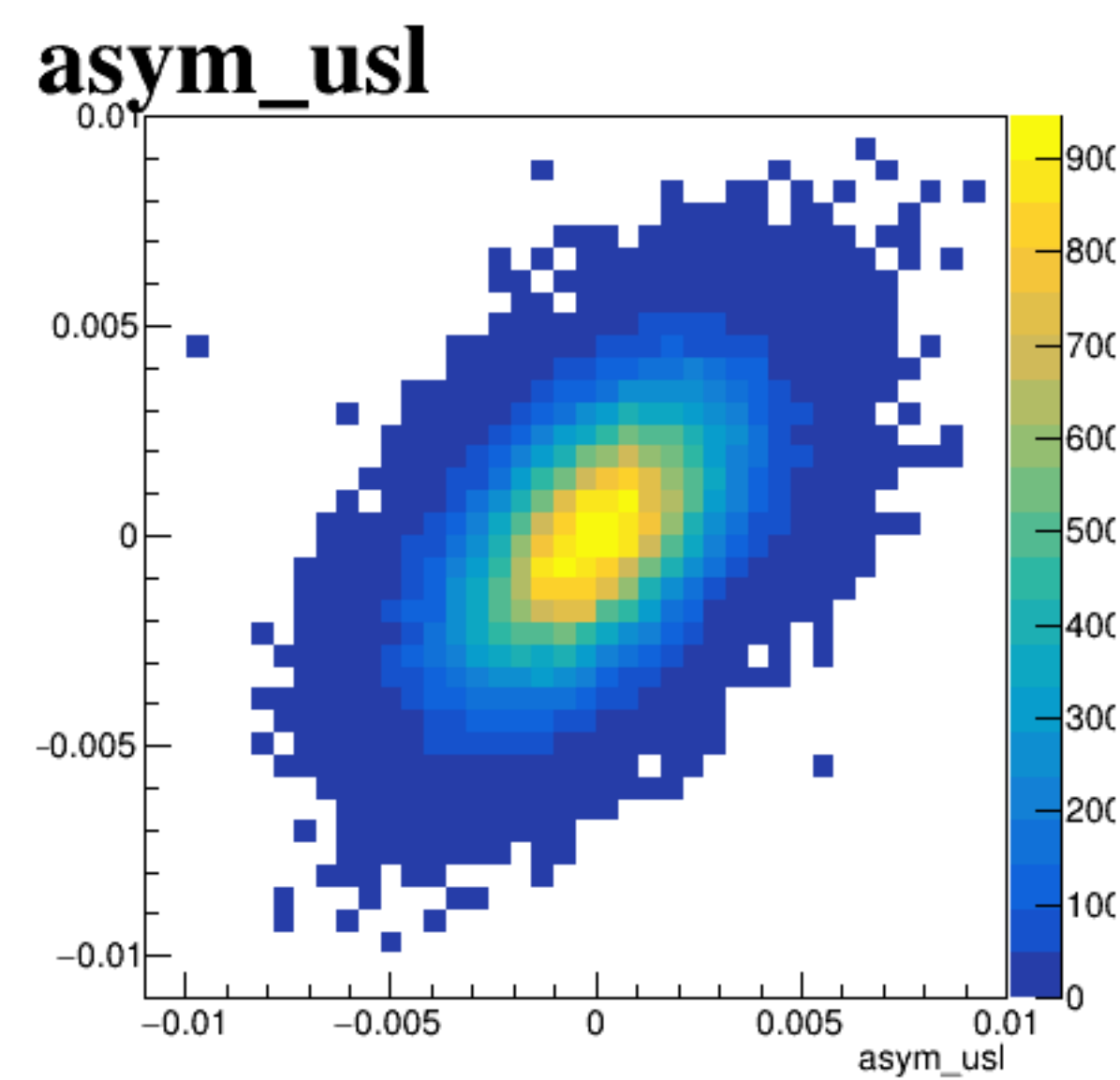
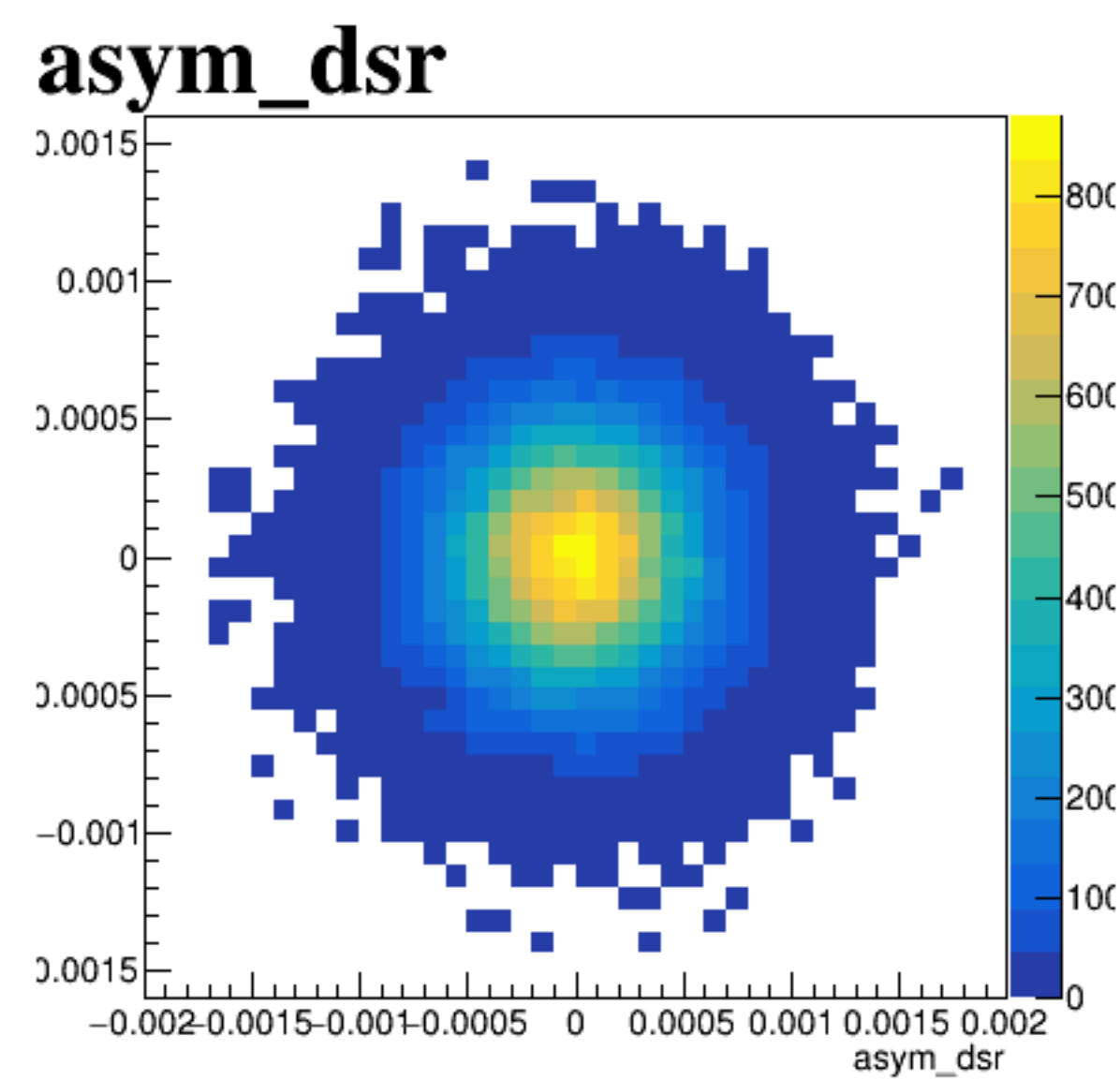
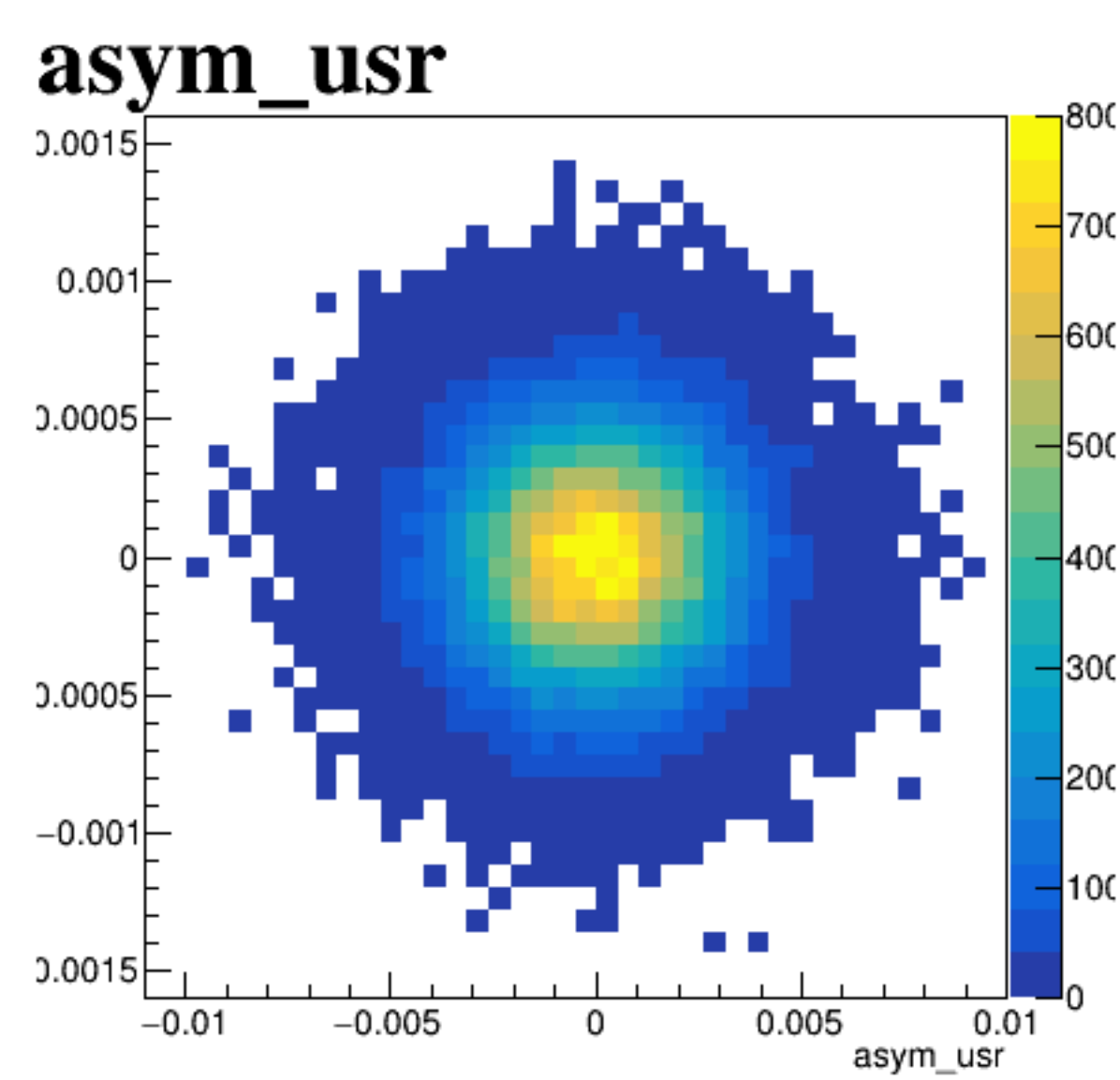
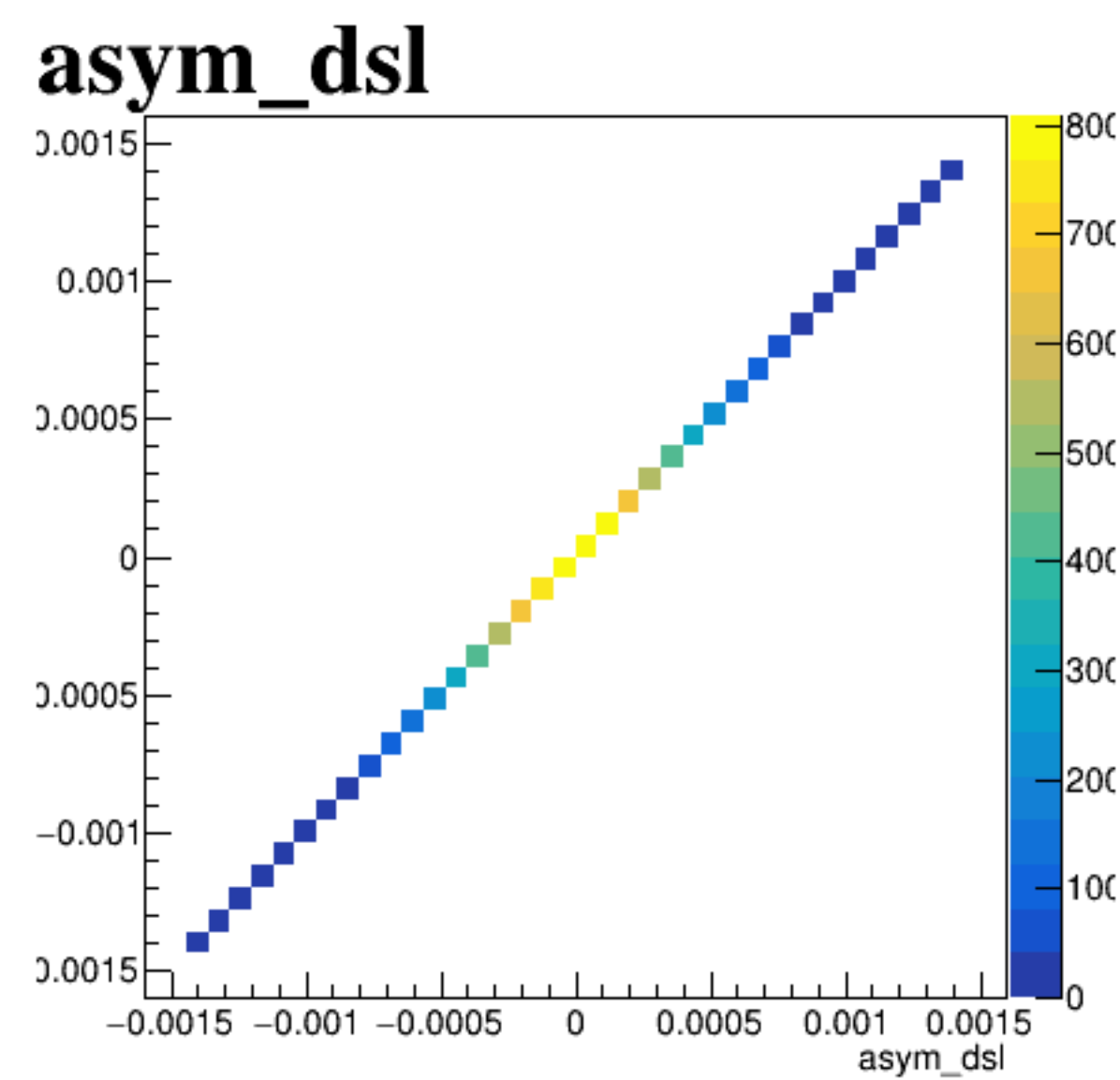
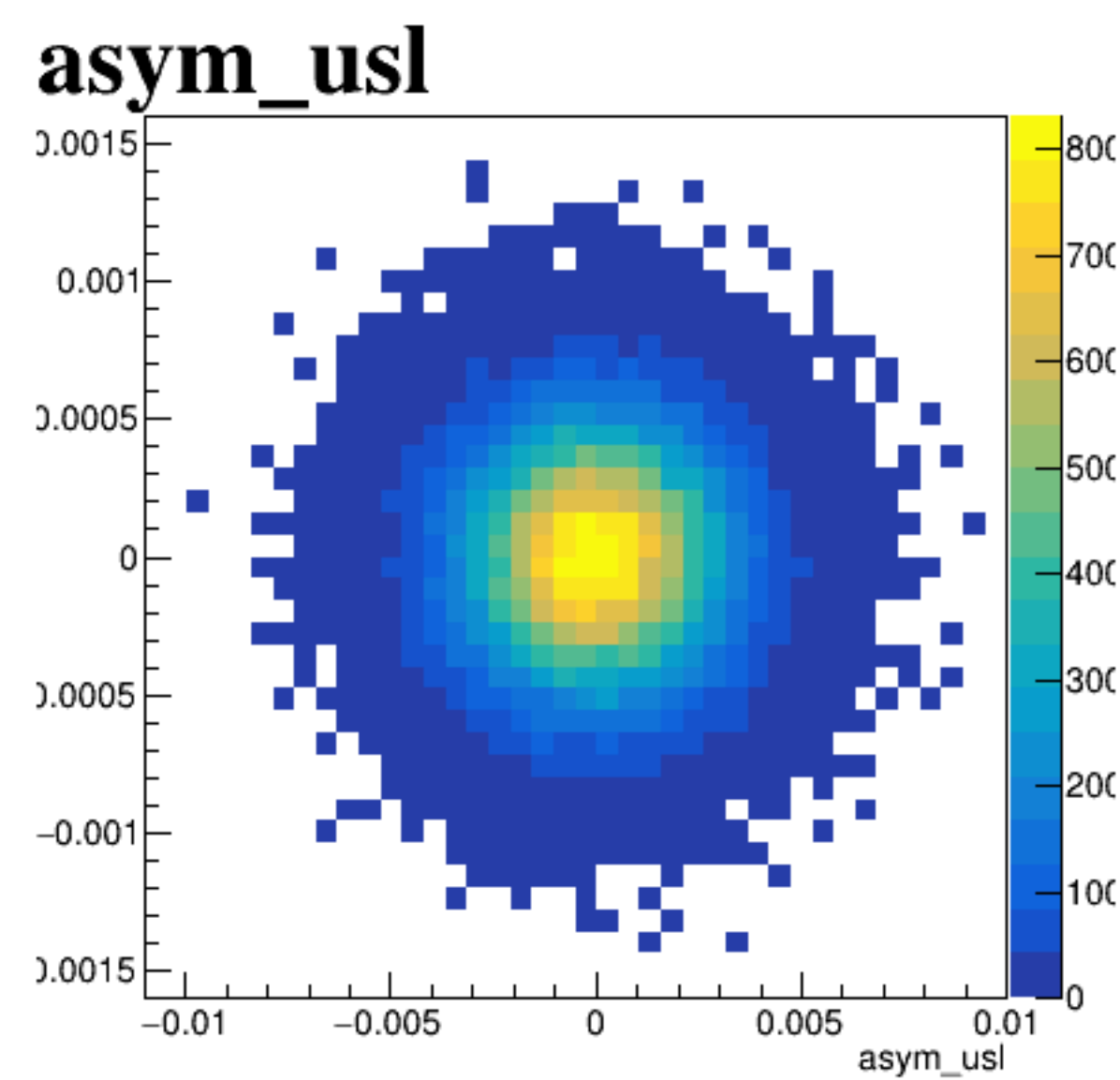
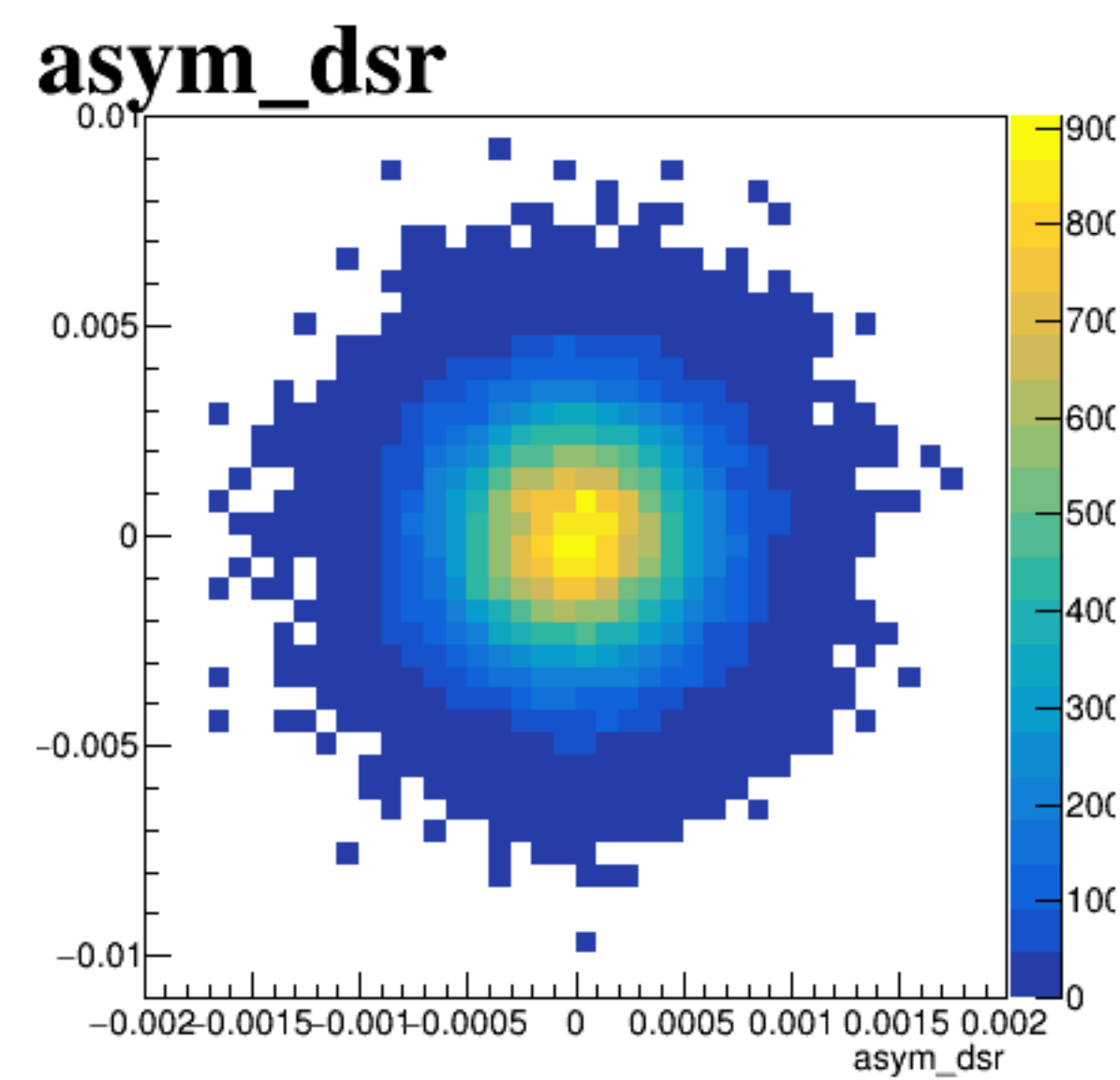
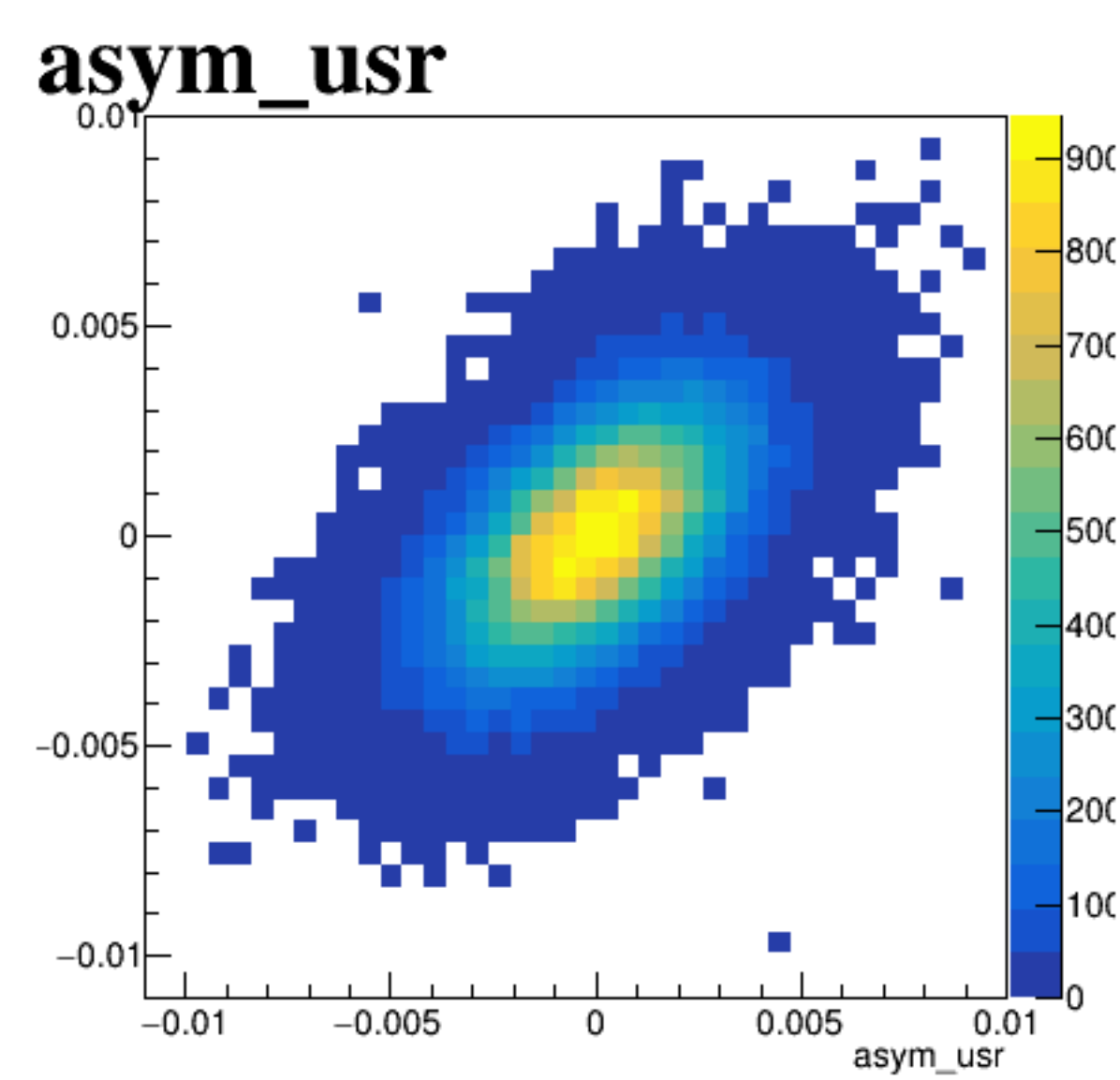
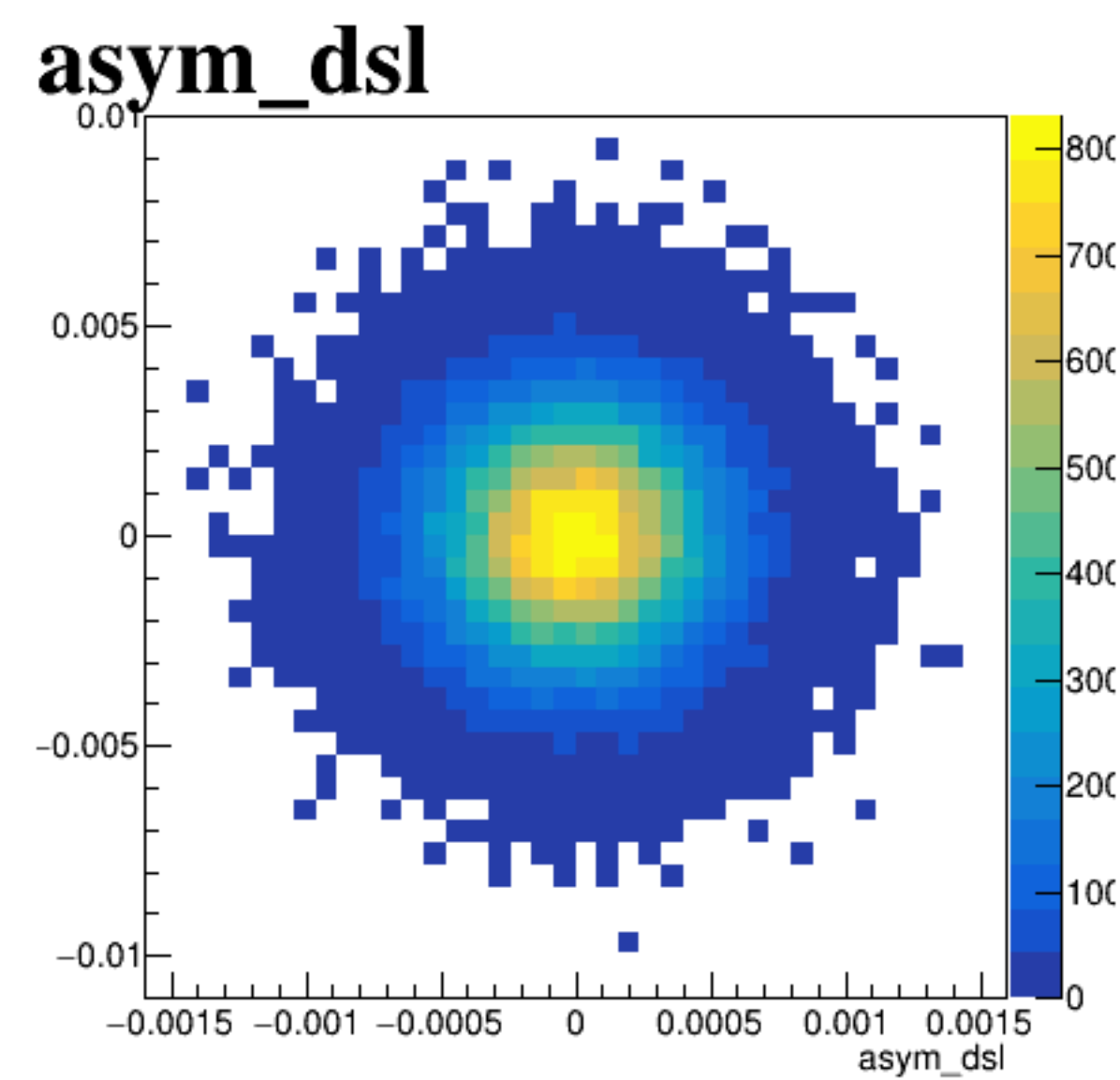
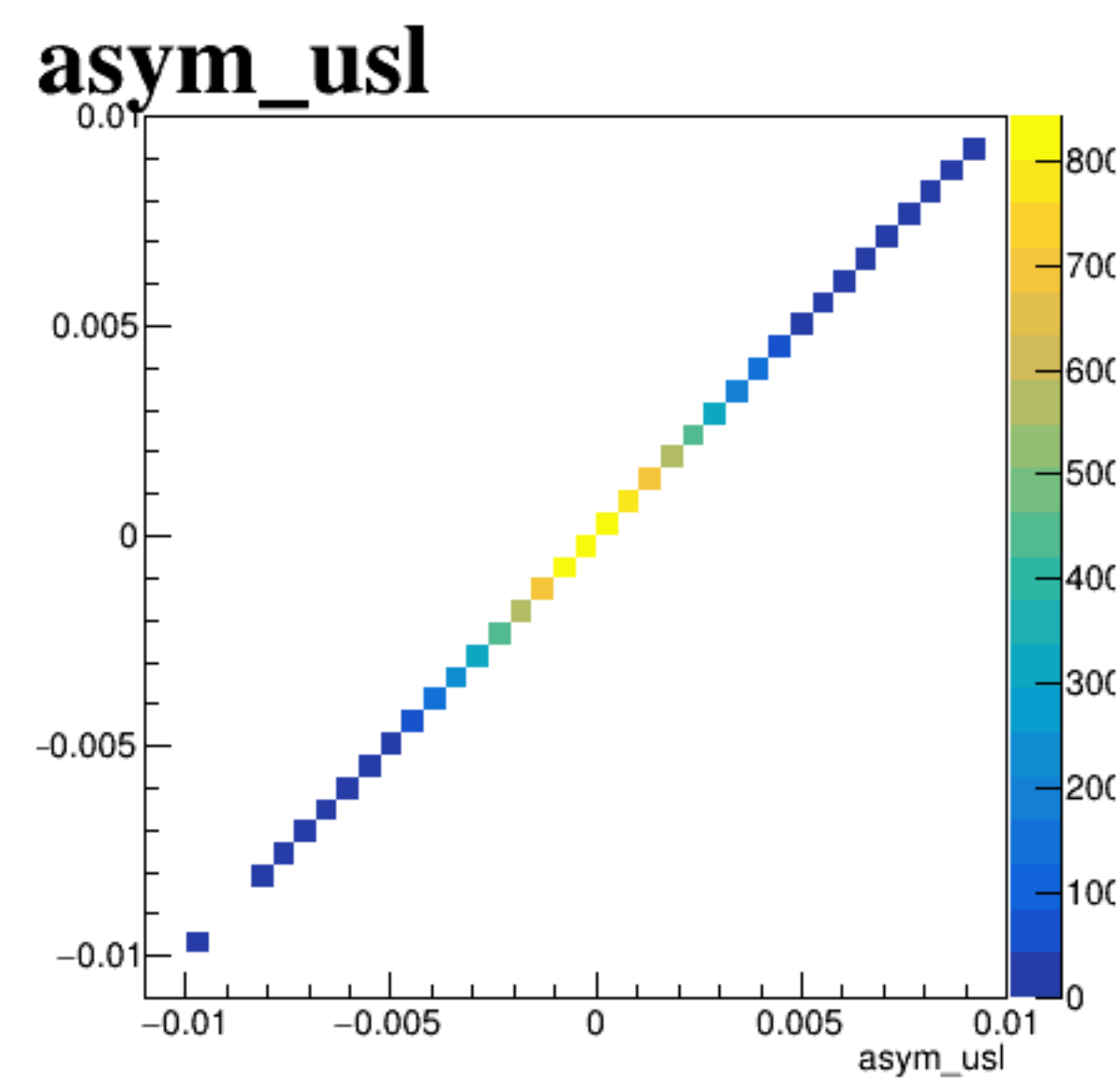


asym_dsr/ppm:pattern_number {ErrorFlag==0}

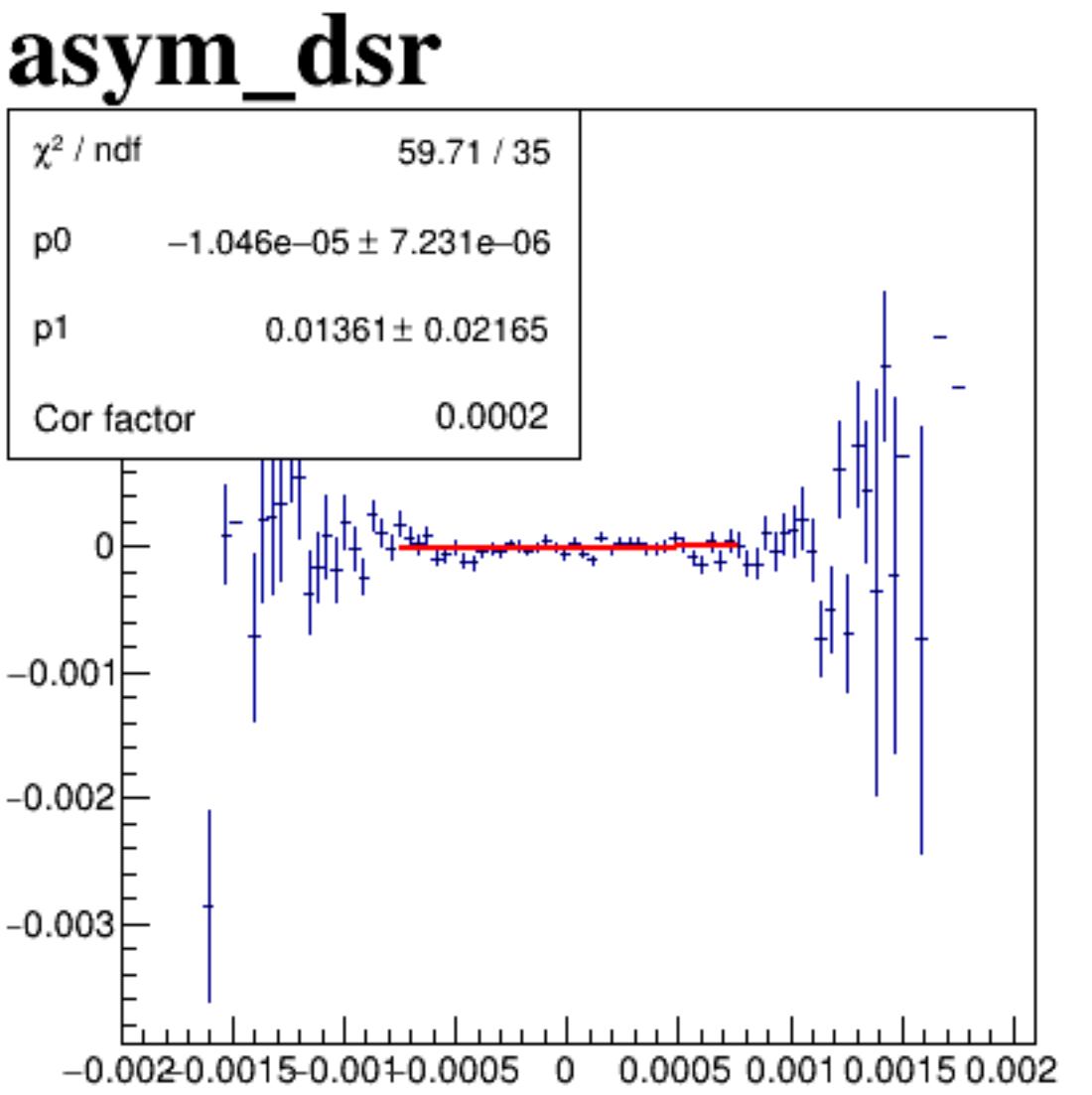
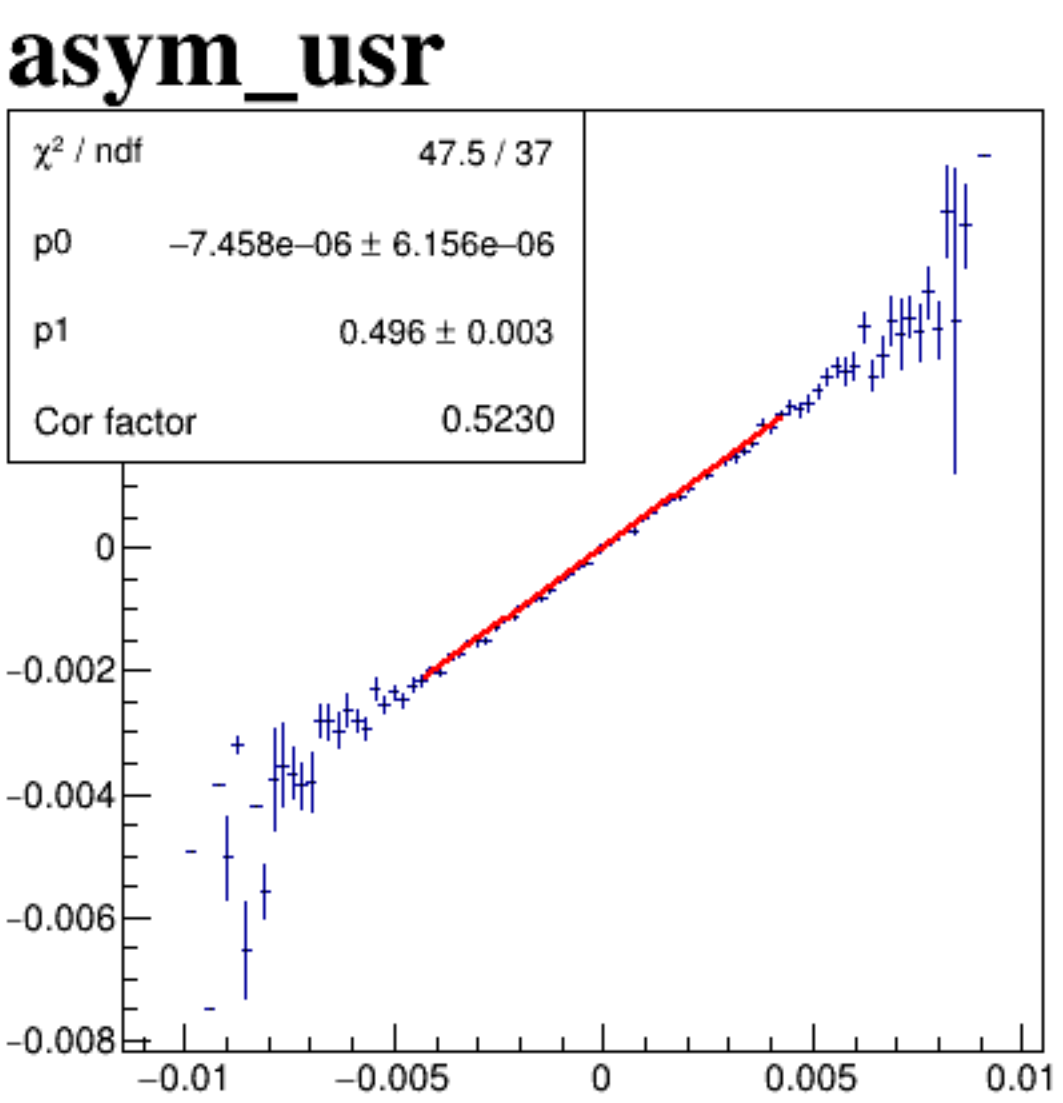
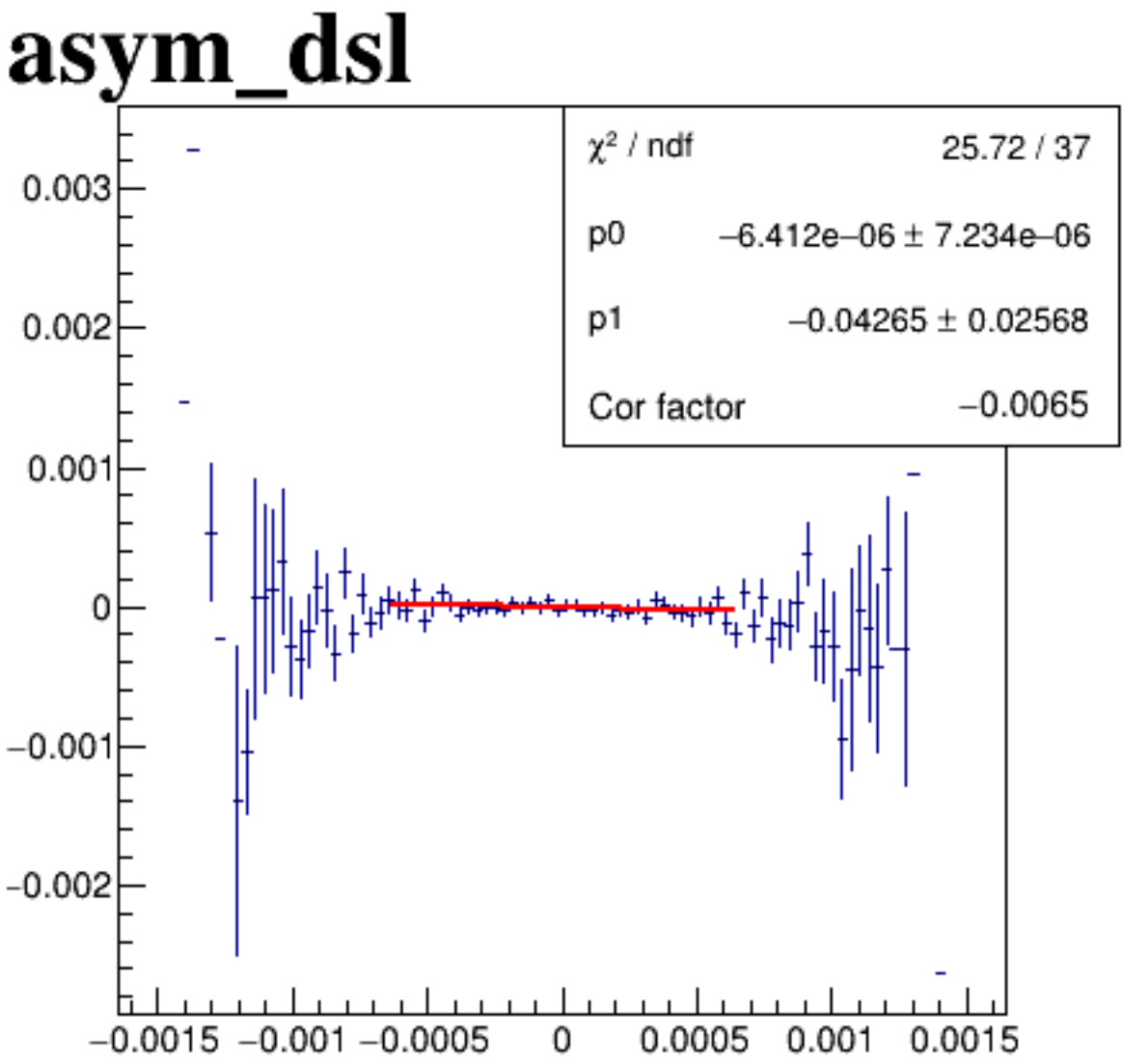
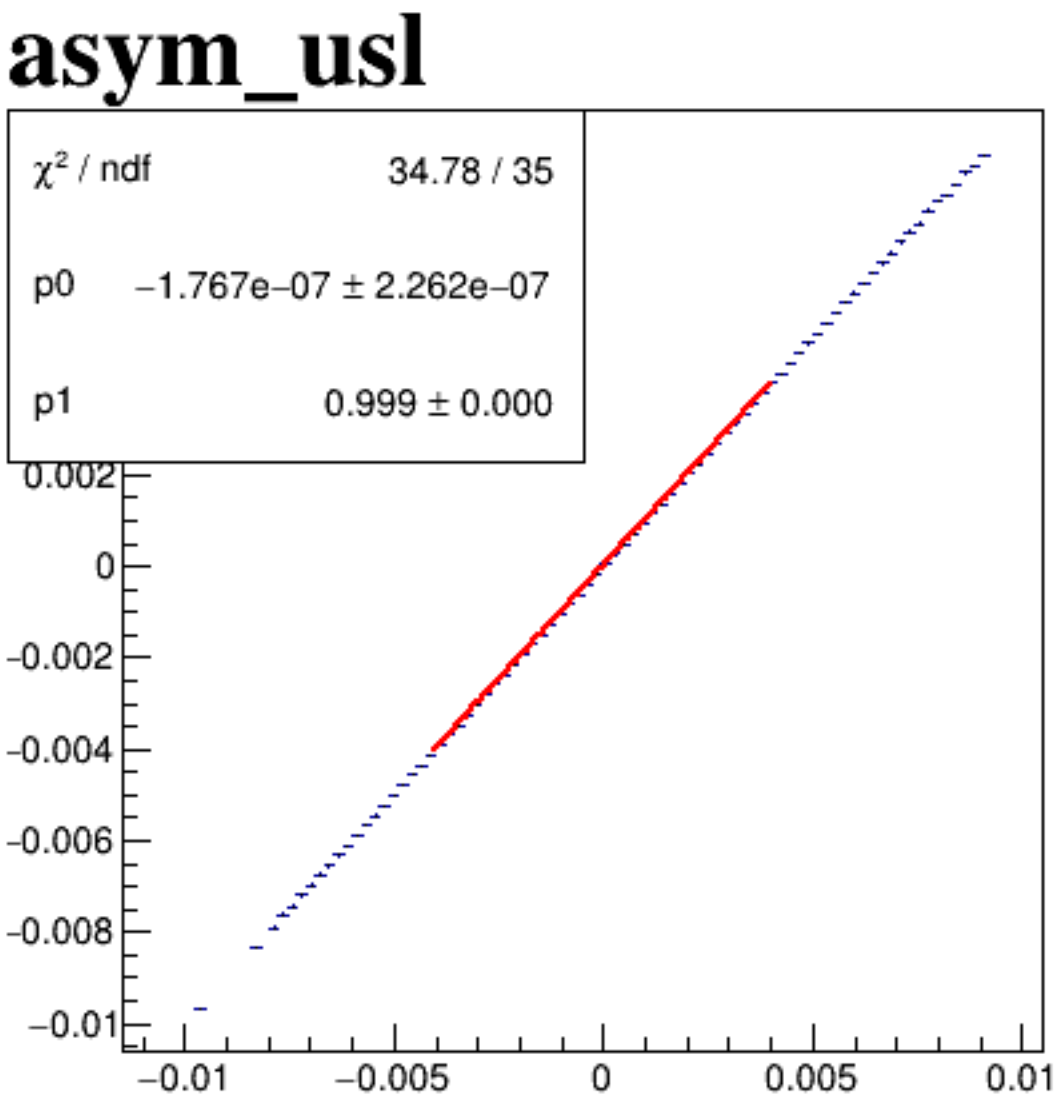


asym_dsr/ppm {ErrorFlag==0}

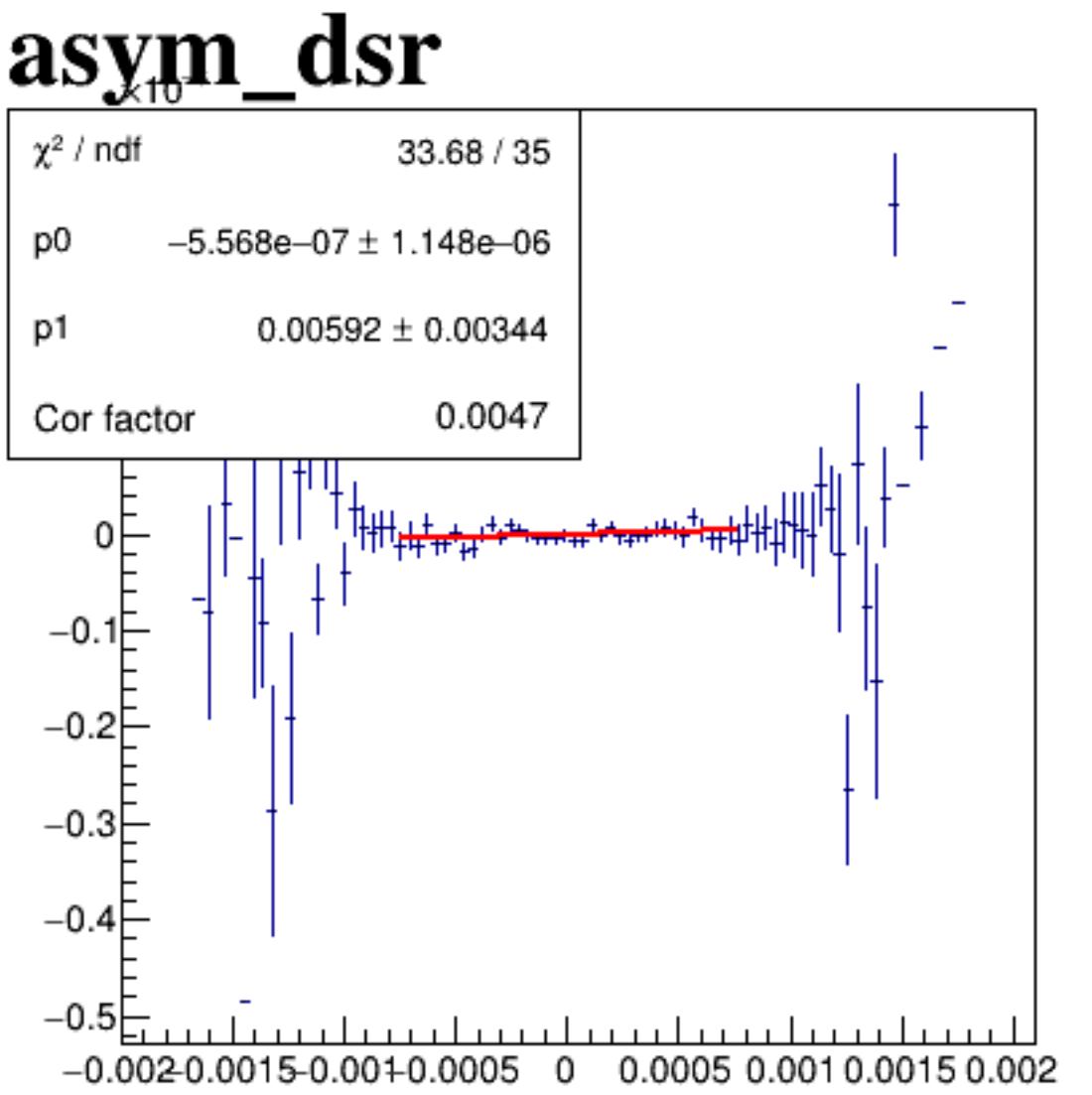
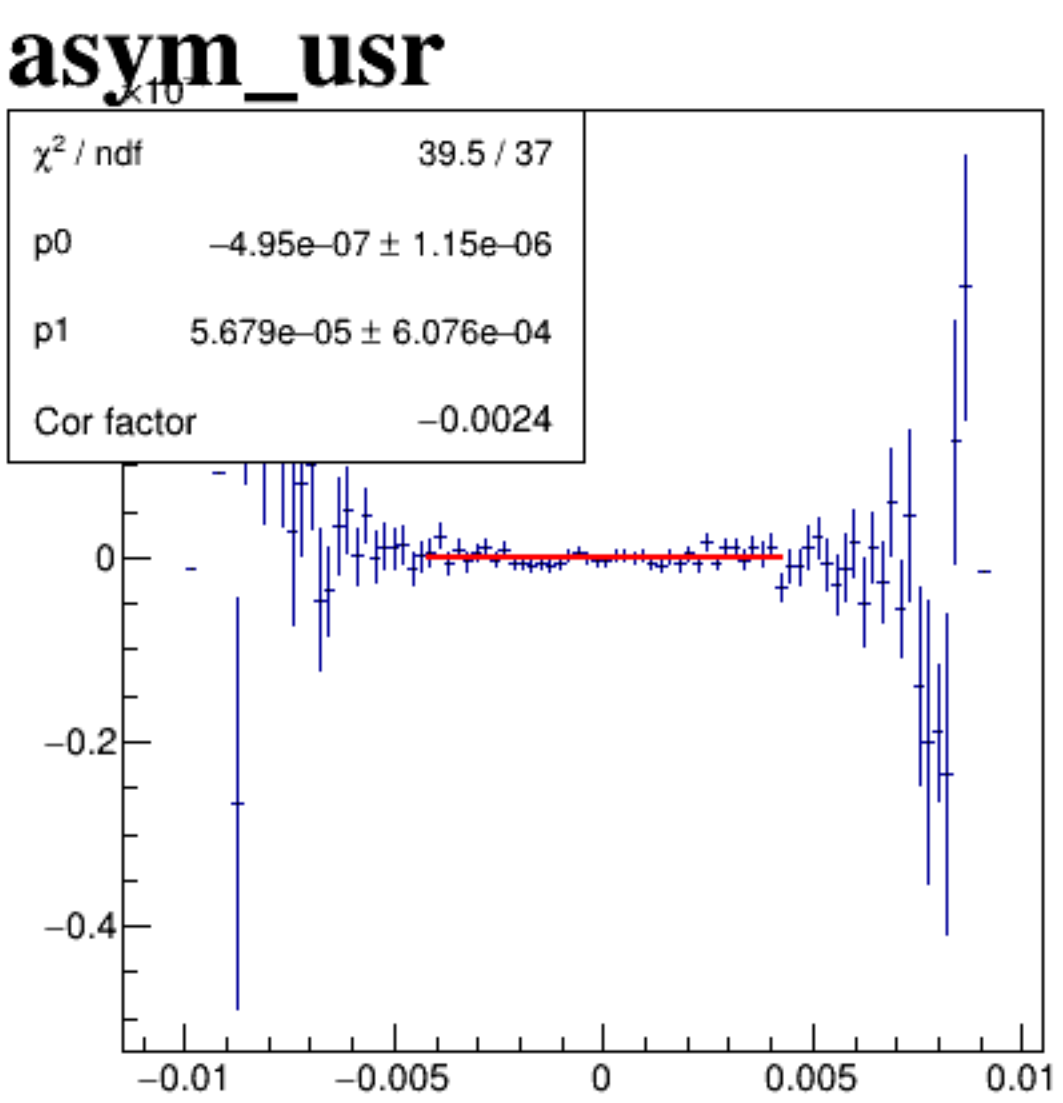
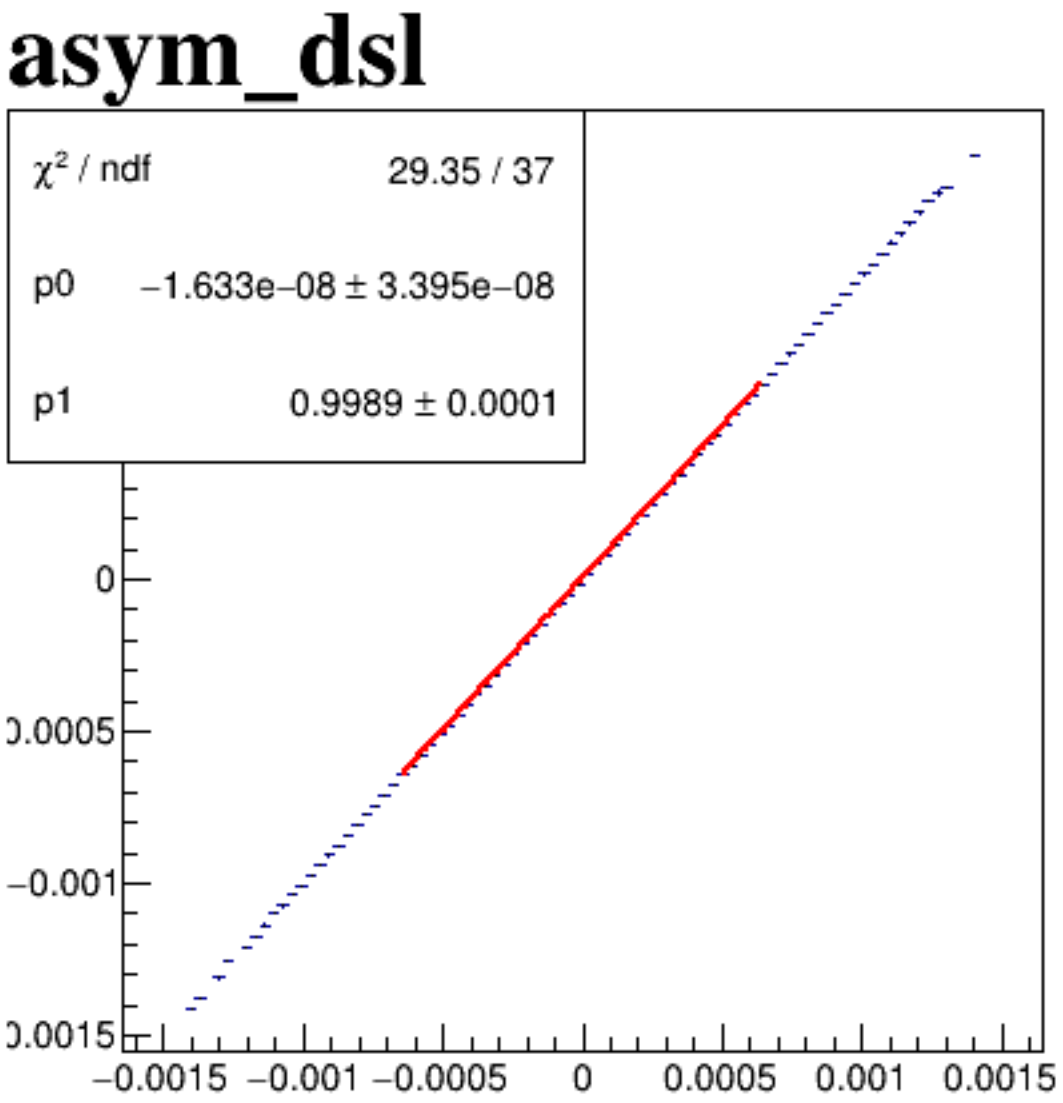
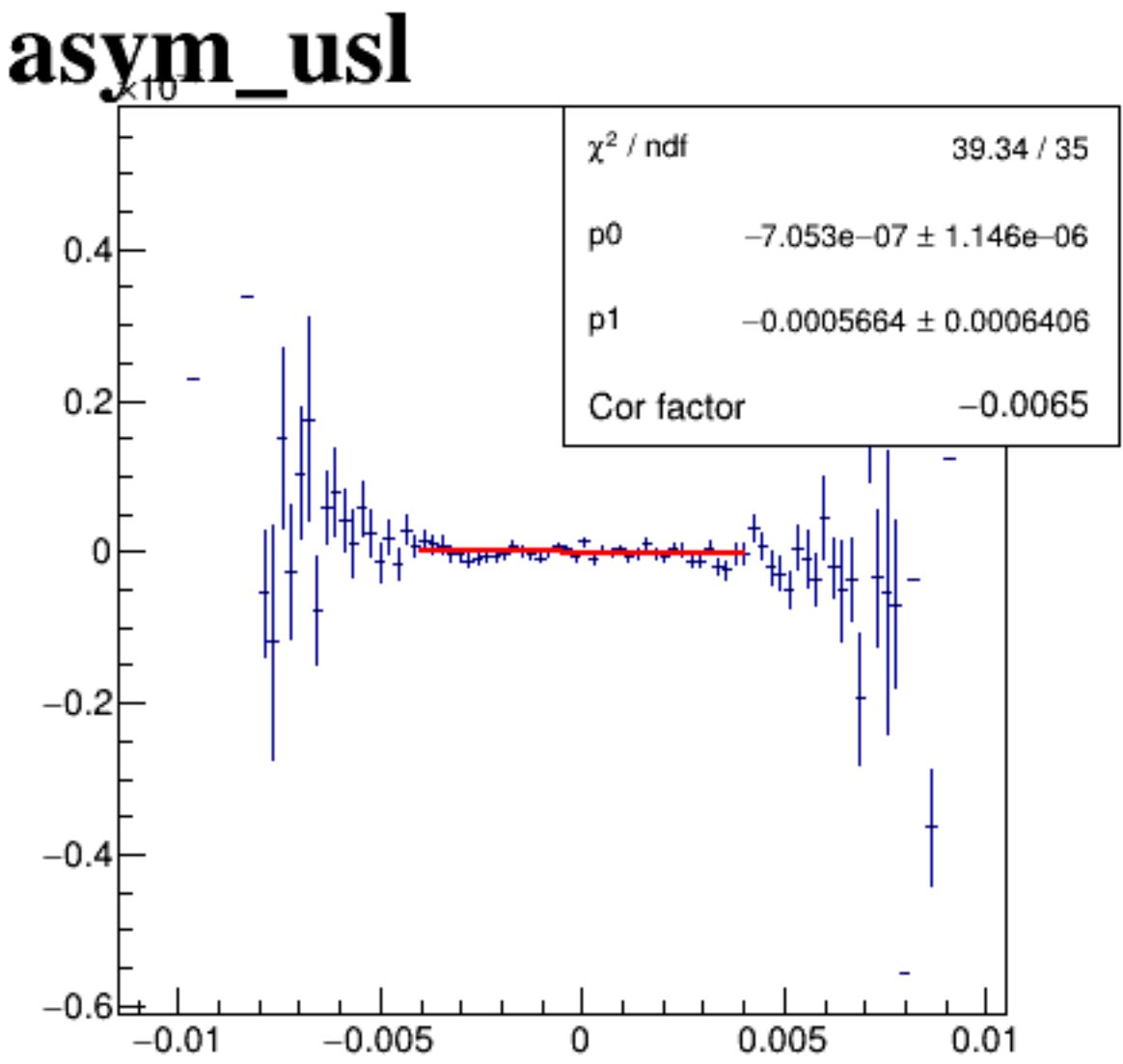




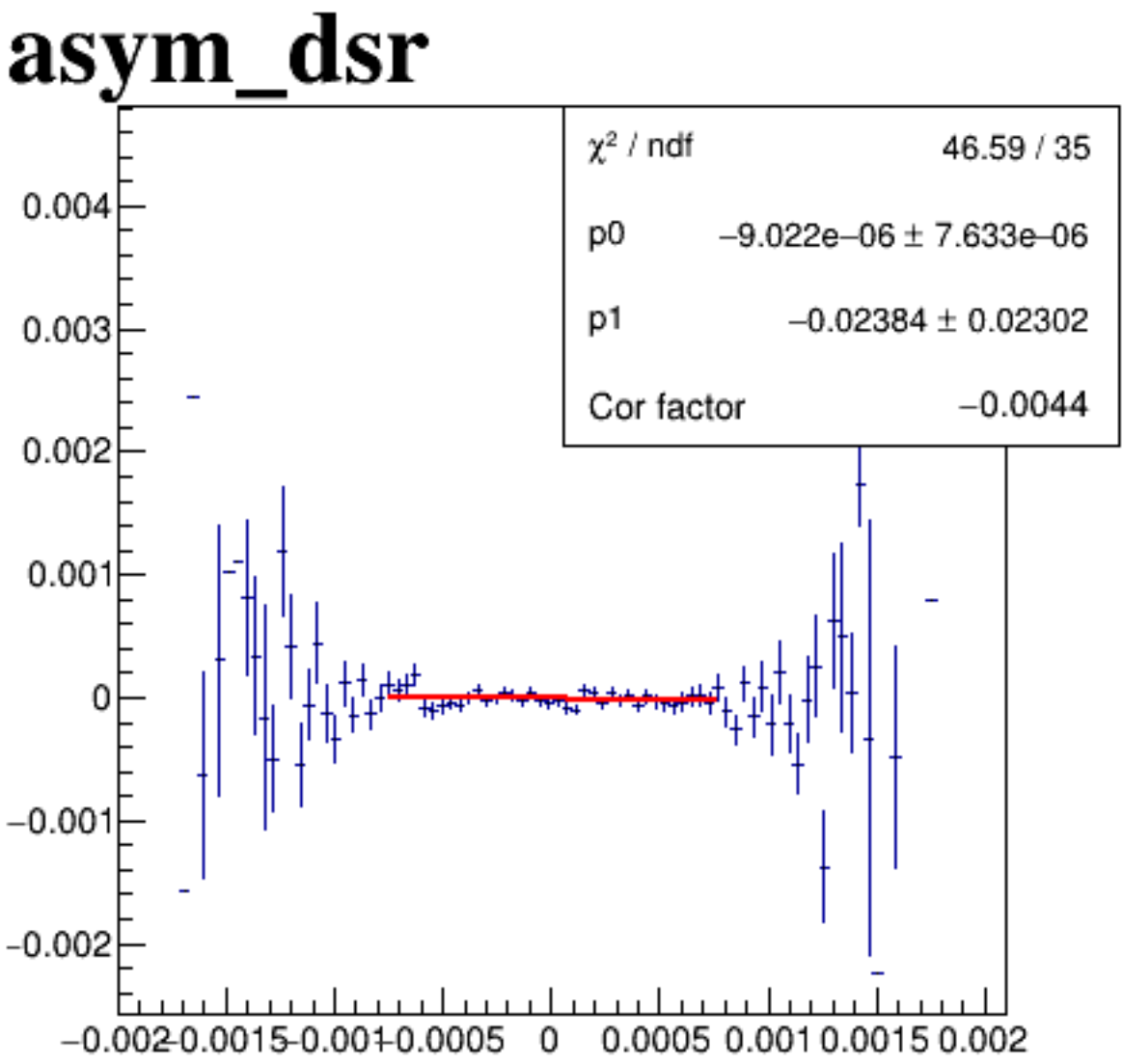
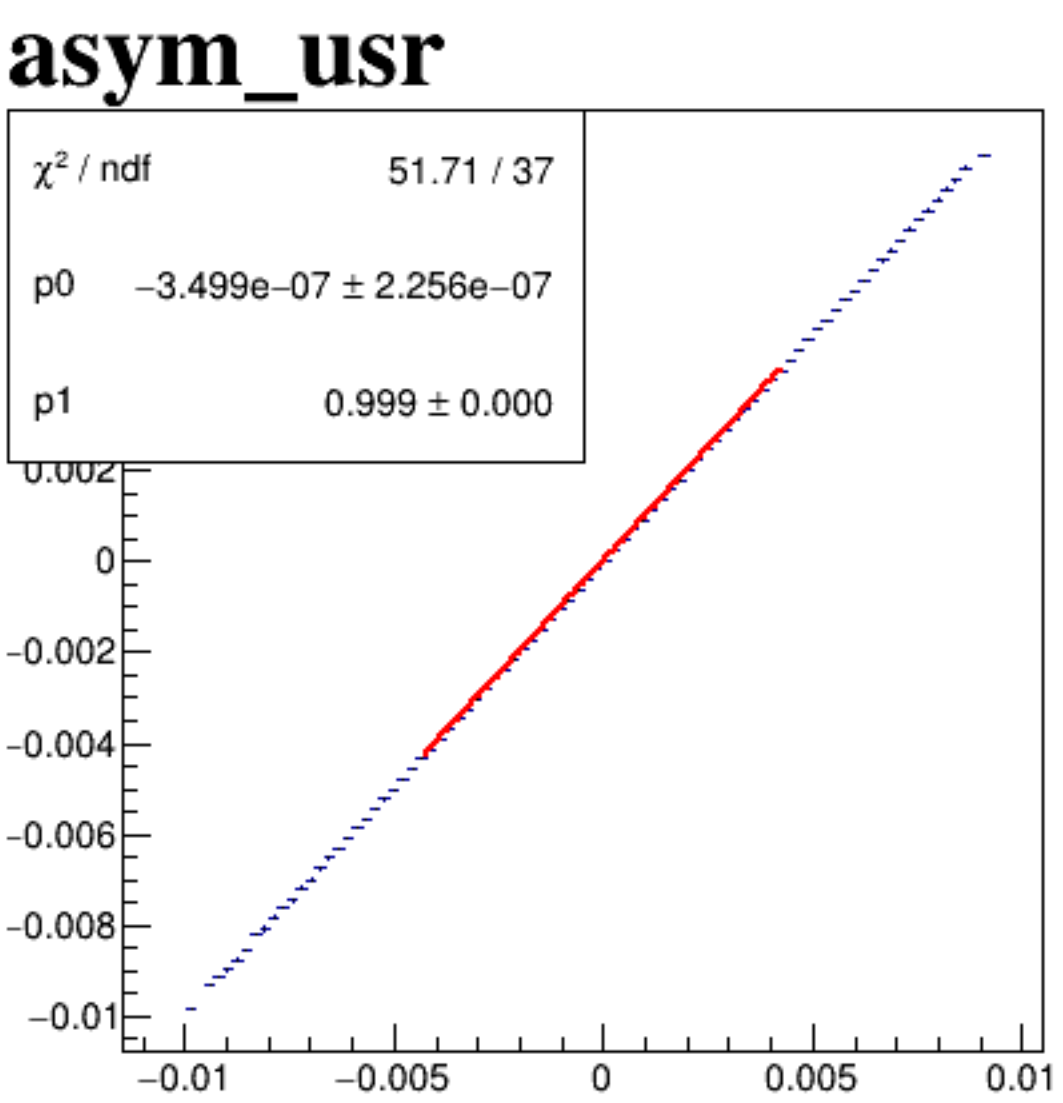
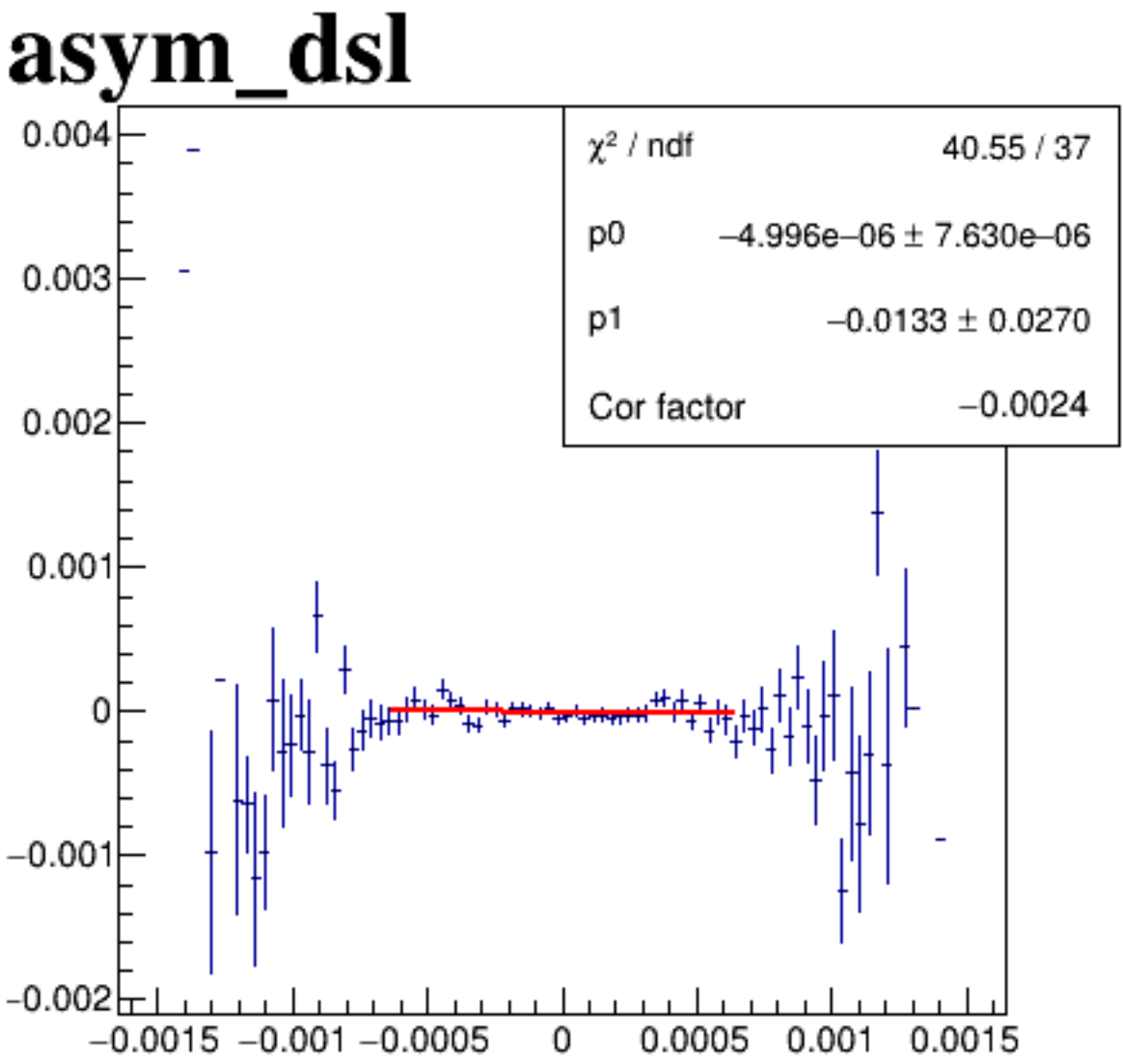
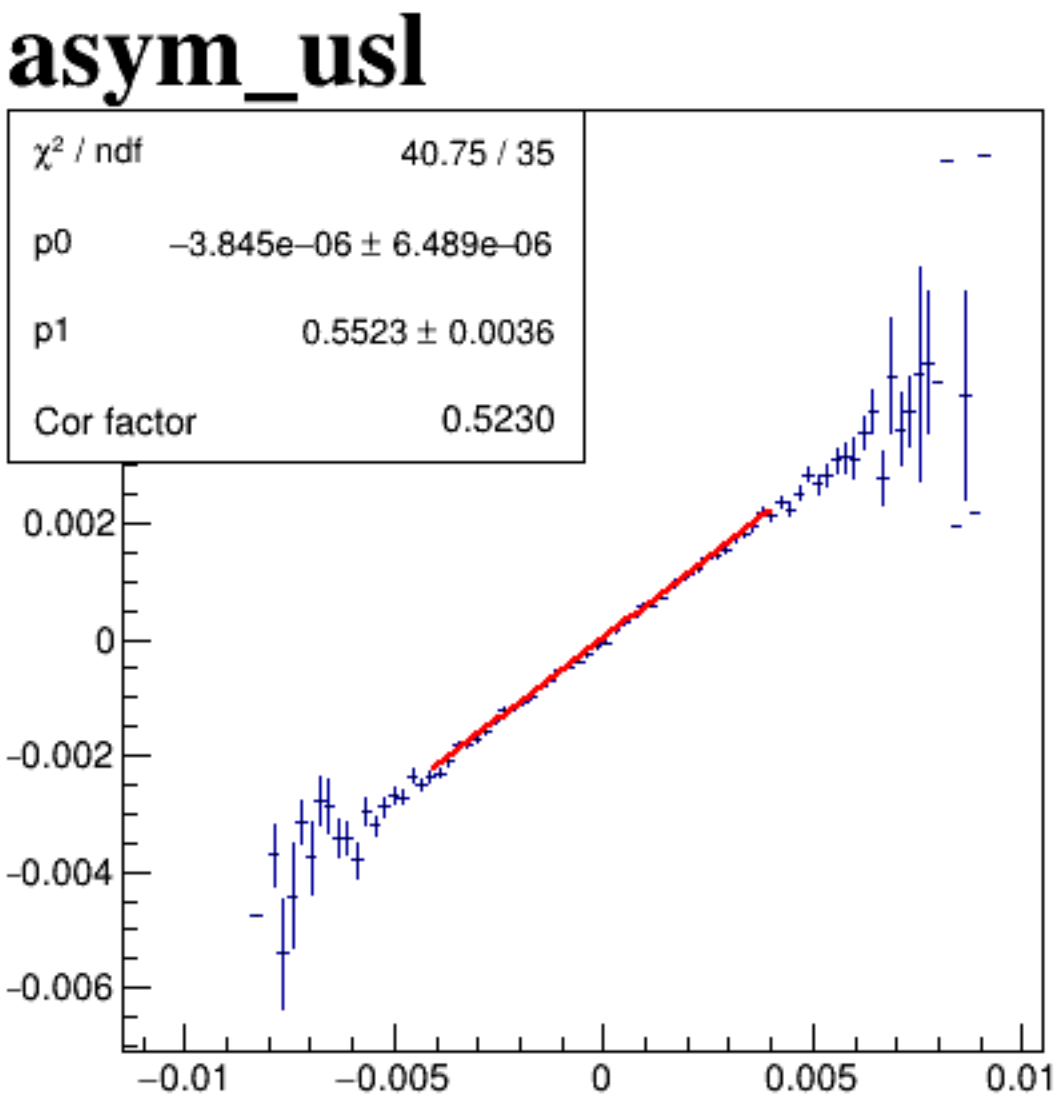
asym_usl



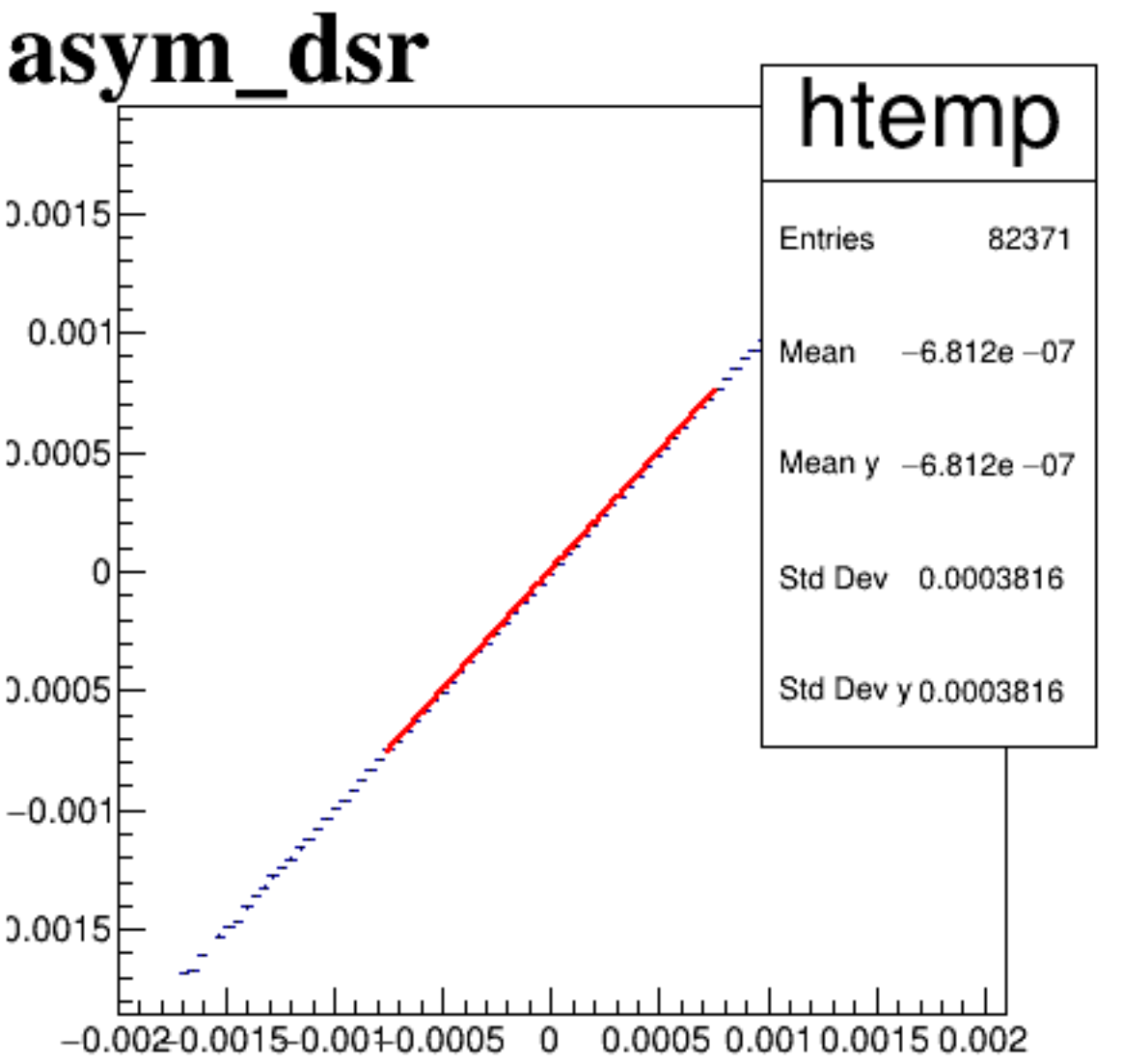
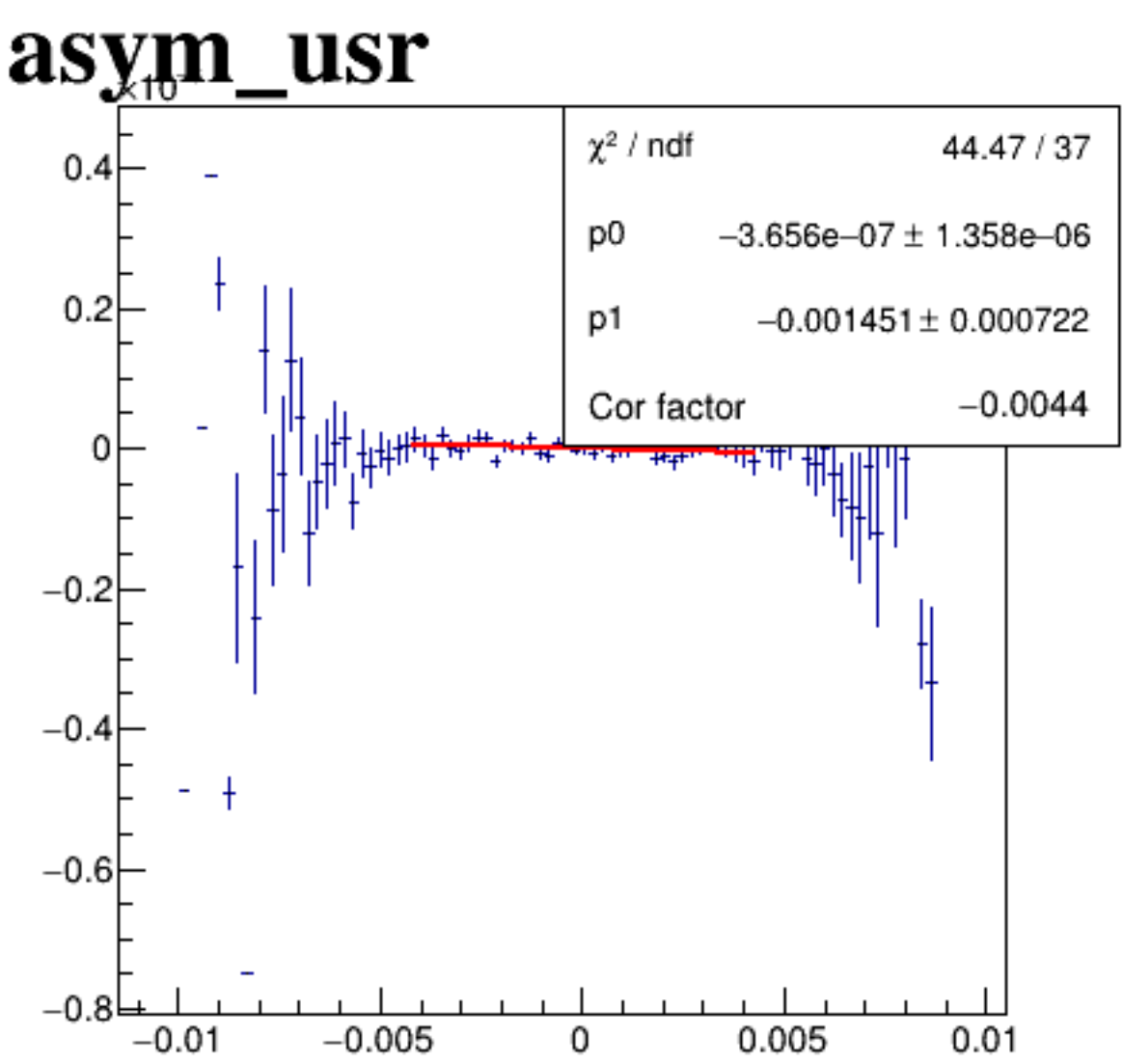
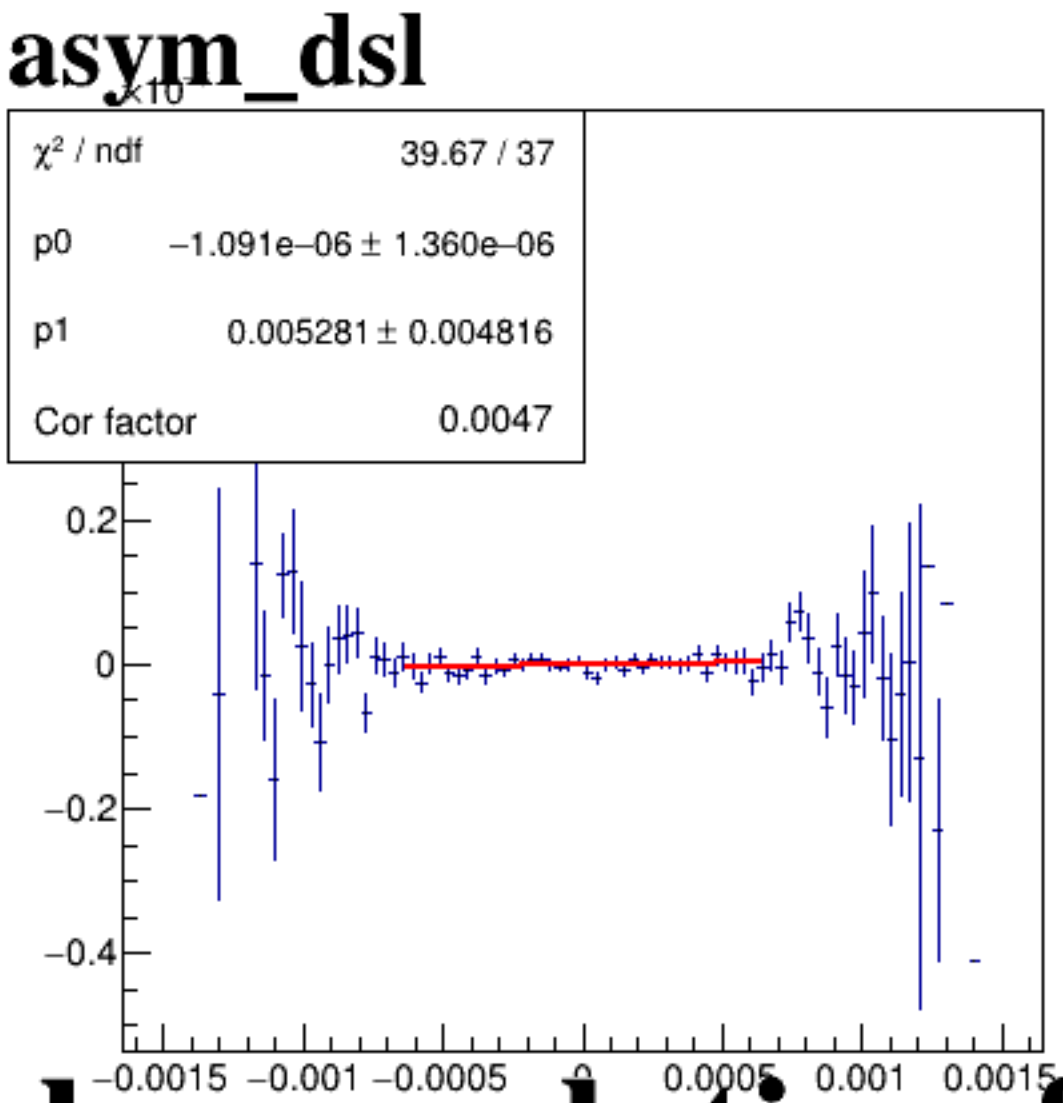
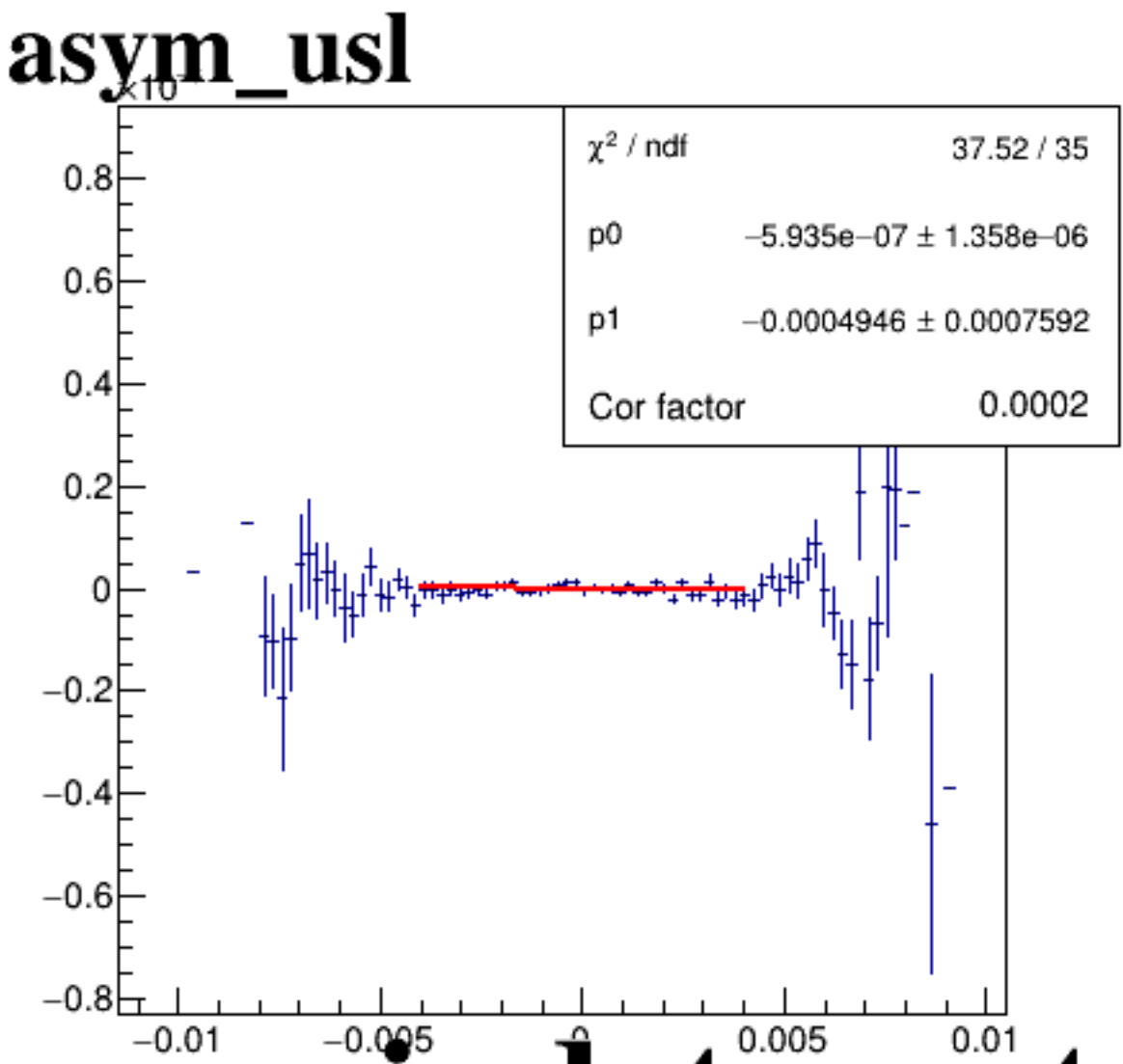
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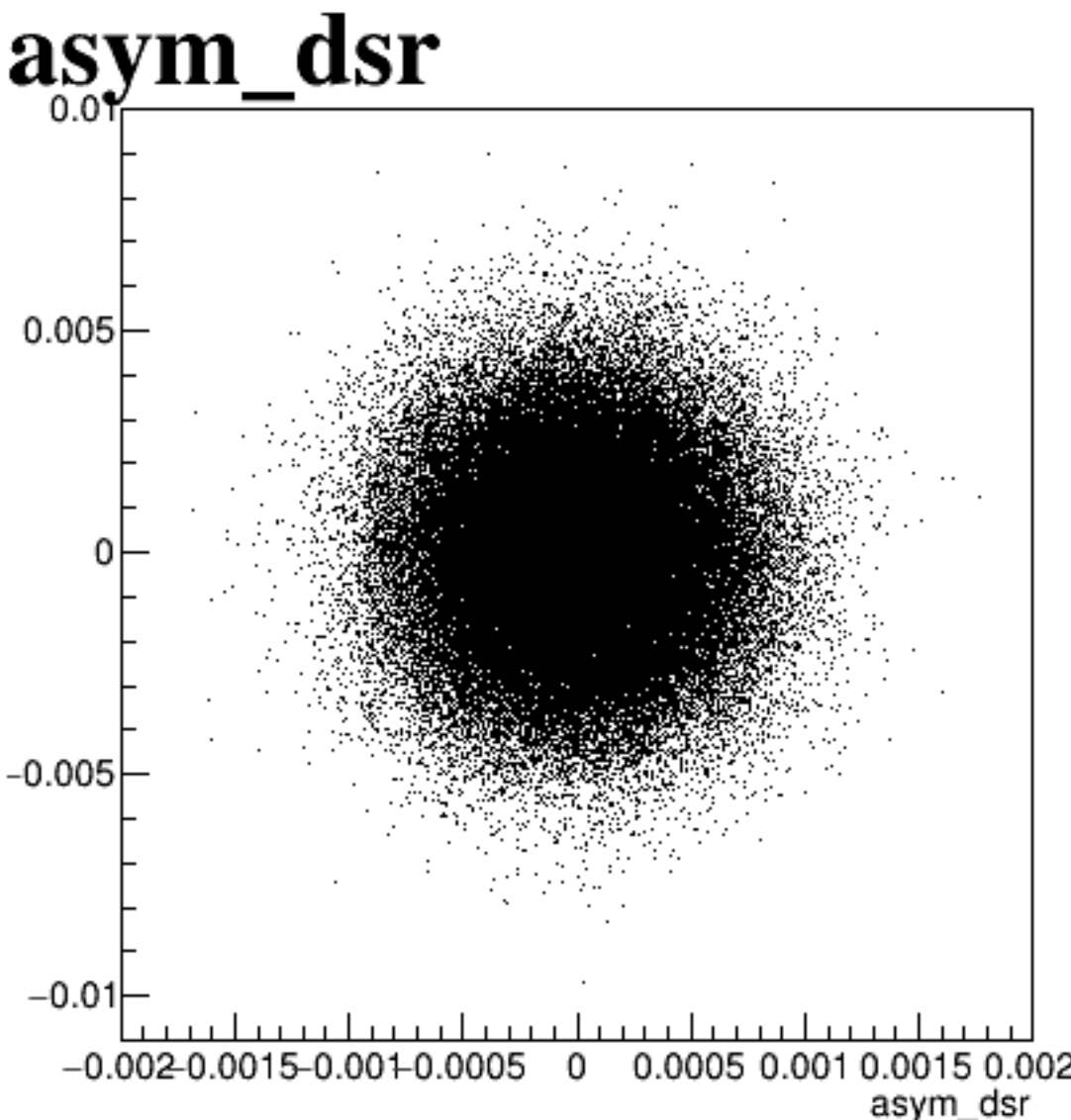
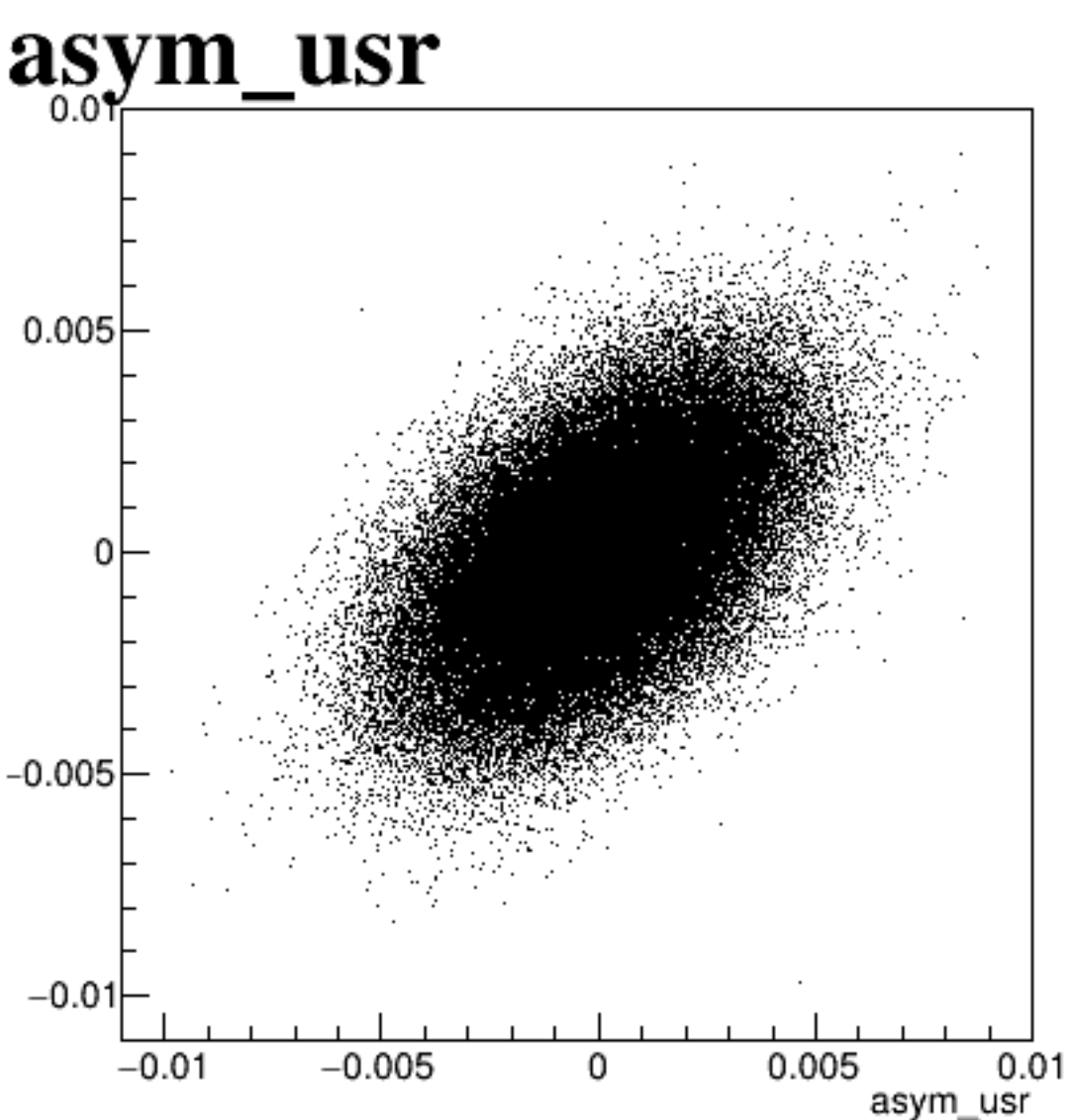
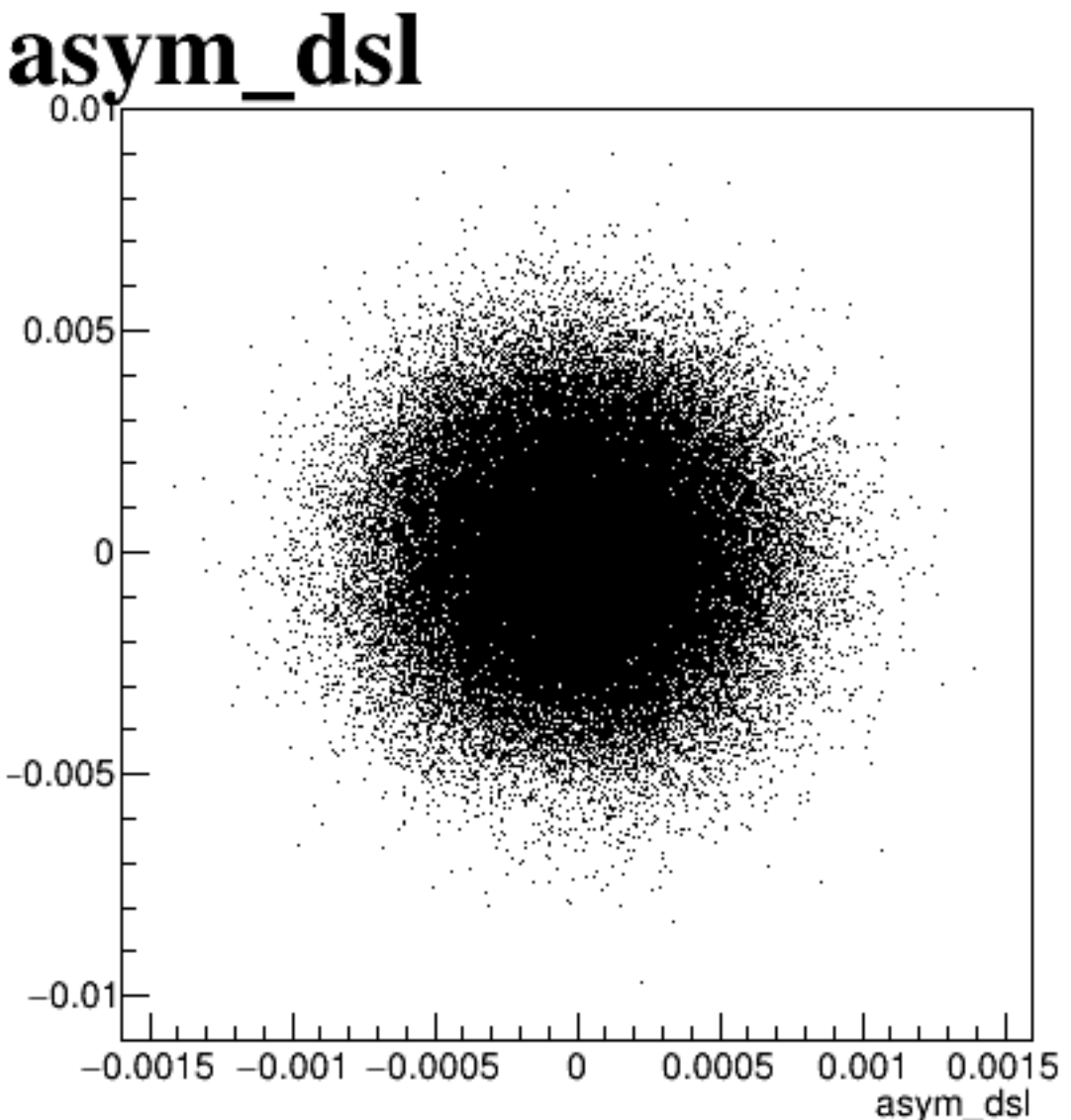
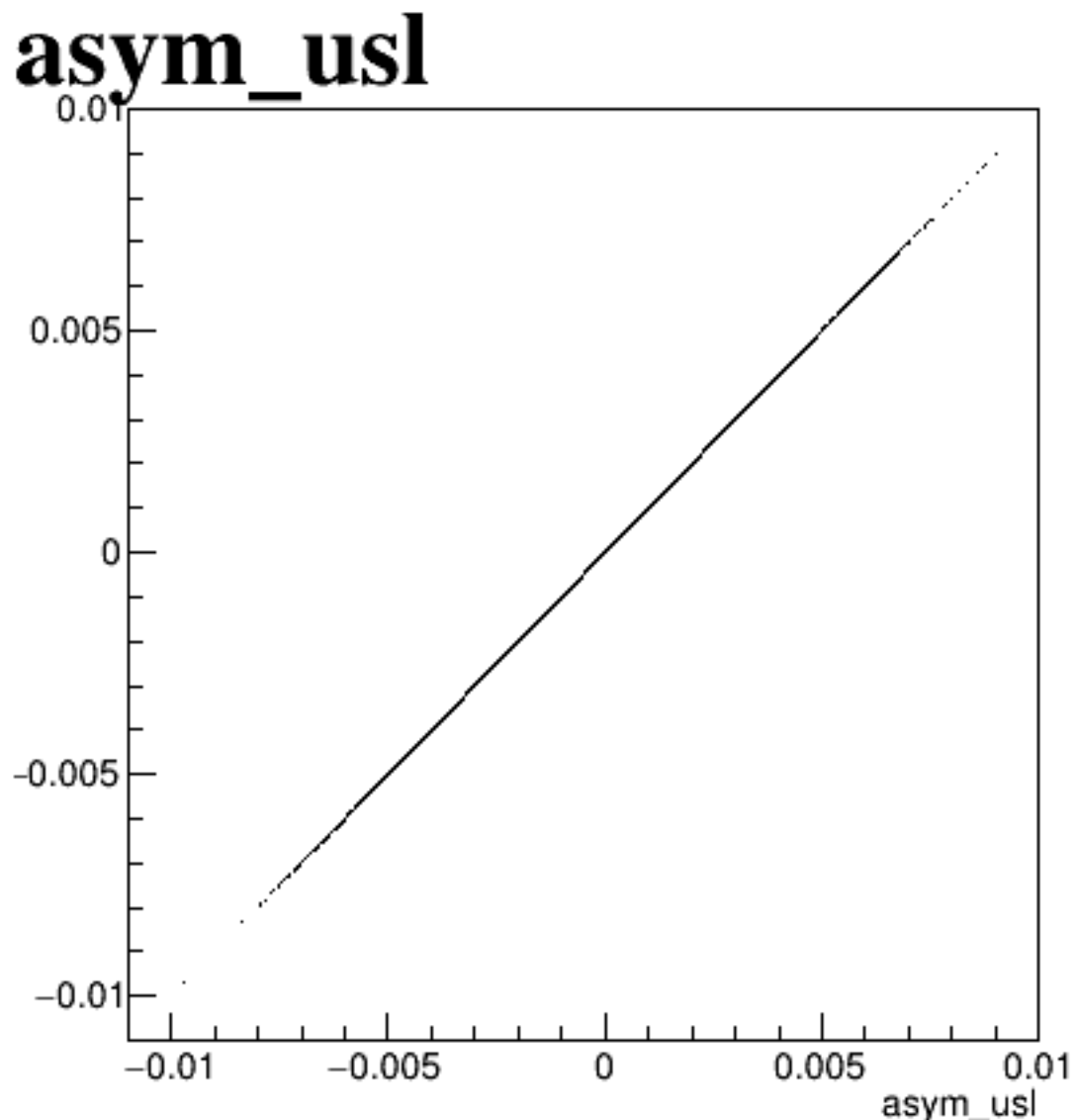
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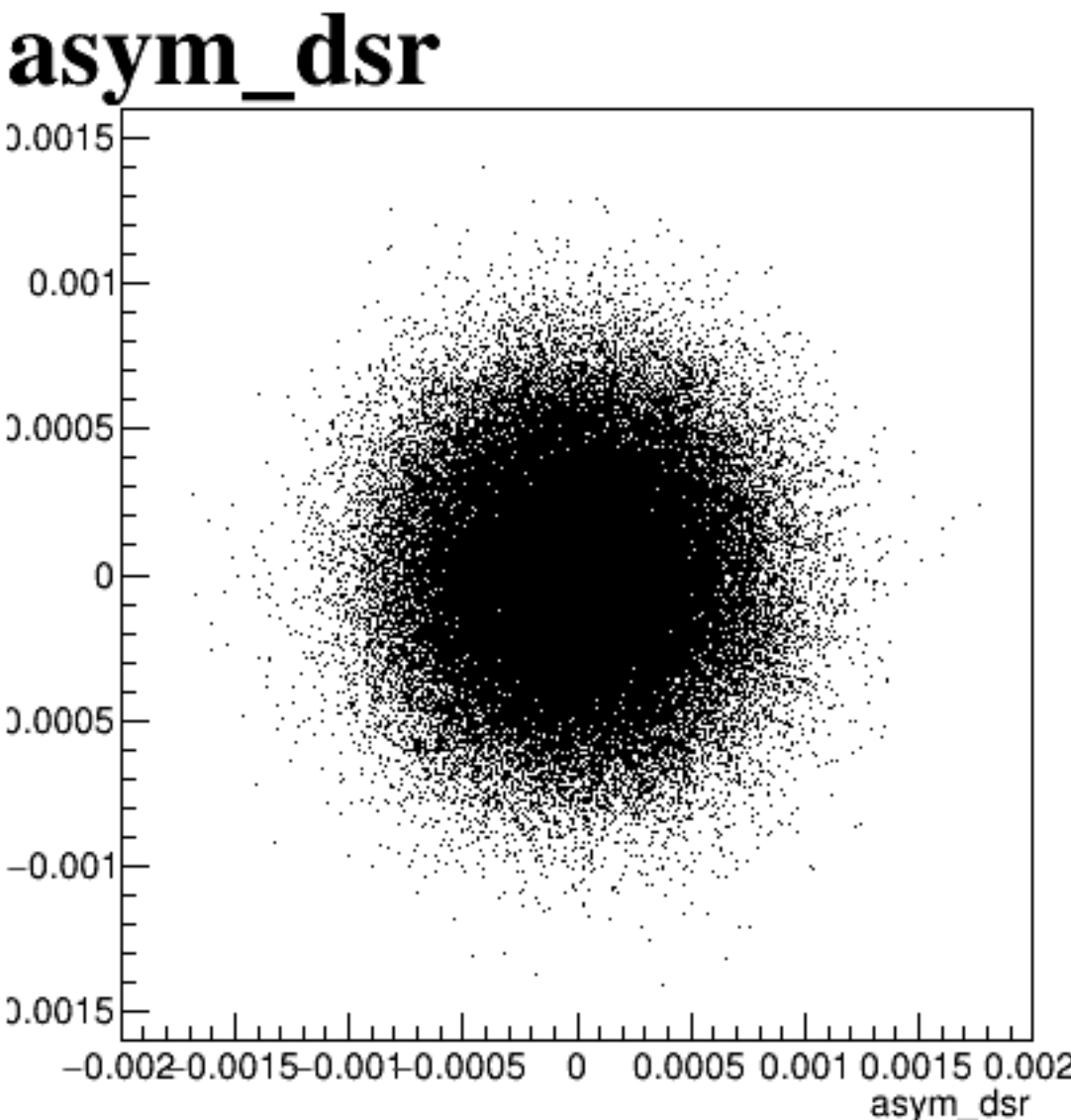
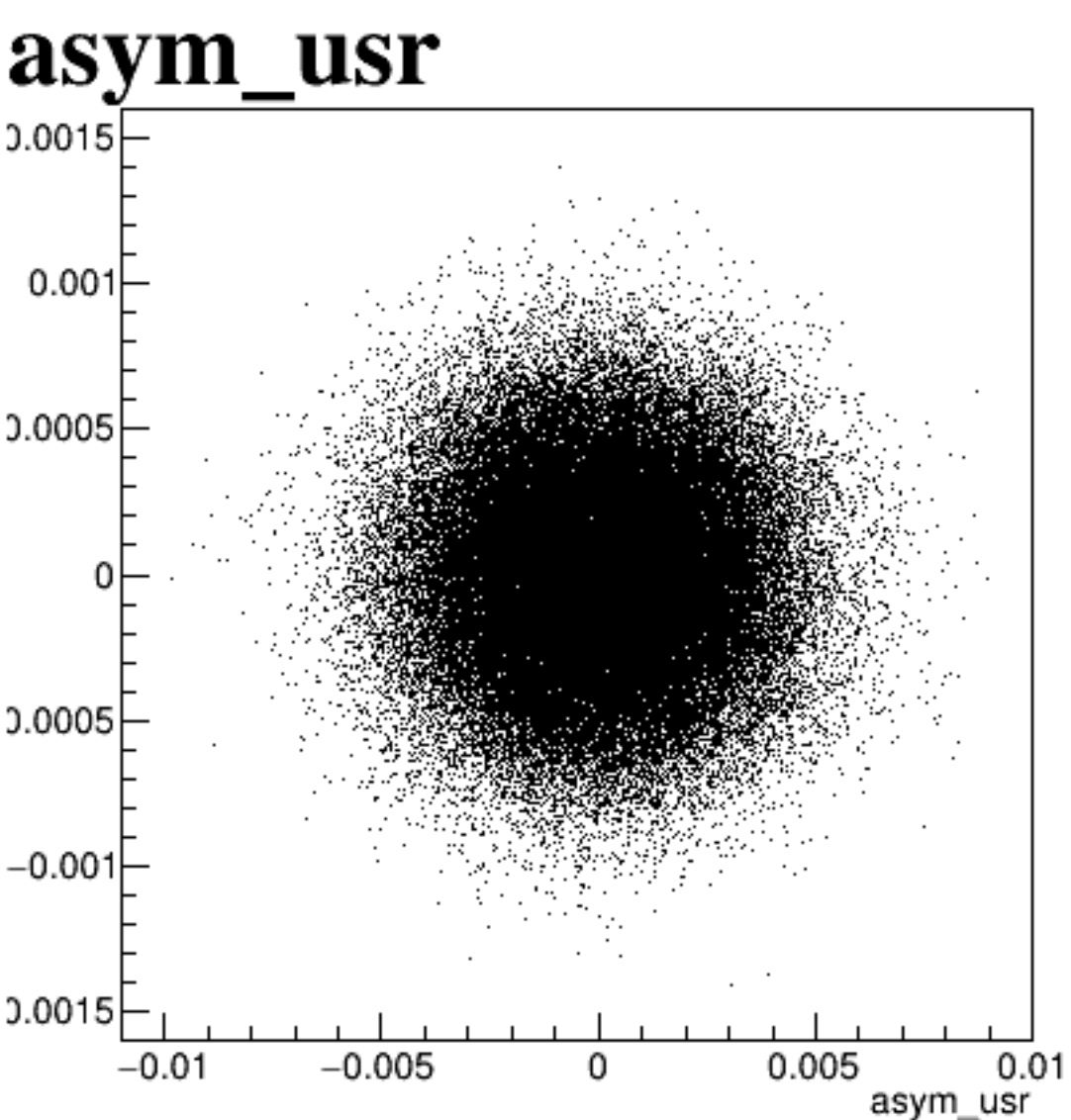
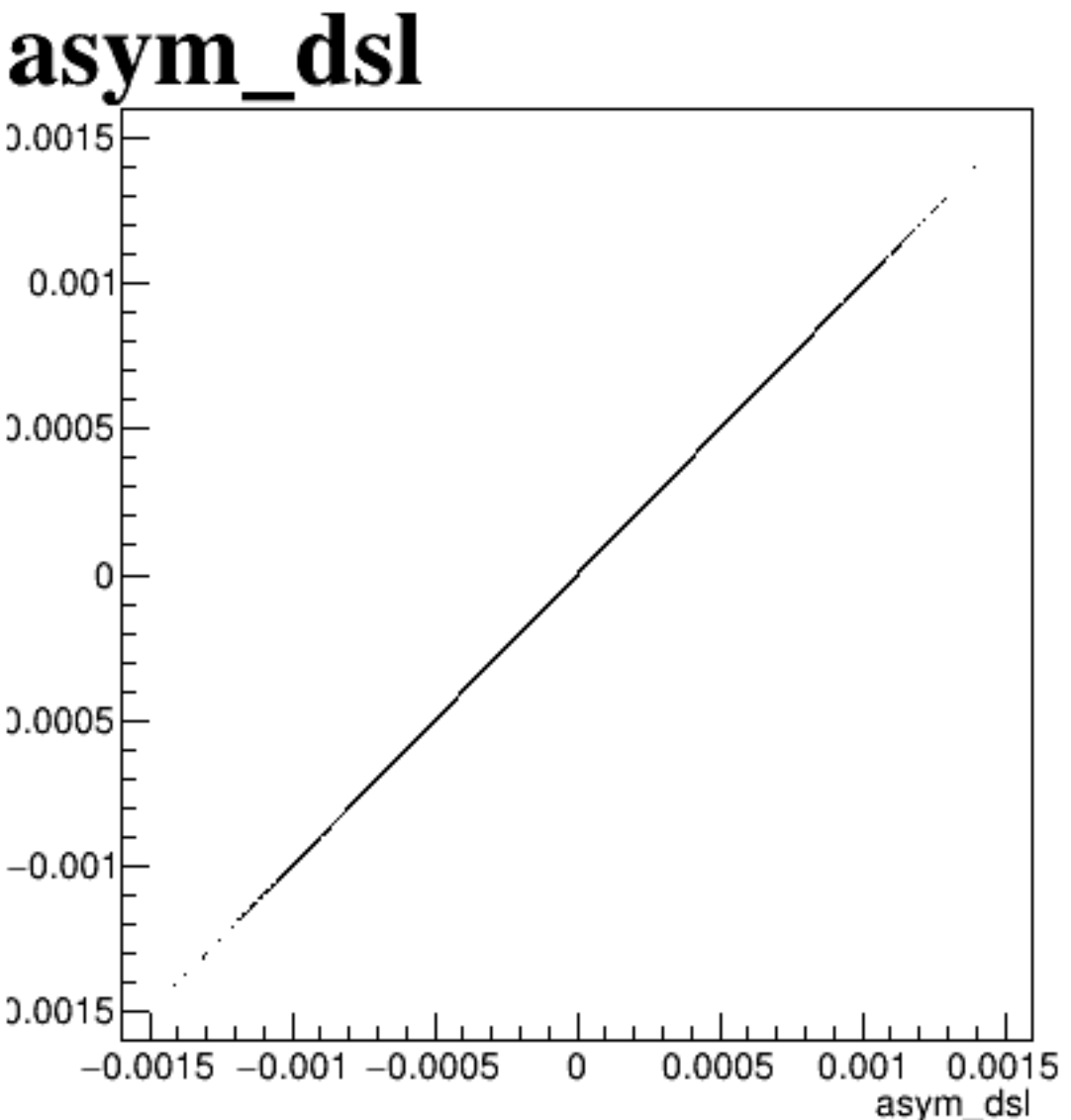
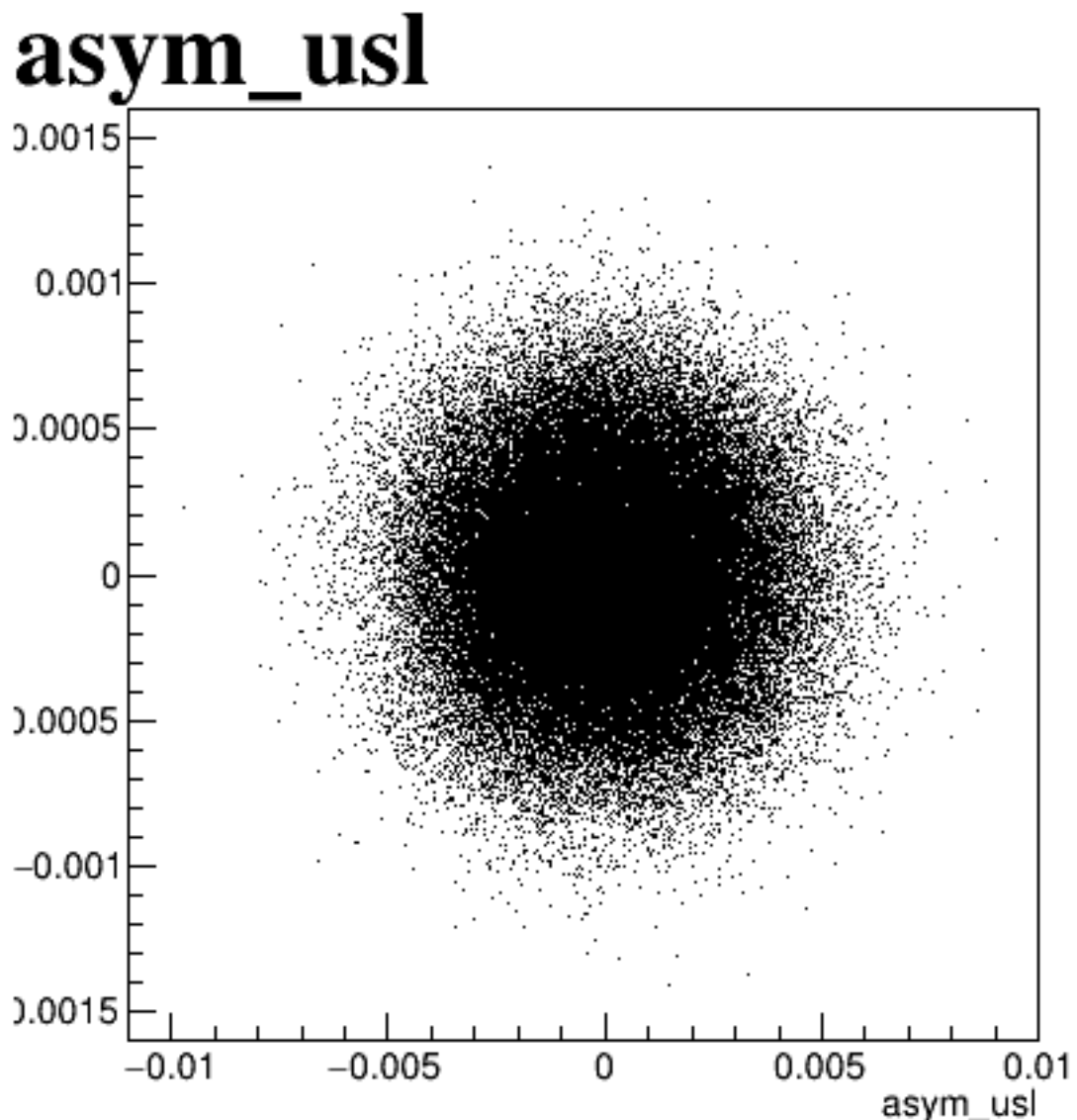
asym_dsr



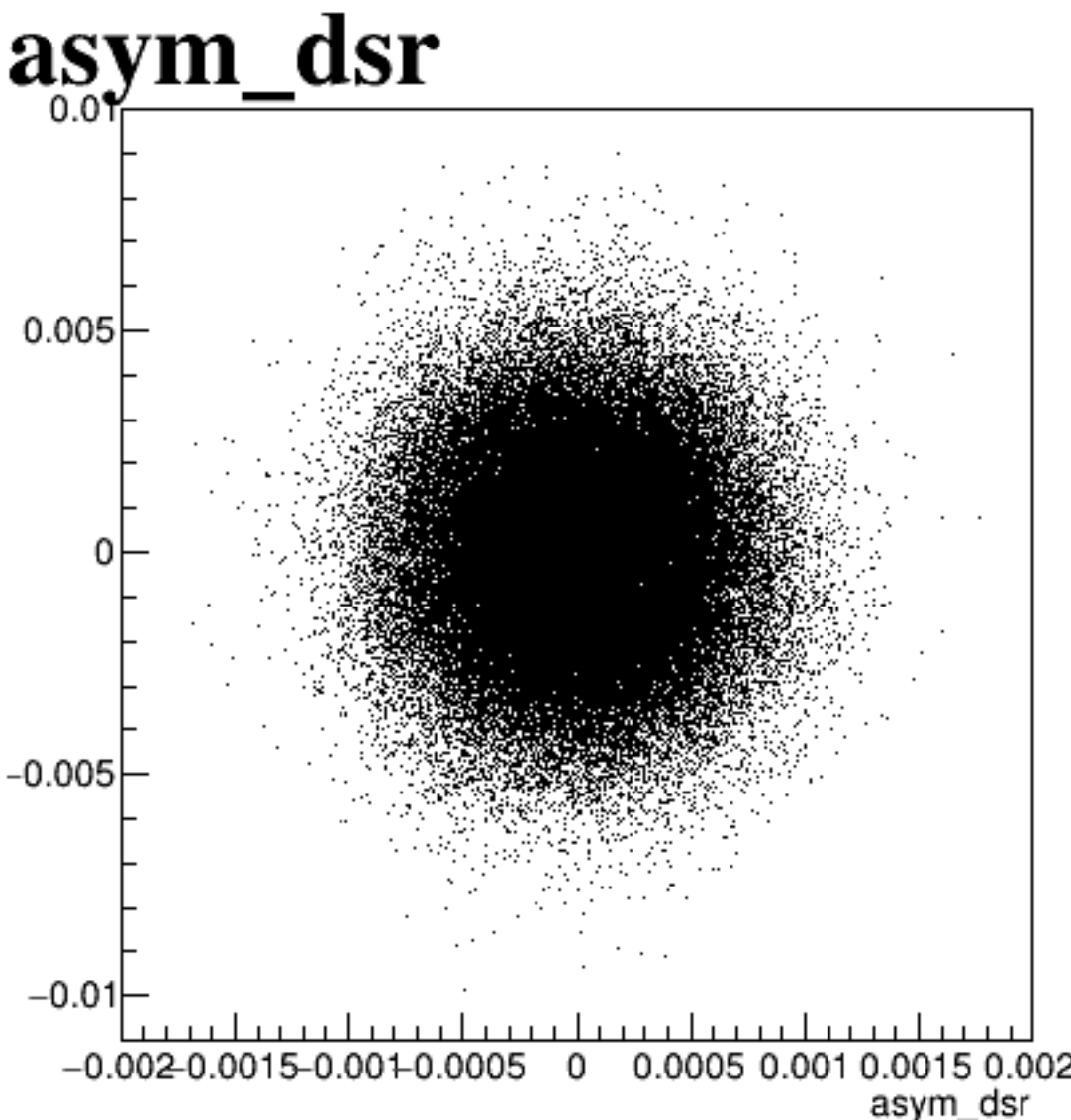
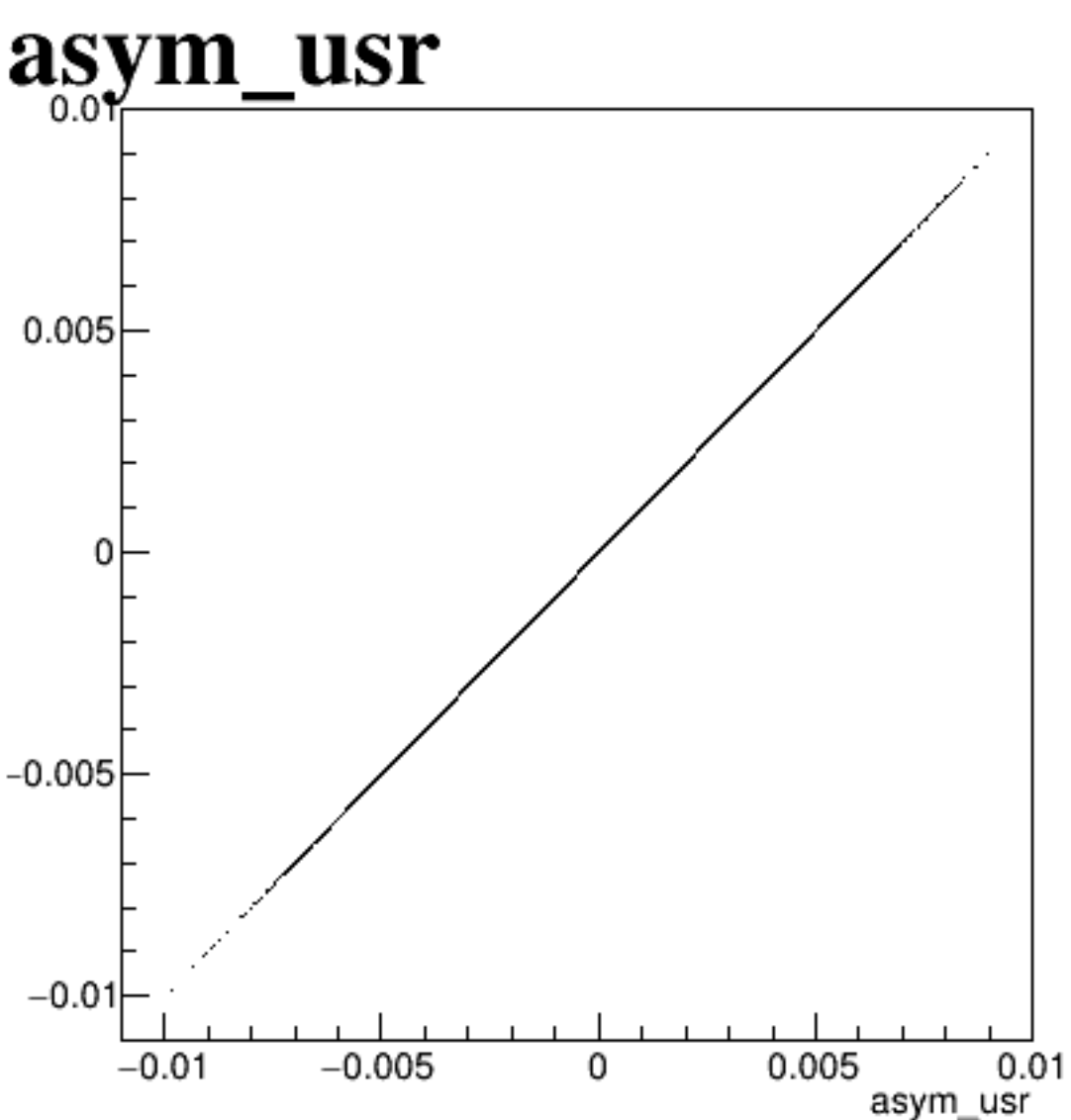
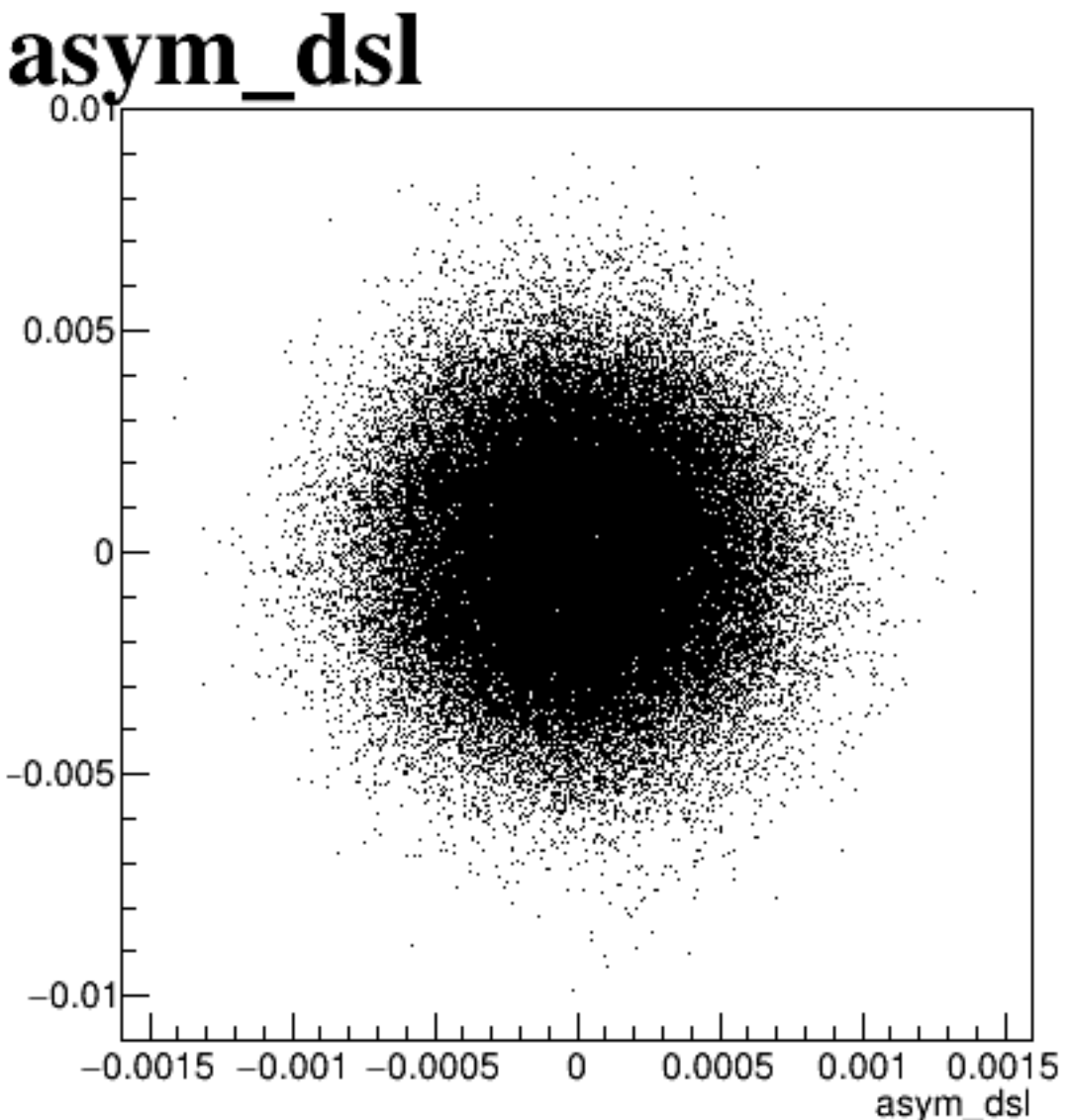
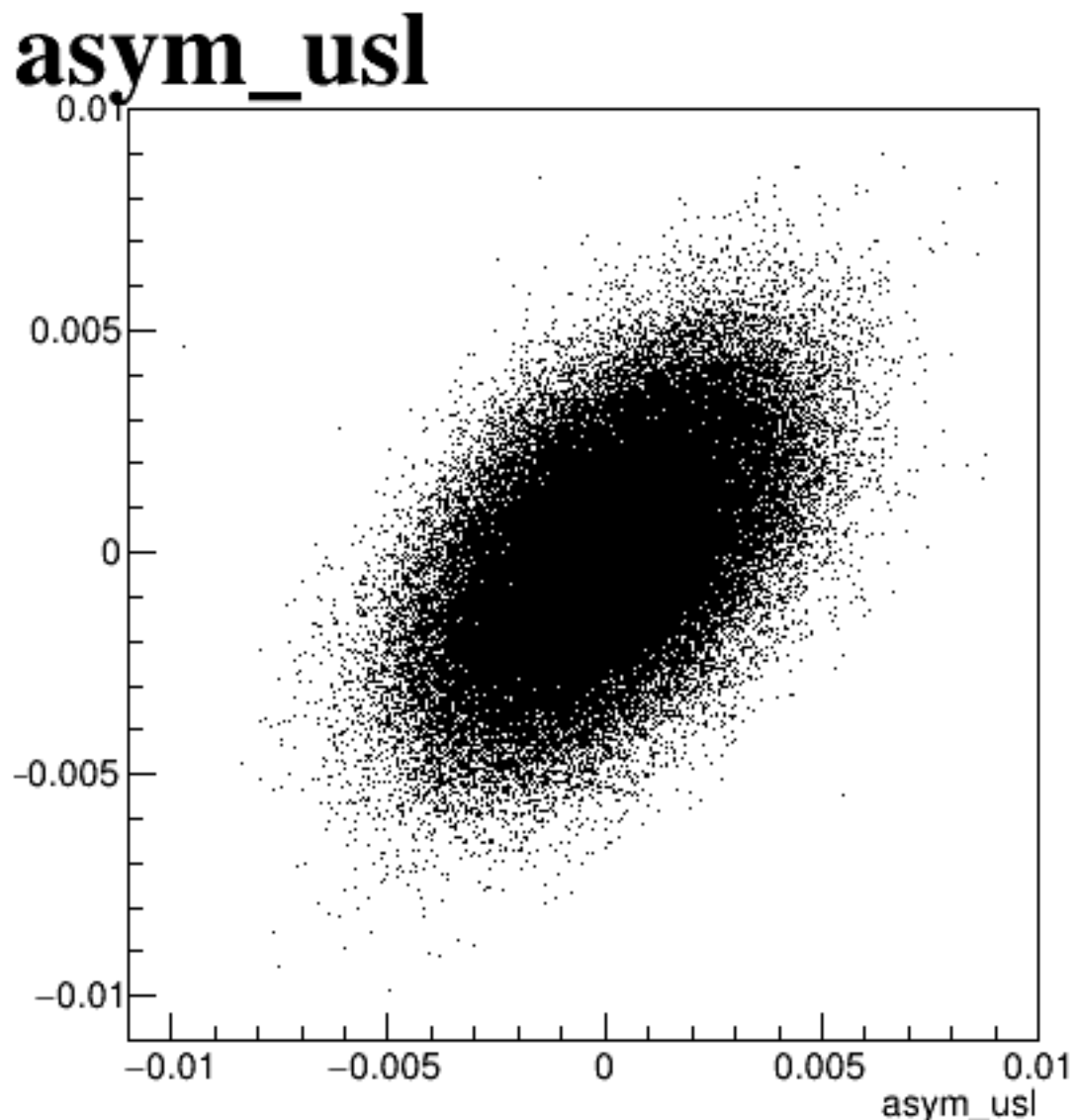
asym_usl



asym_dsl



asym_usr



asym_dsr

