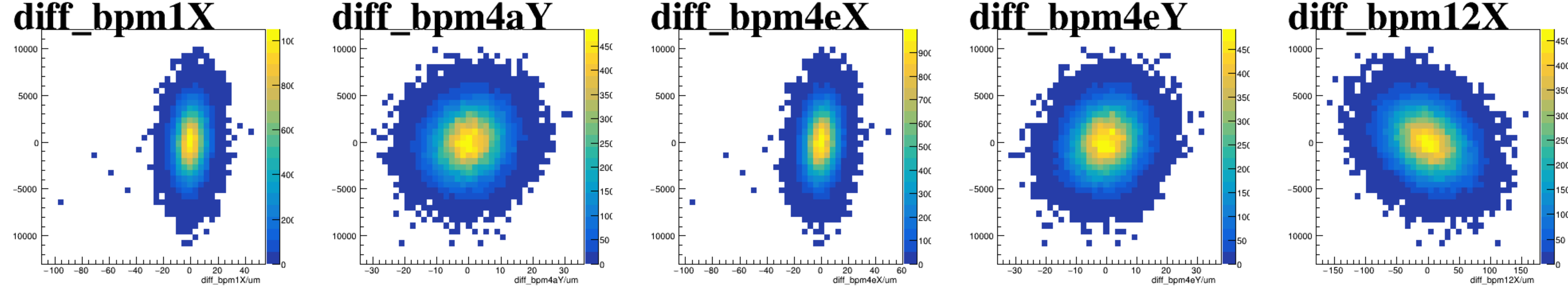
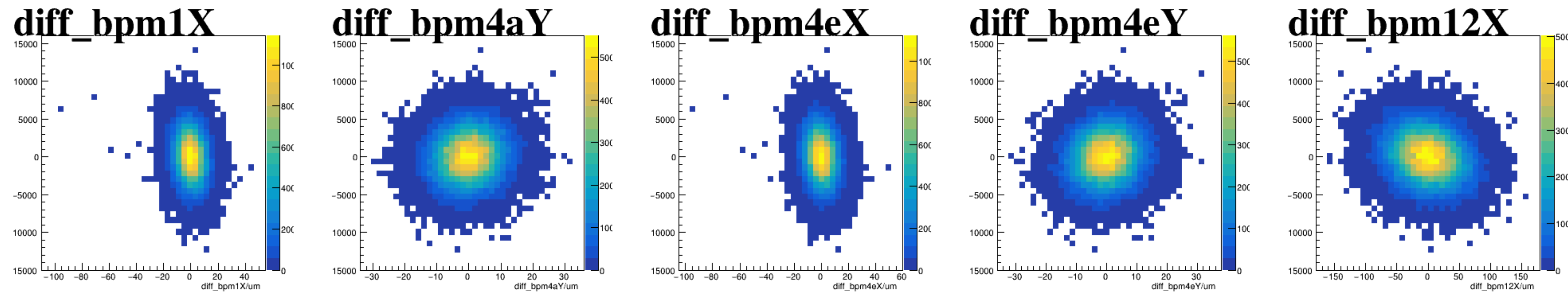


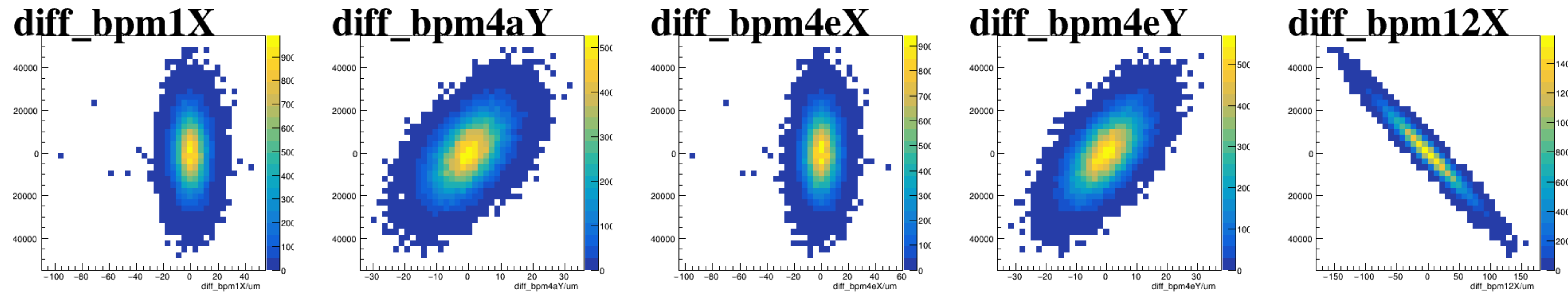
asym\_atl1



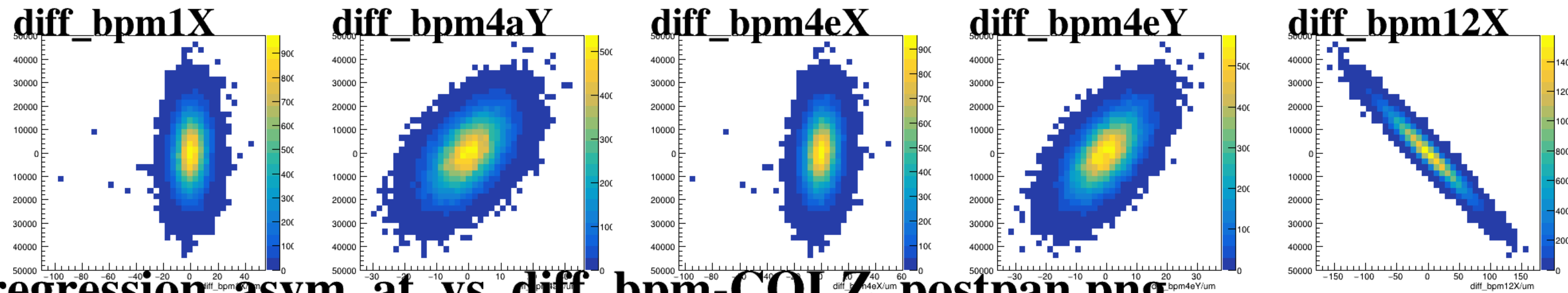
asym\_atr1



asym\_atl2

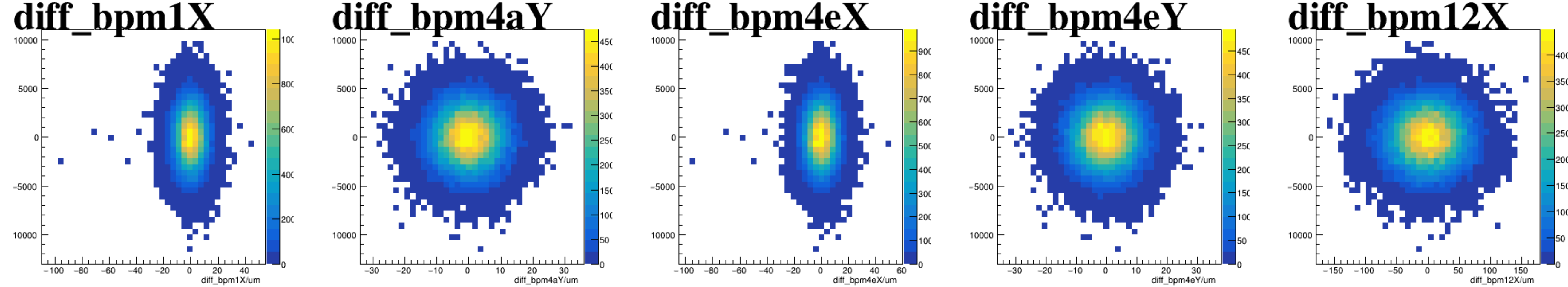


asym\_atr2

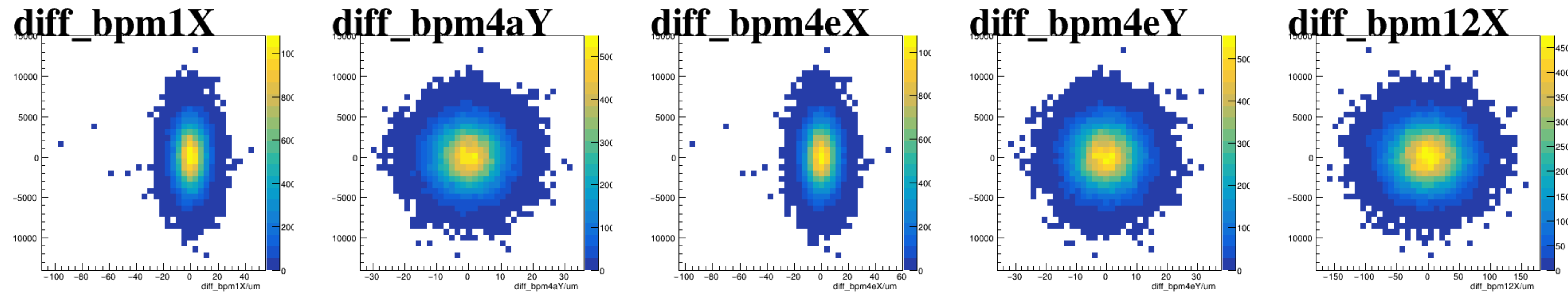




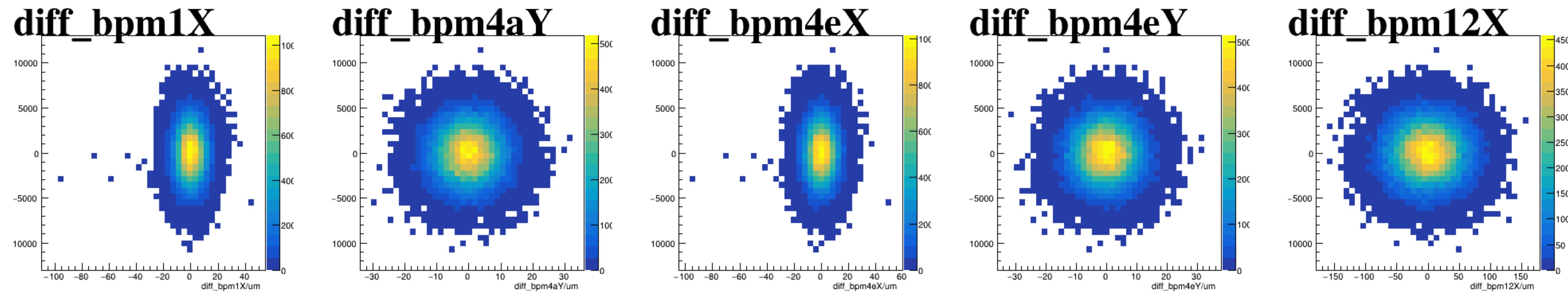
reg\_asym\_atl1



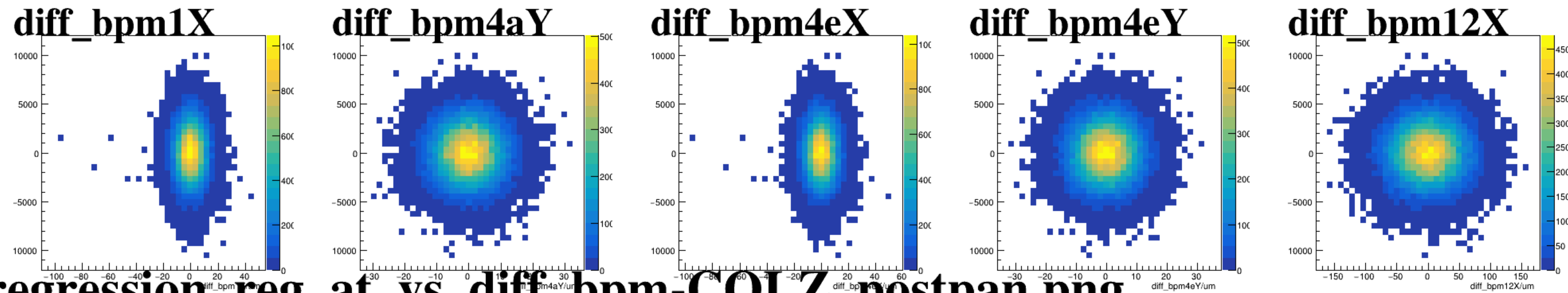
reg\_asym\_atr1



reg\_asym\_atl2

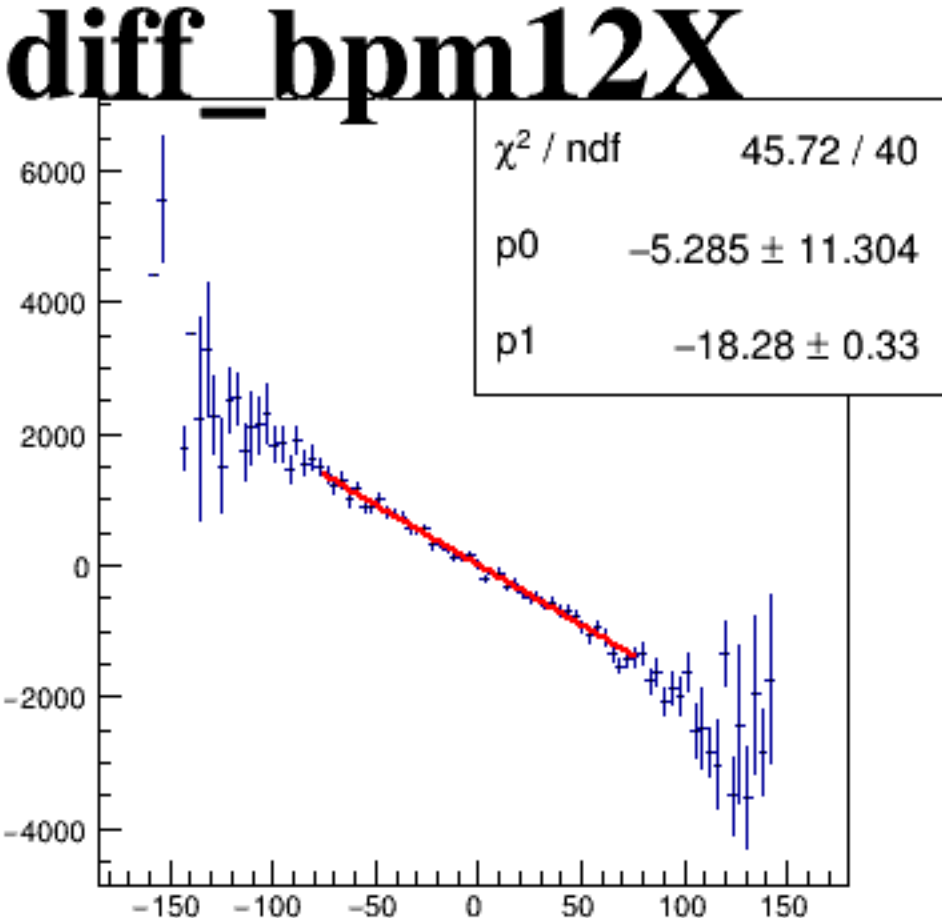
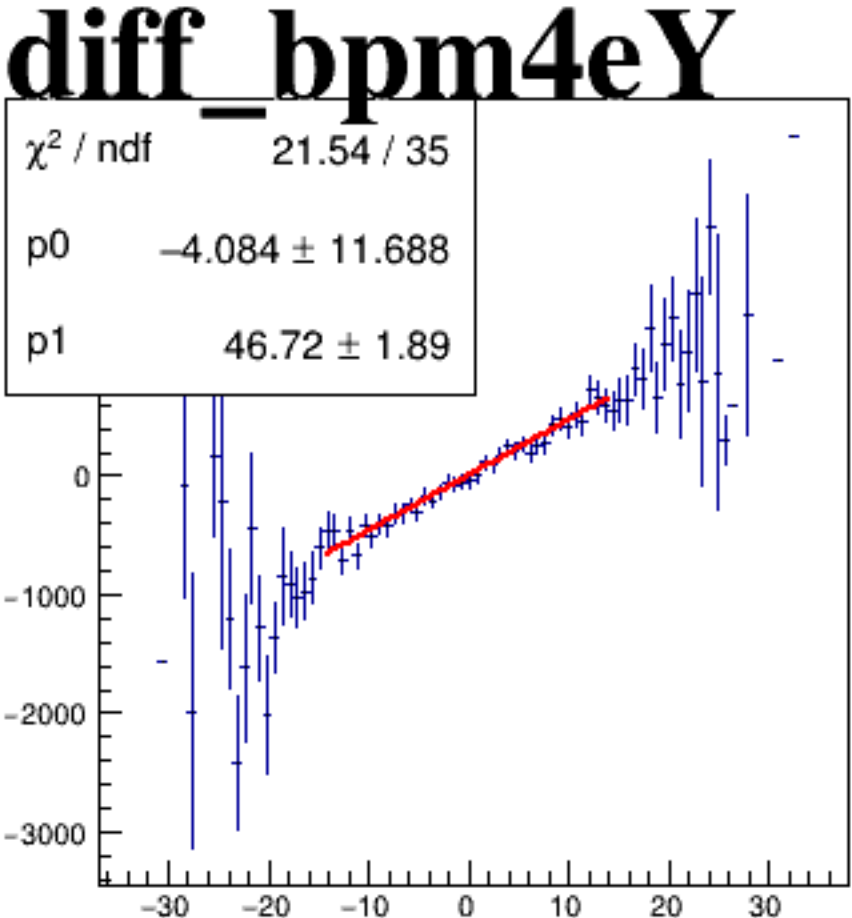
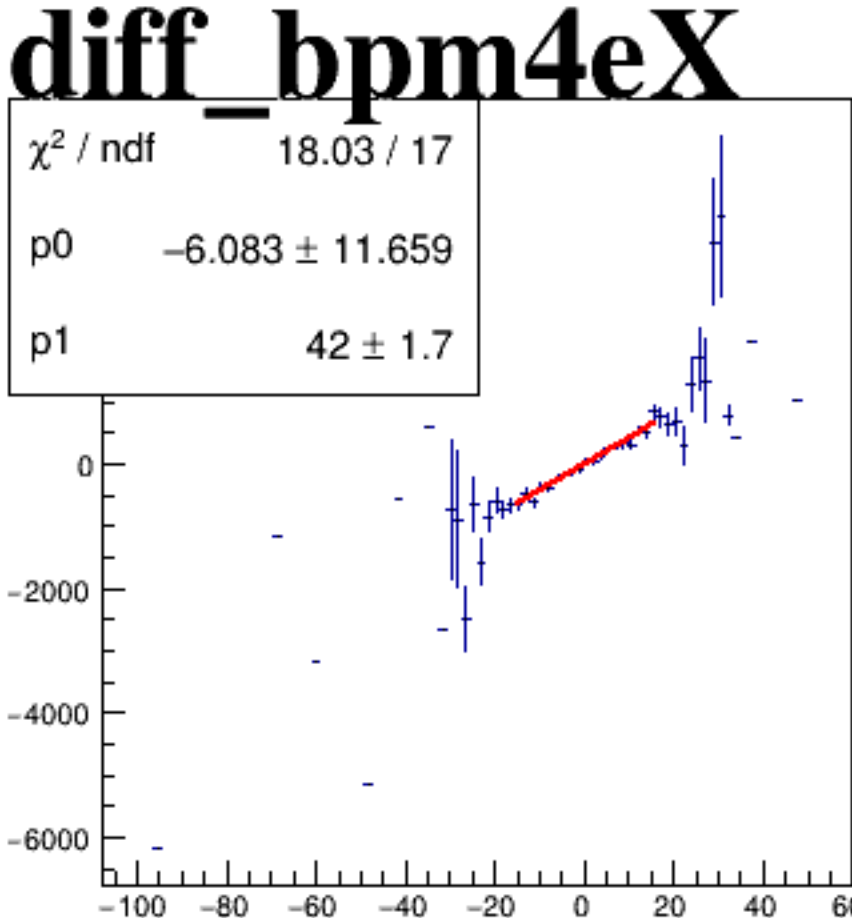
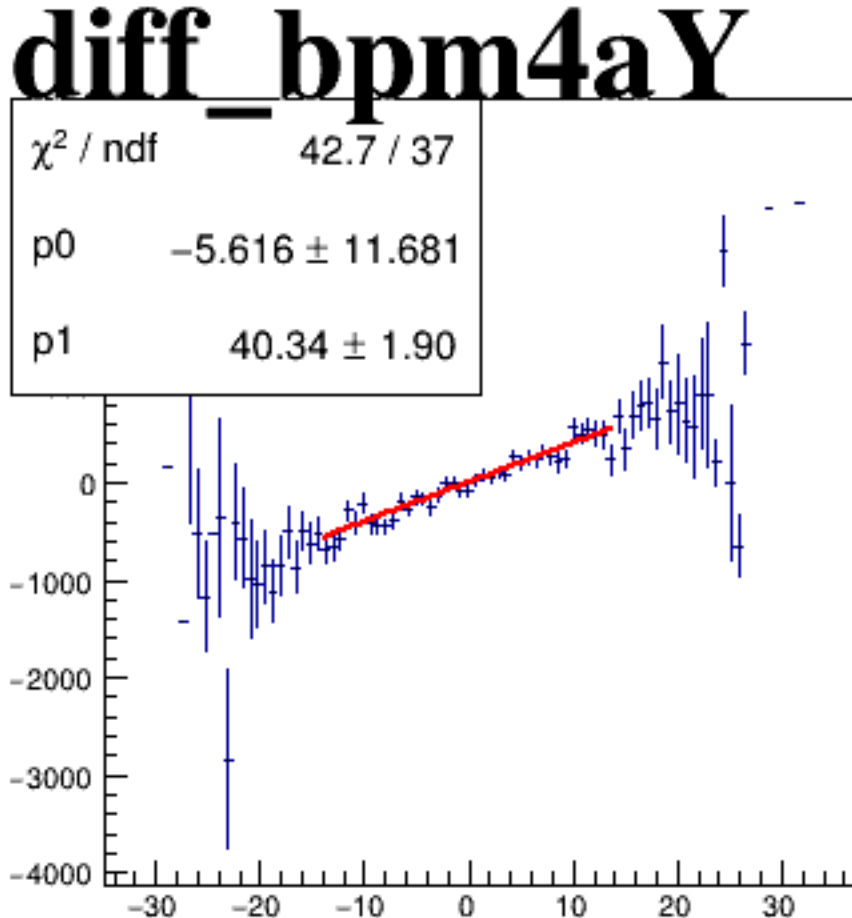
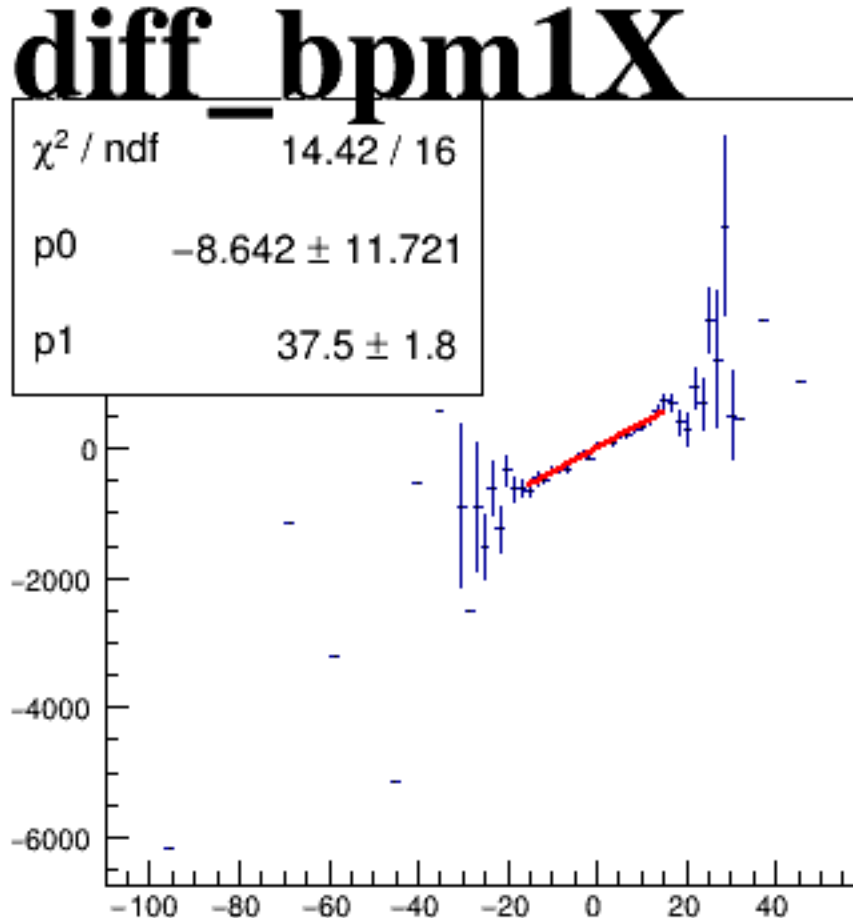


reg\_asym\_atr2

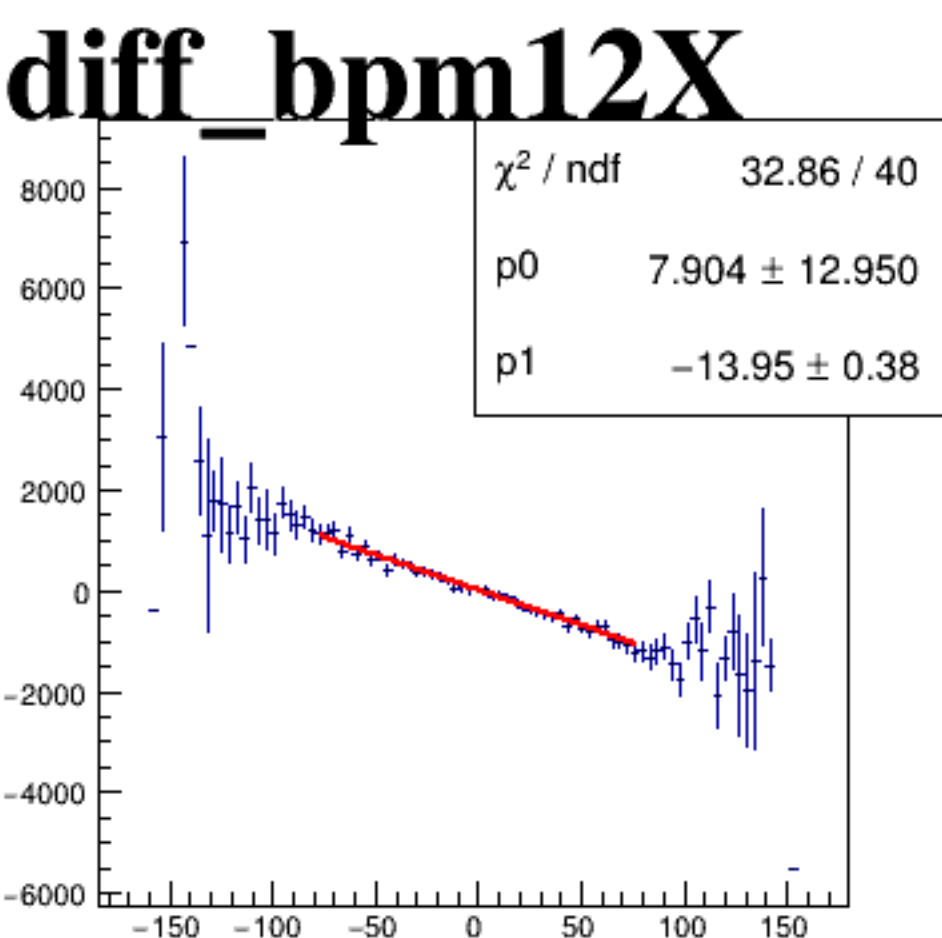
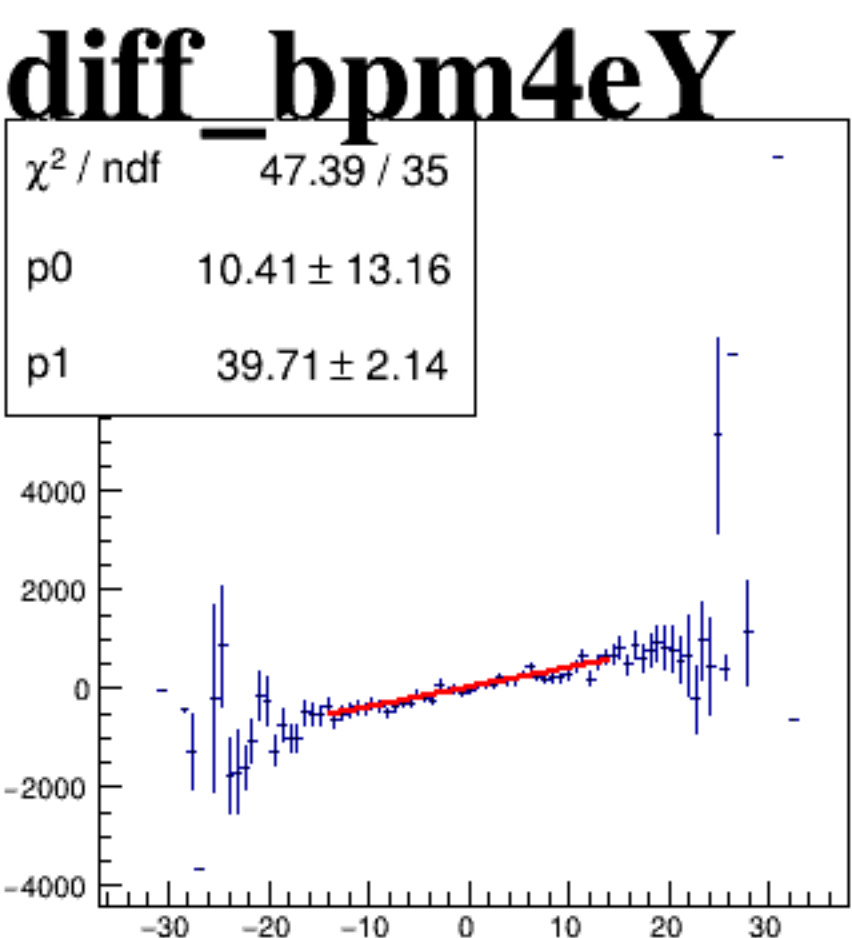
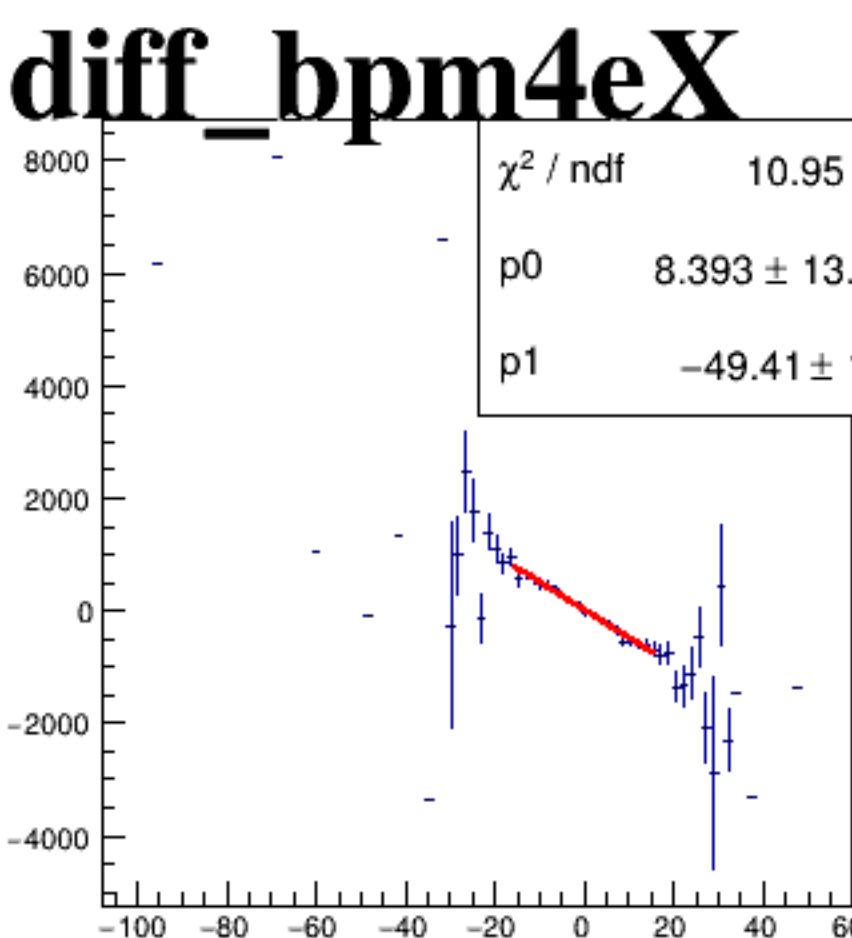
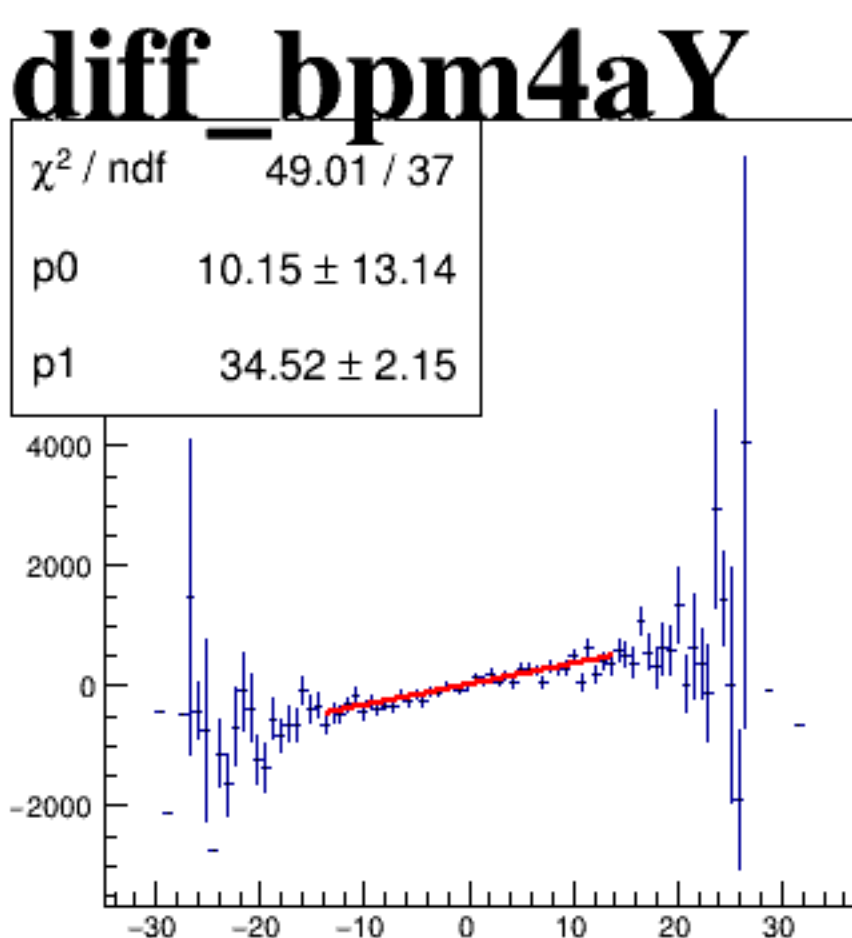
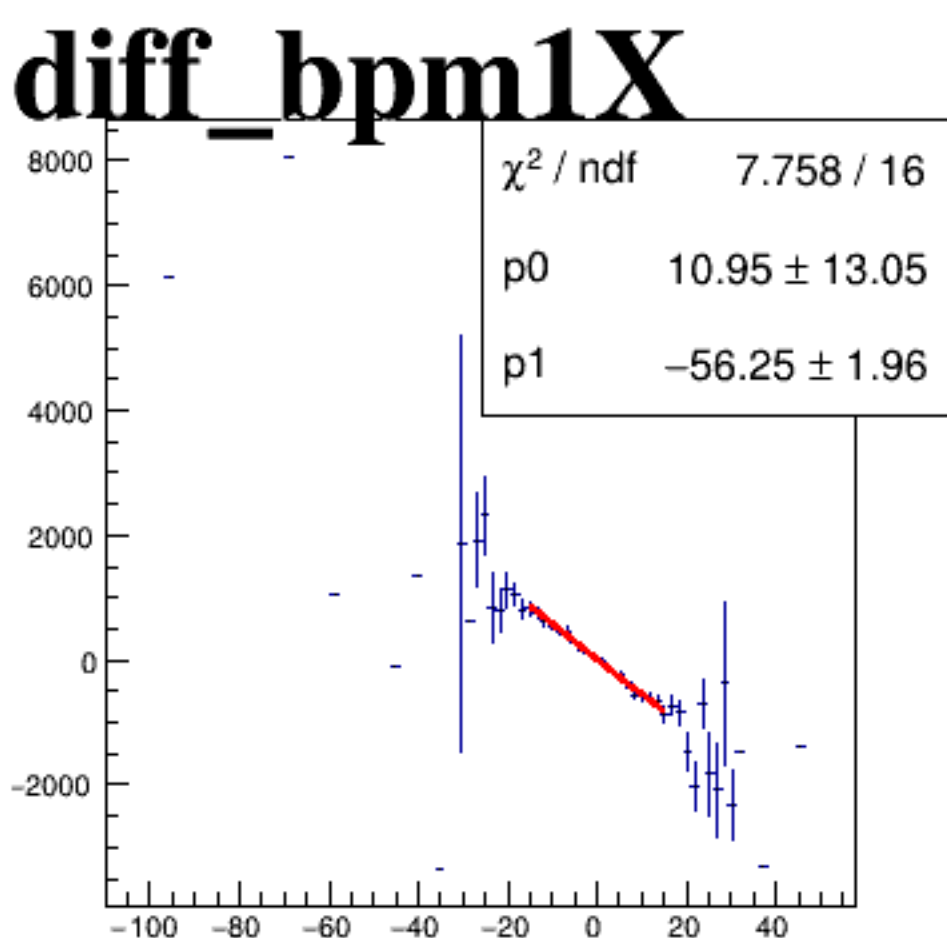




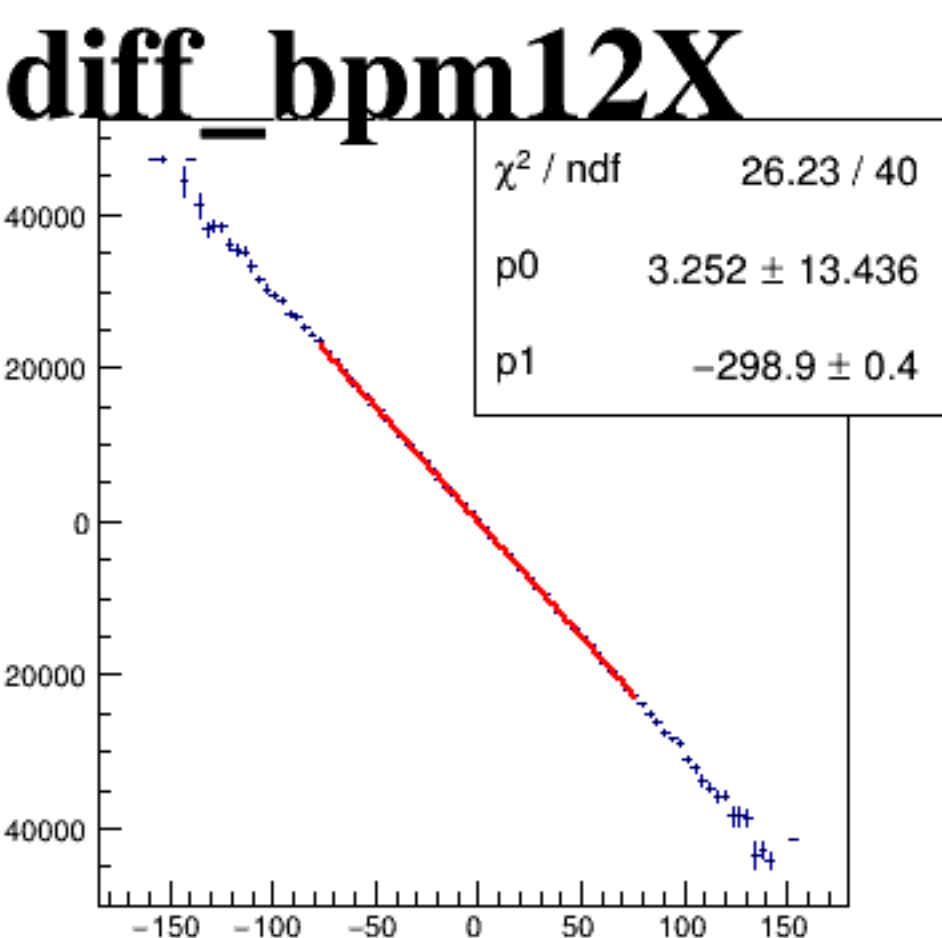
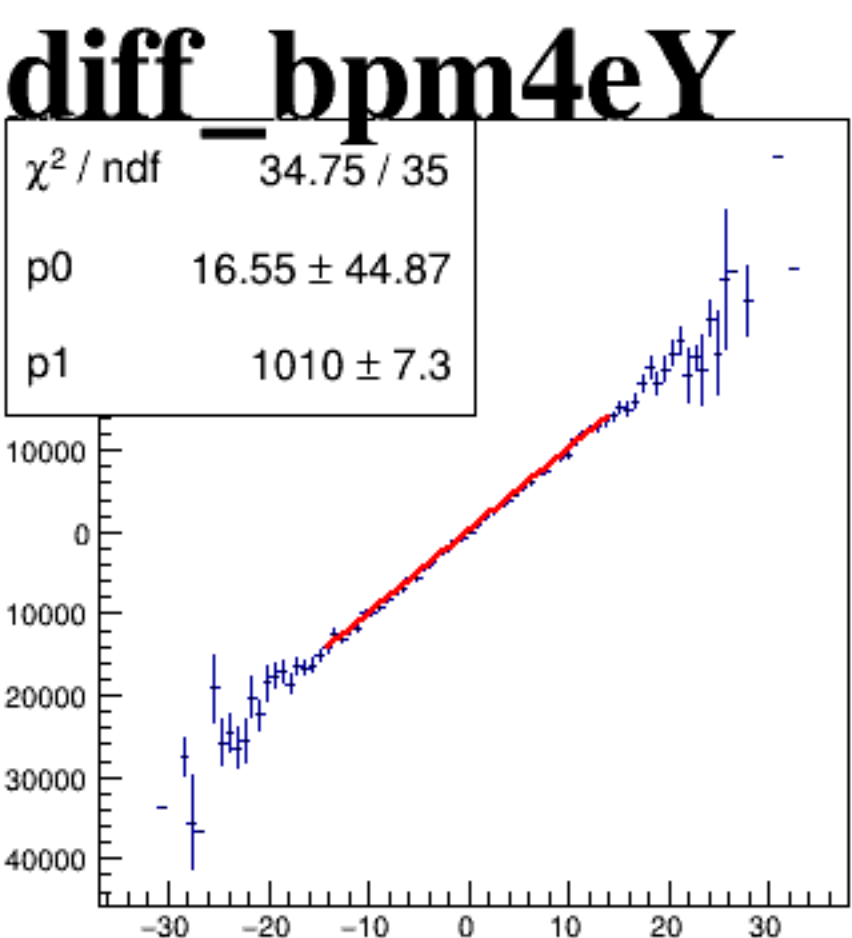
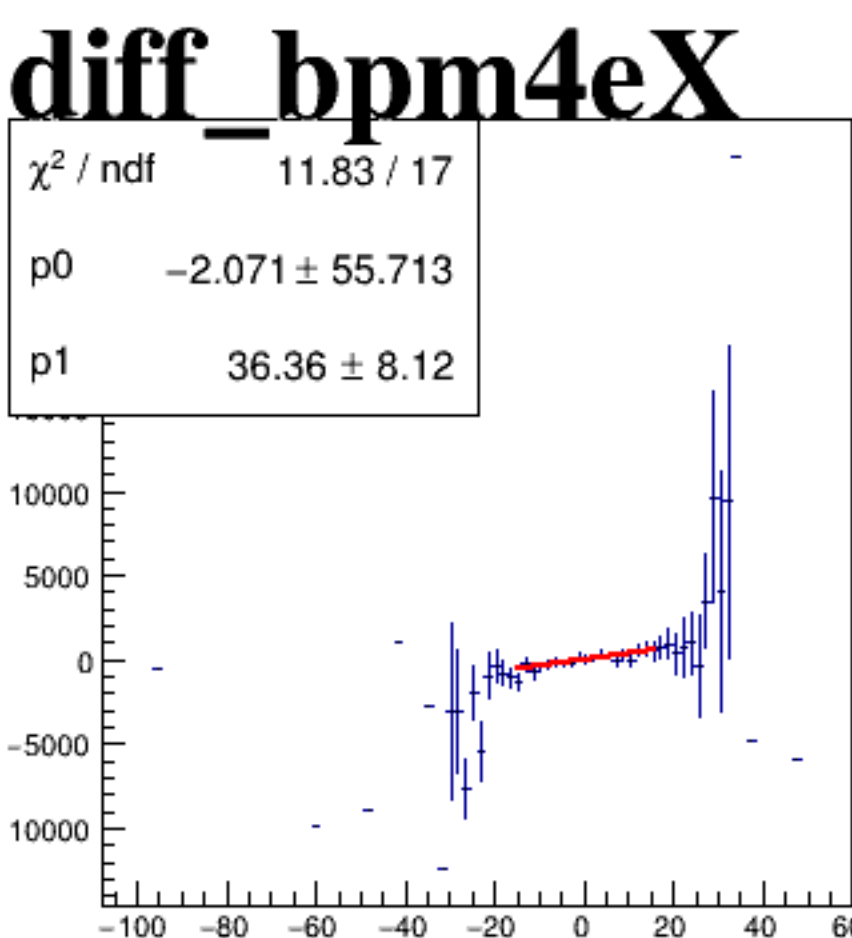
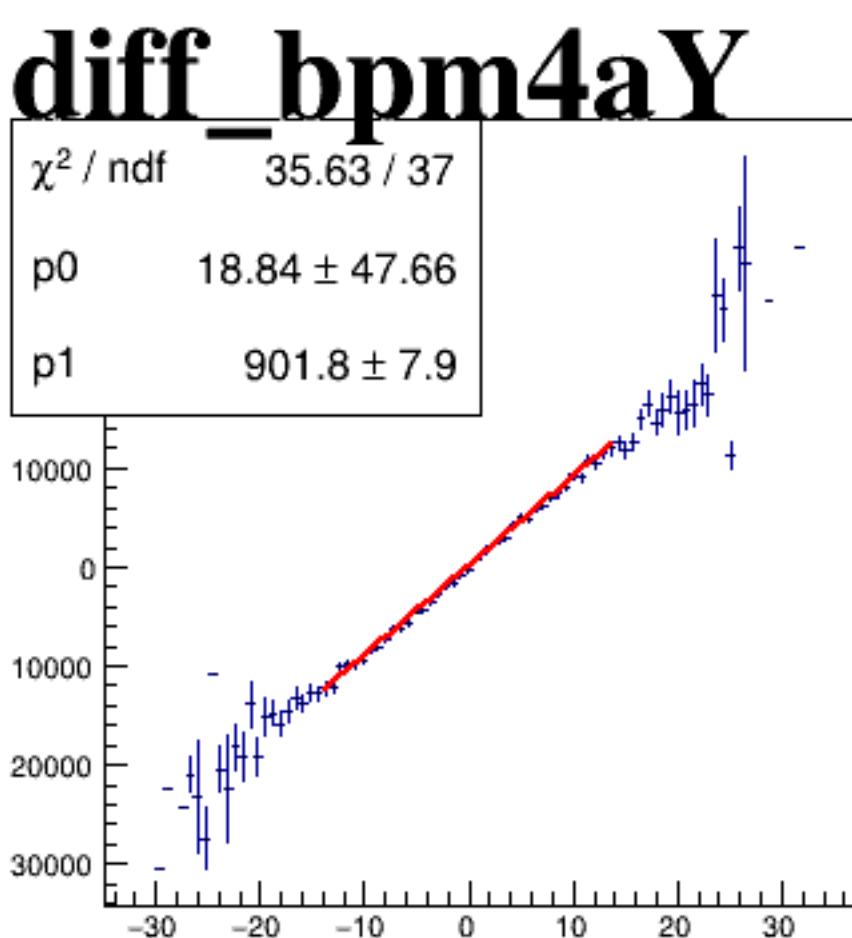
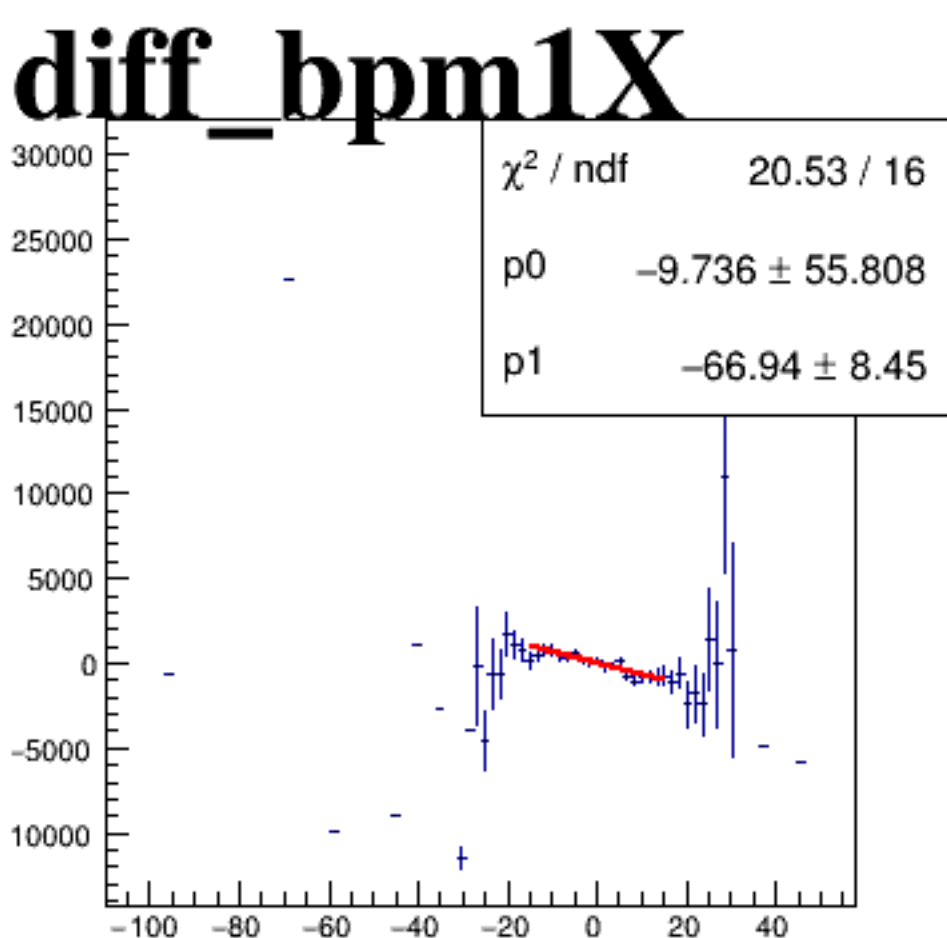
asym\_atl1



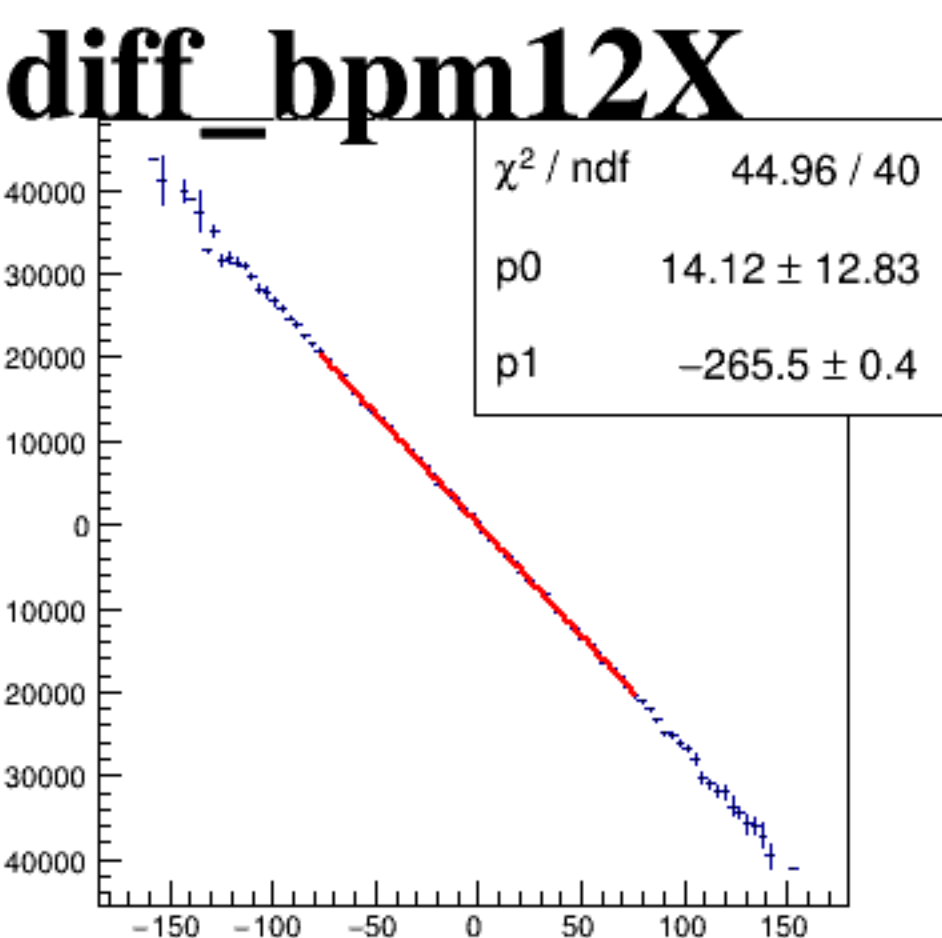
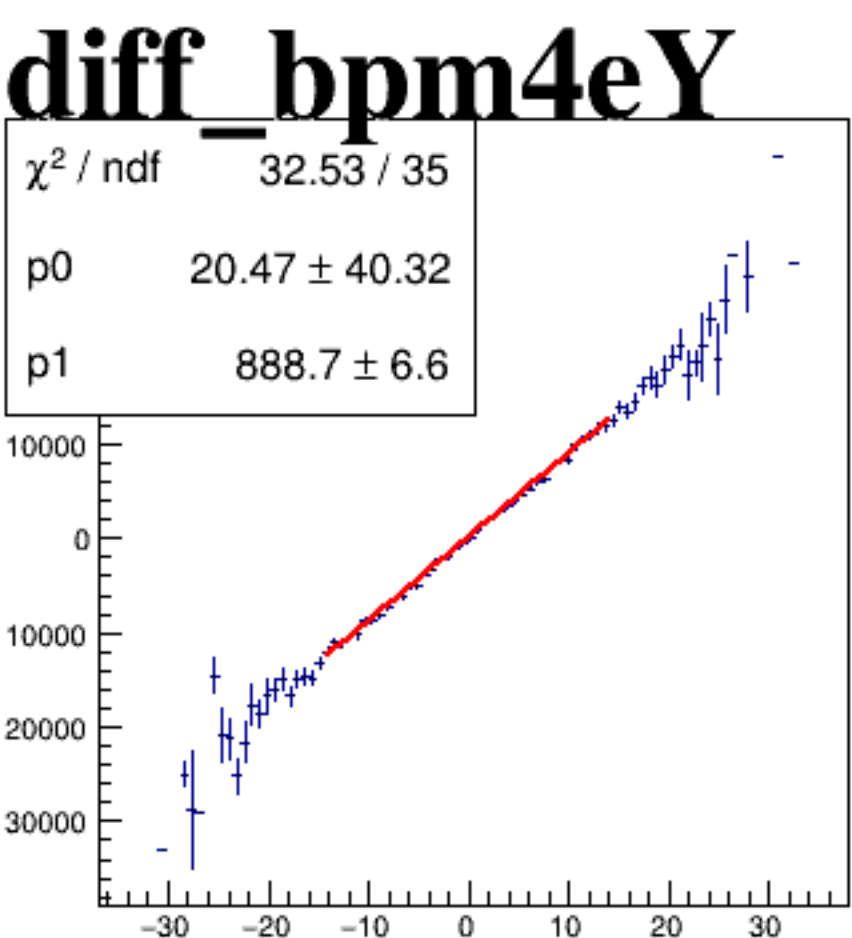
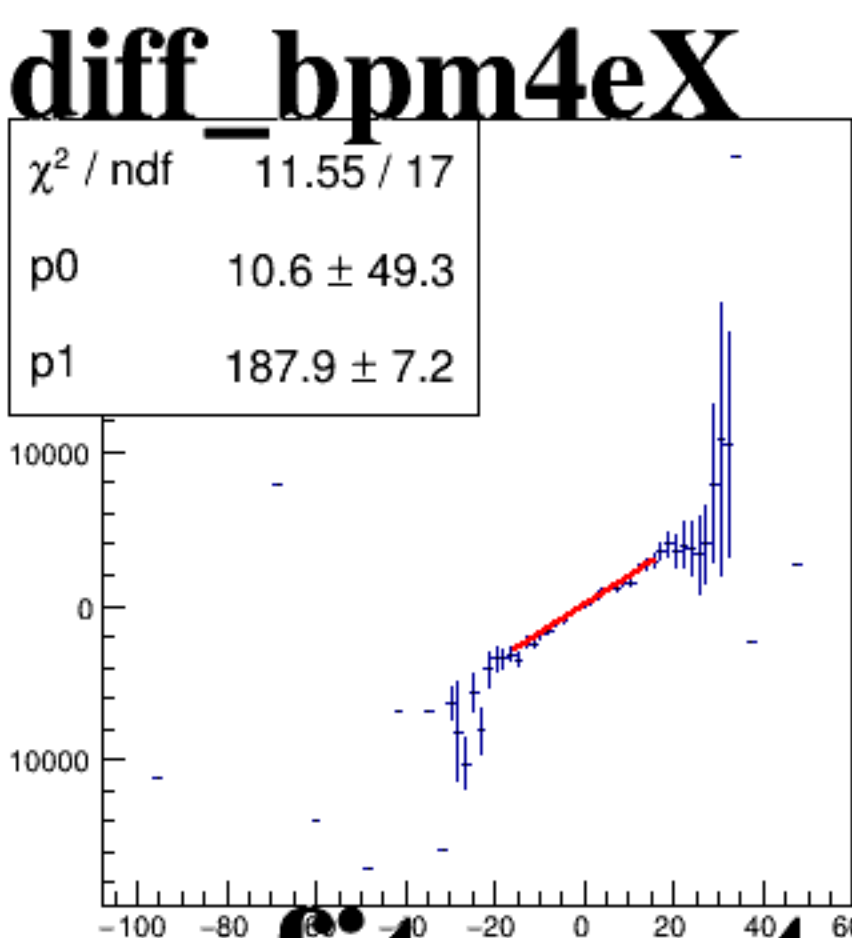
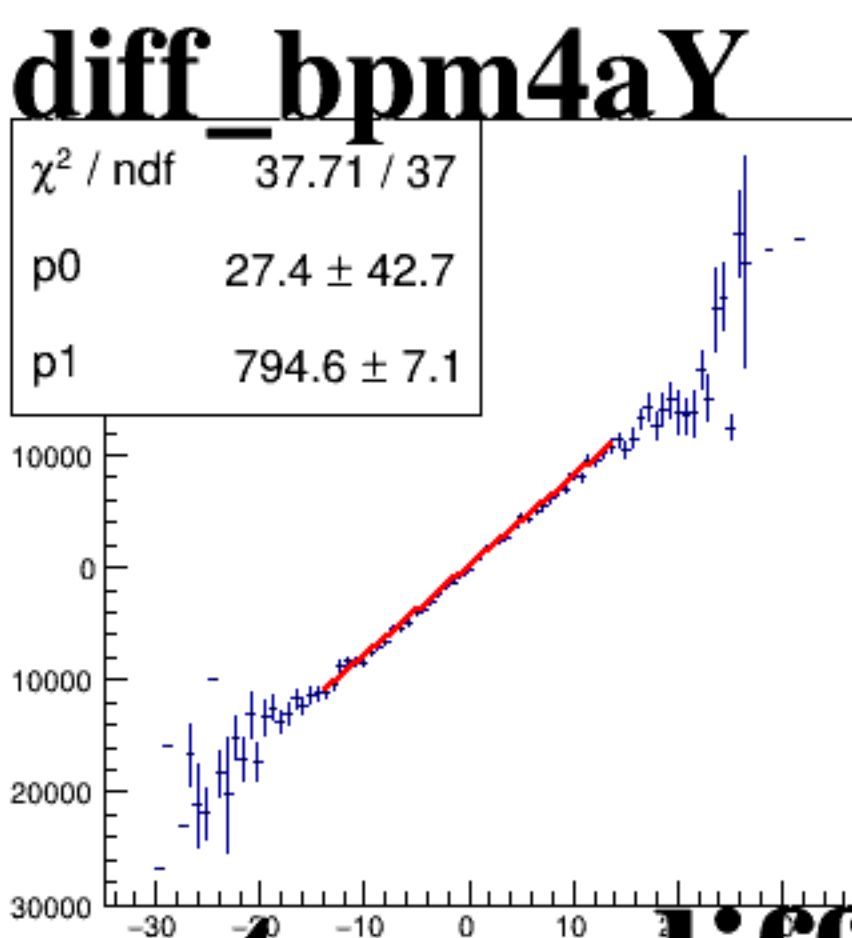
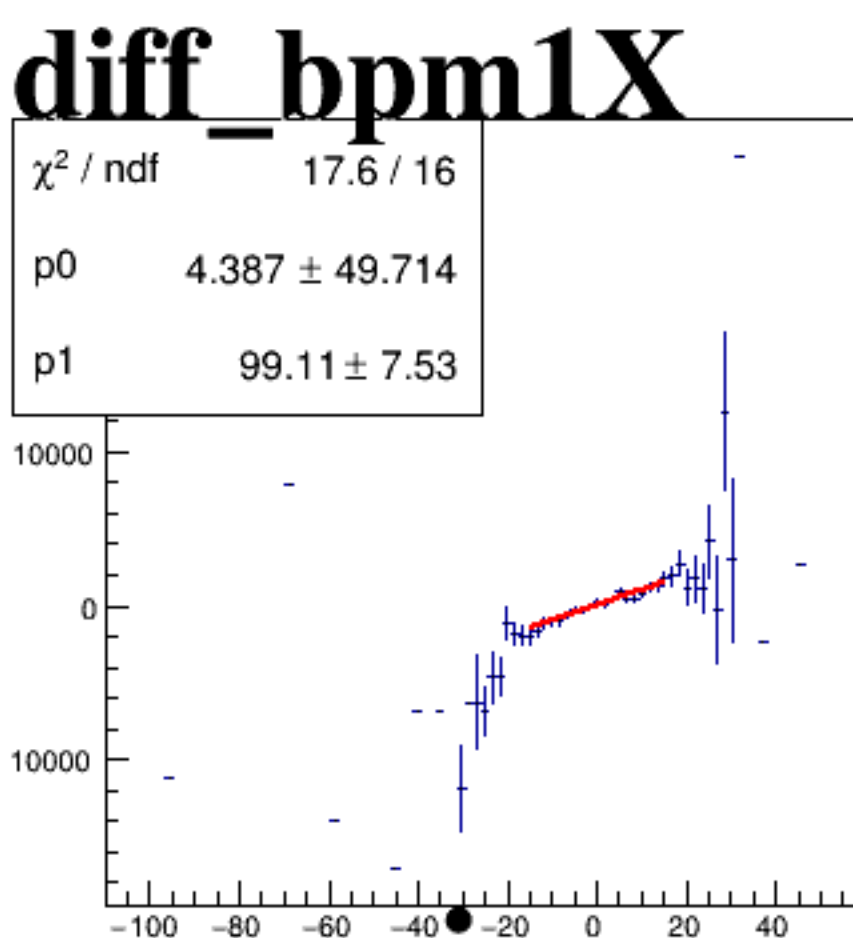
asym\_atr1



asym\_atl2

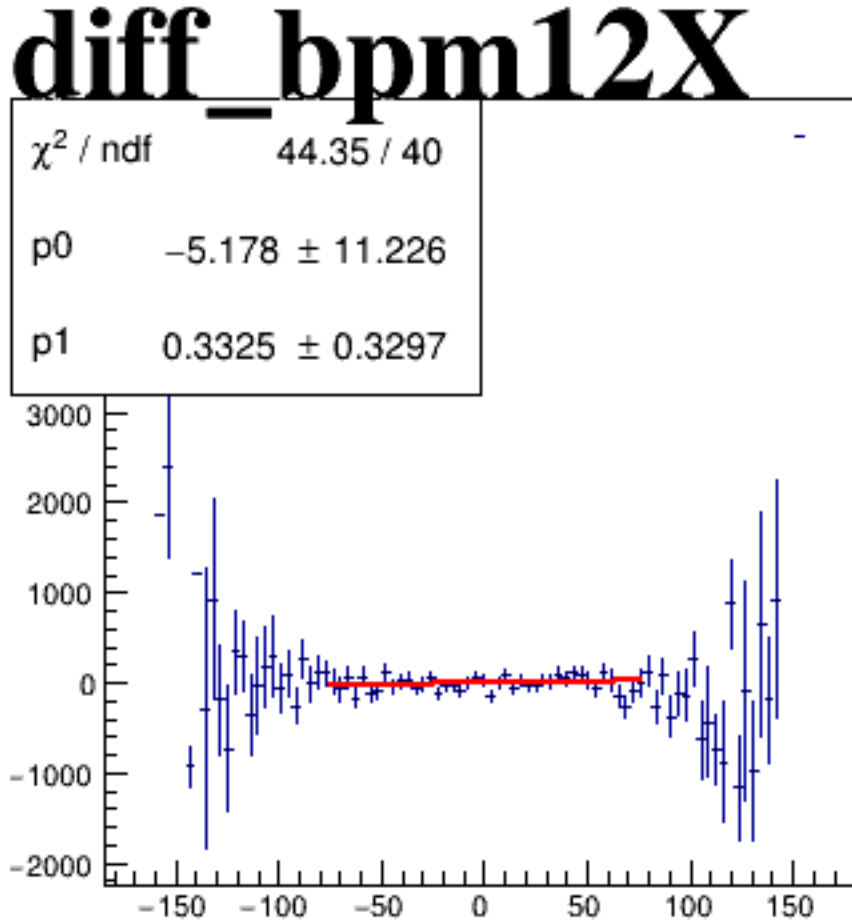
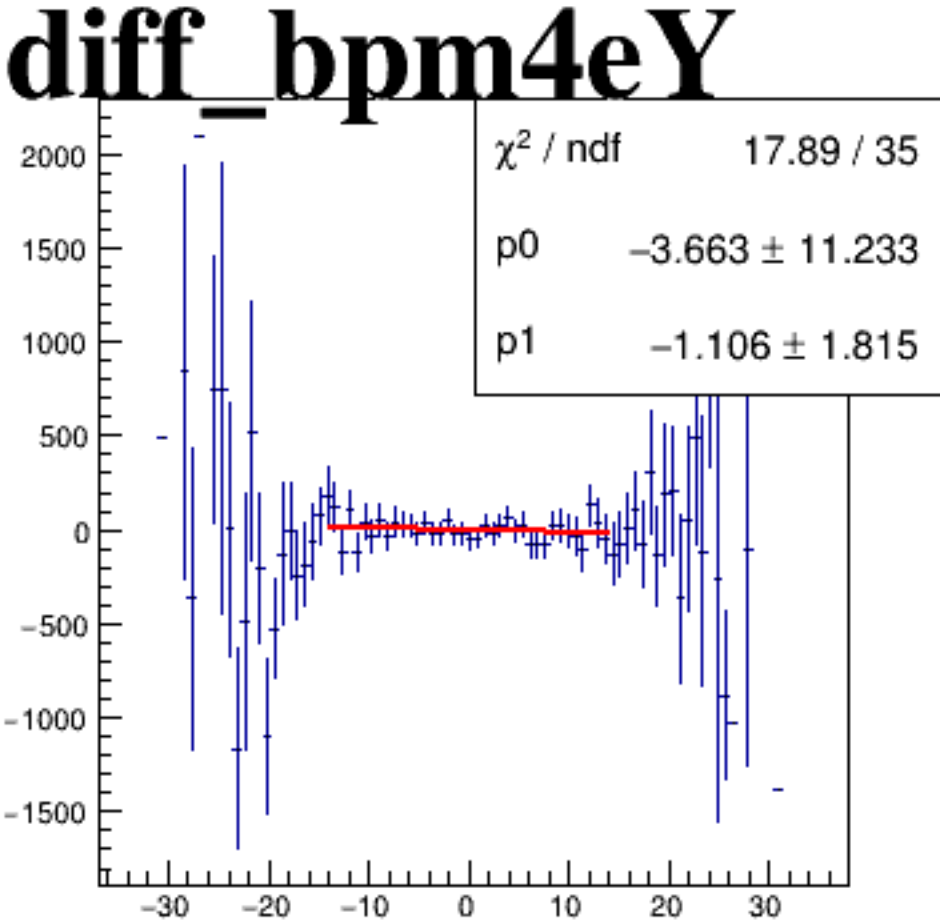
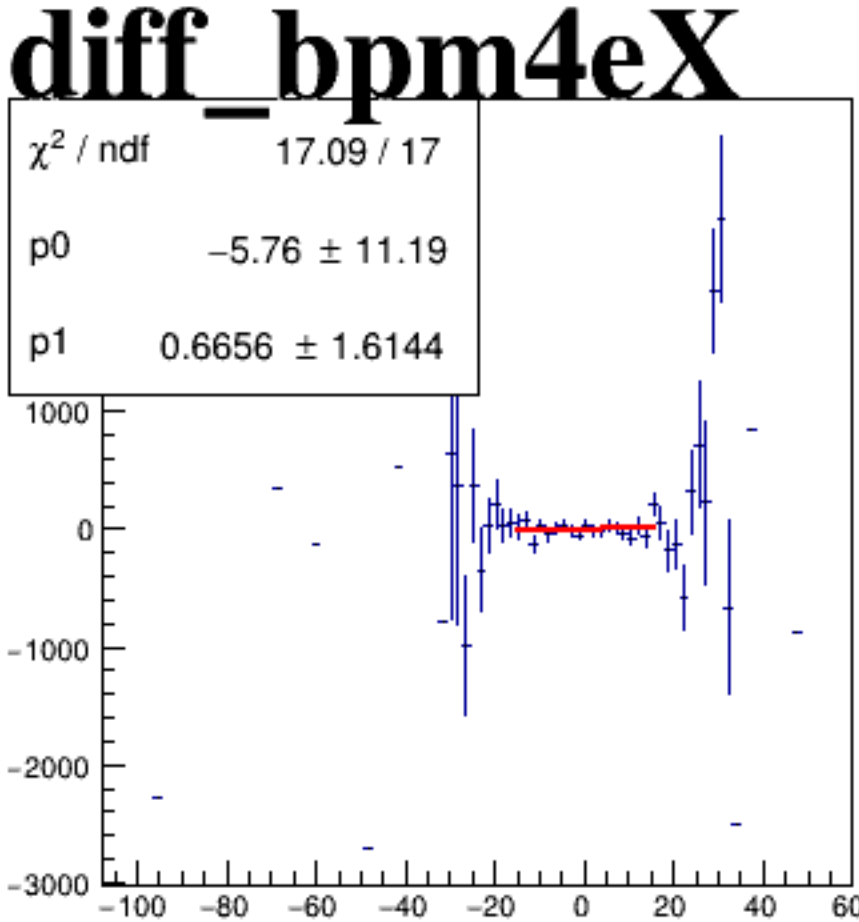
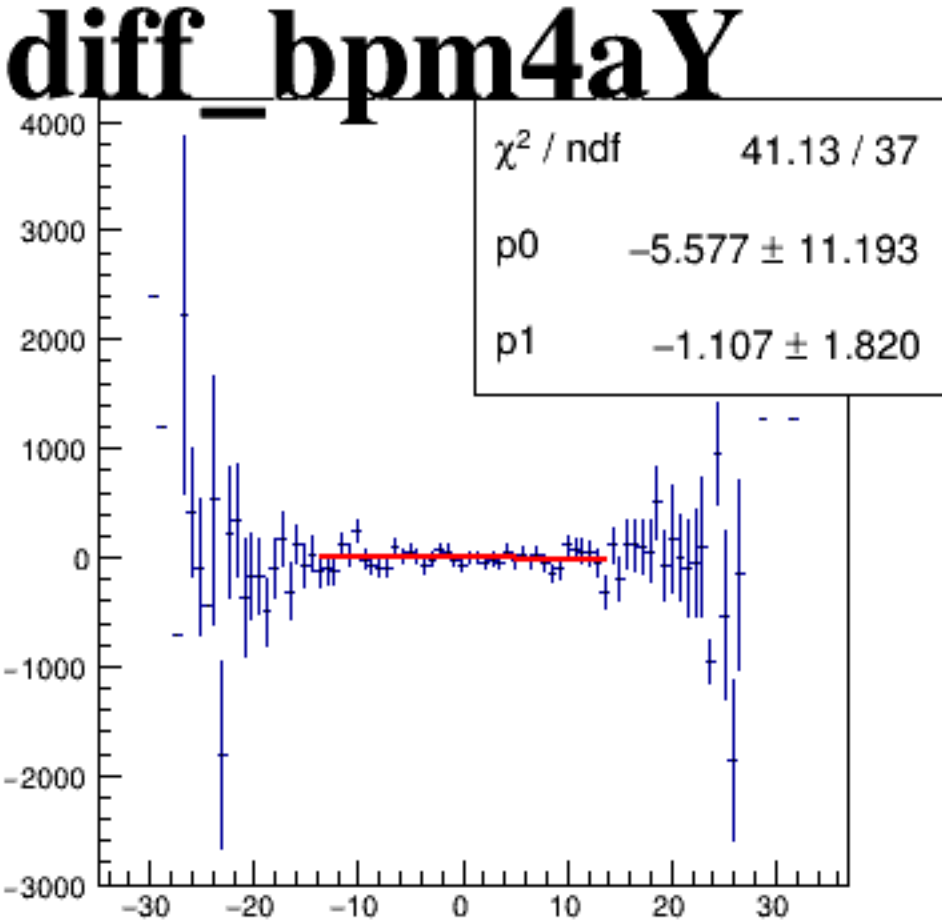
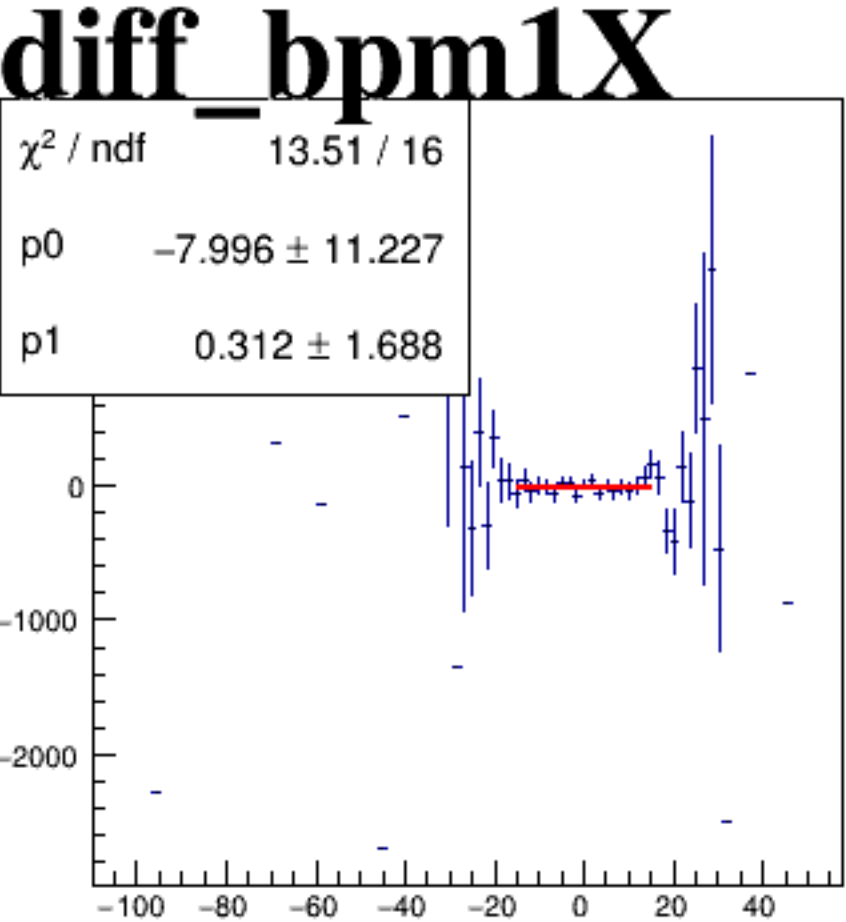


asym\_atr2

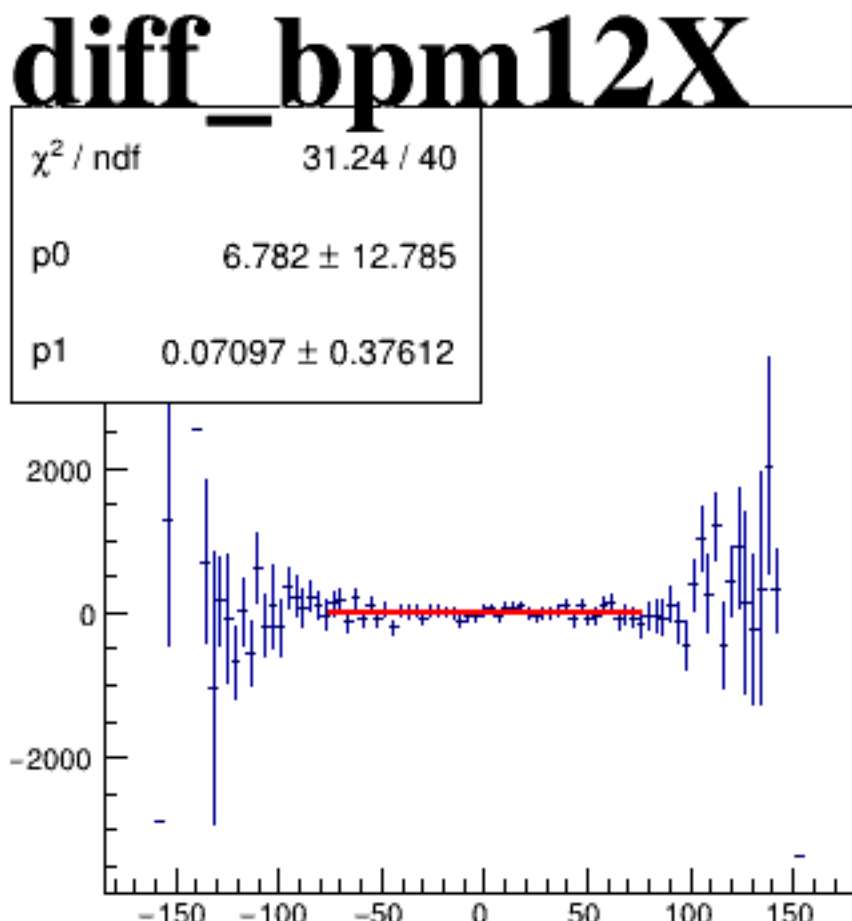
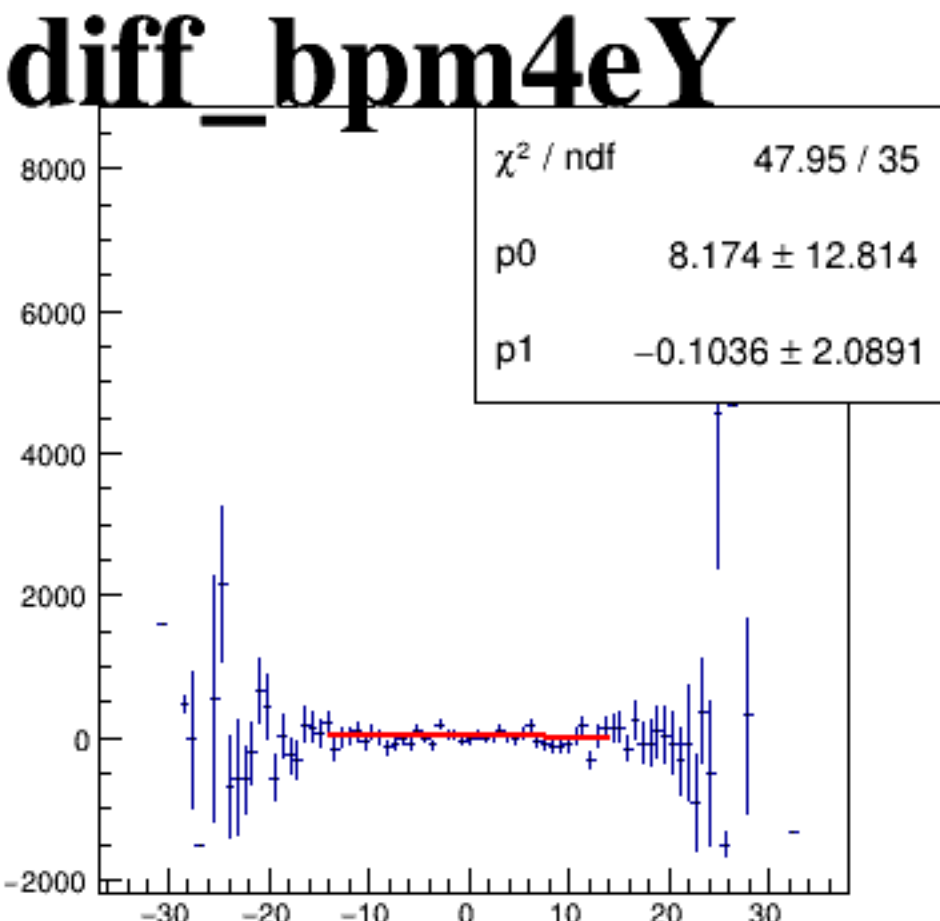
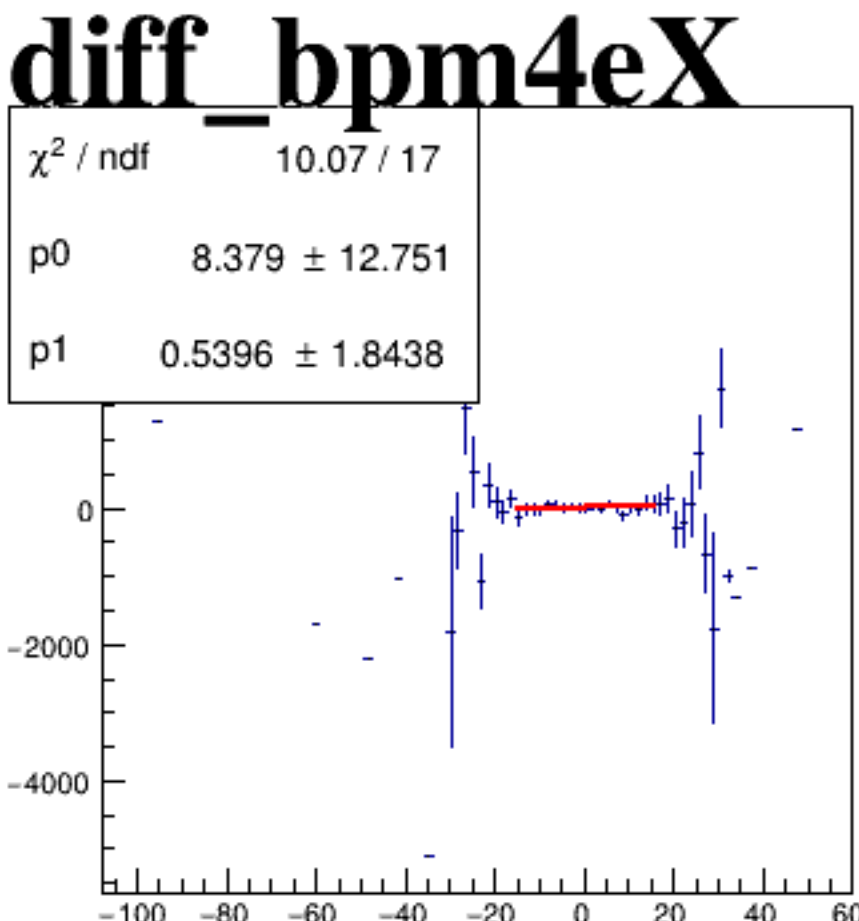
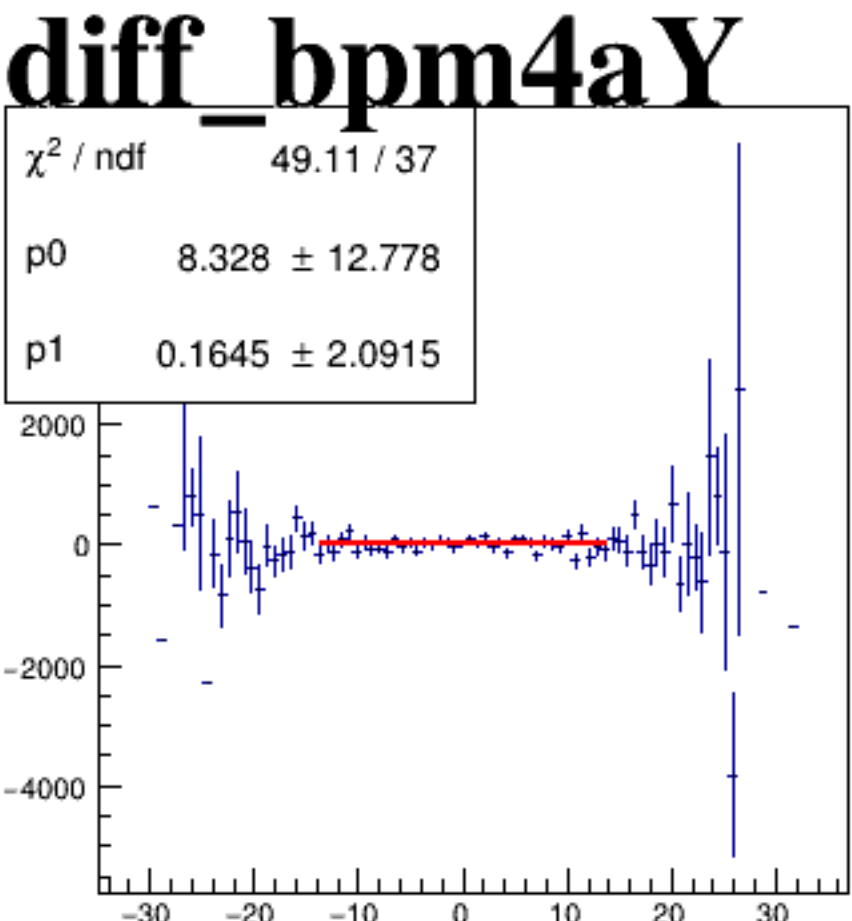
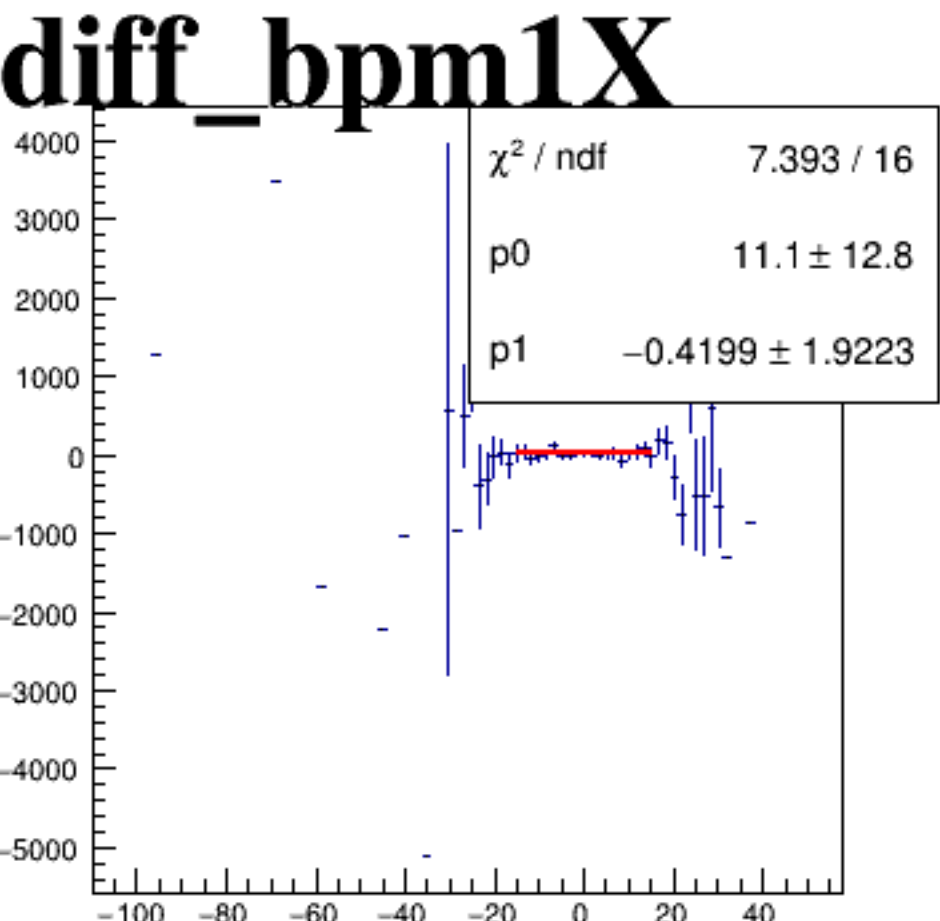




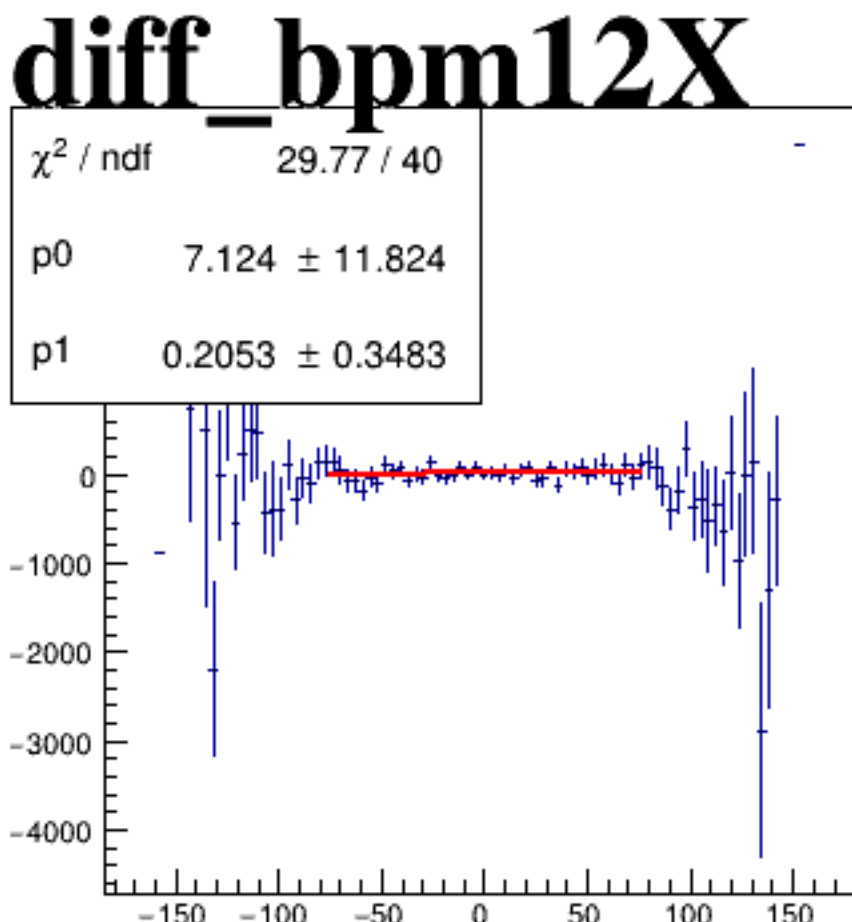
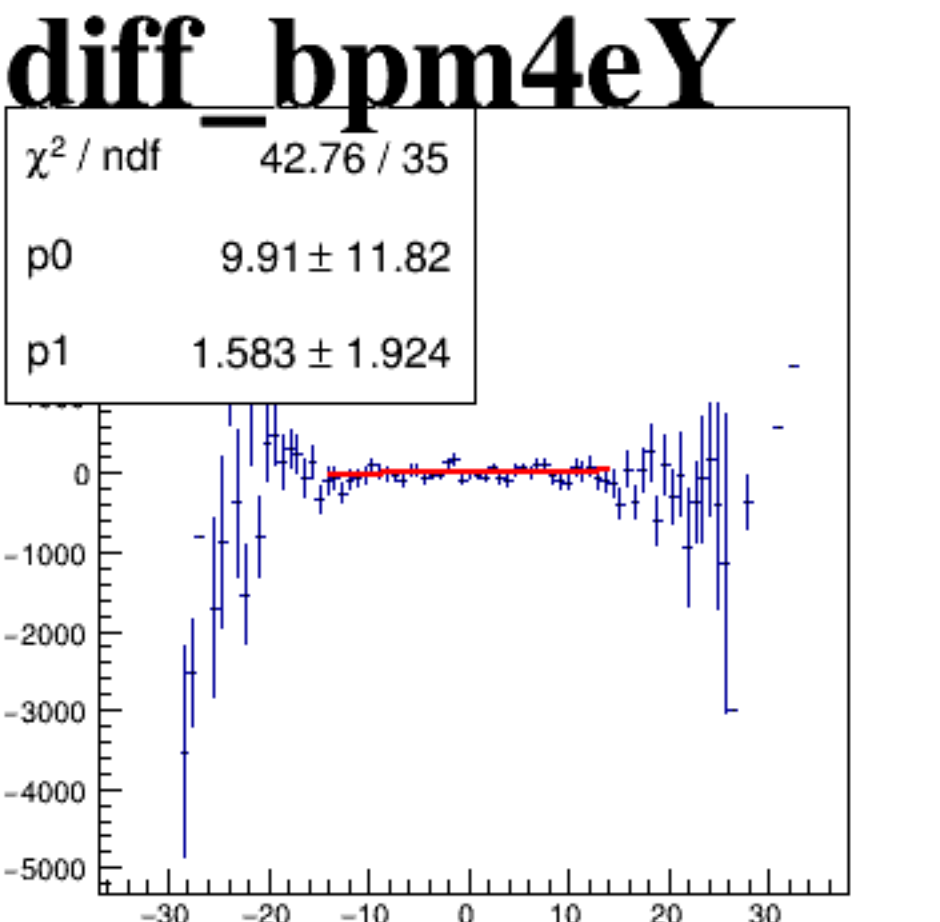
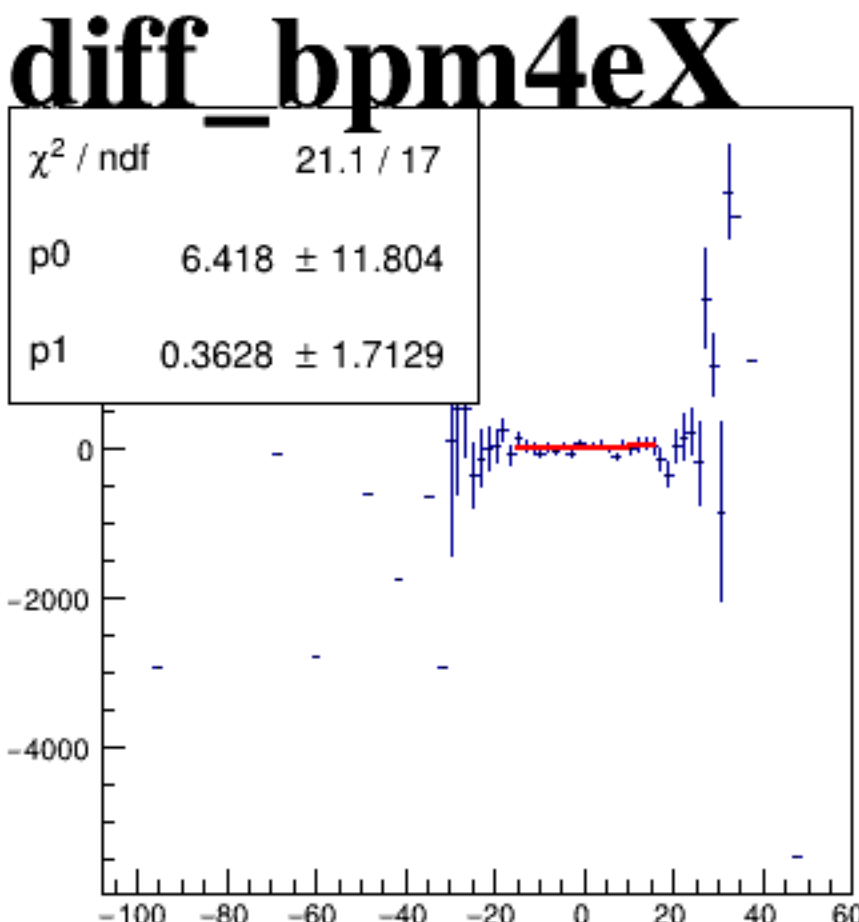
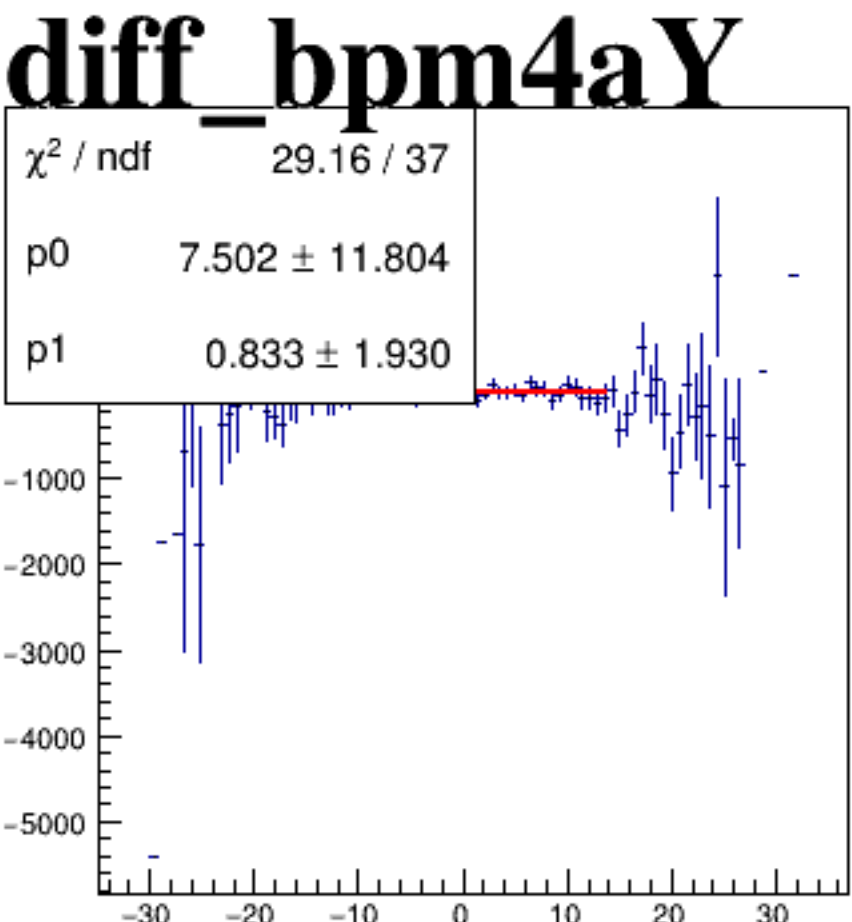
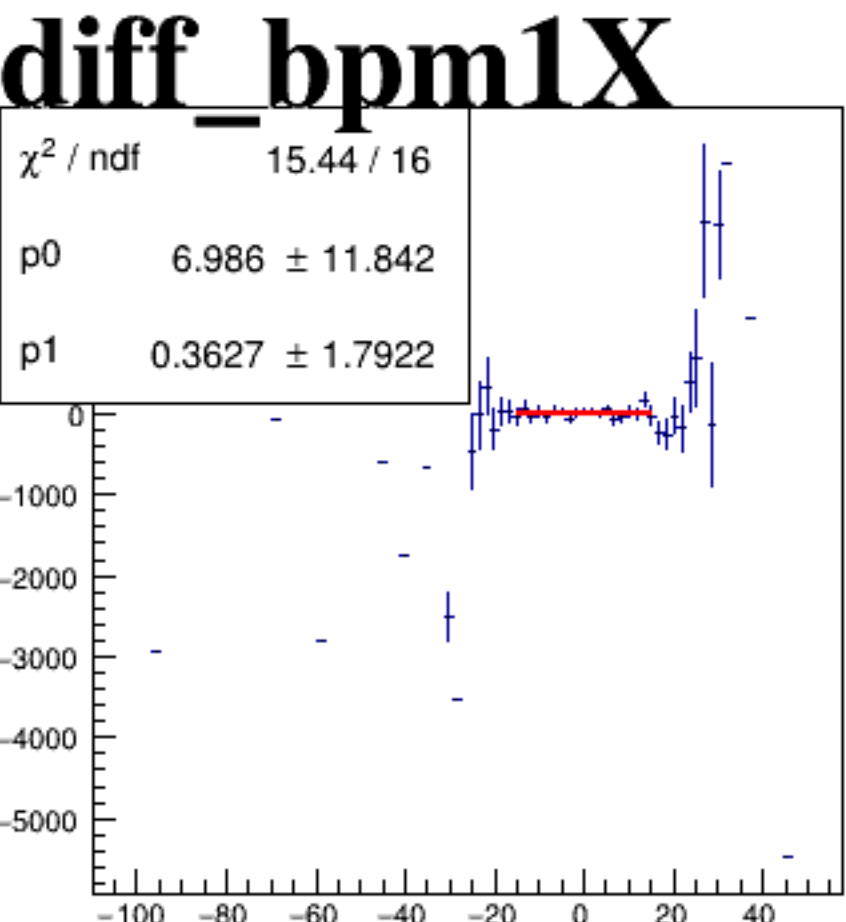
reg\_asym\_atl1



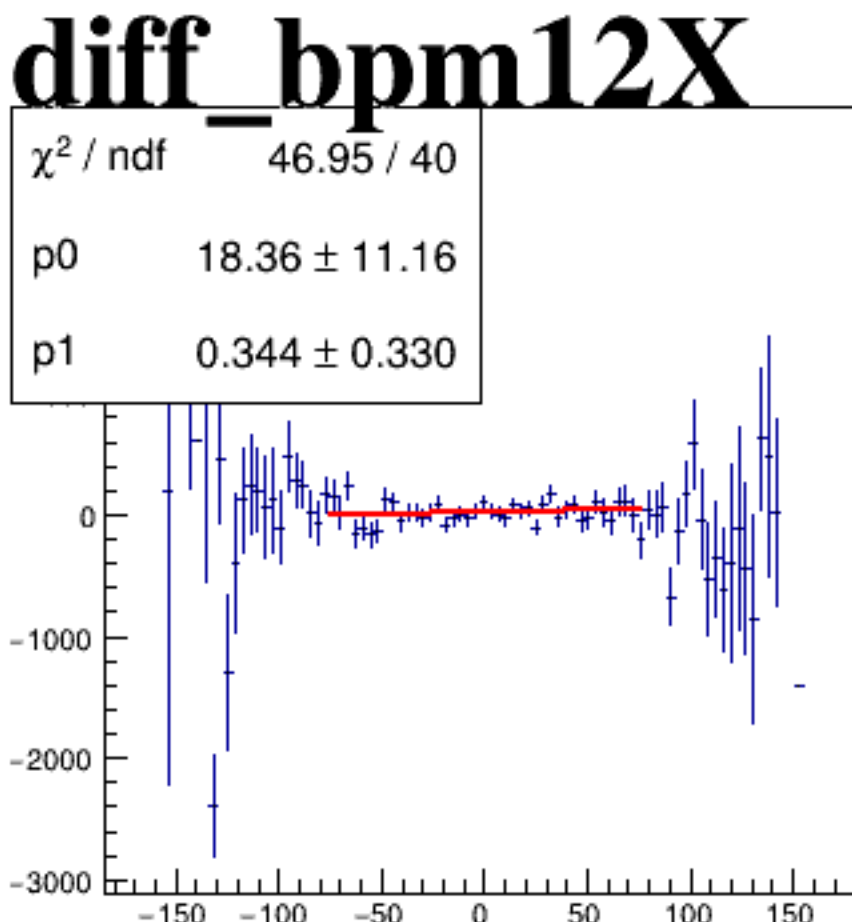
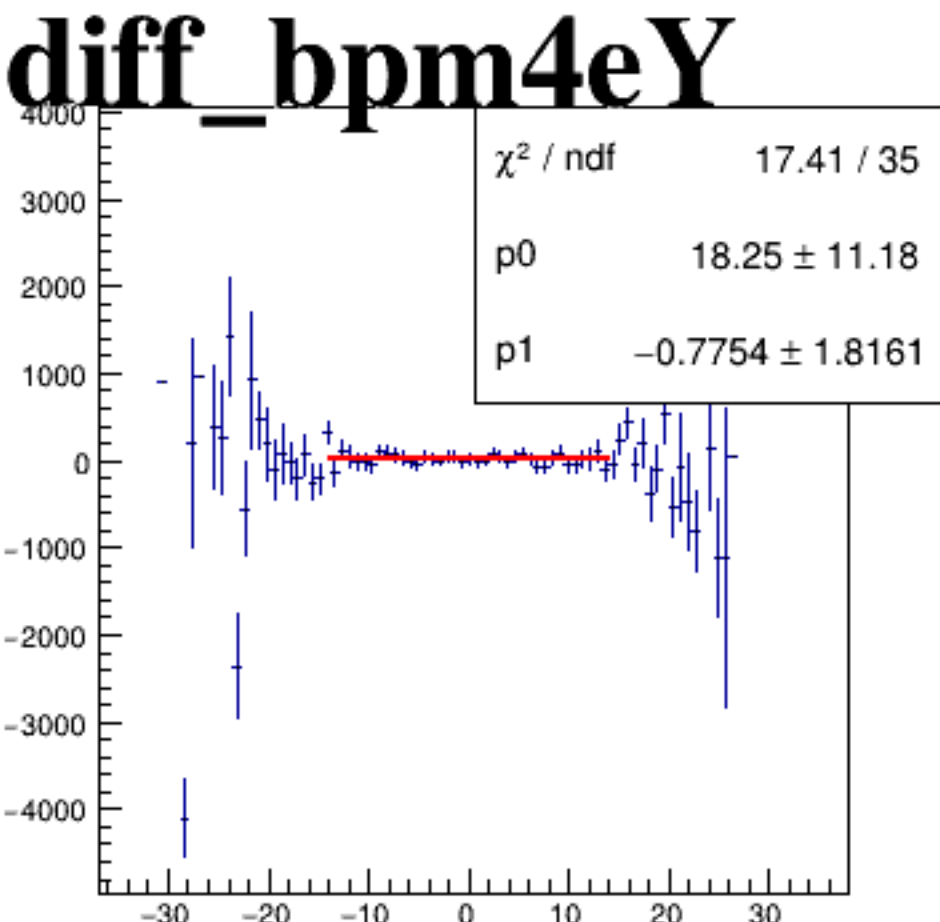
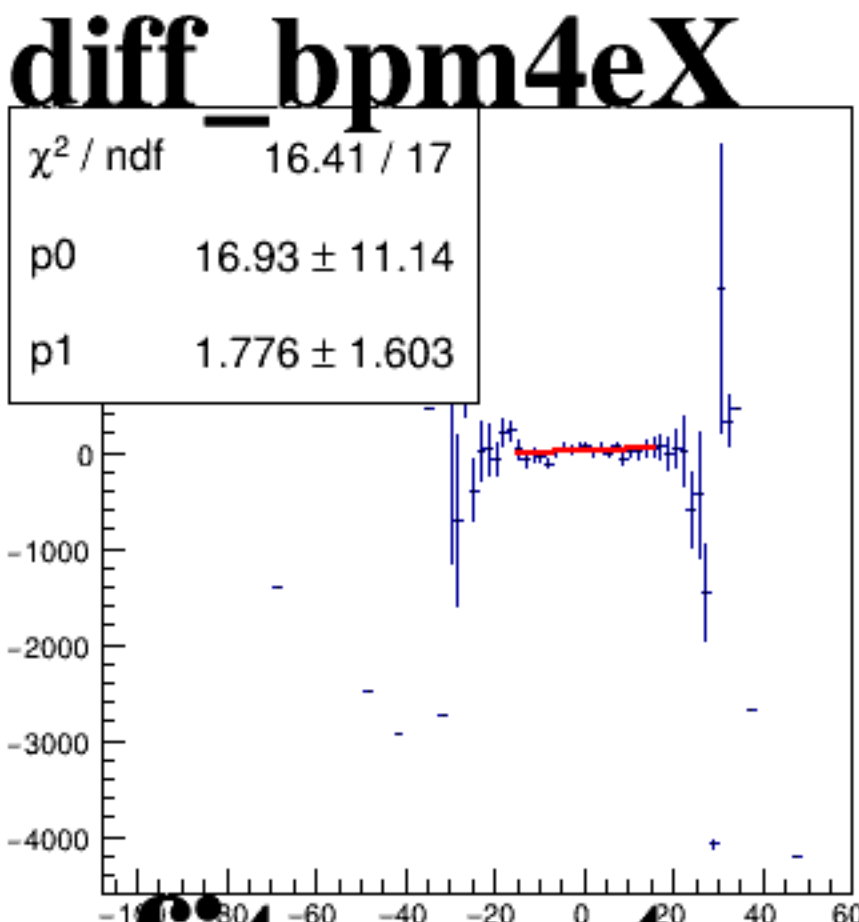
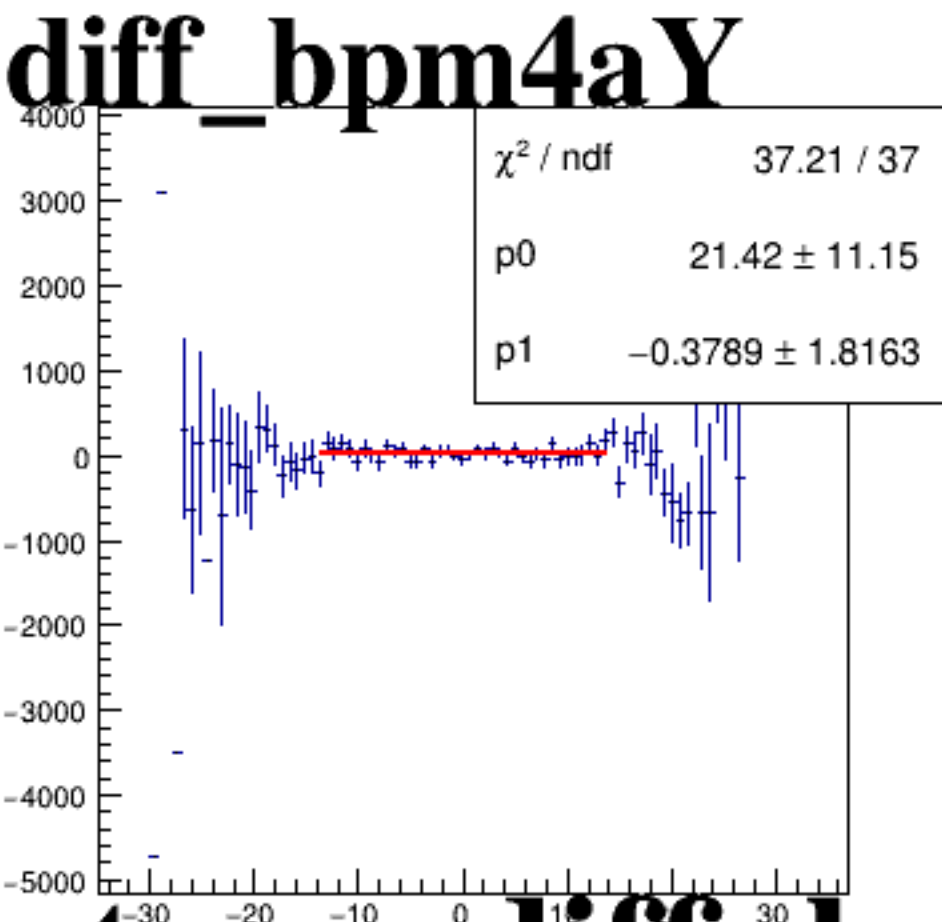
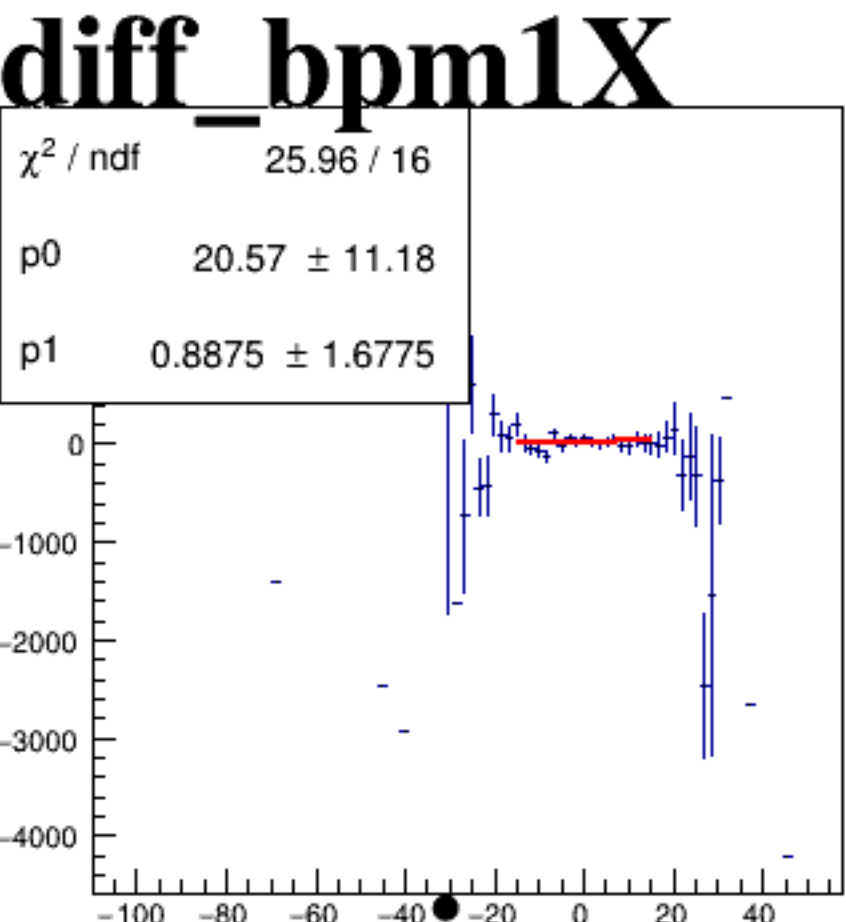
reg\_asym\_atr1



reg\_asym\_atl2

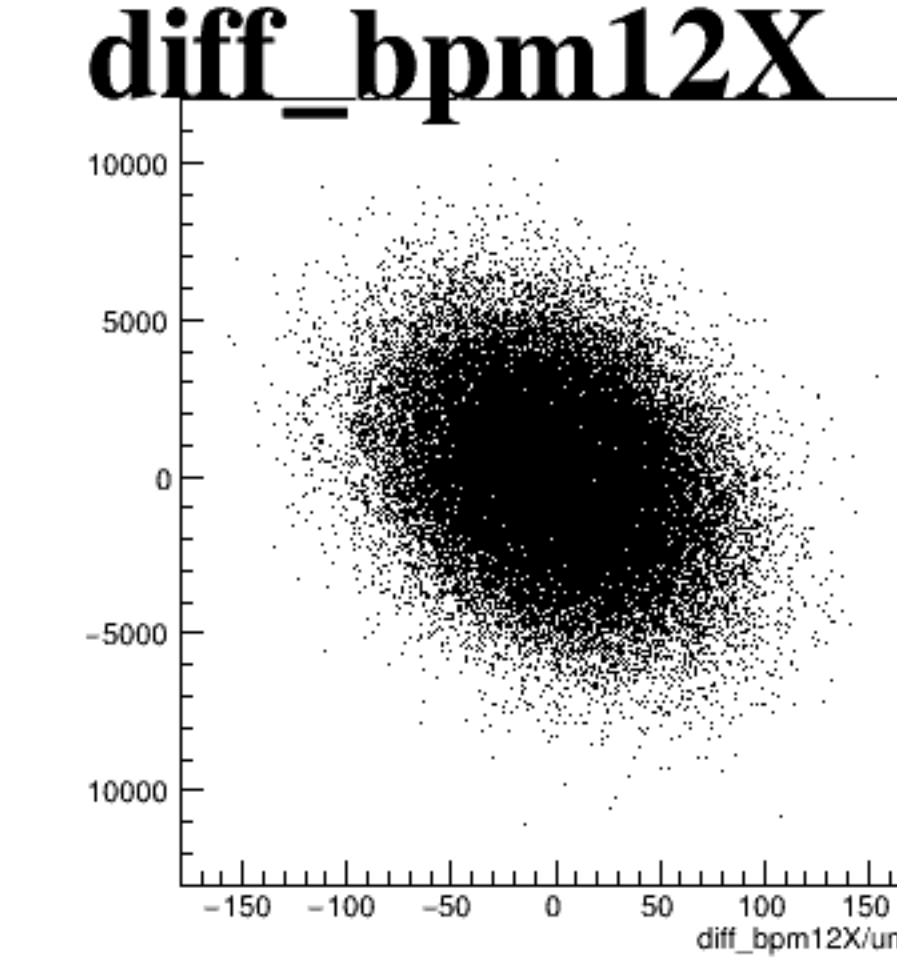
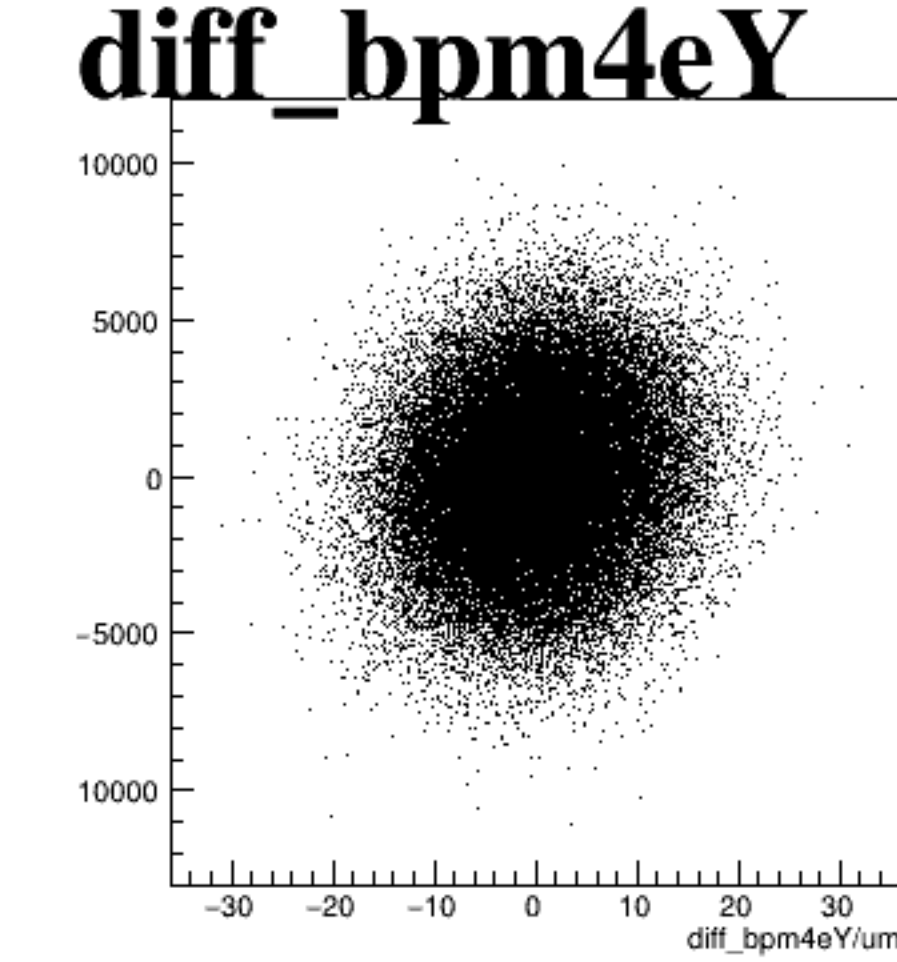
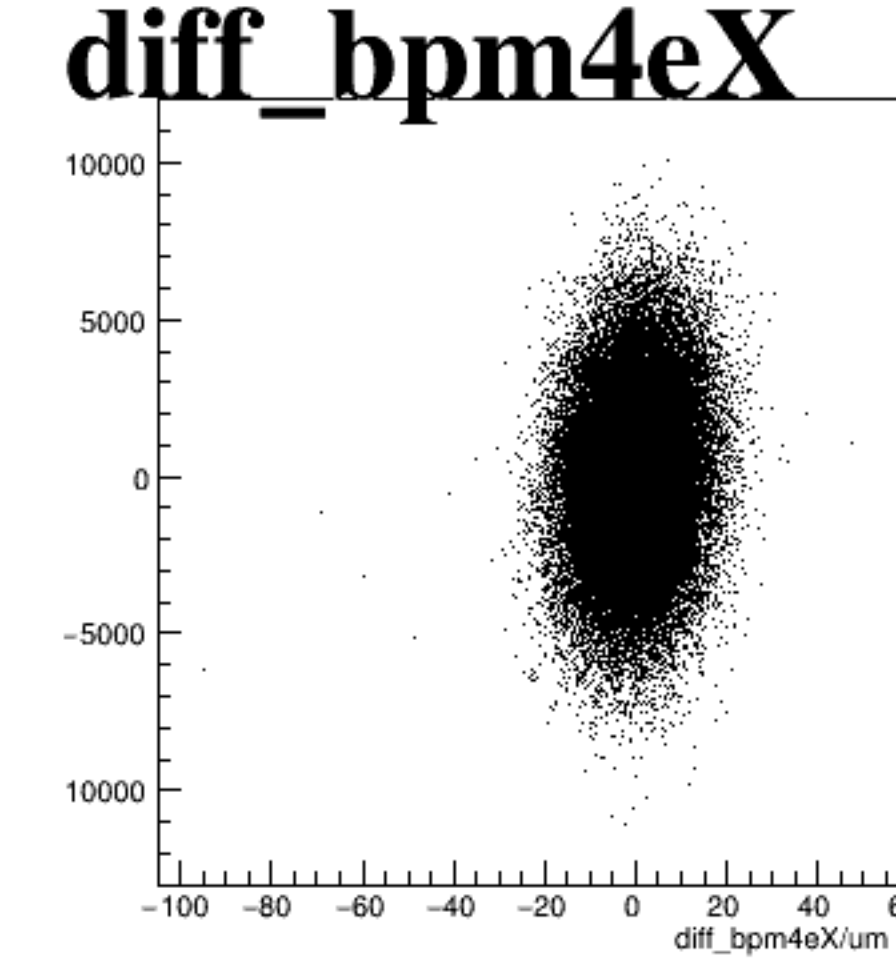
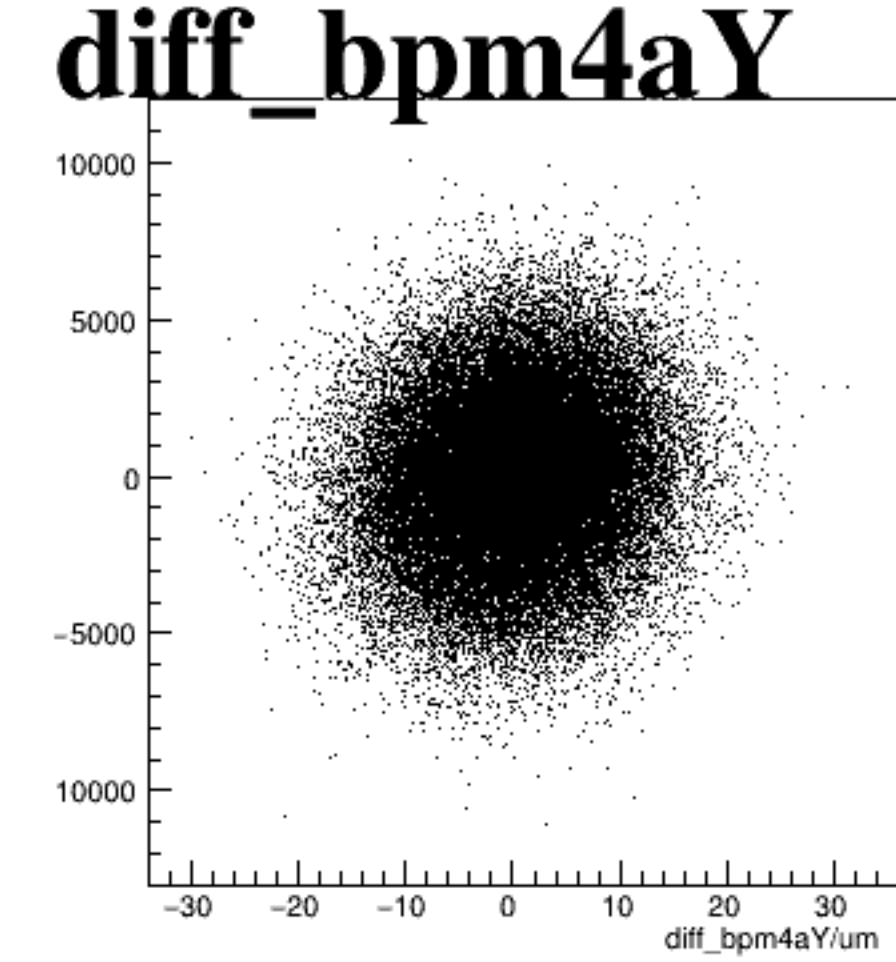
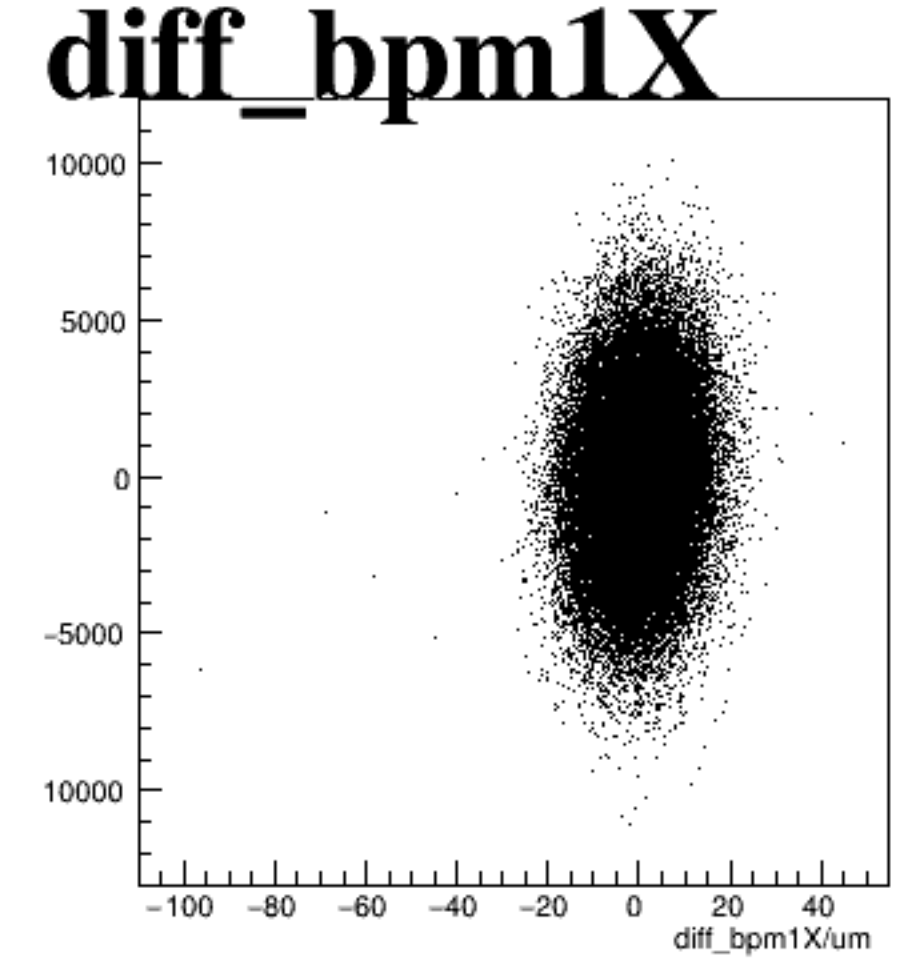


reg\_asym\_atr2

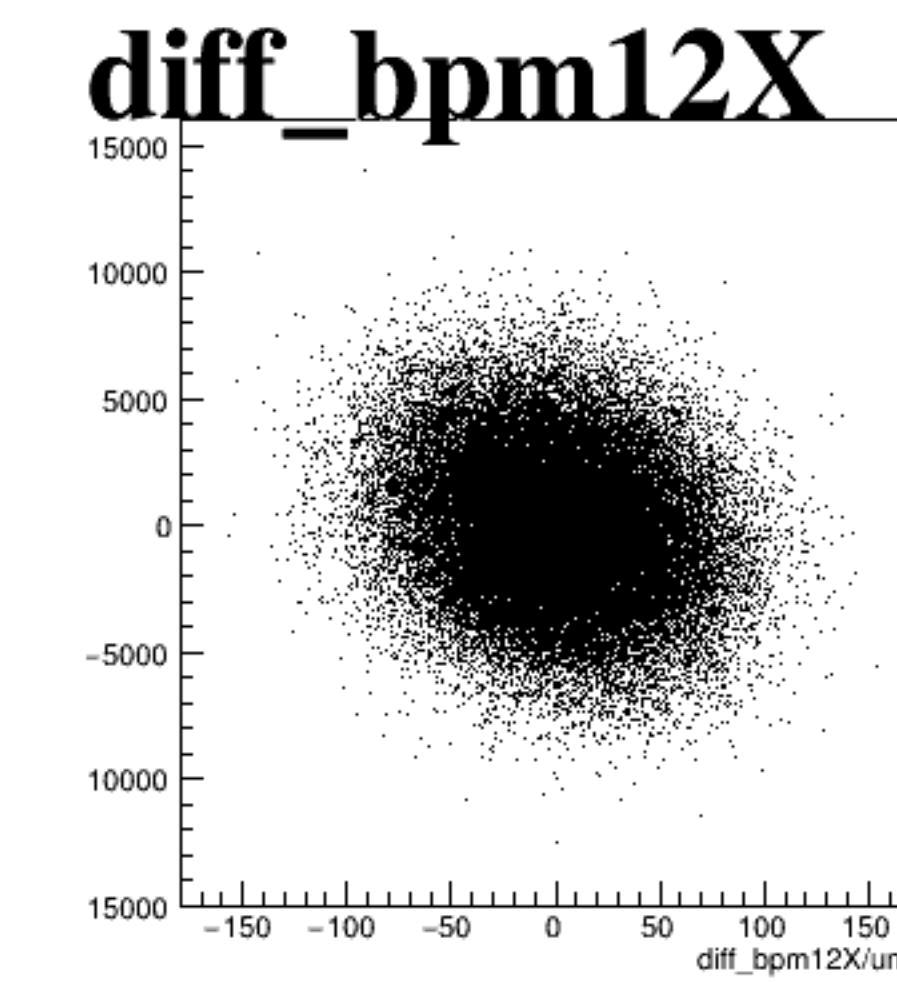
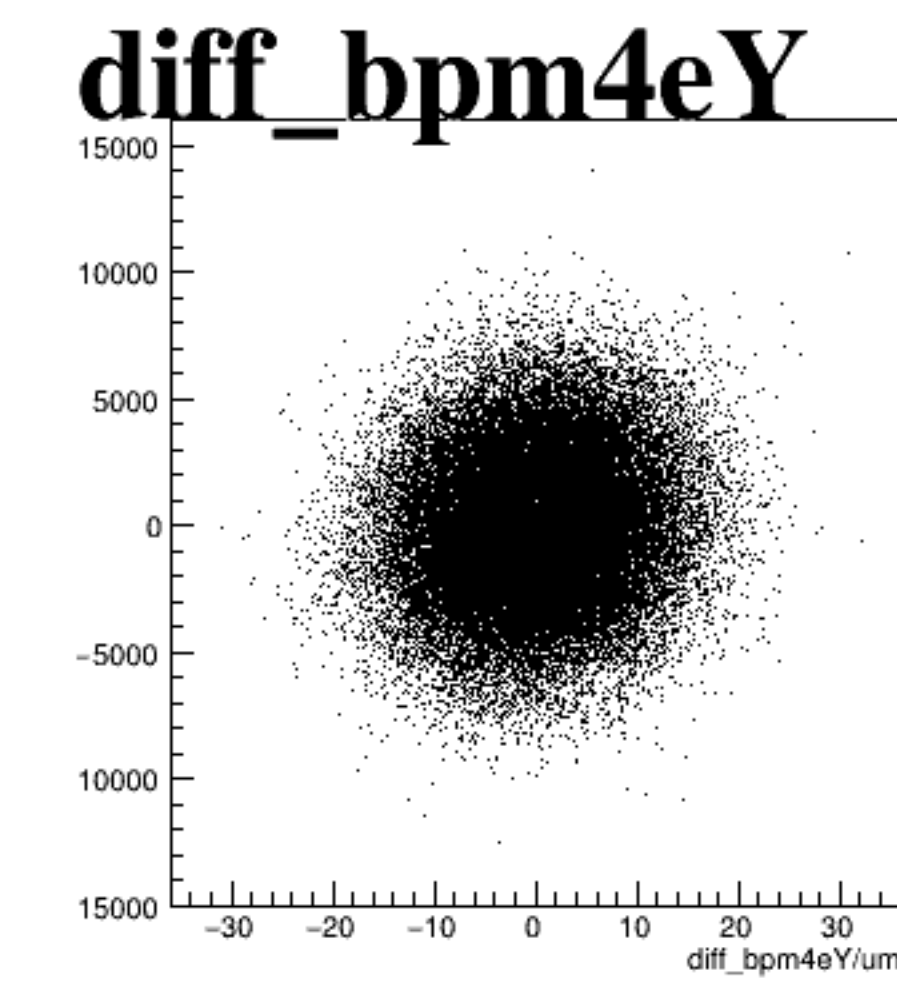
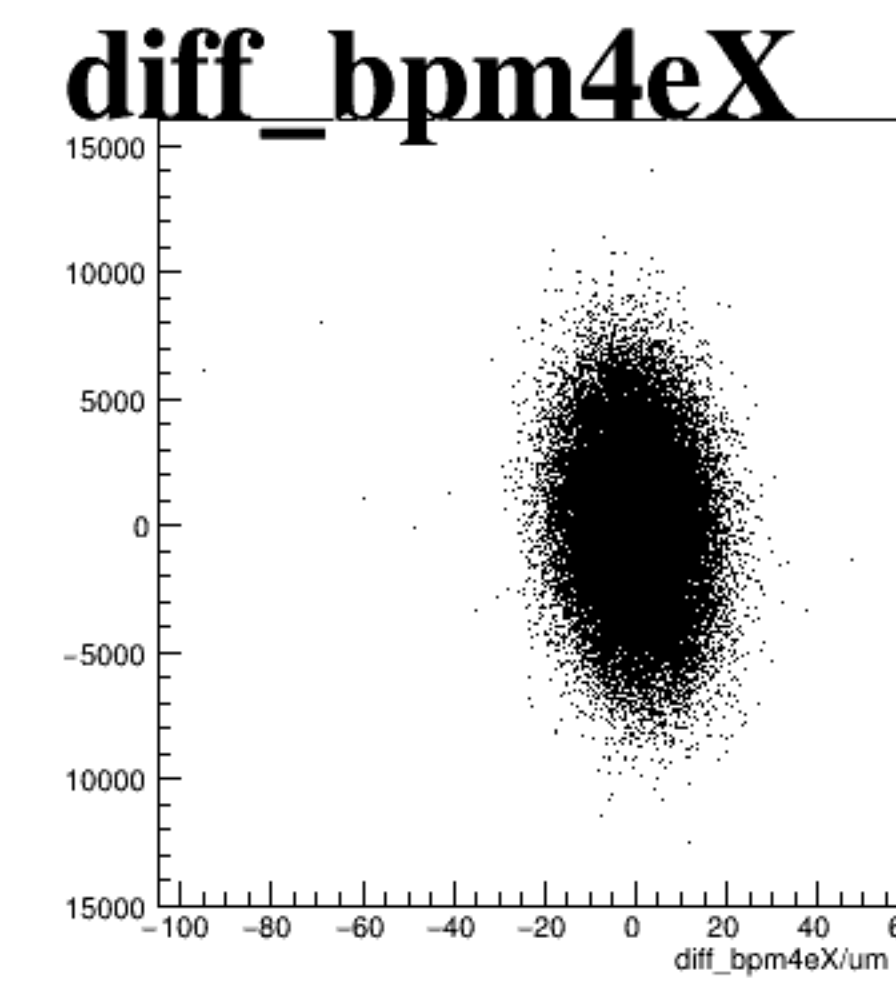
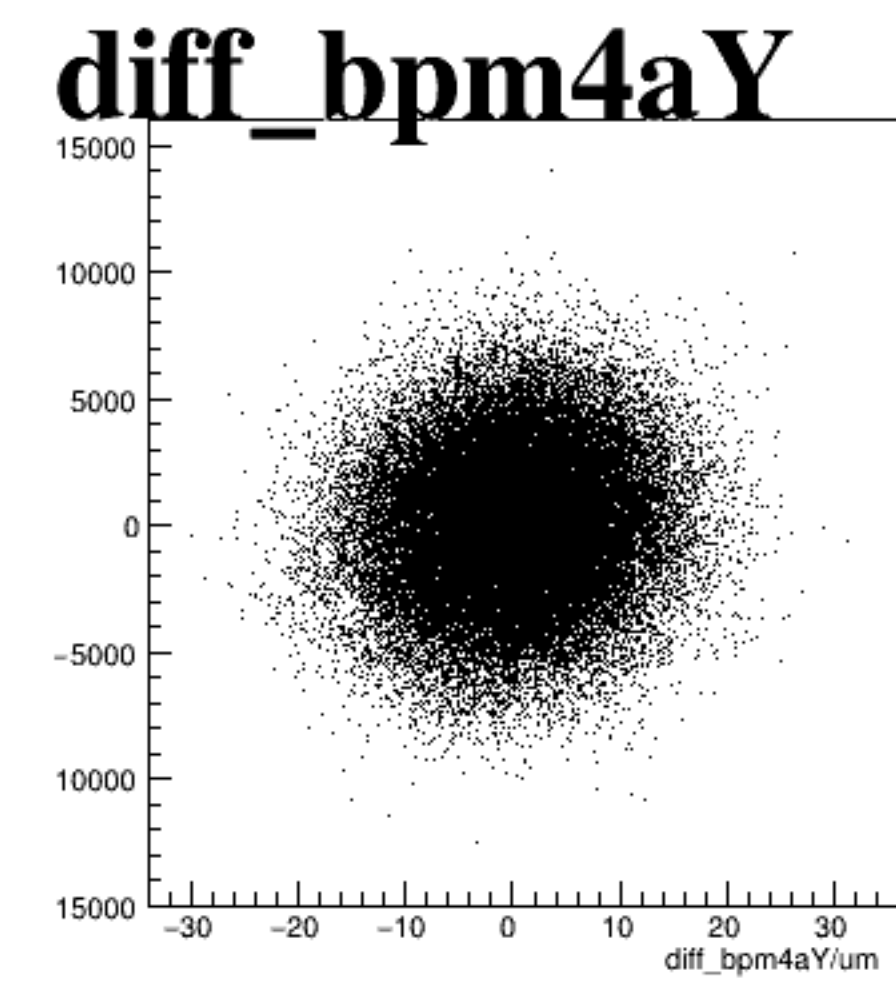
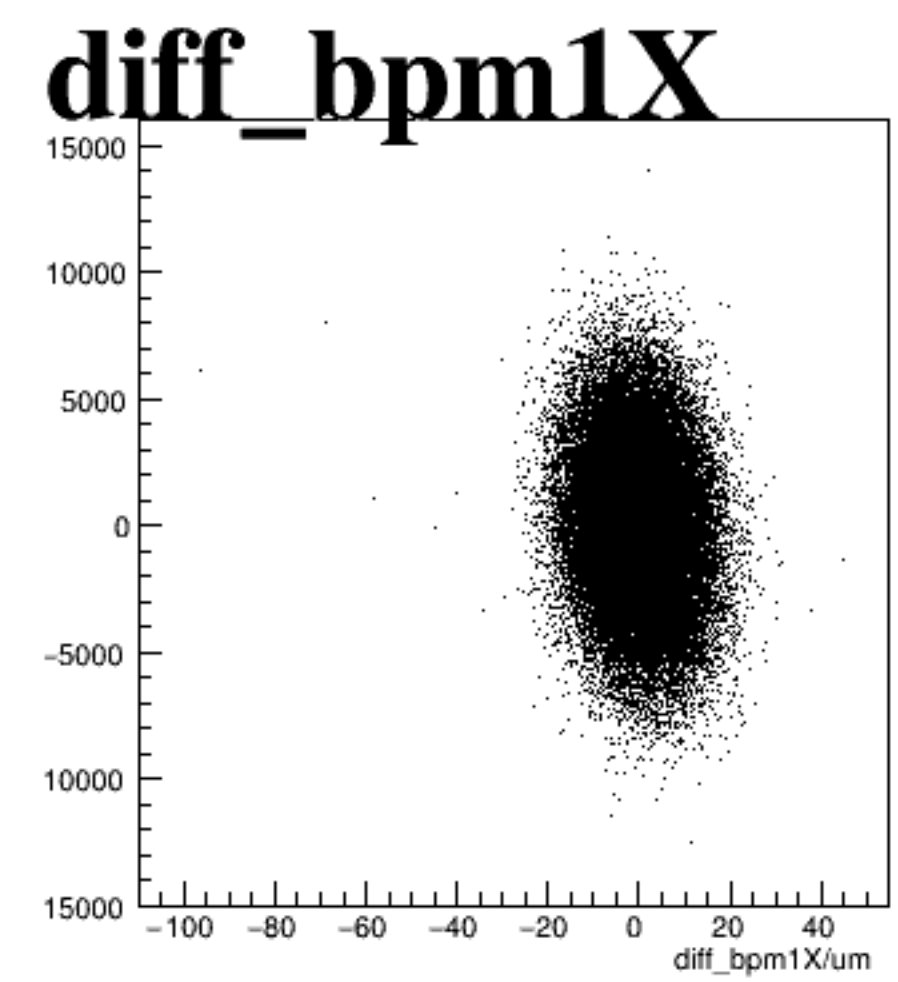




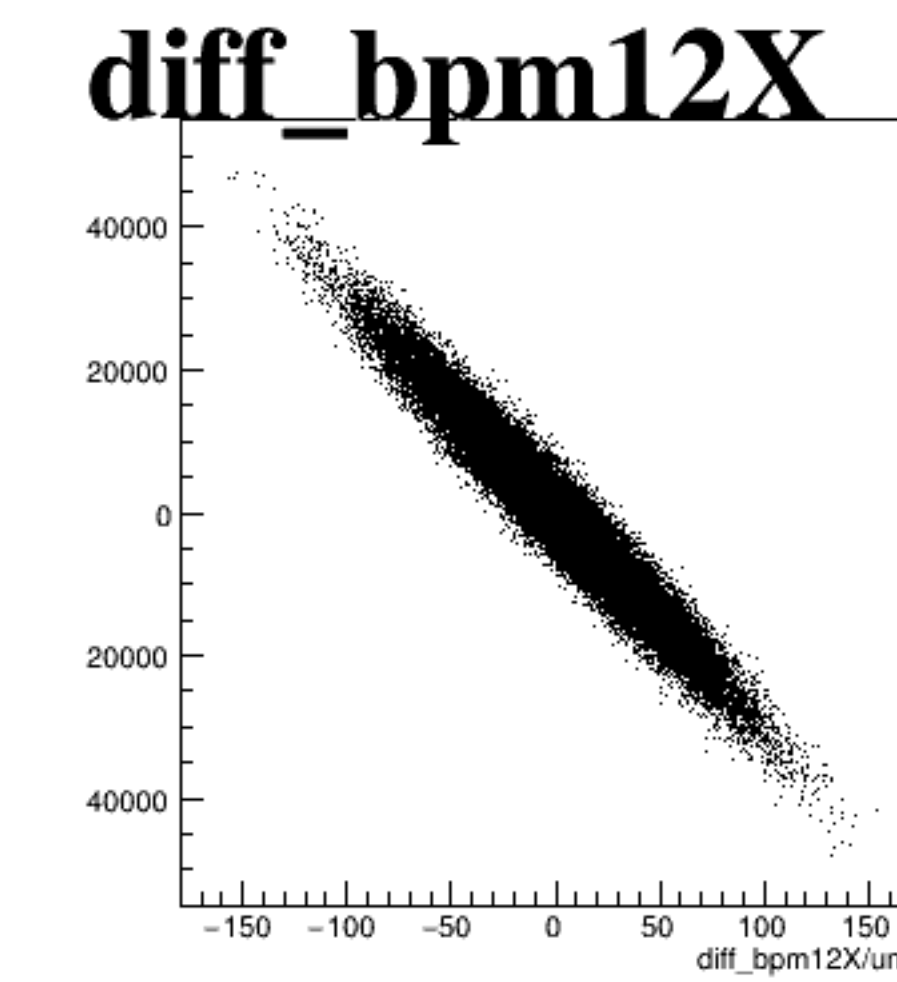
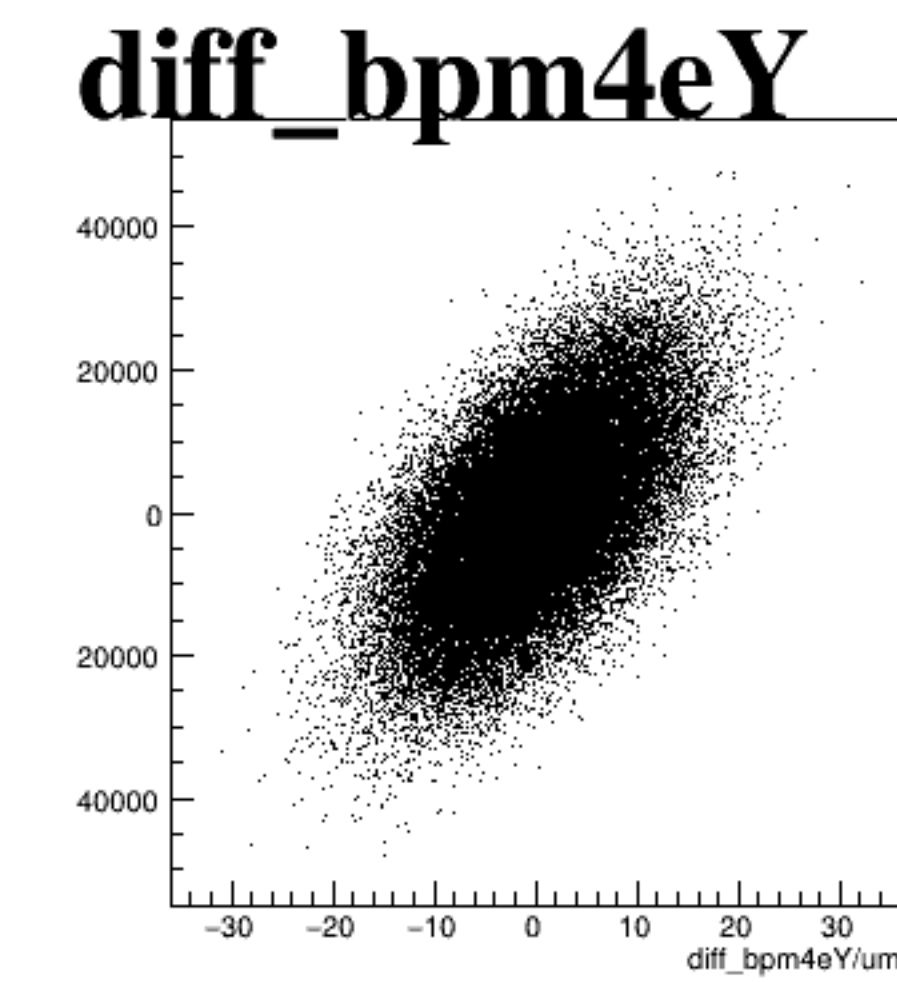
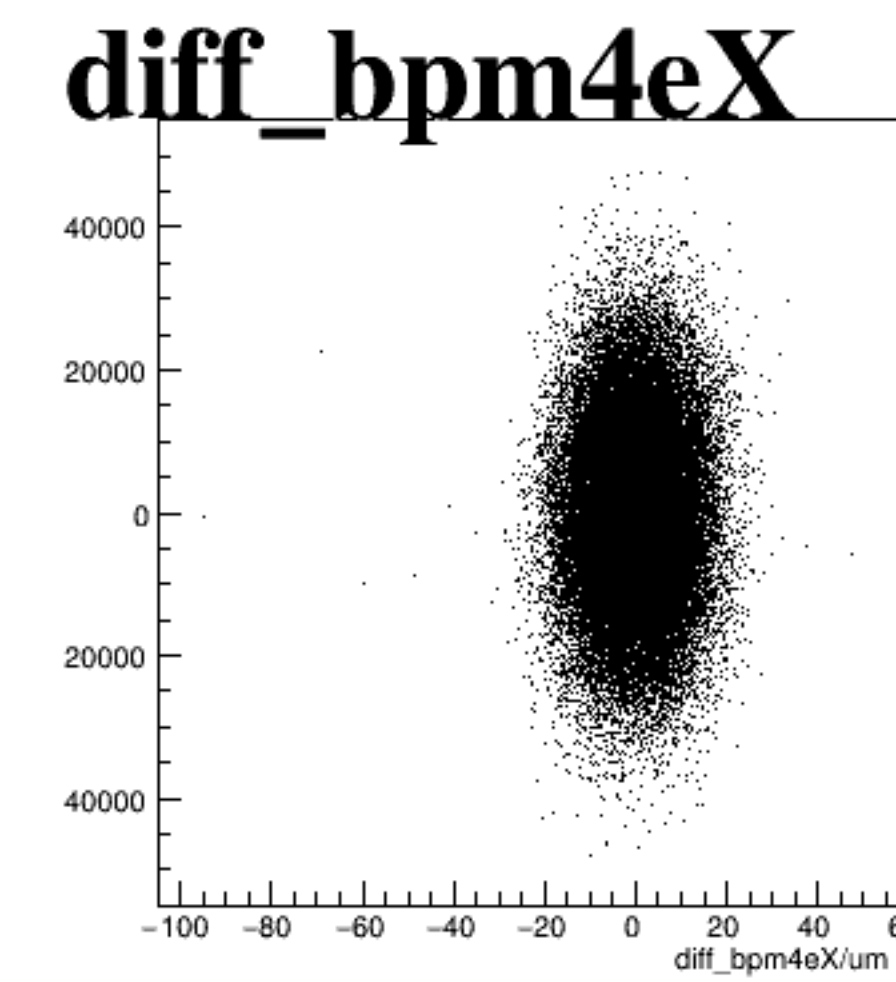
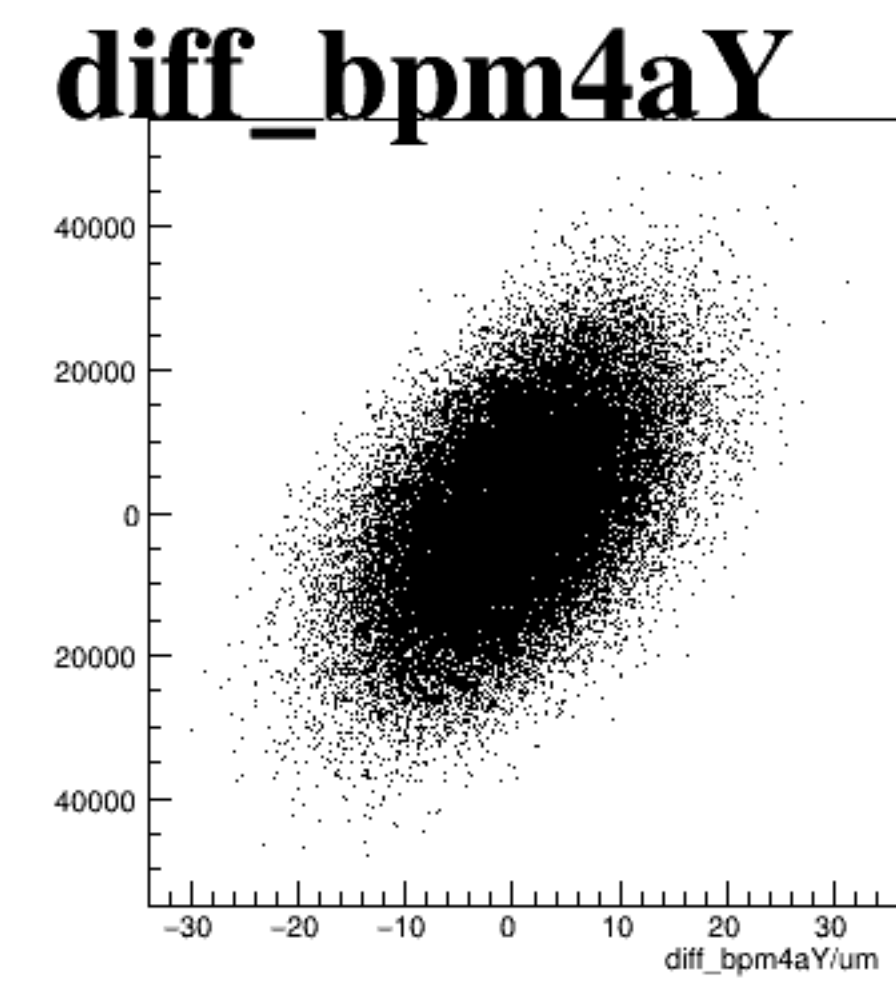
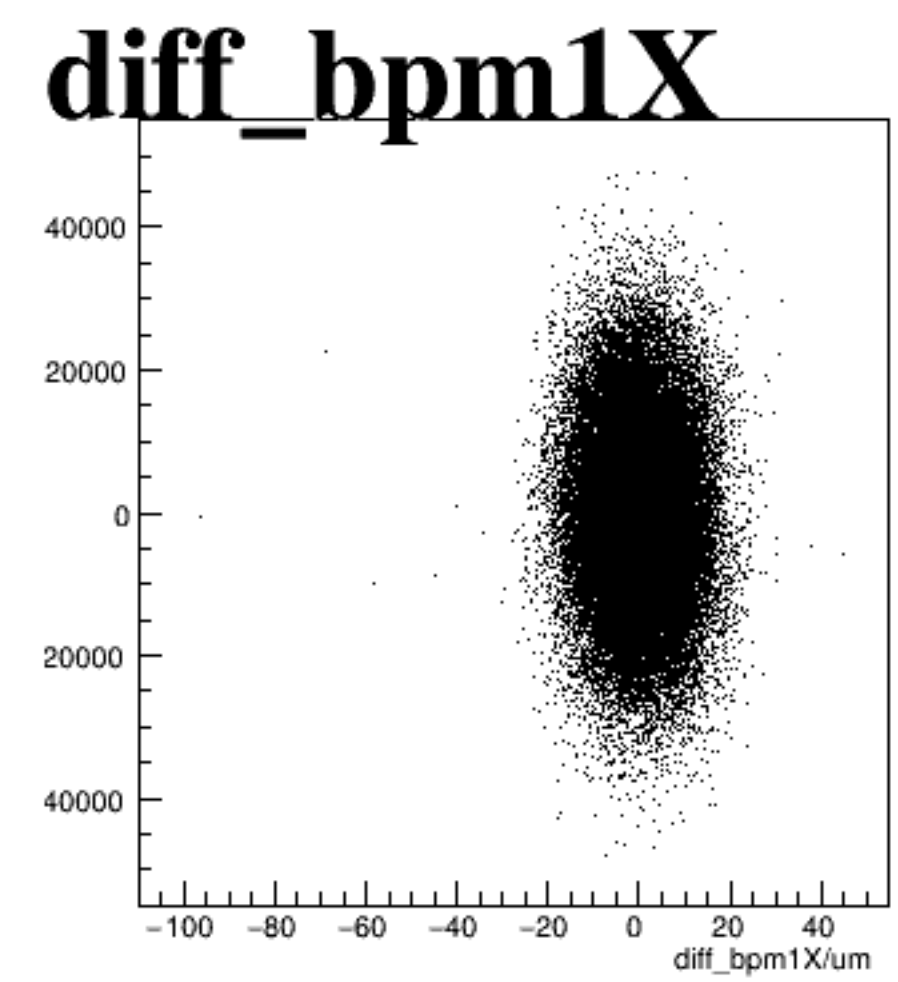
asym\_atl1



asym\_atr1



asym\_atl2



asym\_atr2

