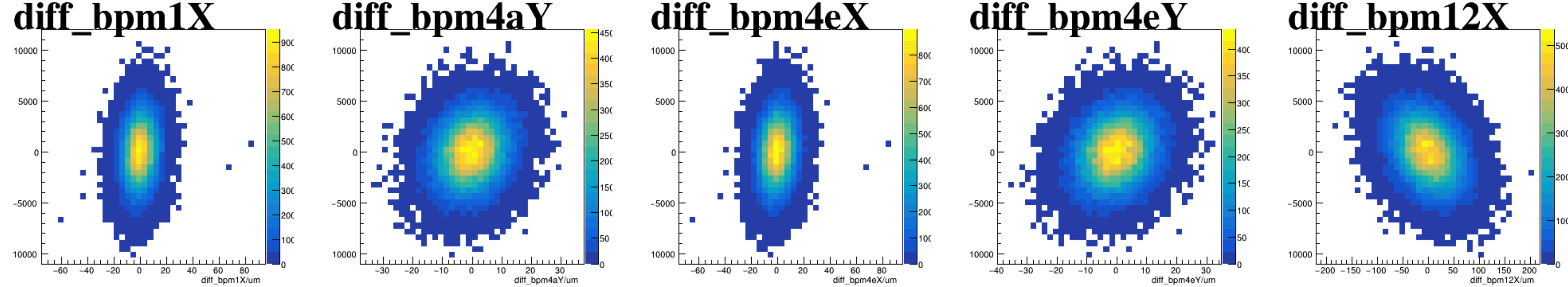
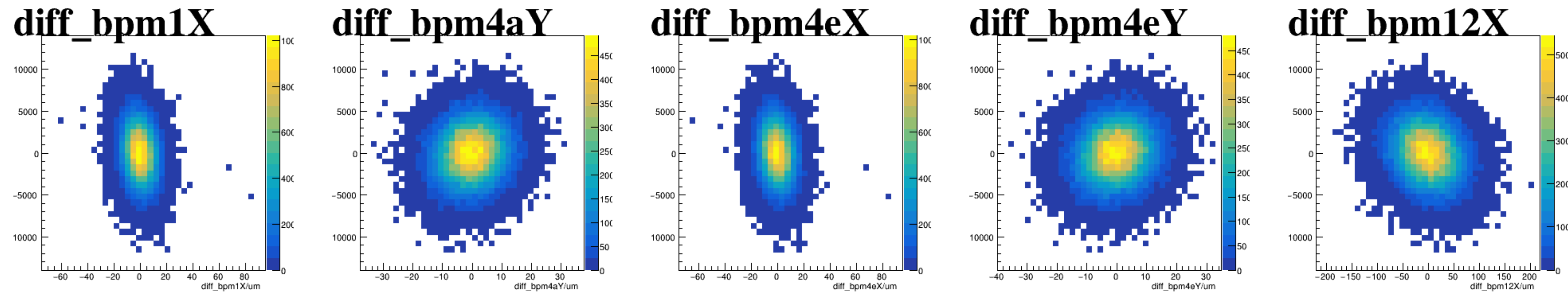


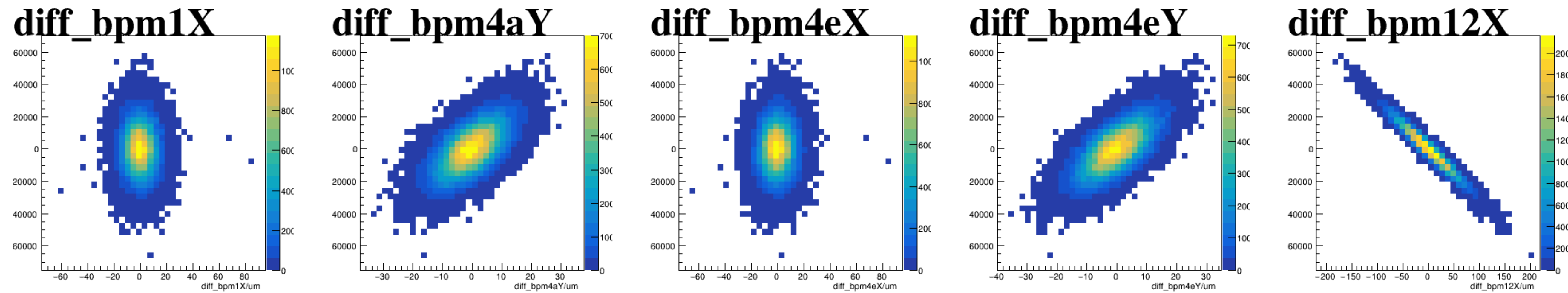
asym_atl1



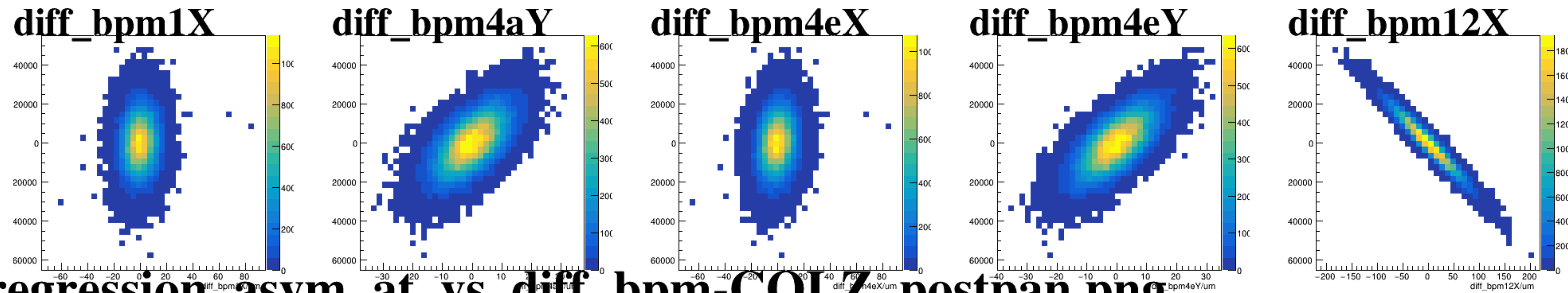
asym_atr1



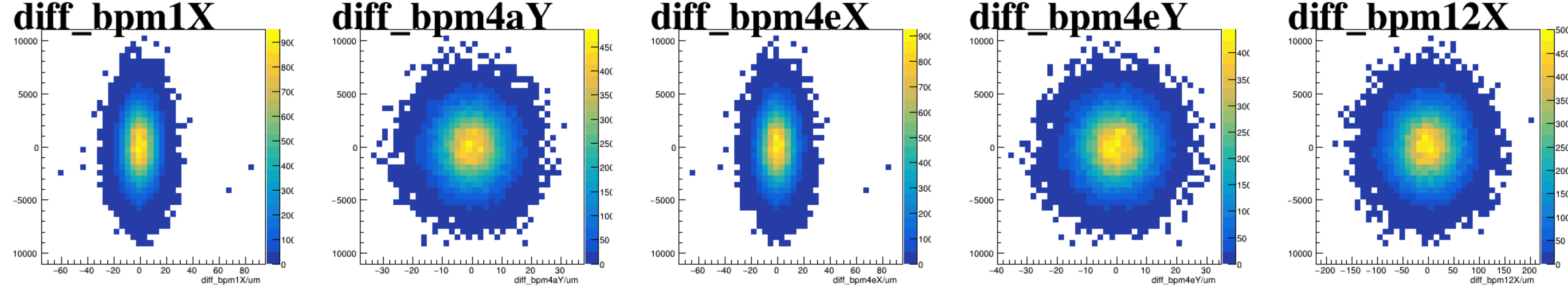
asym_atl2



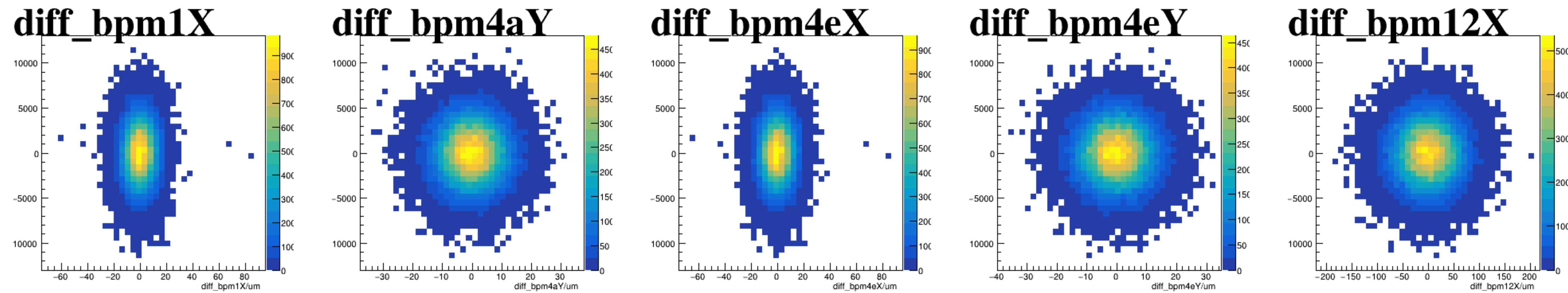
asym_atr2



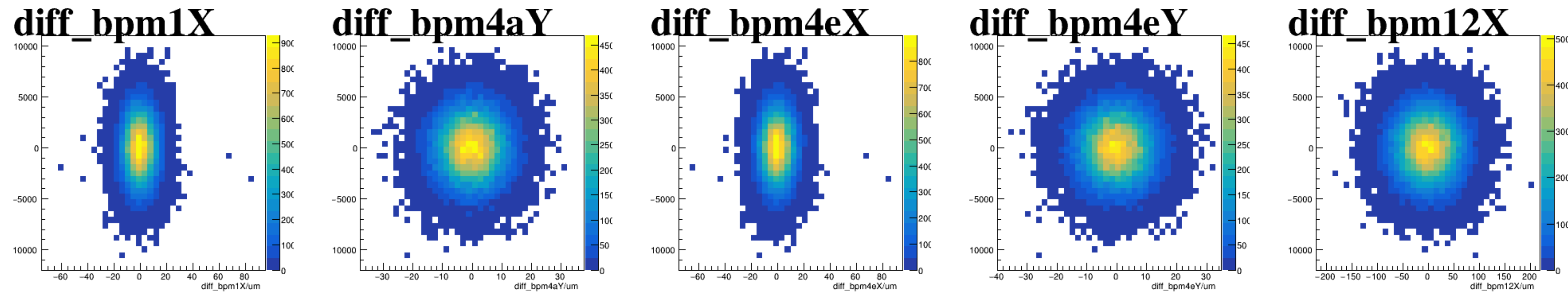
reg_asym_atl1



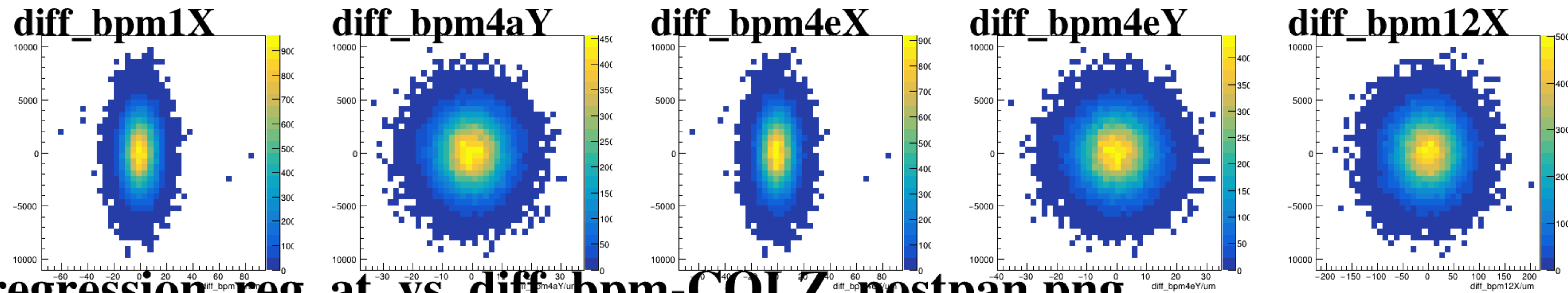
reg_asym_atr1



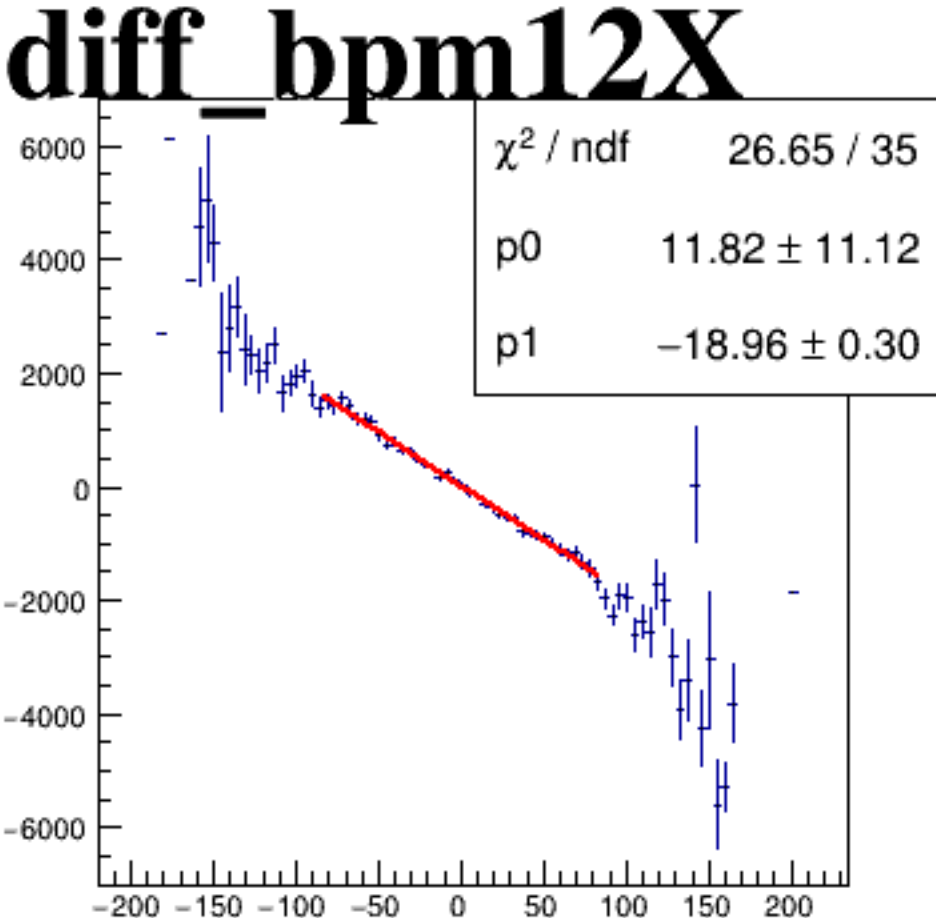
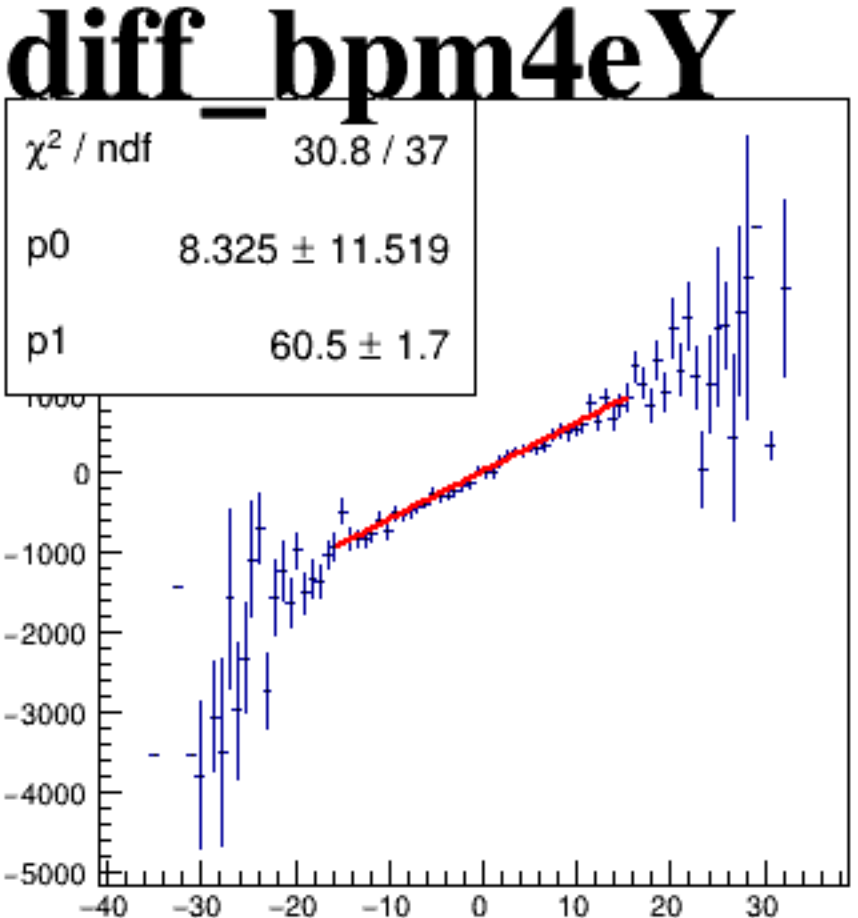
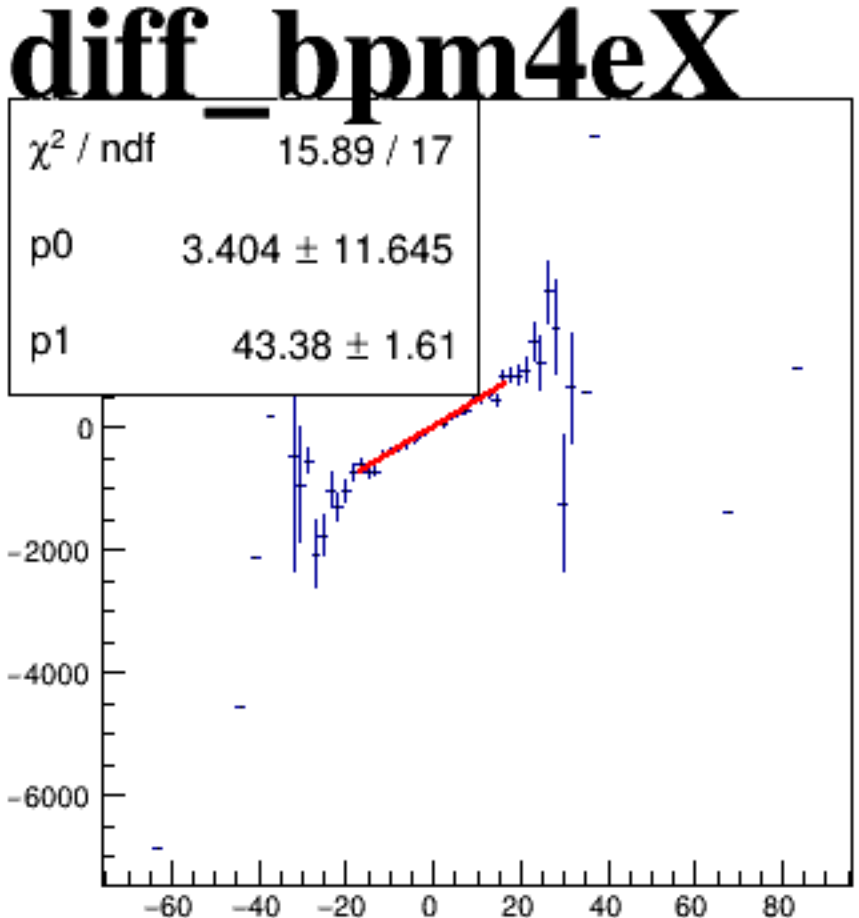
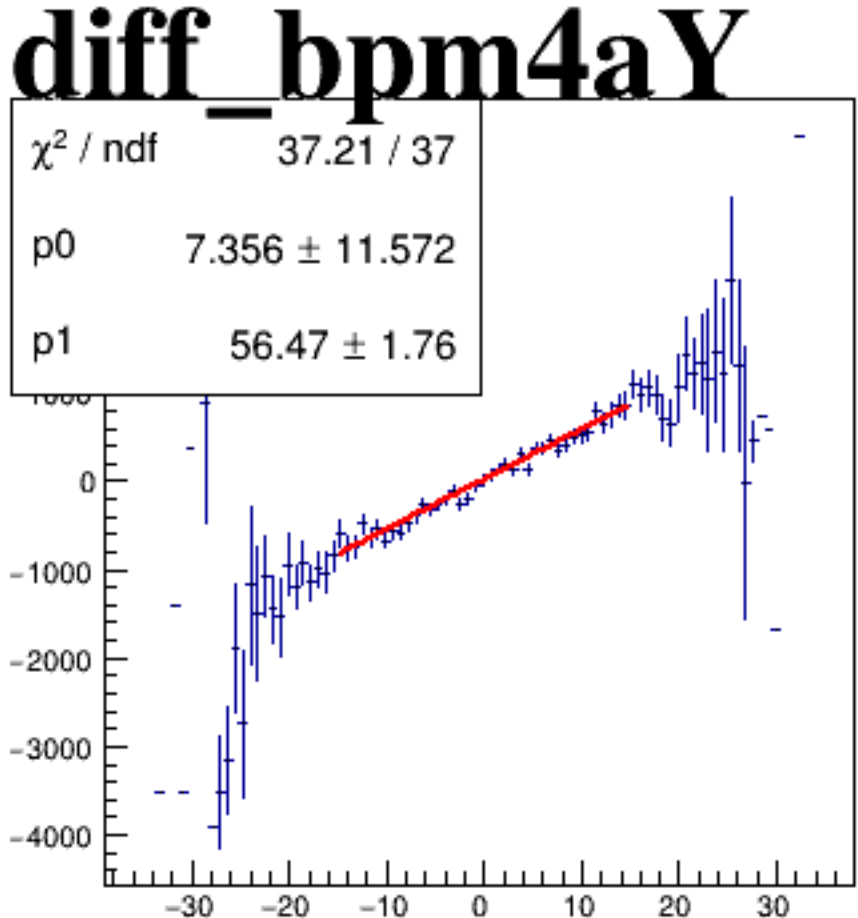
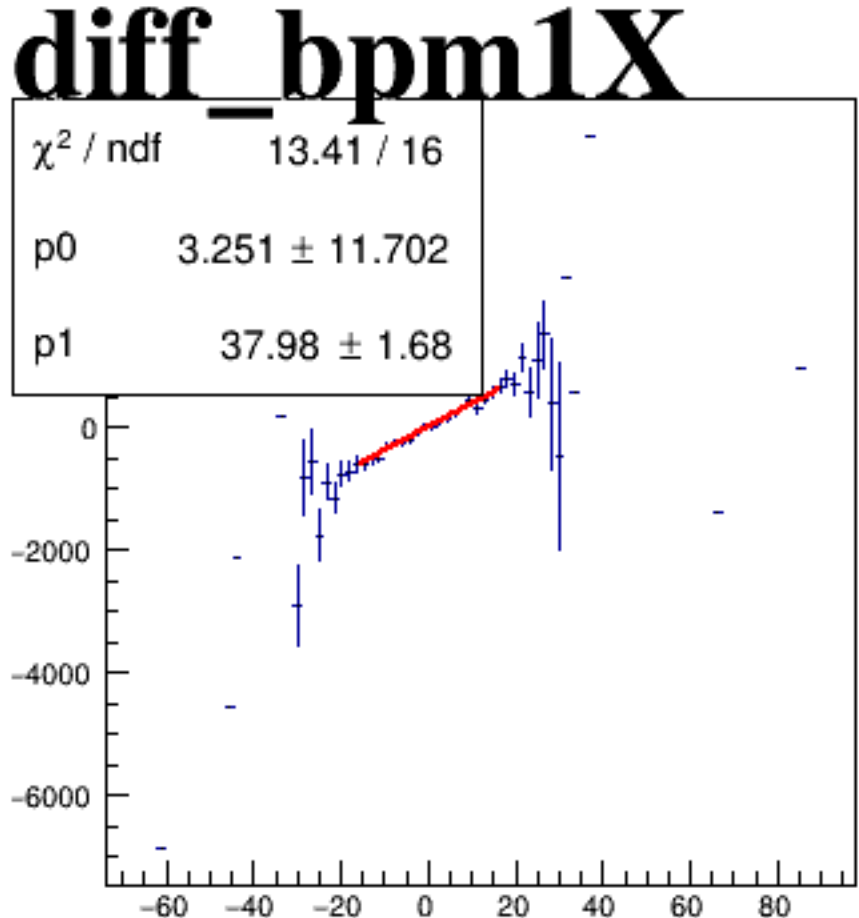
reg_asym_atl2



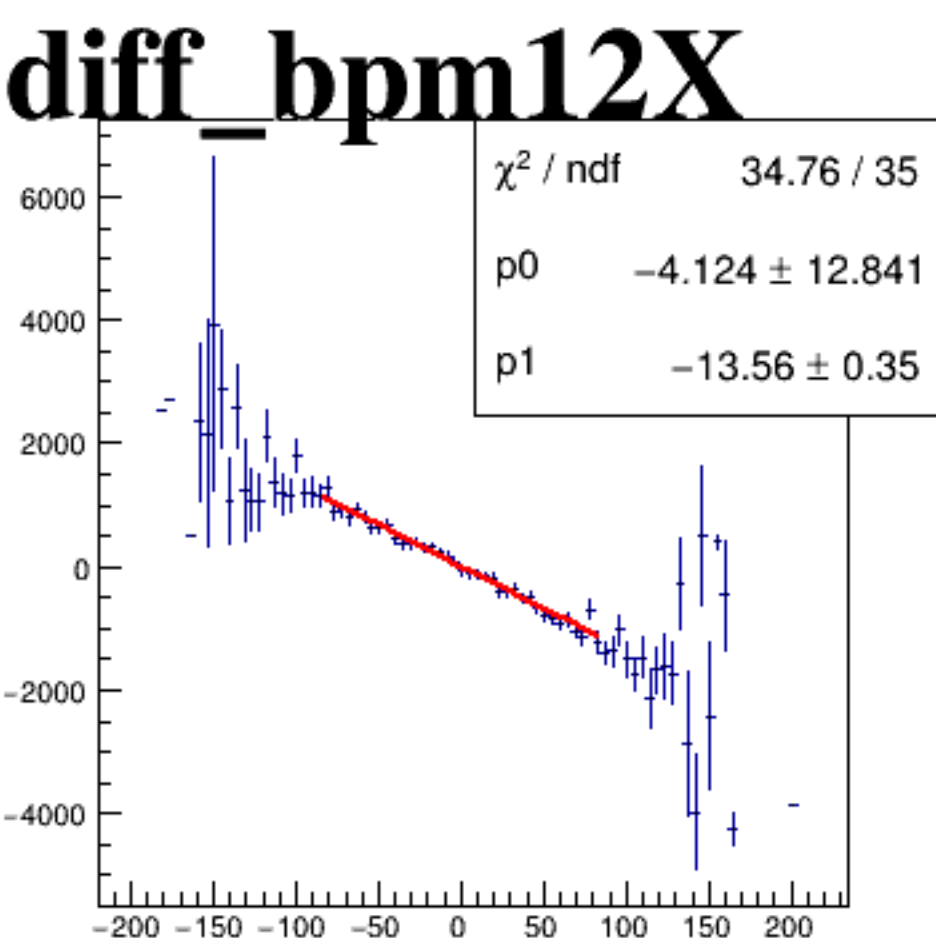
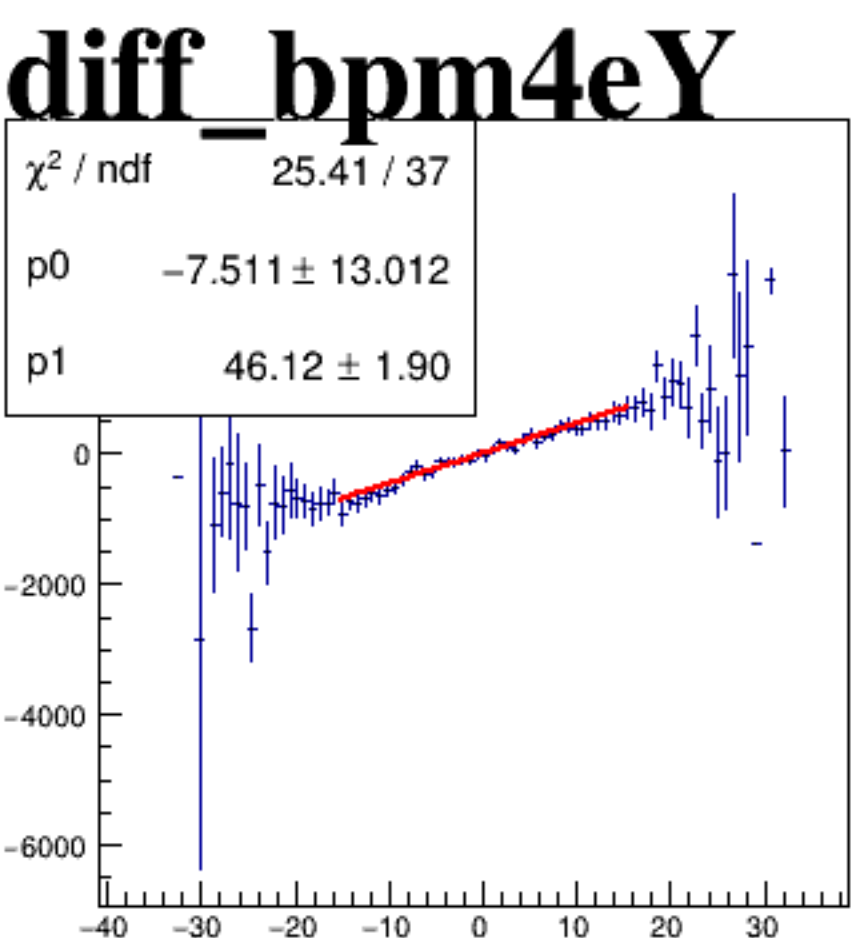
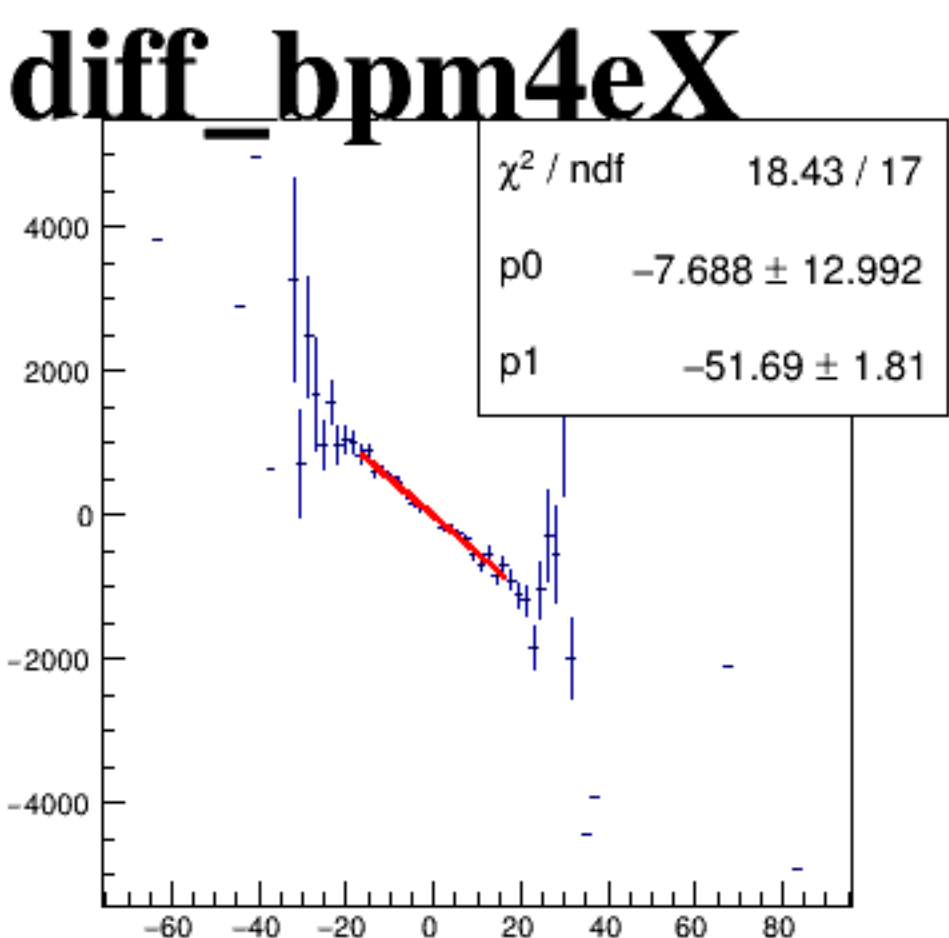
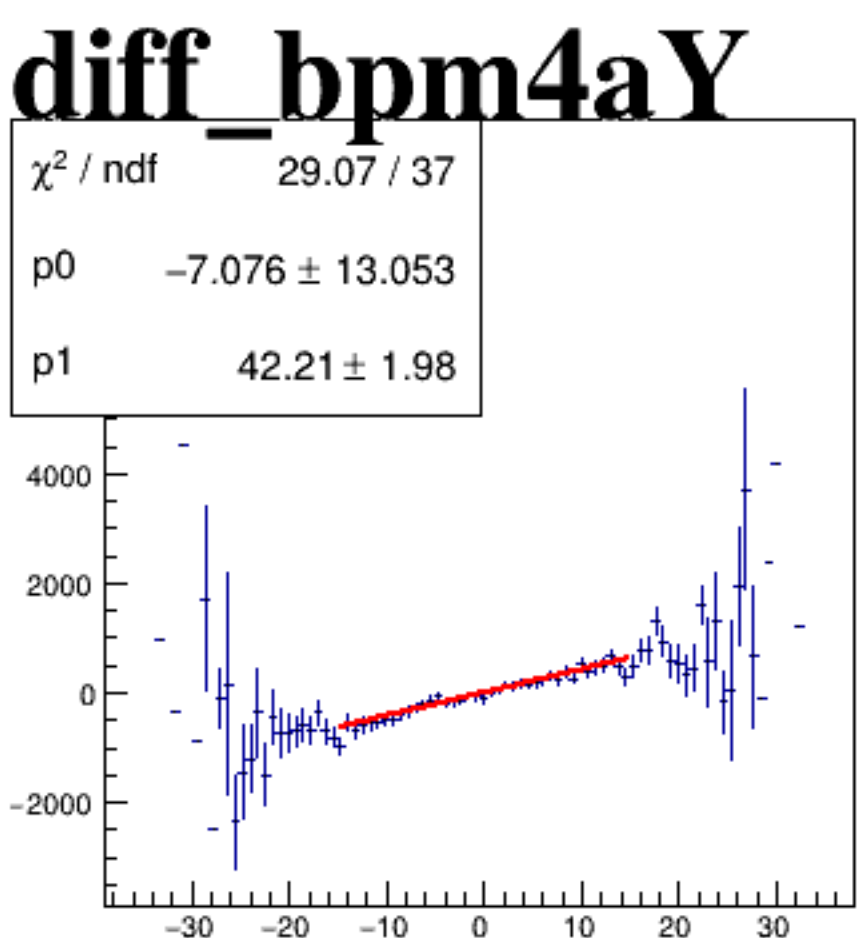
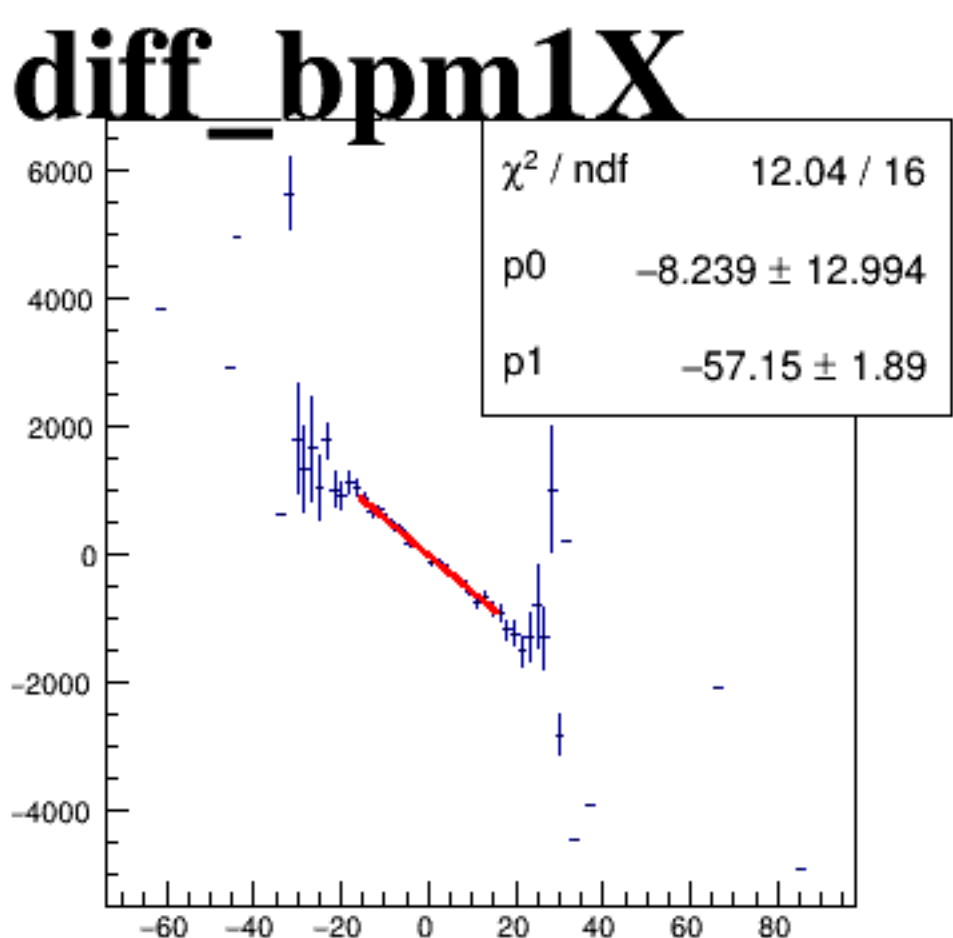
reg_asym_atr2



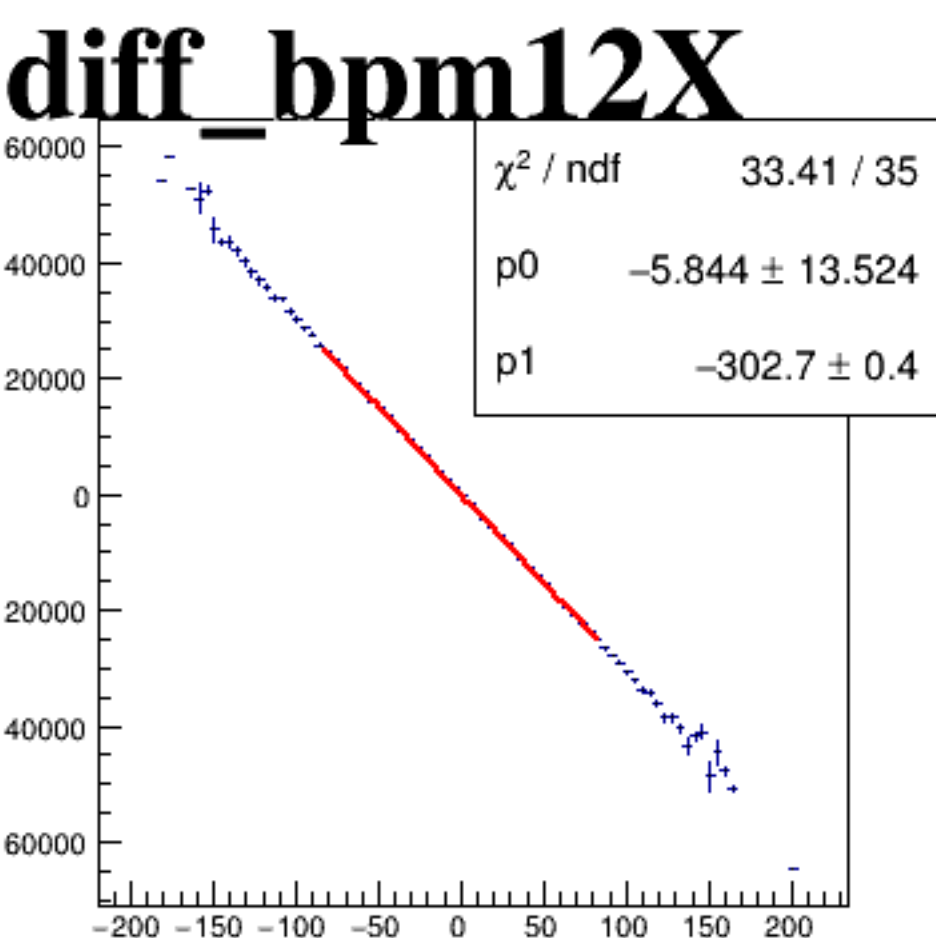
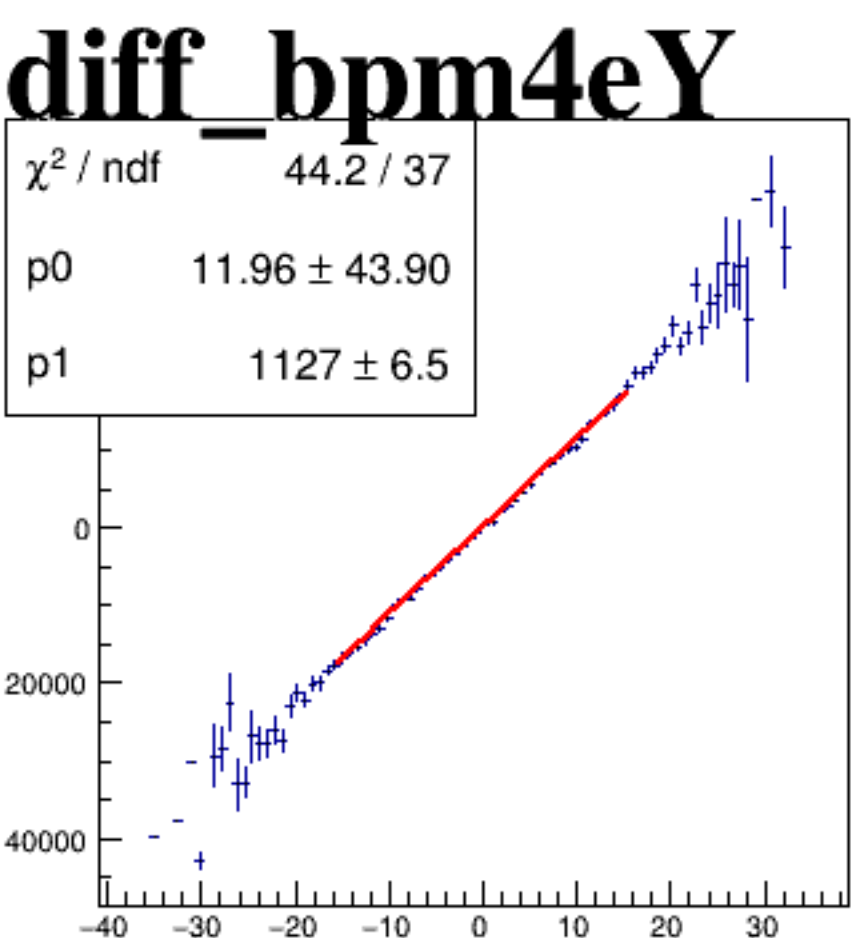
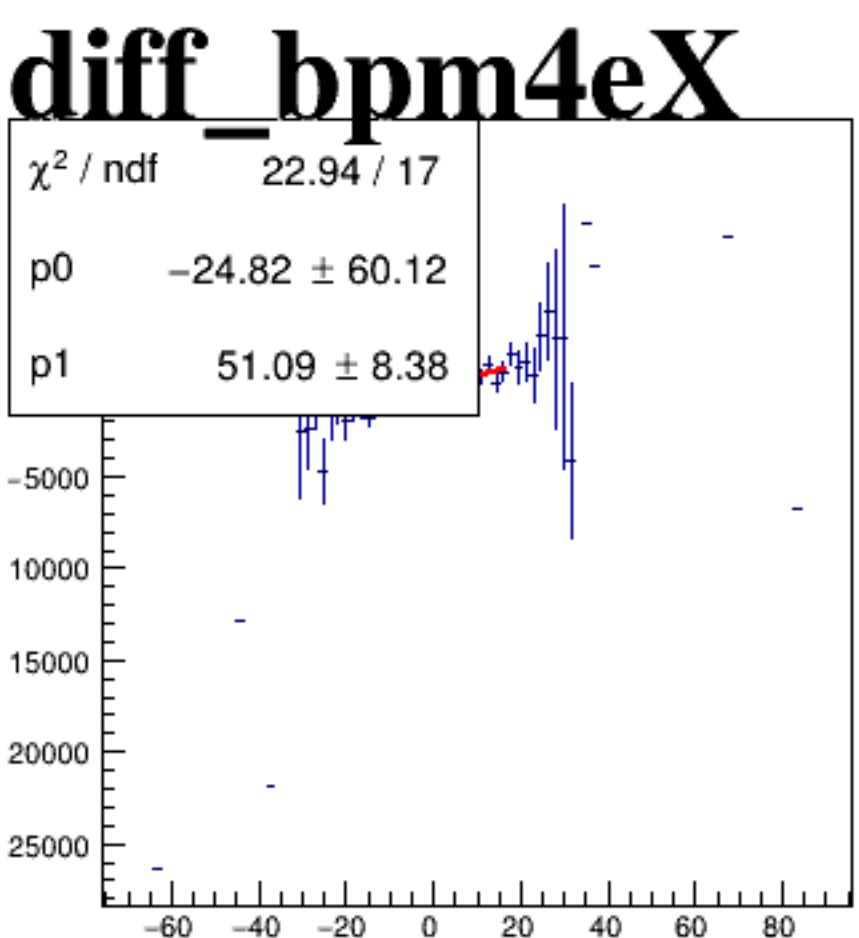
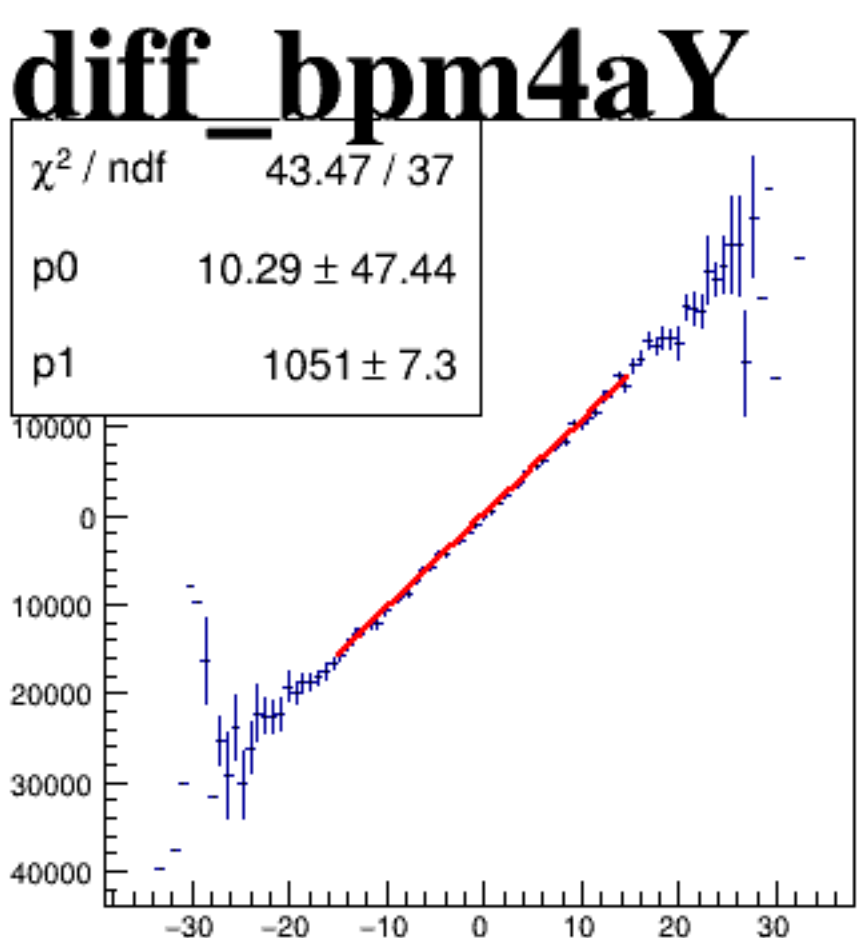
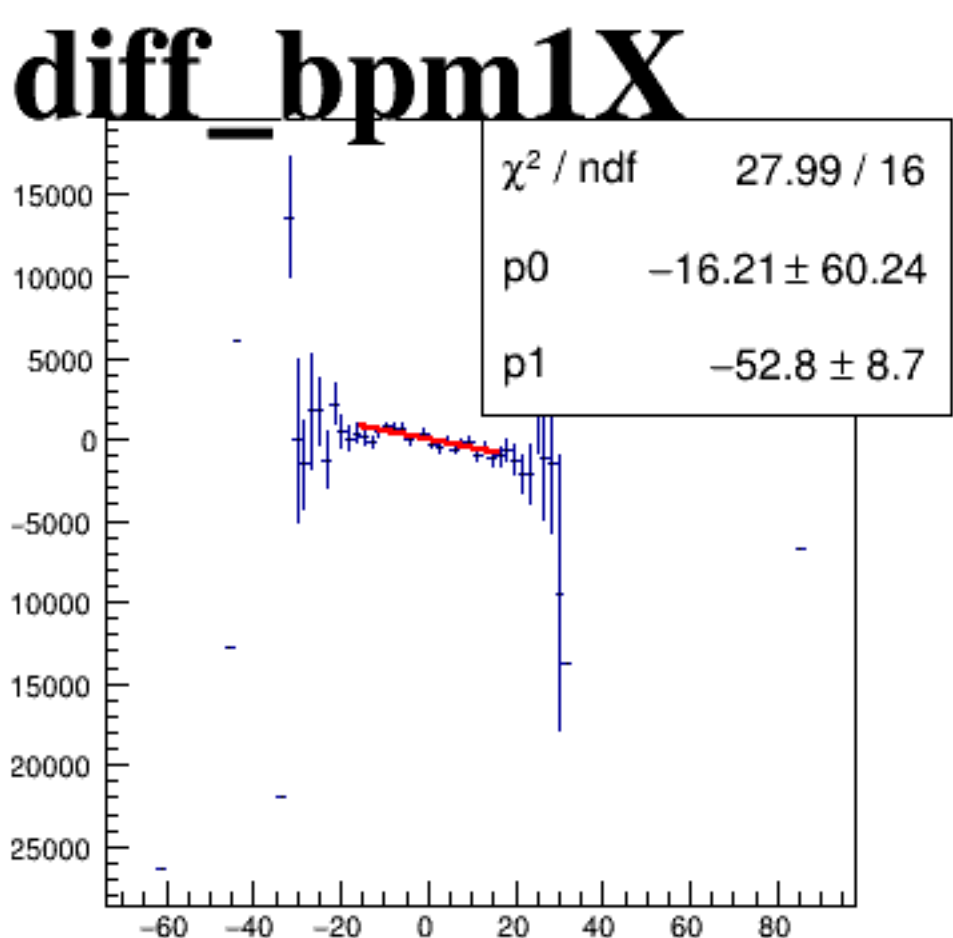
asym_atl1



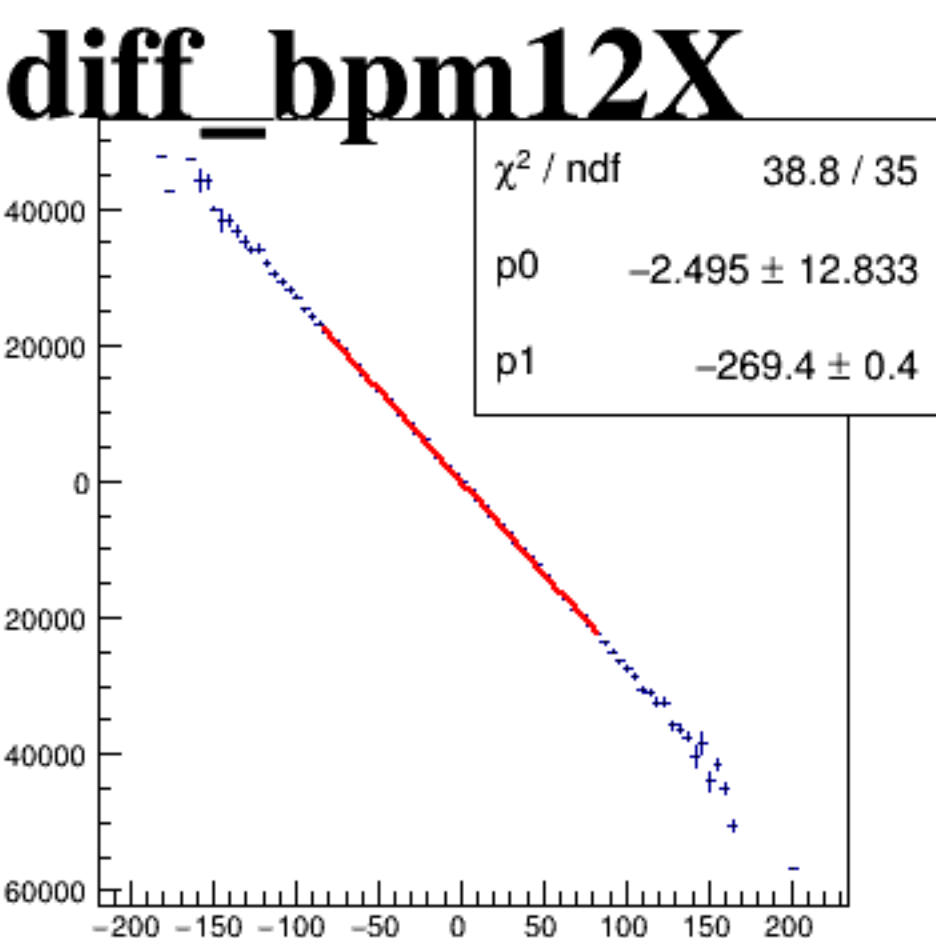
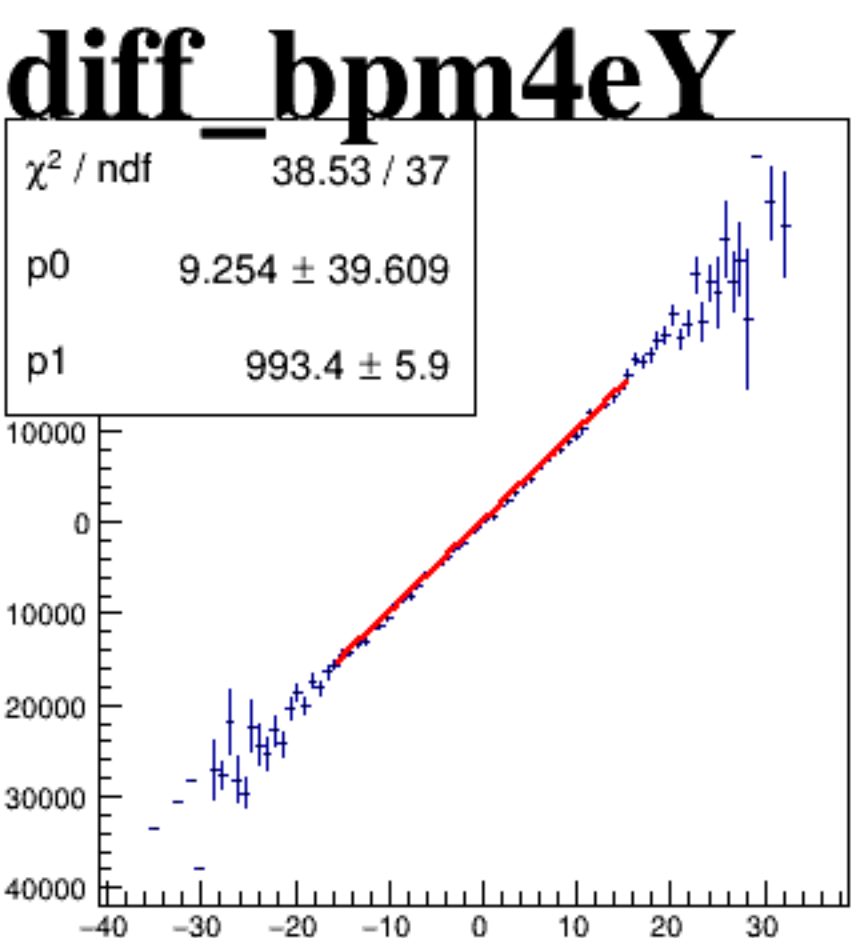
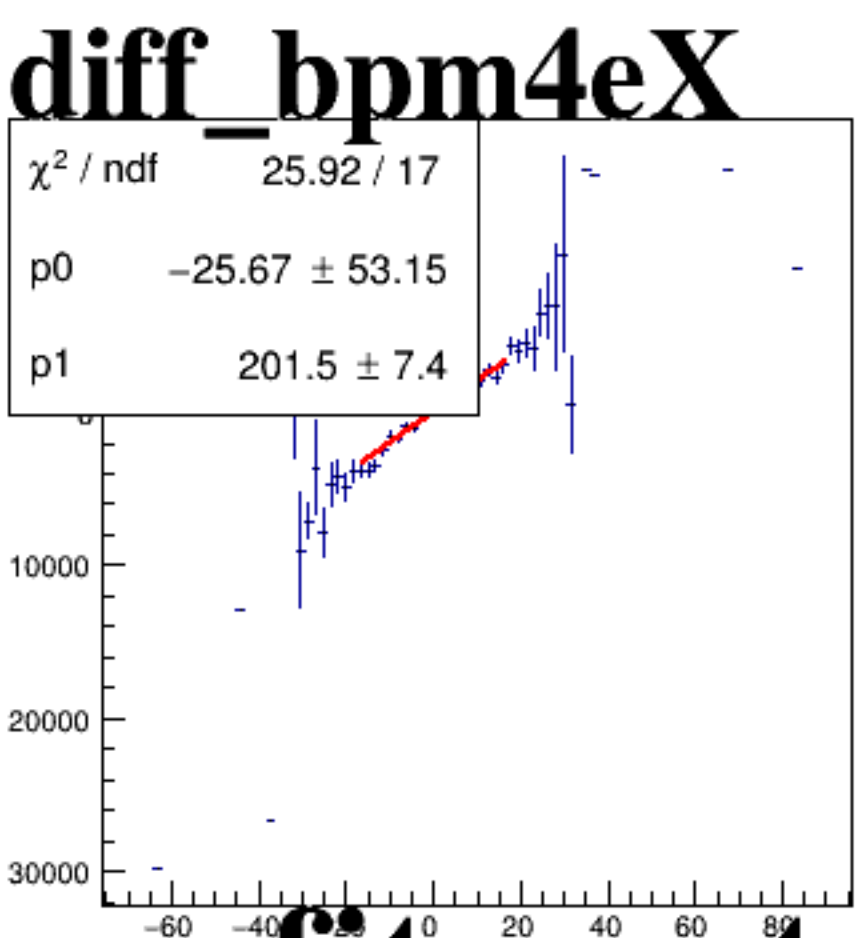
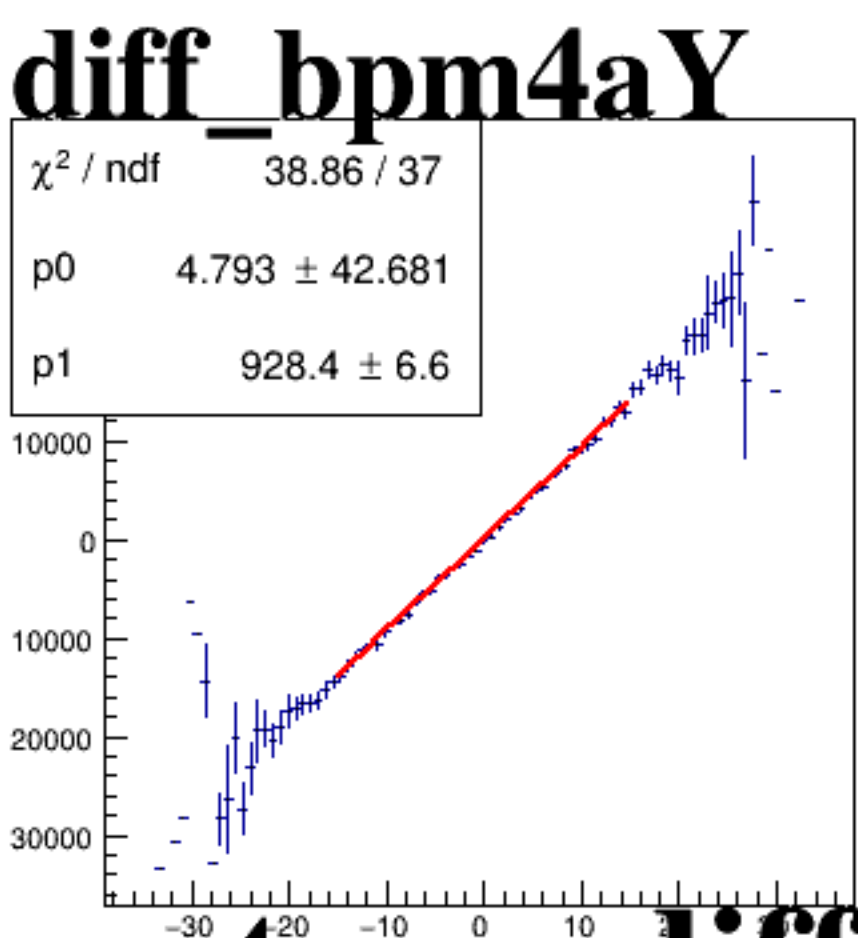
asym_atr1



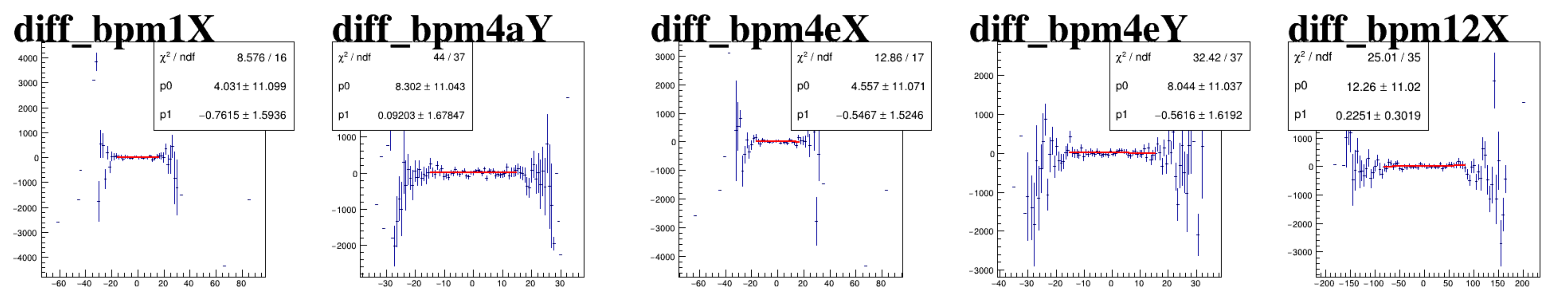
asym_atl2



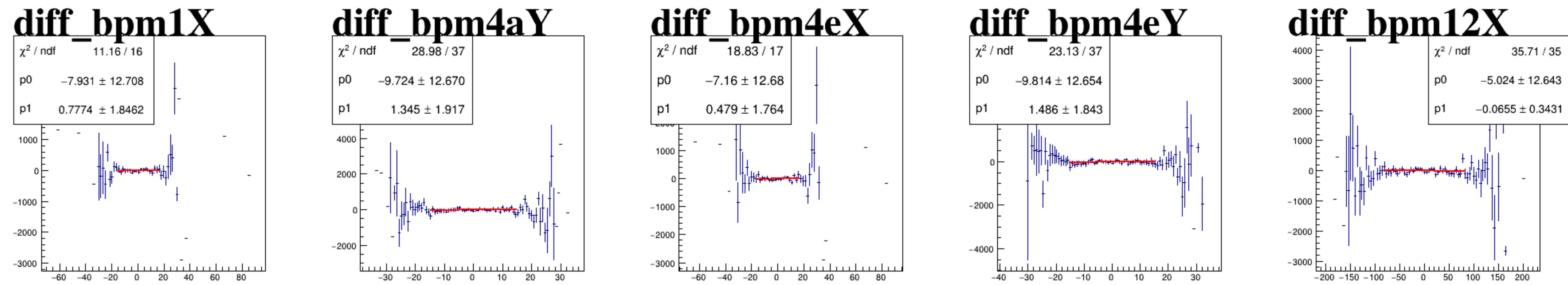
asym_atr2



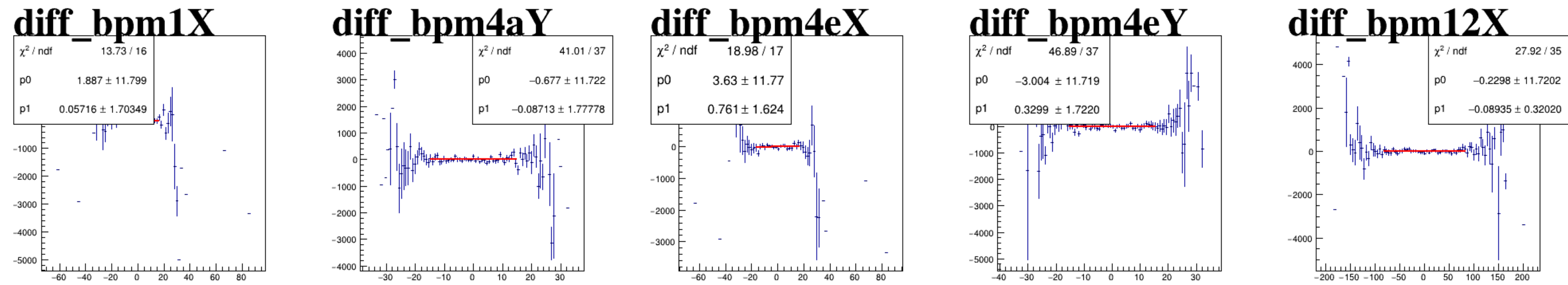
reg_asym_atl1



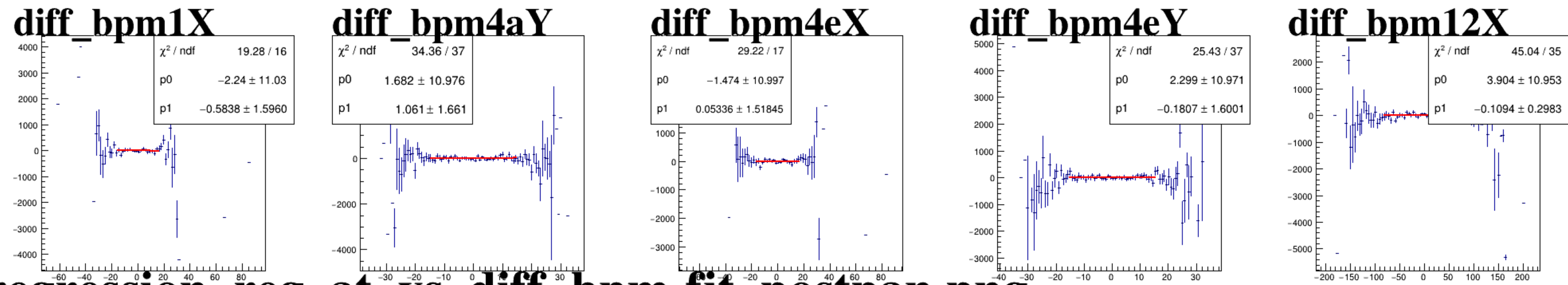
reg_asym_atr1



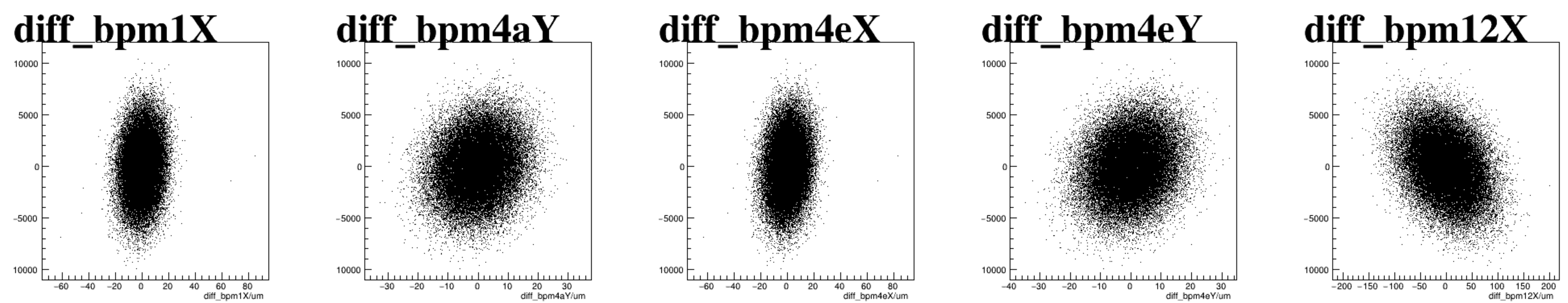
reg_asym_atl2



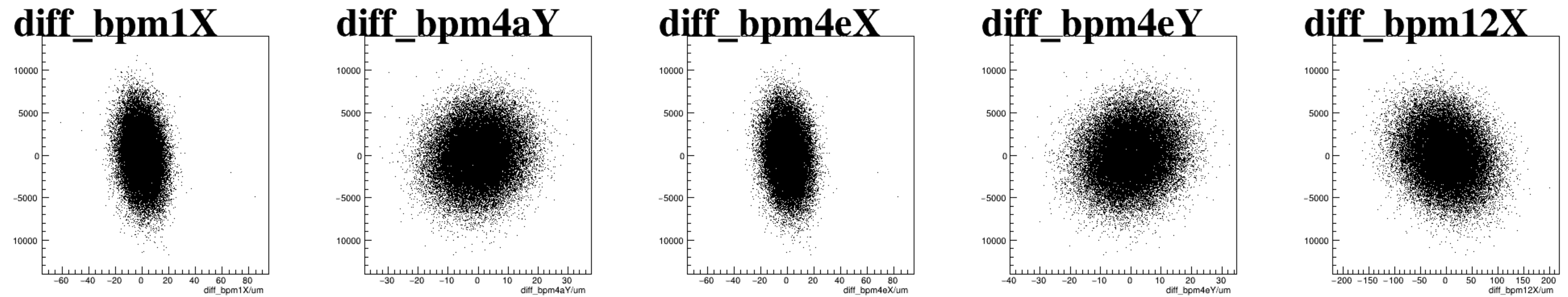
reg_asym_atr2



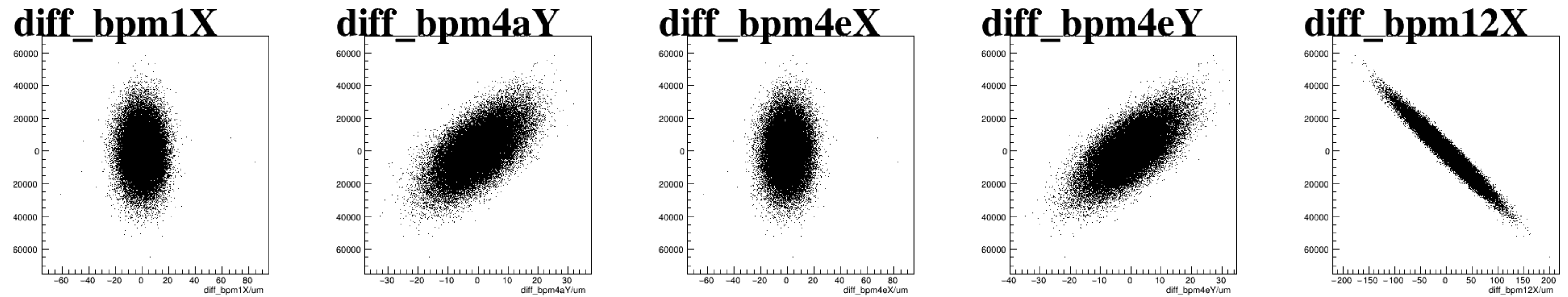
asym_atl1



asym_atr1



asym_atl2



asym_atr2

