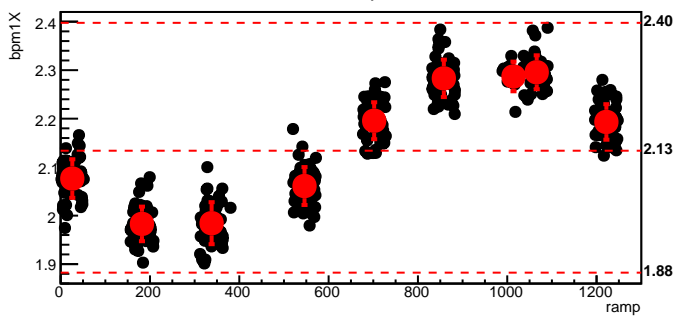
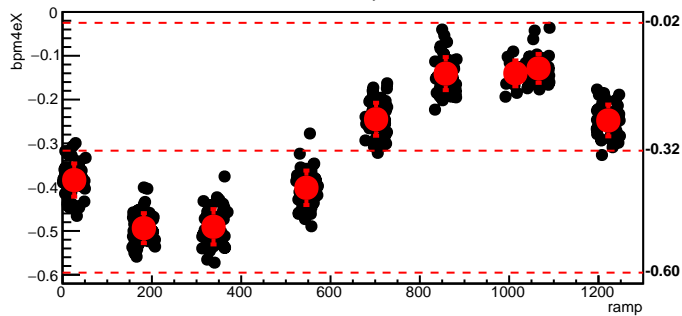


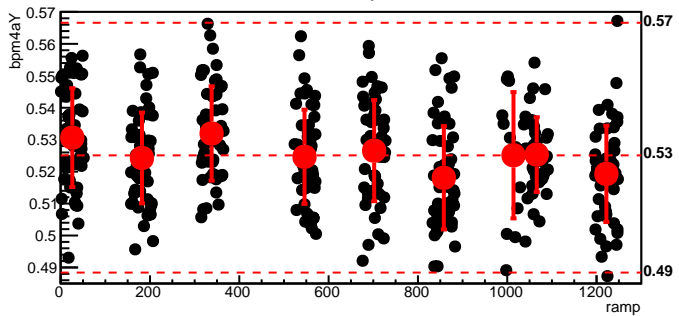
bmod_trim1,cyc=8011



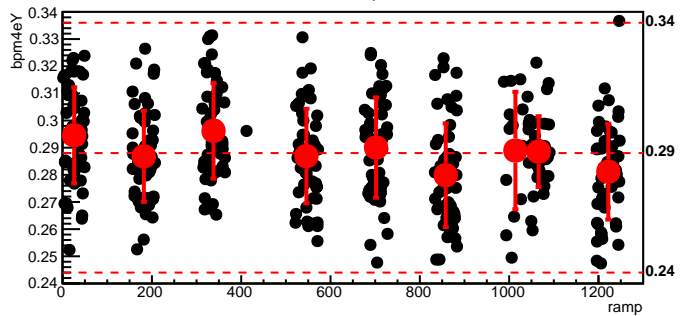
bmod_trim1,cyc=8011



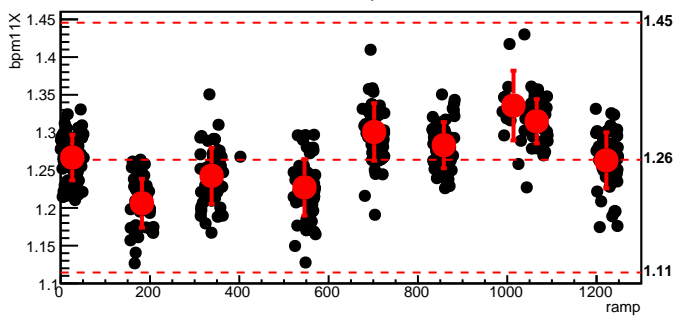
bmod_trim1,cyc=8011



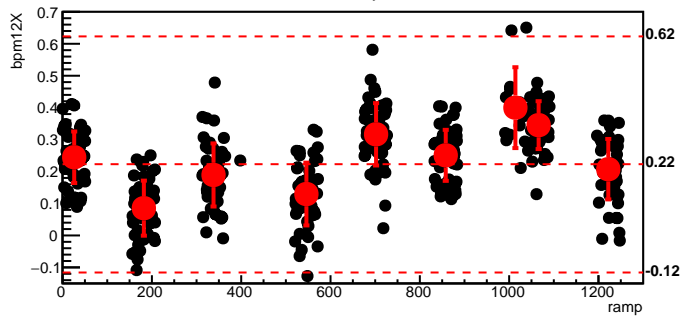
bmod_trim1,cyc=8011



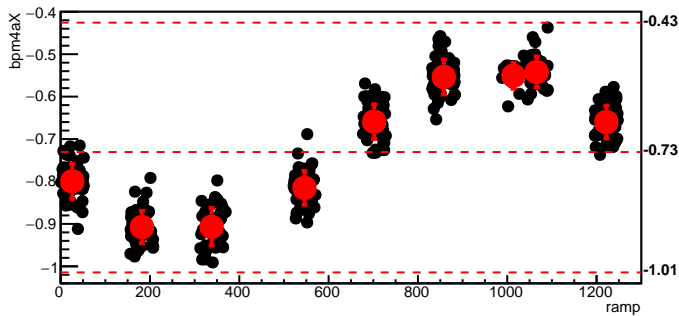
bmod_trim1,cyc=8011



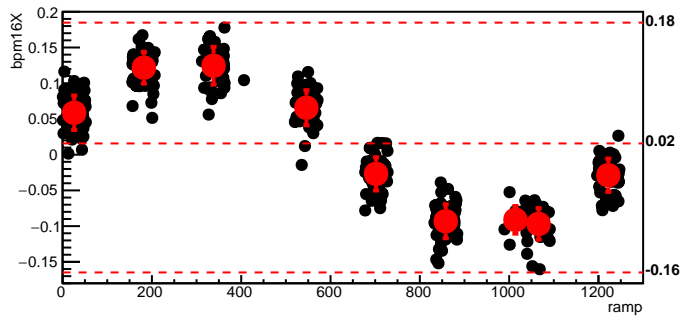
bmod_trim1,cyc=8011



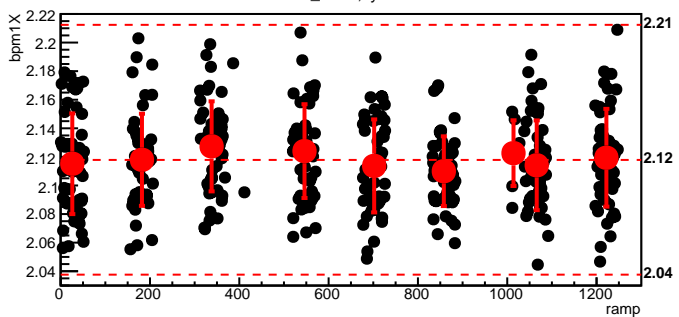
bmod_trim1,cyc=8011



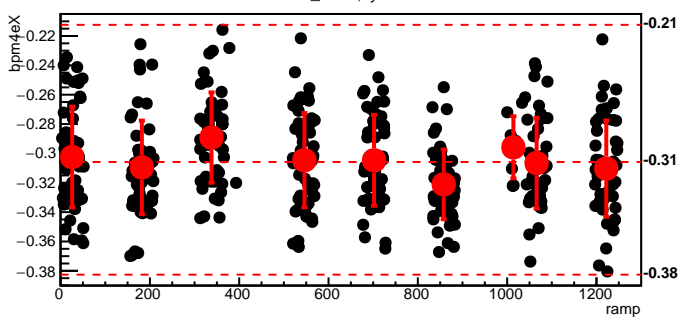
bmod_trim1,cyc=8011



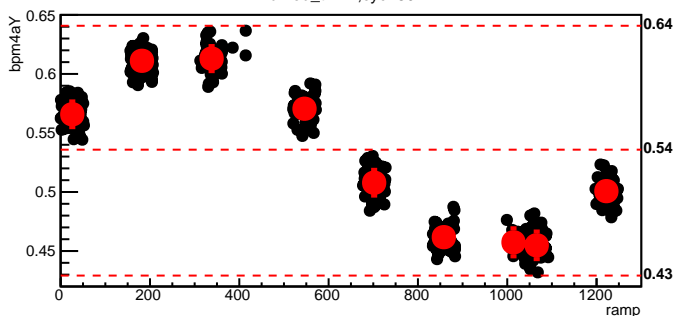
bmod_trim2,cyc=8011



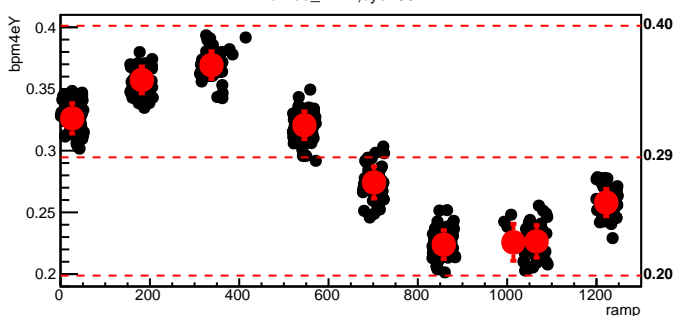
bmod_trim2,cyc=8011



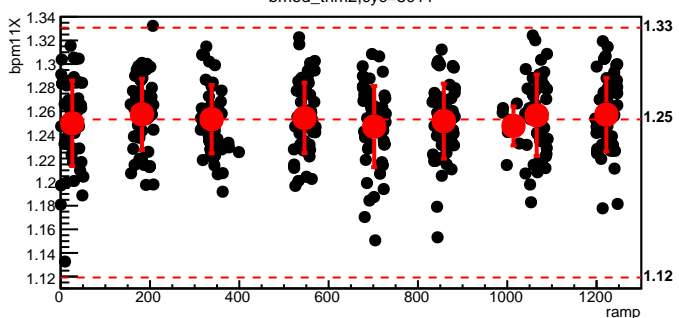
bmod_trim2,cyc=8011



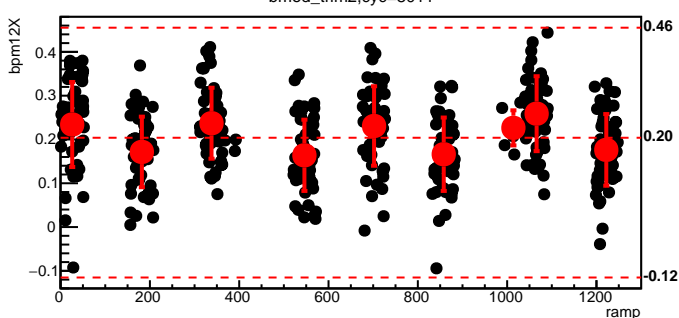
bmod_trim2,cyc=8011



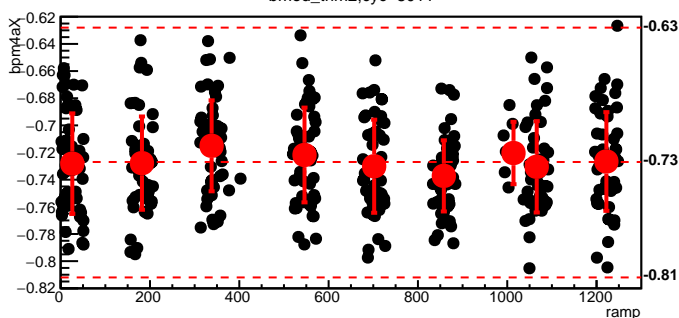
bmod_trim2,cyc=8011



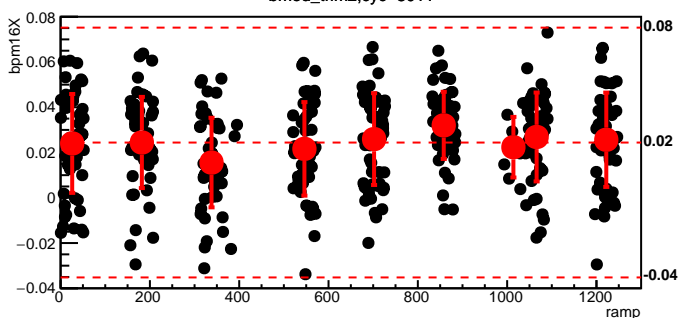
bmod_trim2,cyc=8011



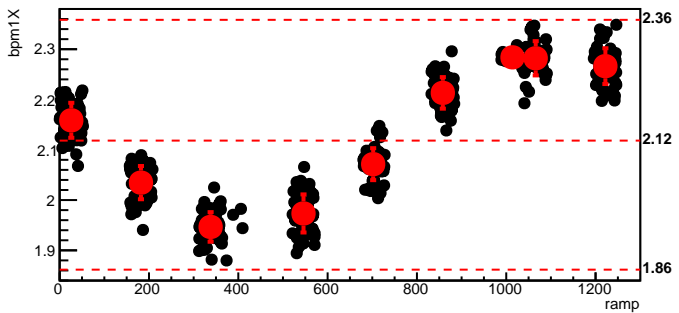
bmod_trim2,cyc=8011



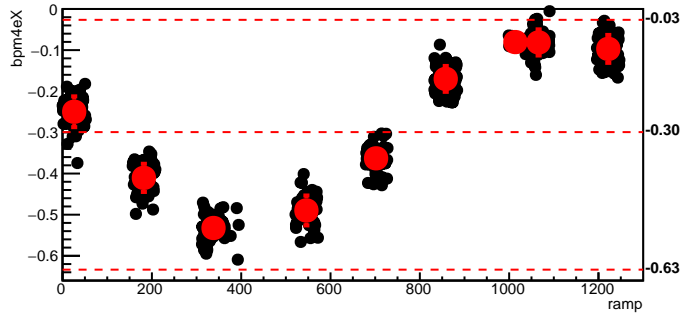
bmod_trim2,cyc=8011



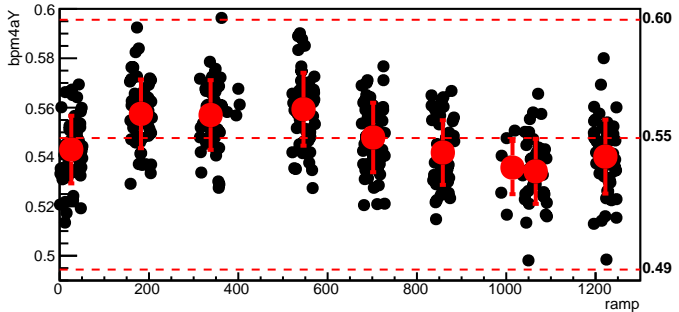
bmod_trim3,cyc=8011



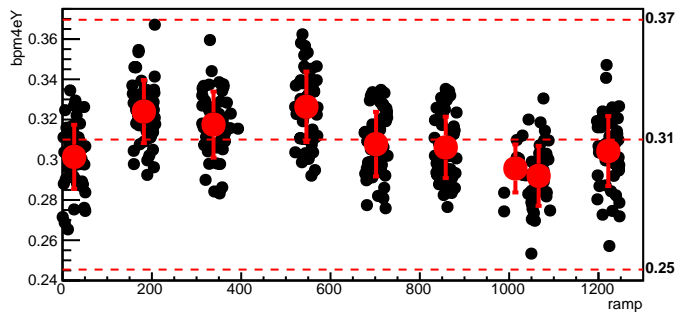
bmod_trim3,cyc=8011



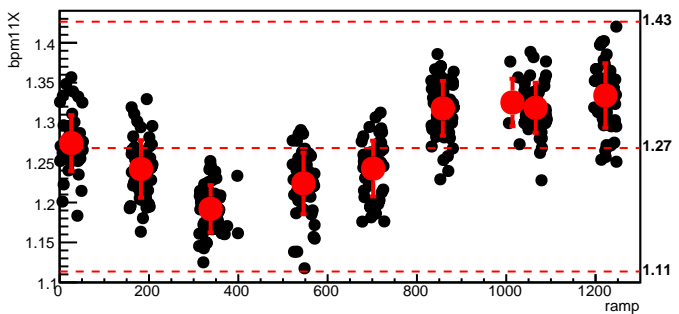
bmod_trim3,cyc=8011



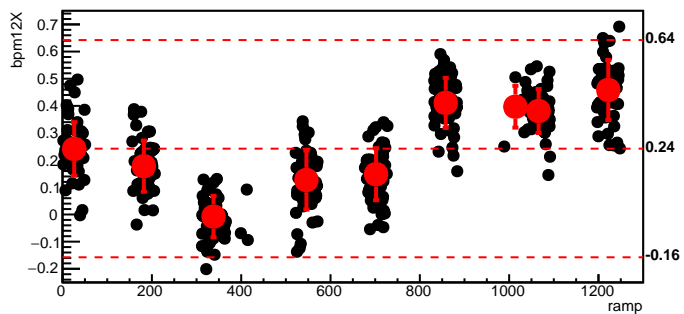
bmod_trim3,cyc=8011



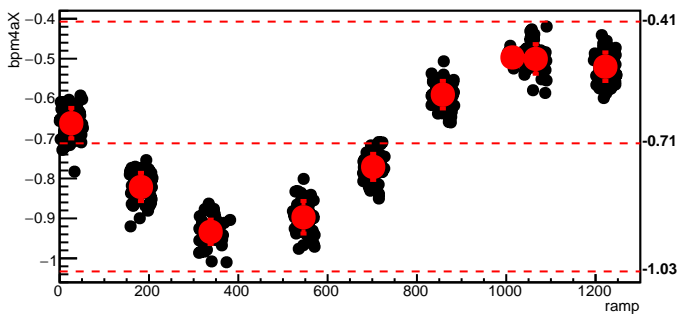
bmod_trim3,cyc=8011



bmod_trim3,cyc=8011



bmod_trim3,cyc=8011



bmod_trim3,cyc=8011

