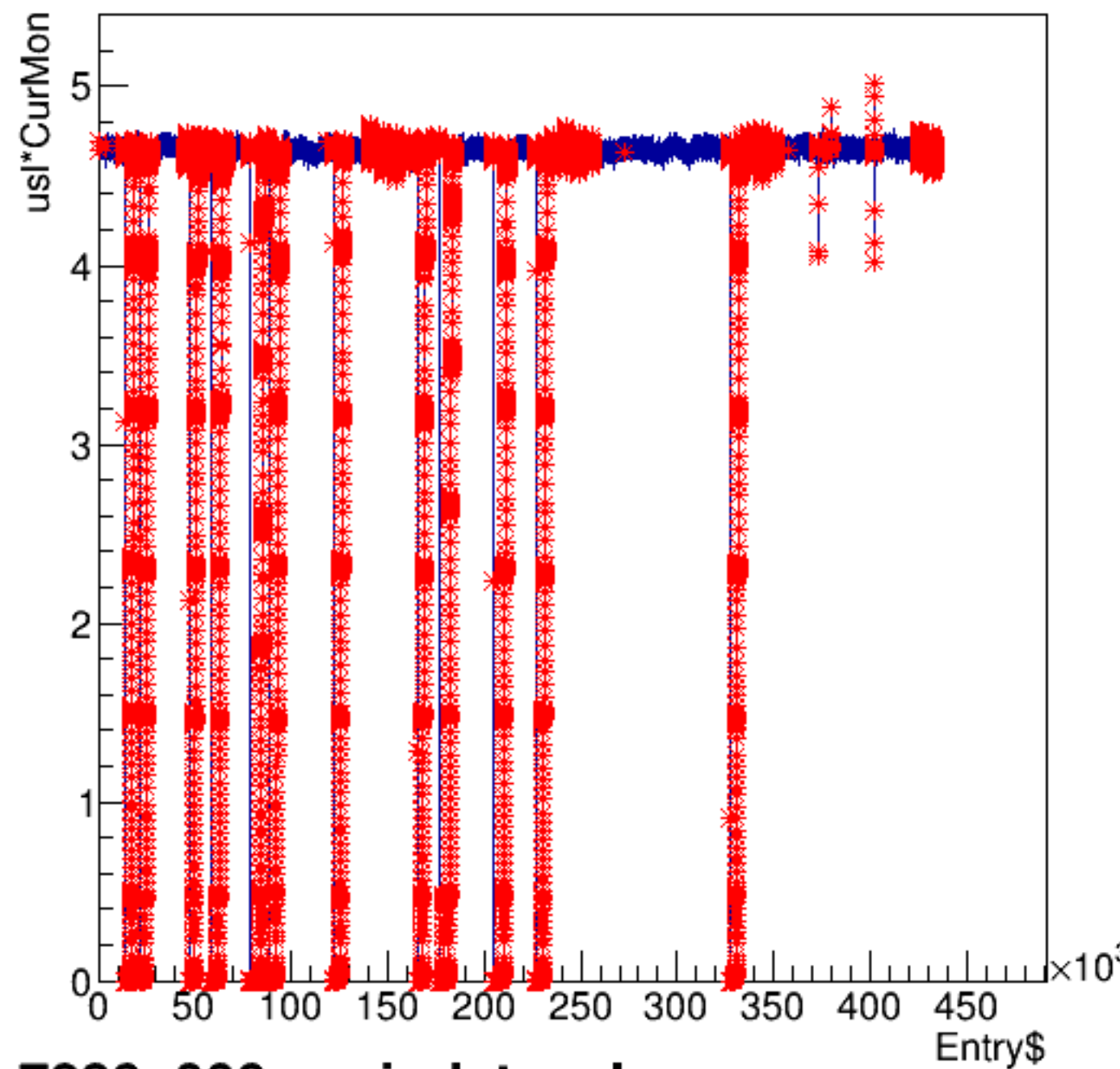
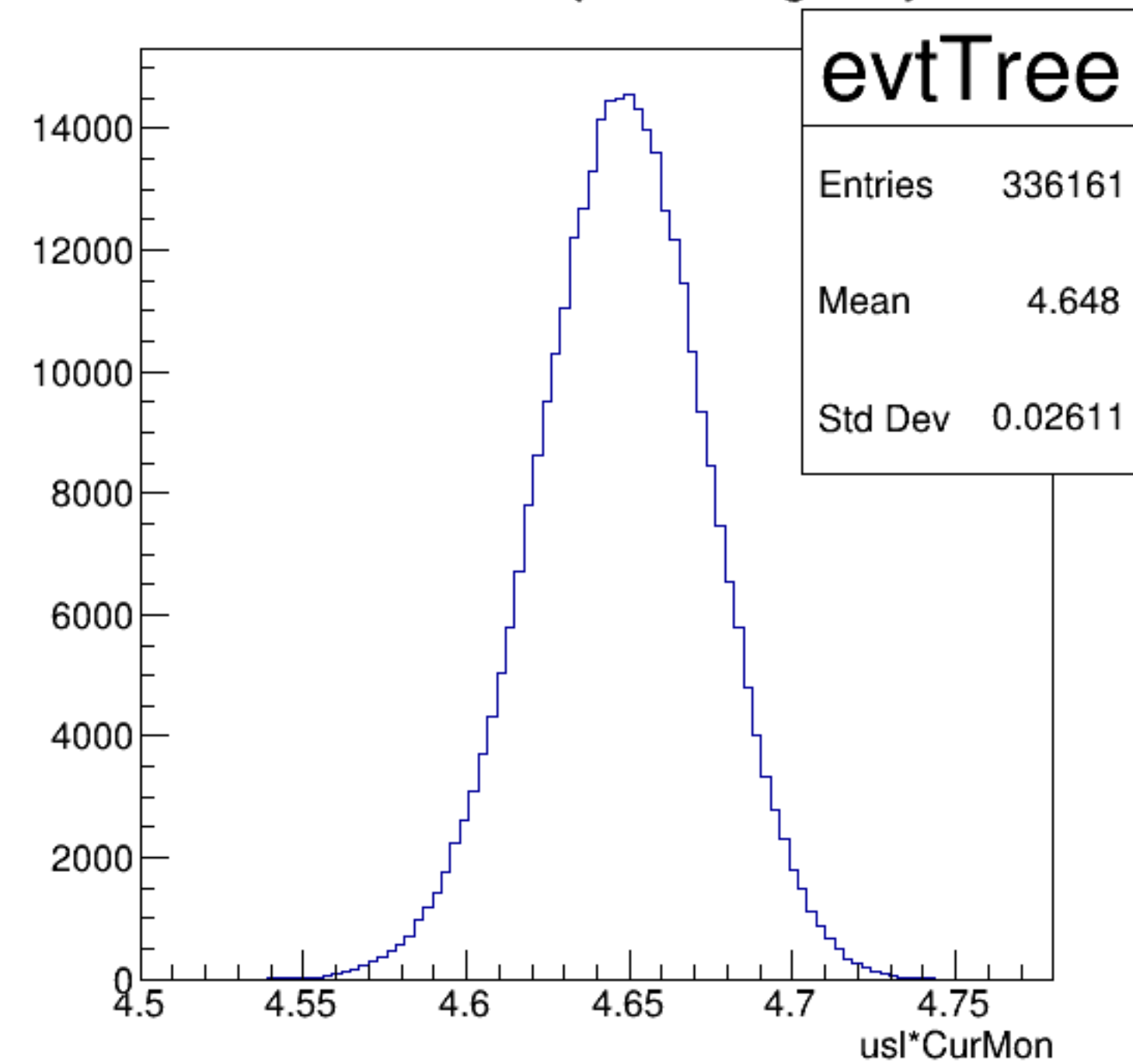


usl*CurMon:Entry\$

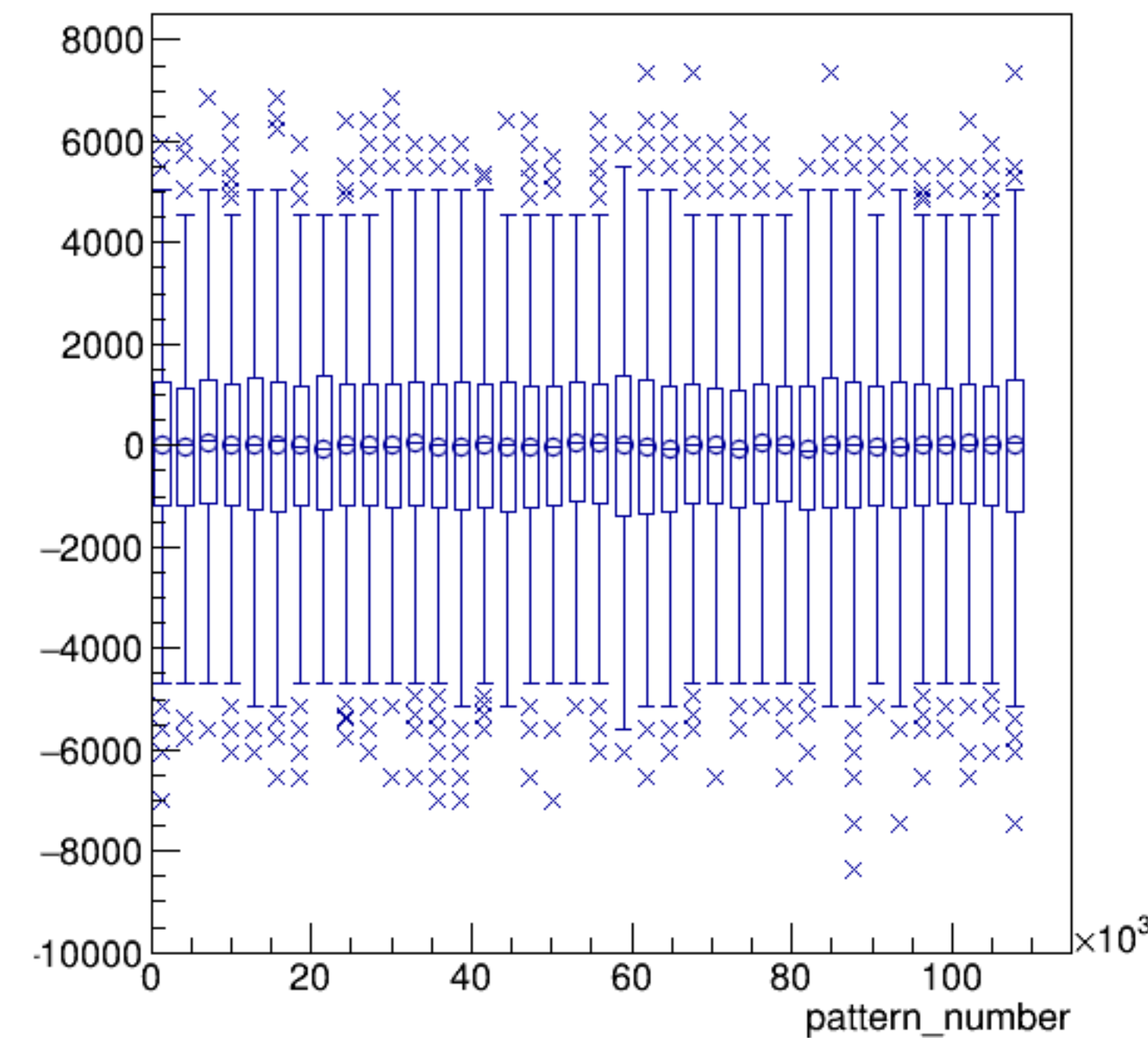


run7239_000_maindet_usl.png

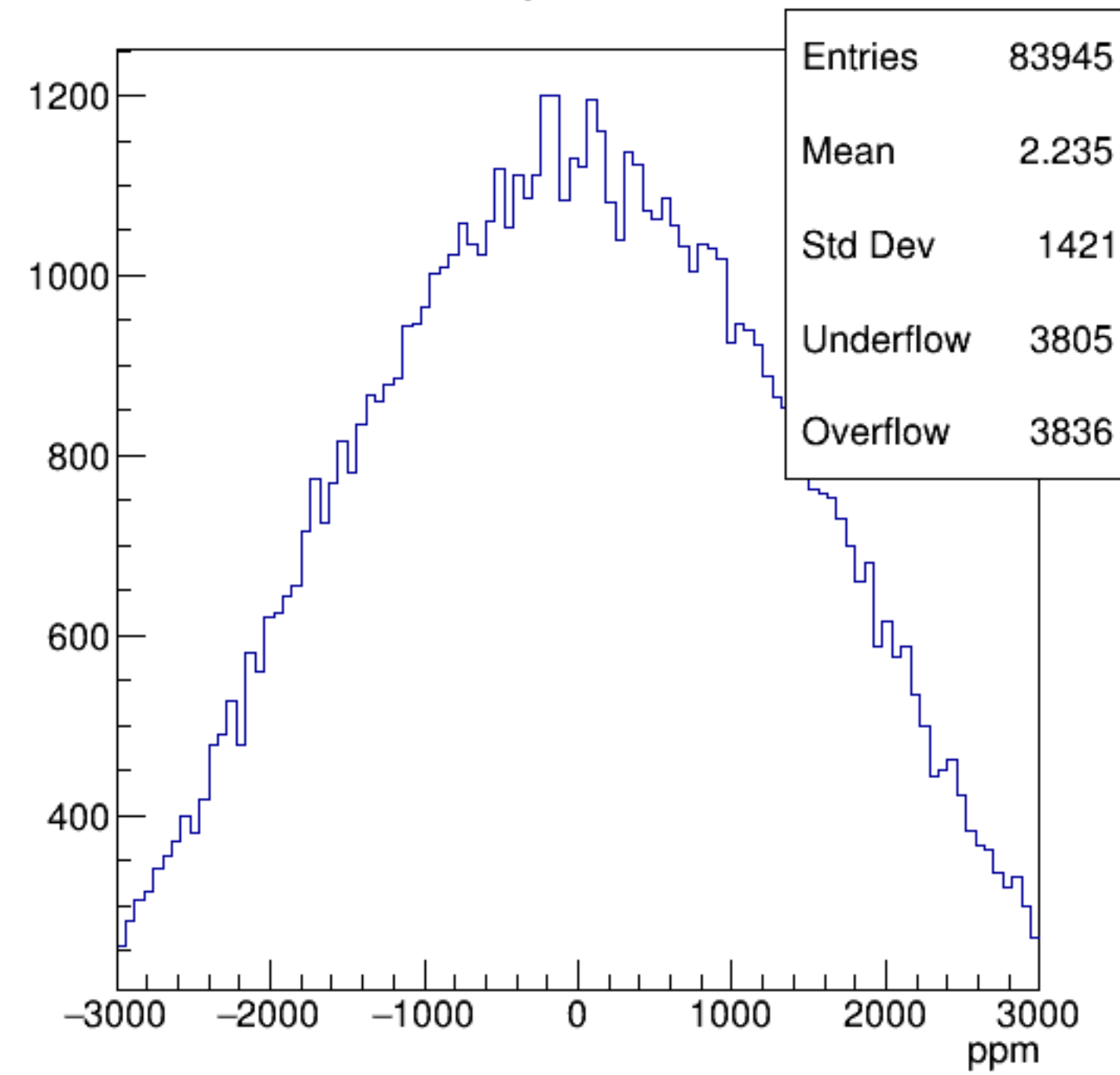
usl*CurMon {ErrorFlag==0}

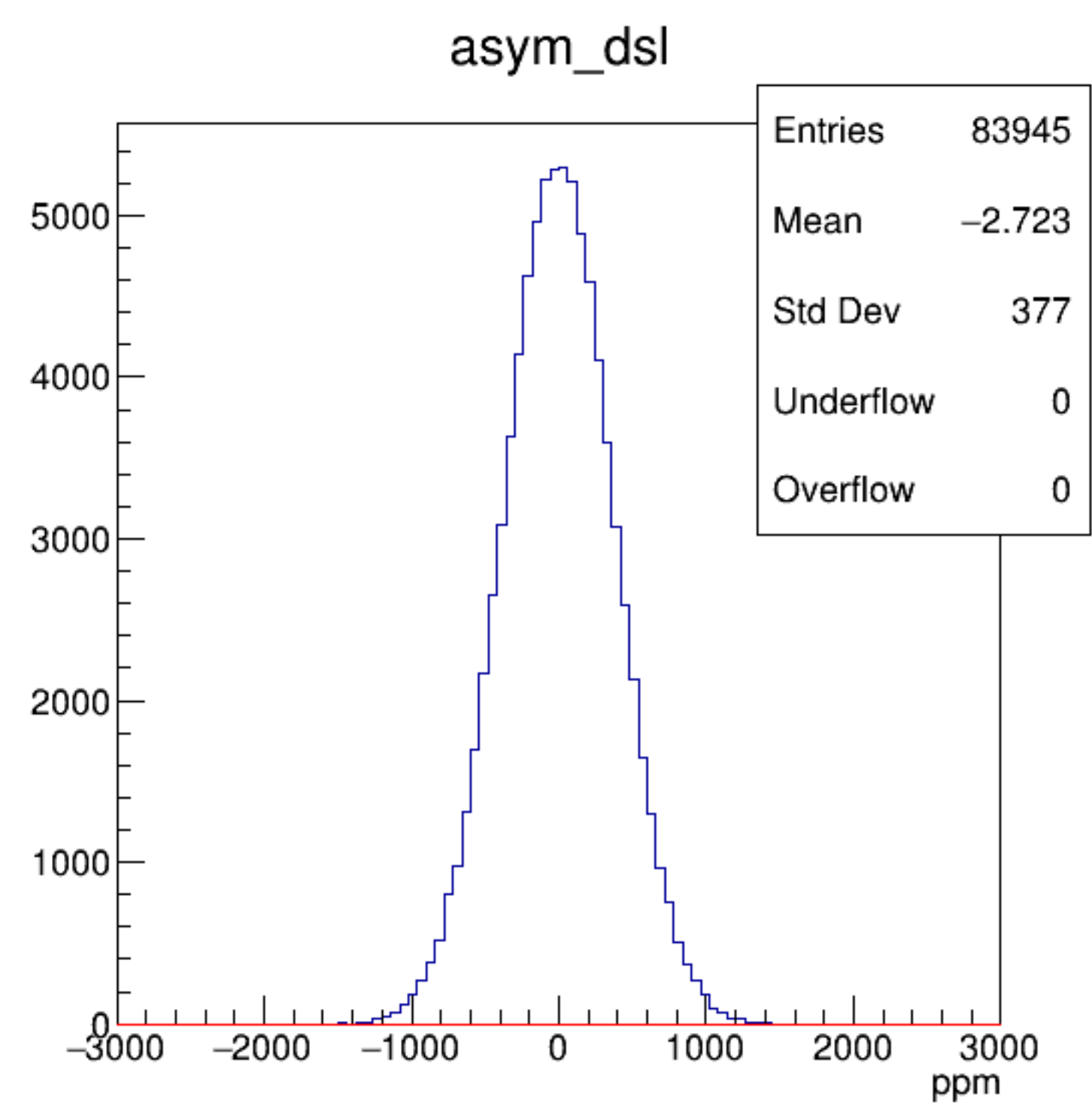
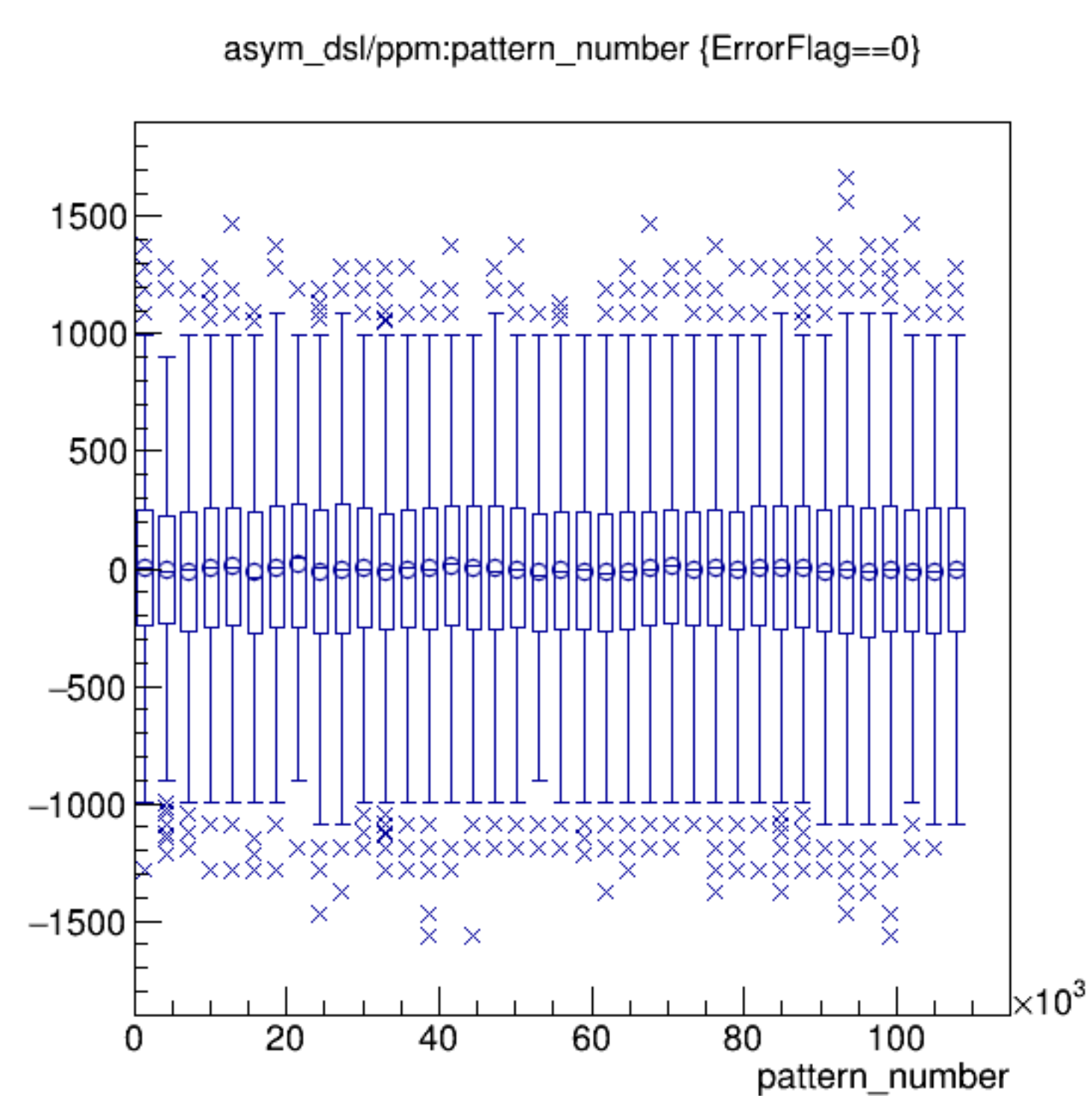
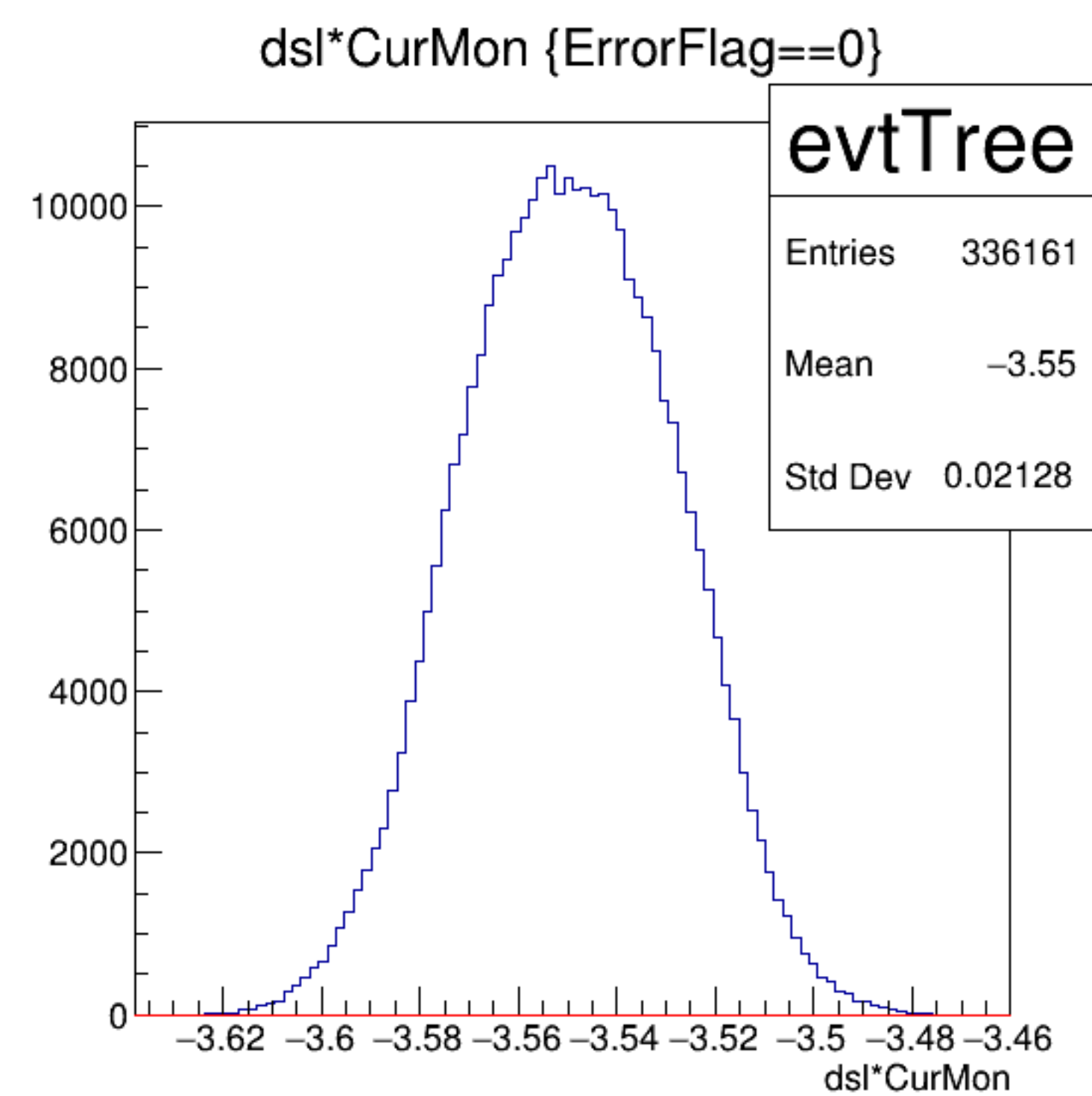
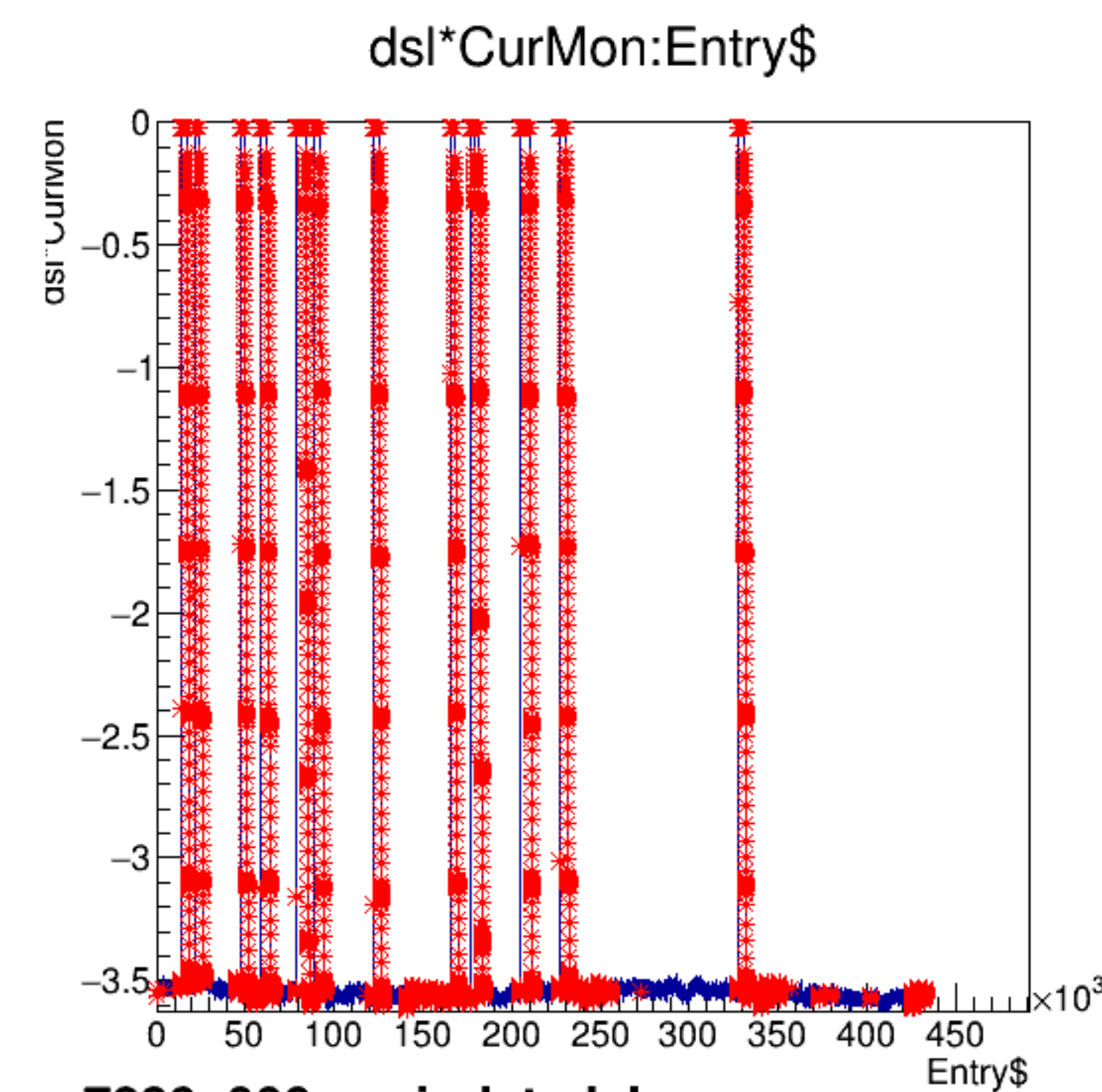


asym_usl/ppm:pattern_number {ErrorFlag==0}

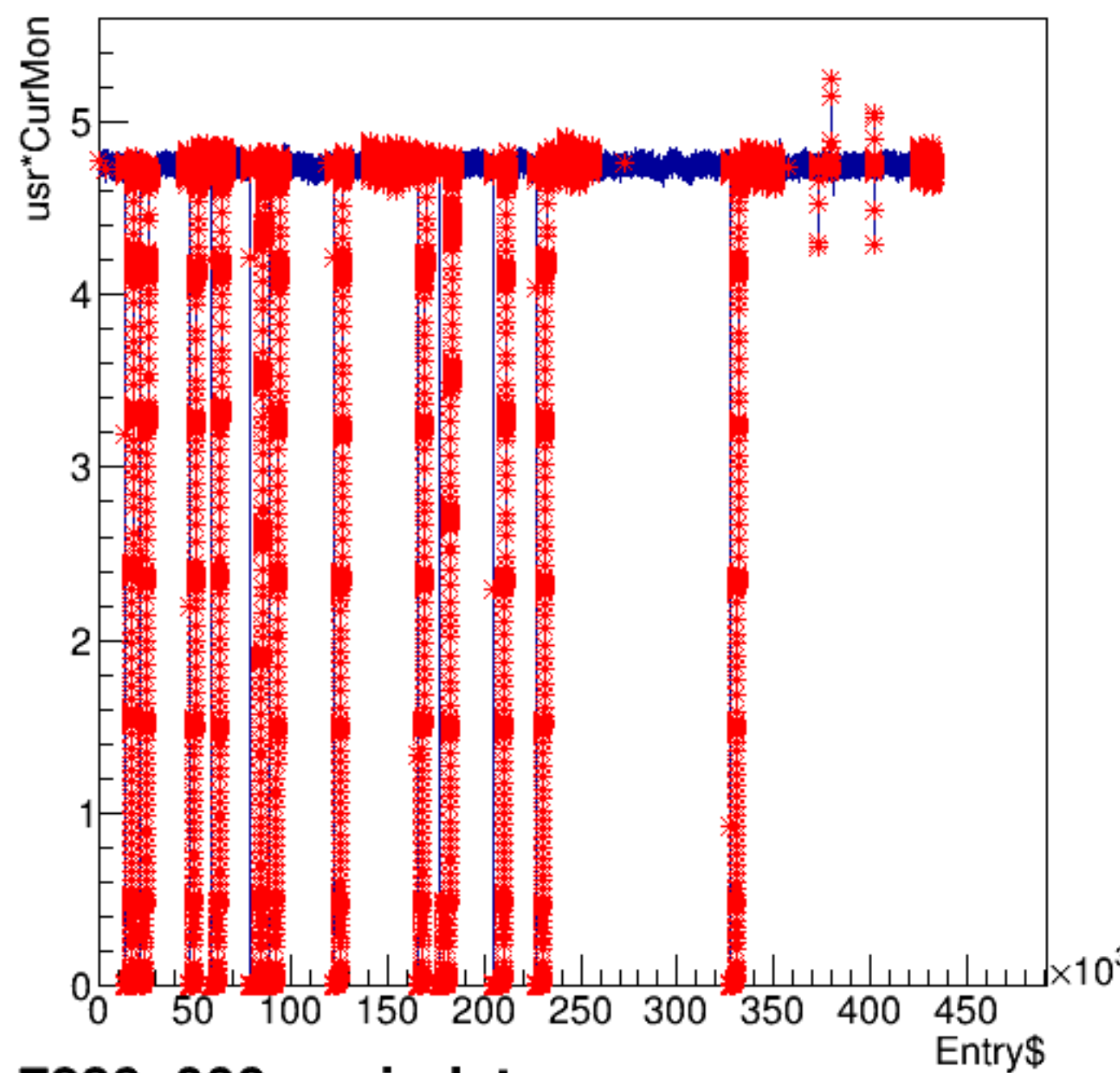


asym_usl



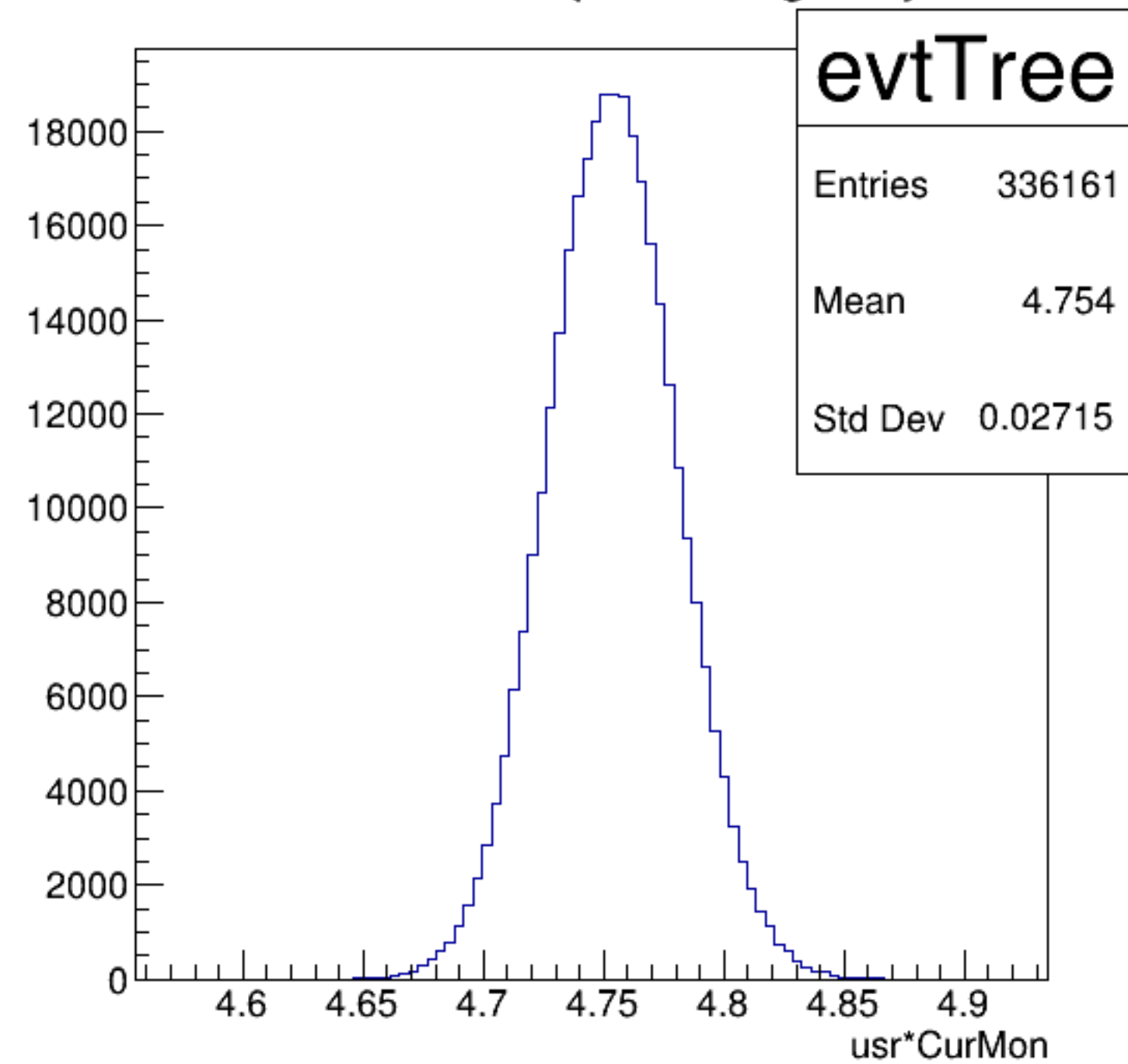


usr*CurMon:Entry\$

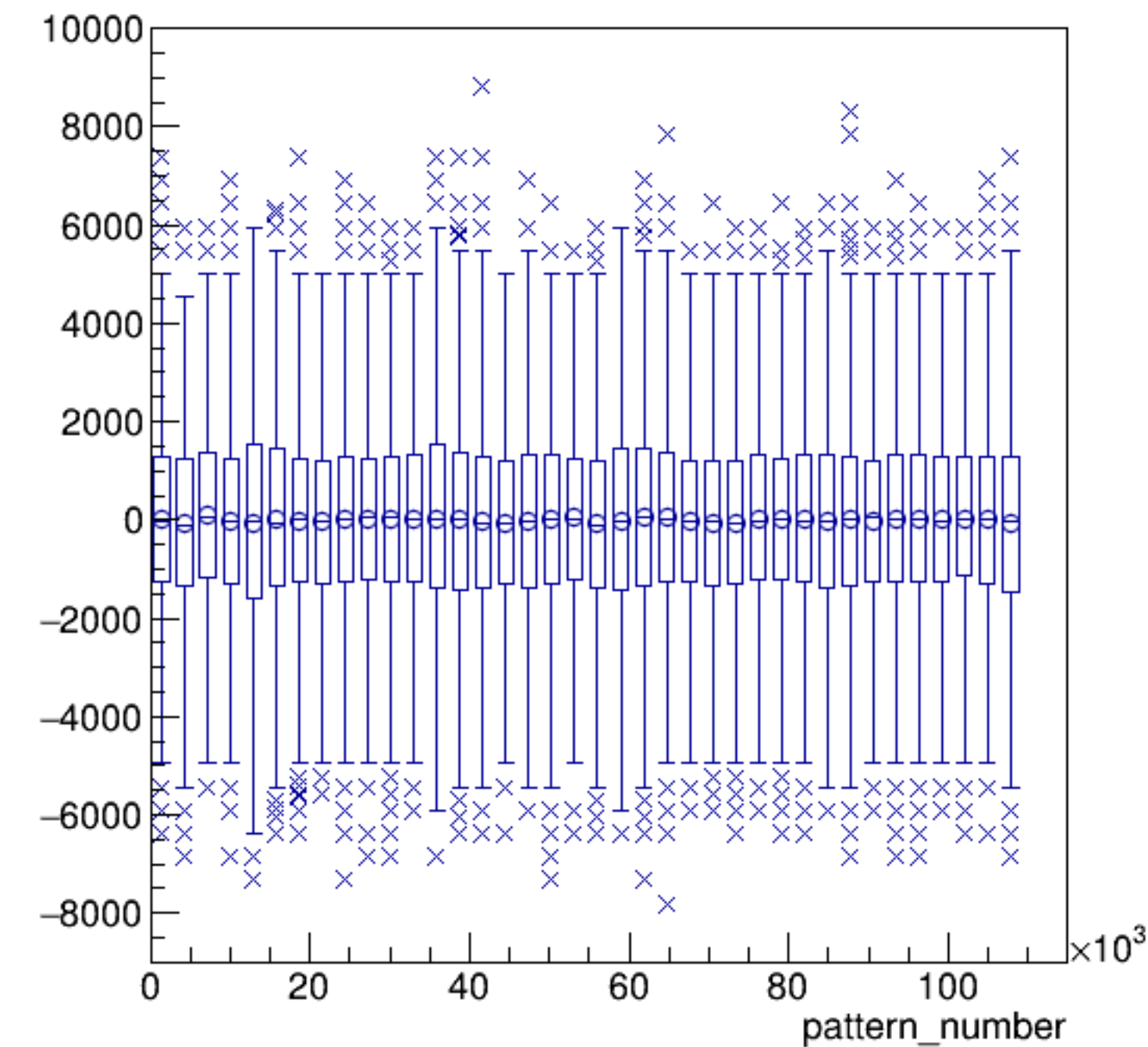


run7239_000_maintet_usr.png

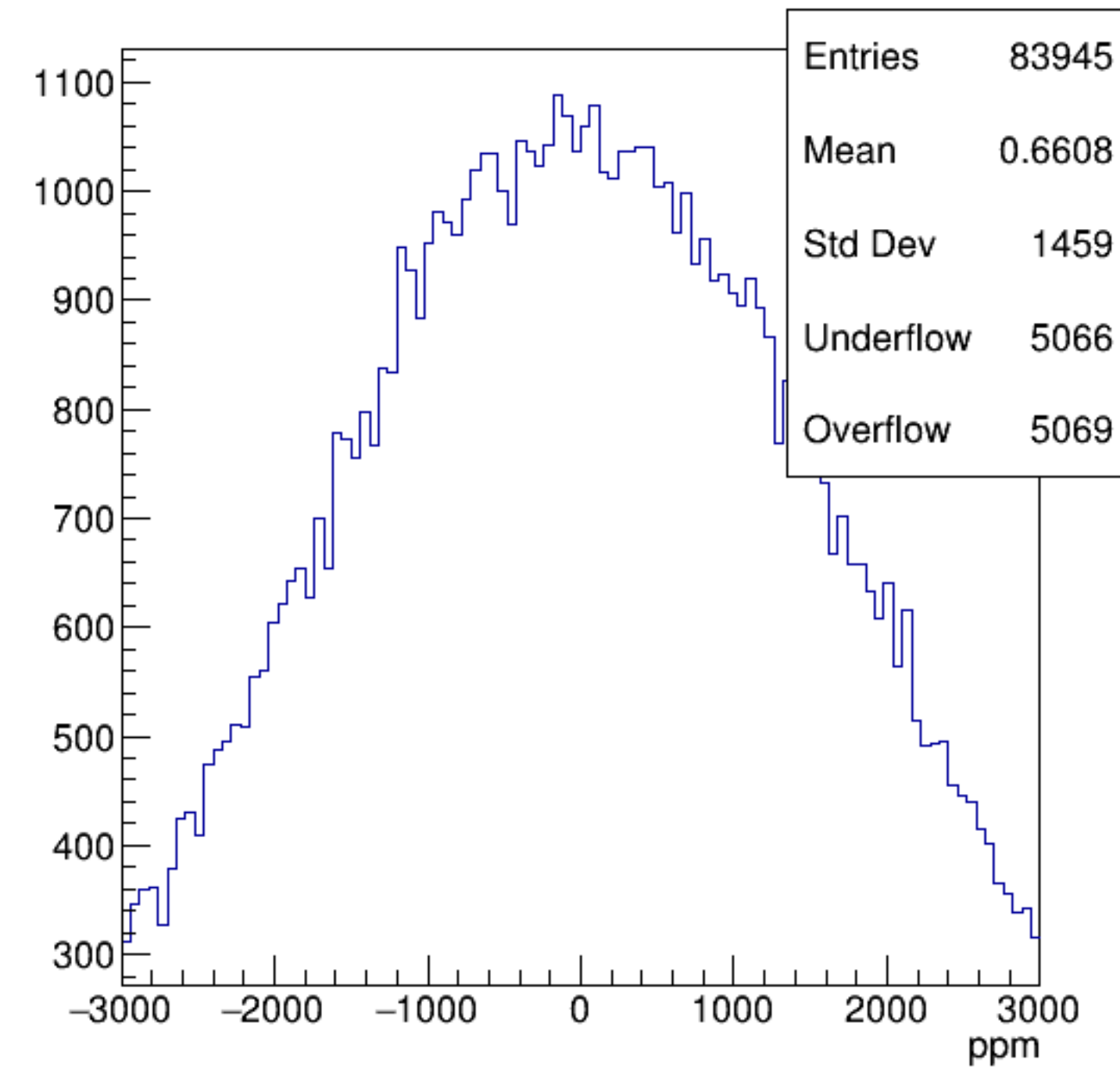
usr*CurMon {ErrorFlag==0}

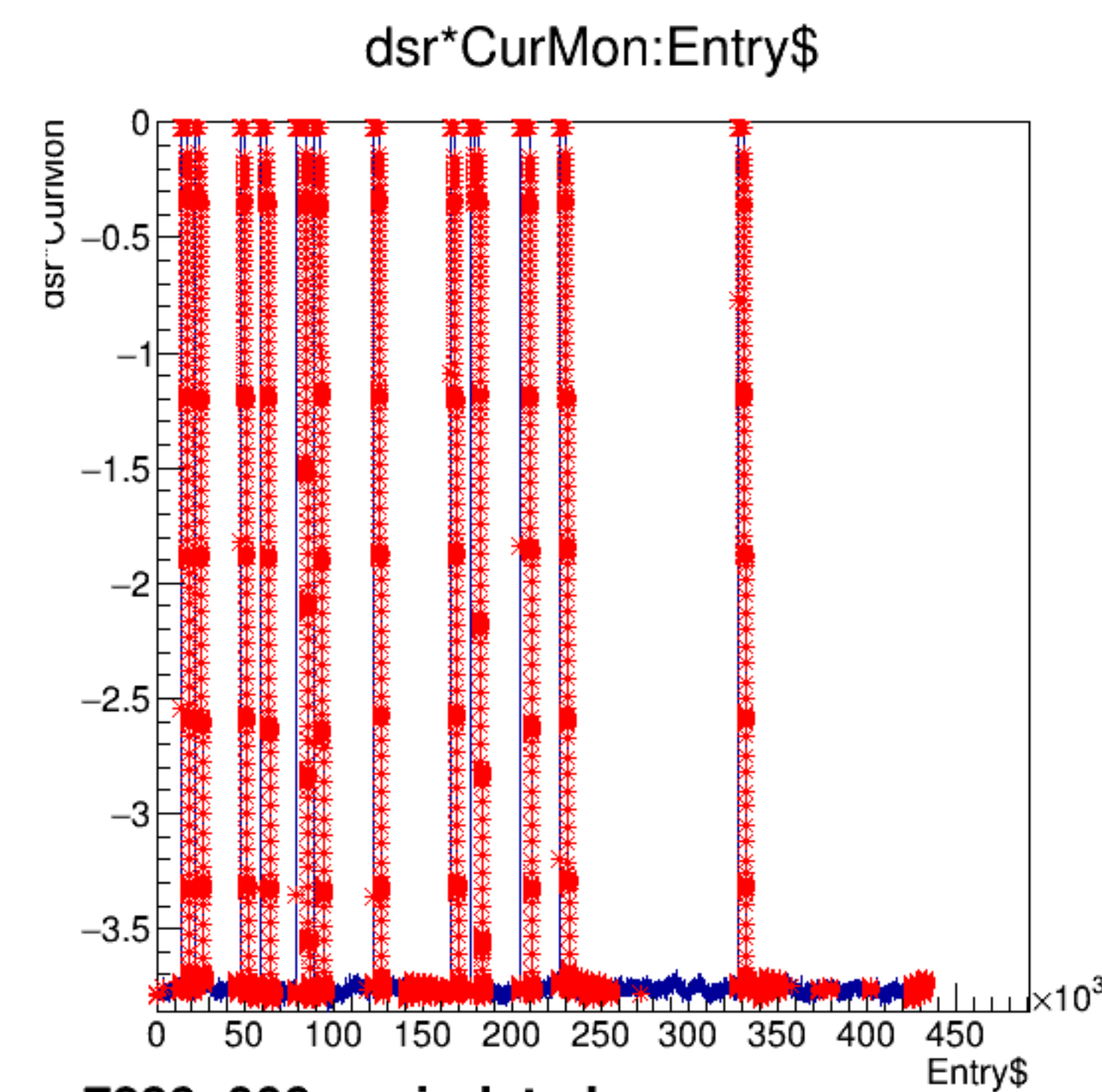


asym_usr/ppm:pattern_number {ErrorFlag==0}

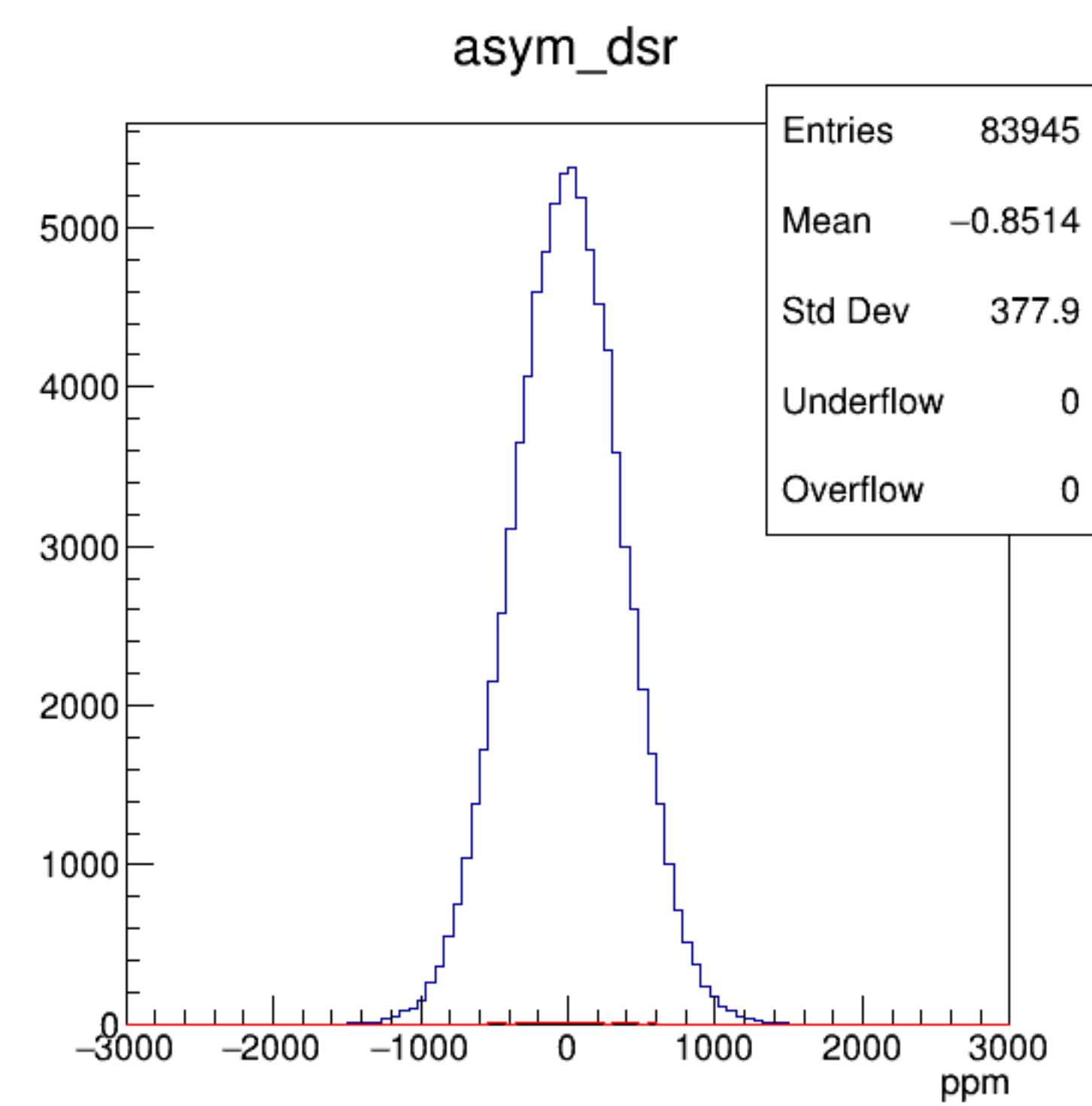
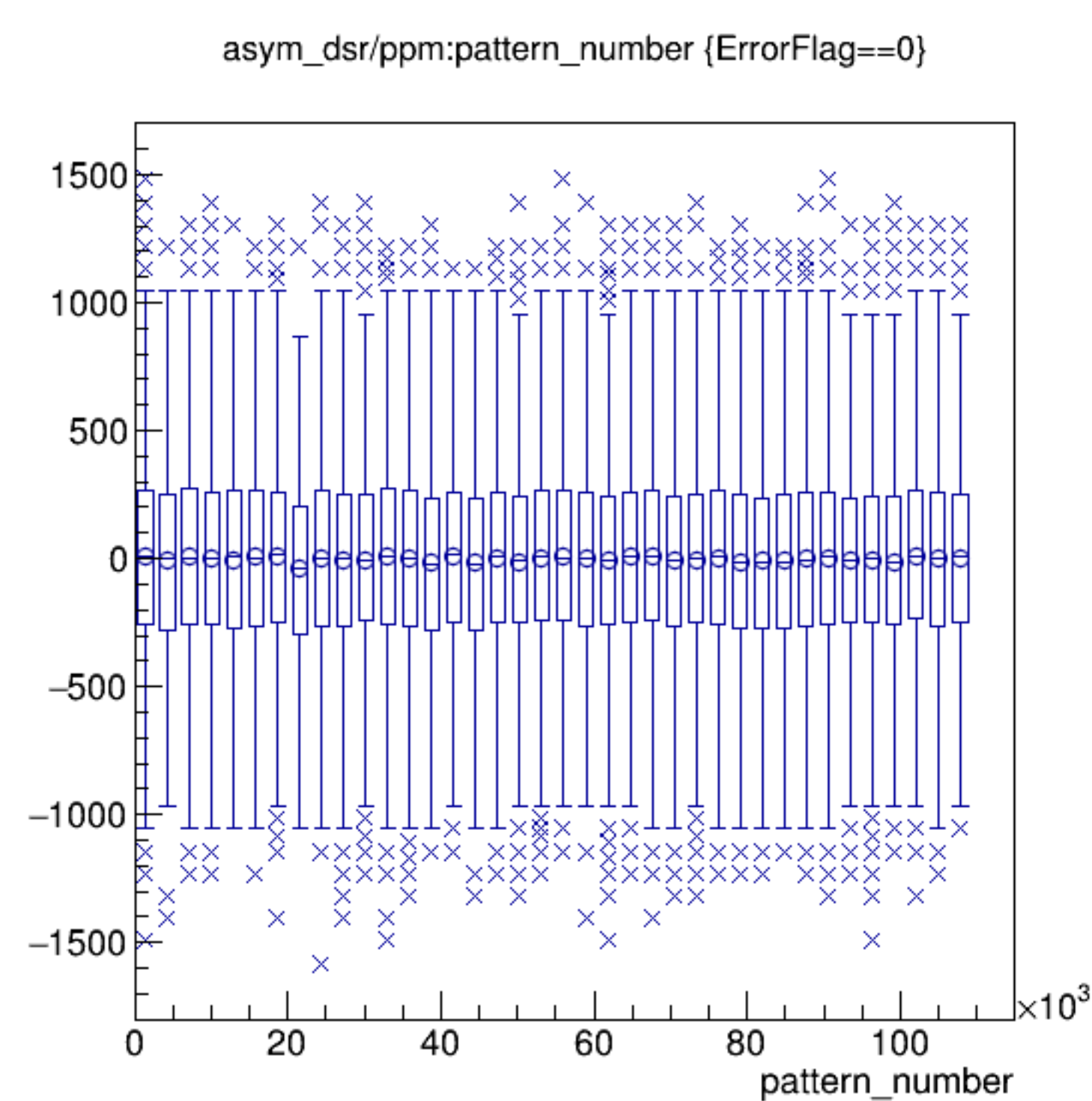
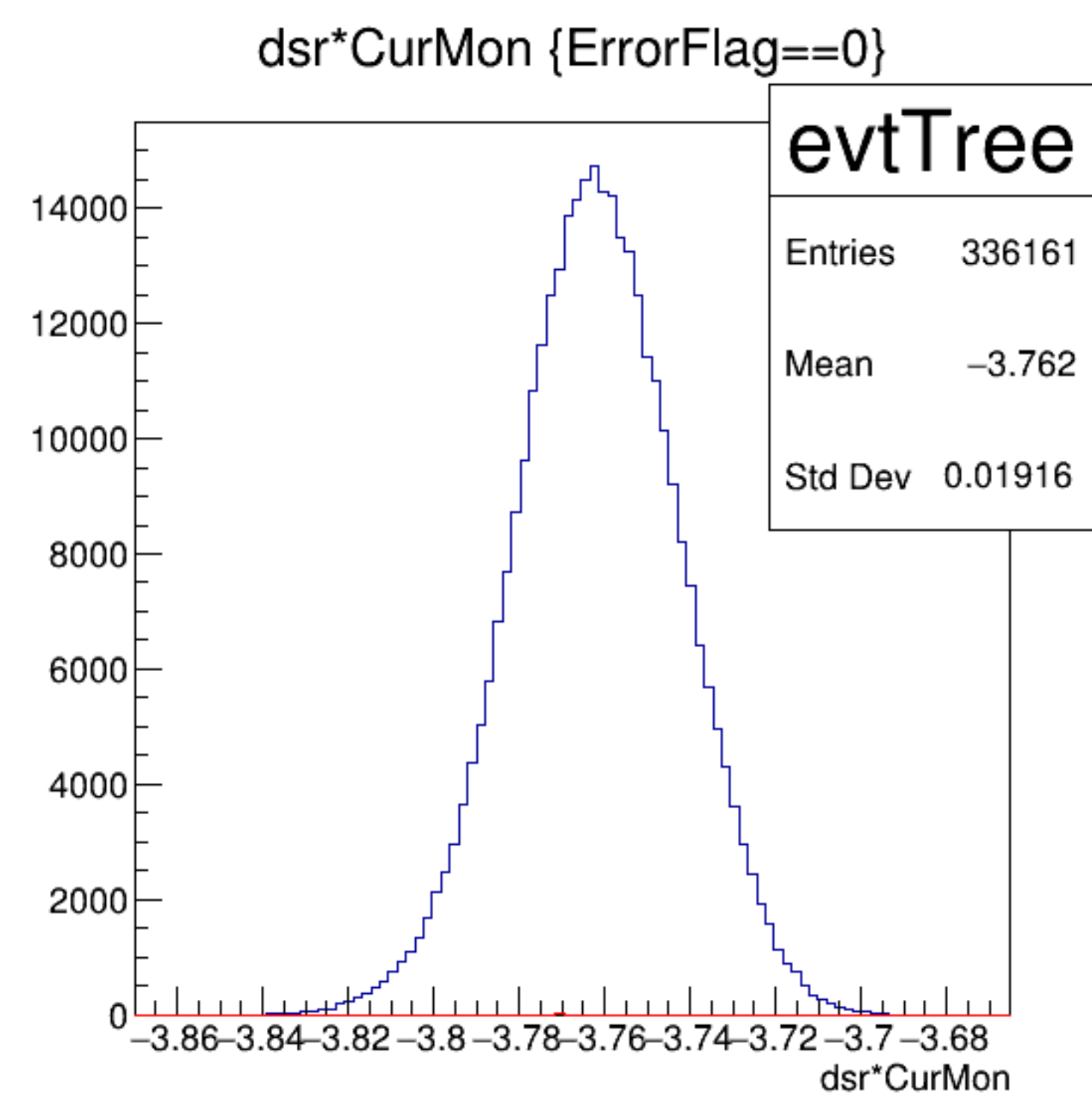


asym_usr

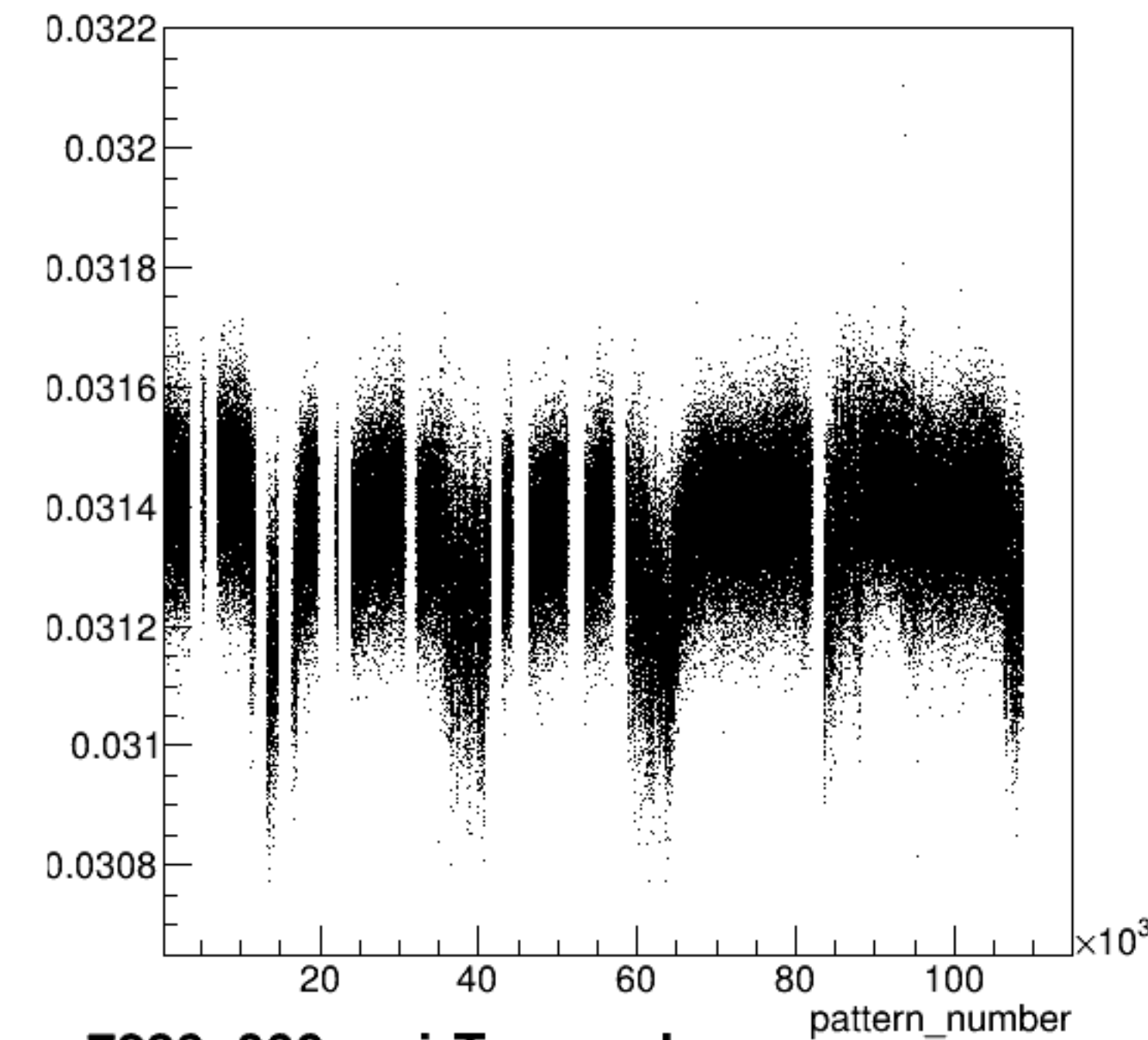




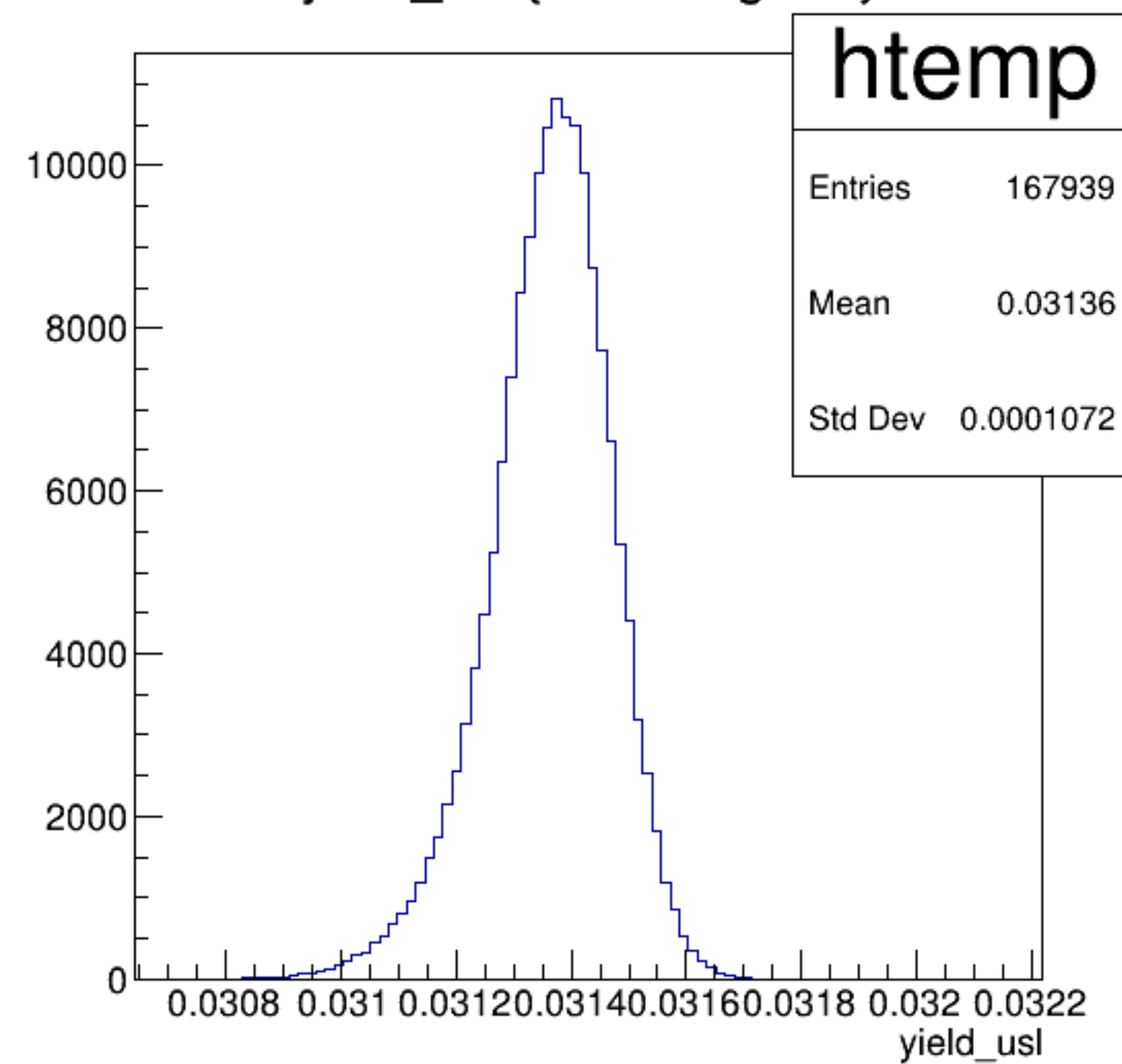
run7239_000_maindet_dsr.png



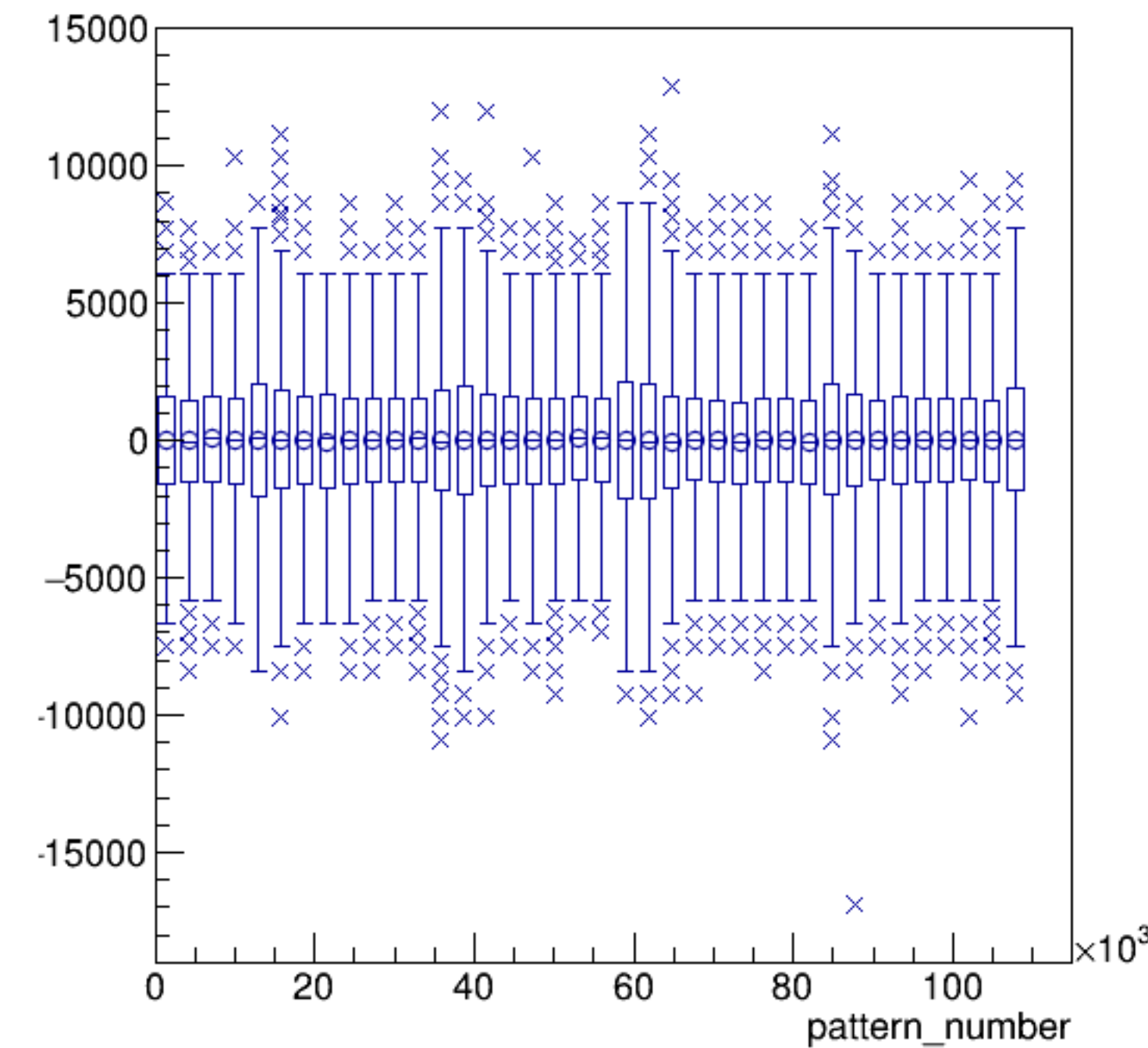
yield_usl:pattern_number {ErrorFlag==0}



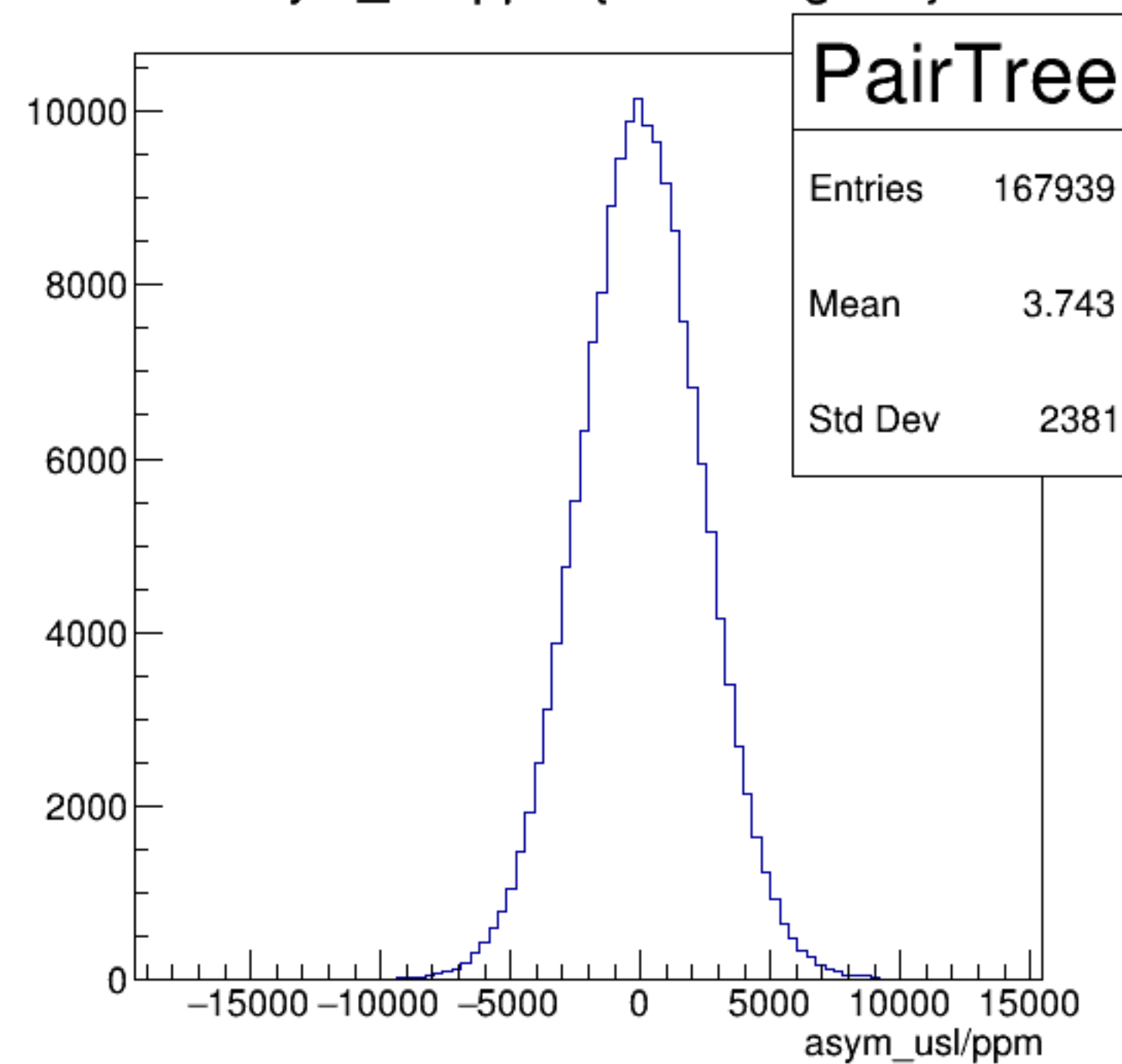
yield_usl {ErrorFlag==0}



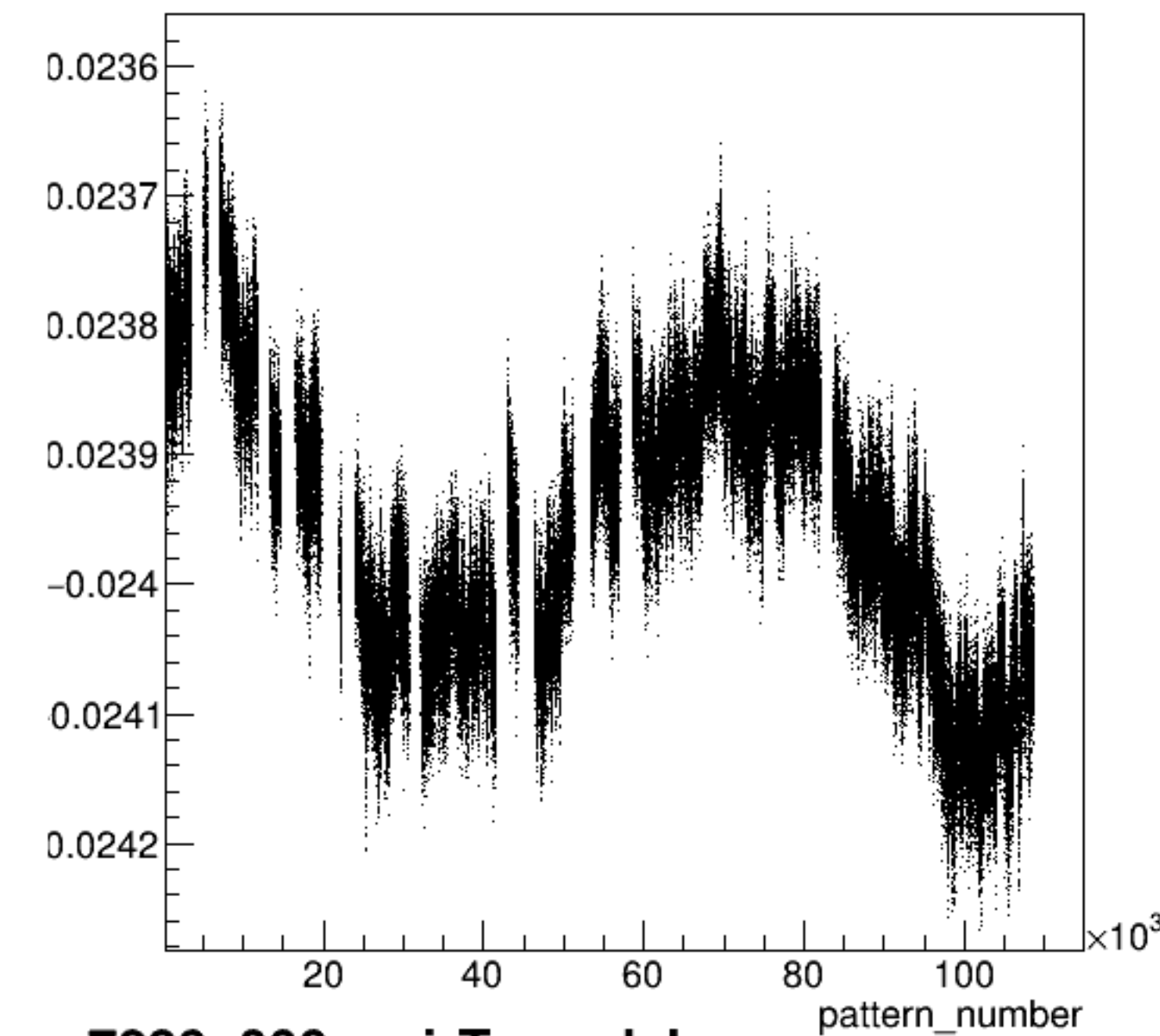
asym_usl/ppm:pattern_number {ErrorFlag==0}



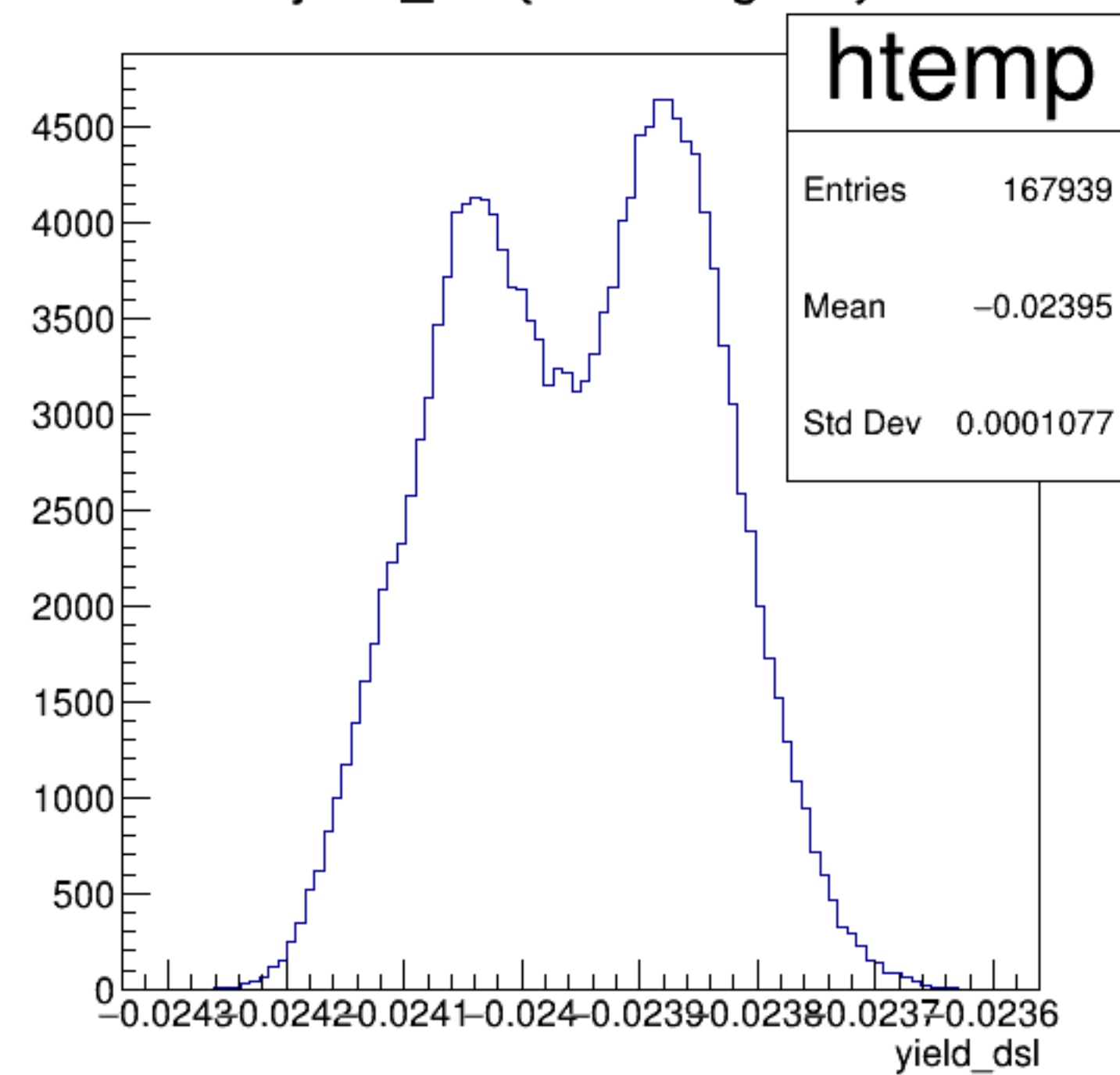
asym_usl/ppm {ErrorFlag==0}



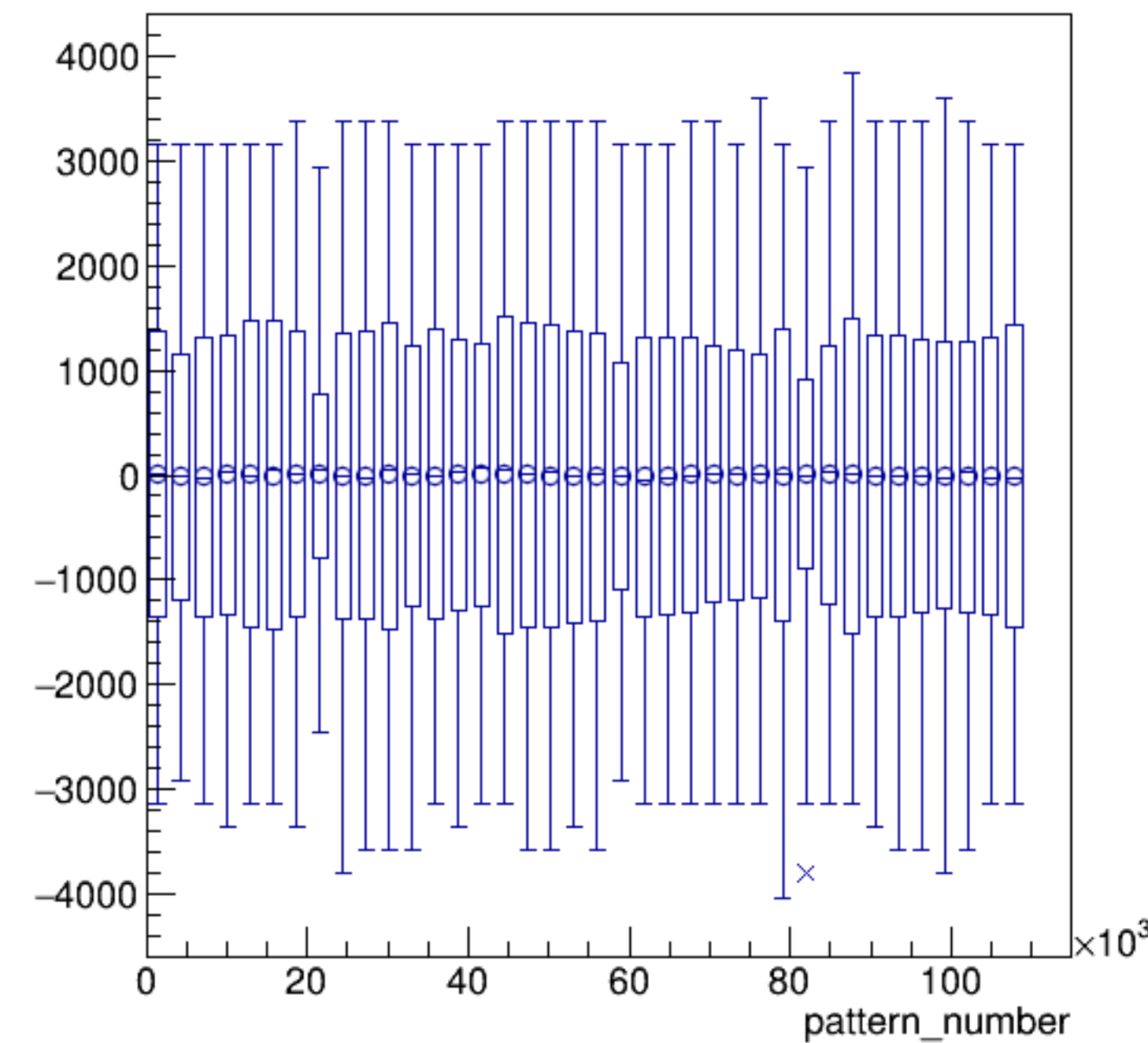
yield_dsl:pattern_number {ErrorFlag==0}



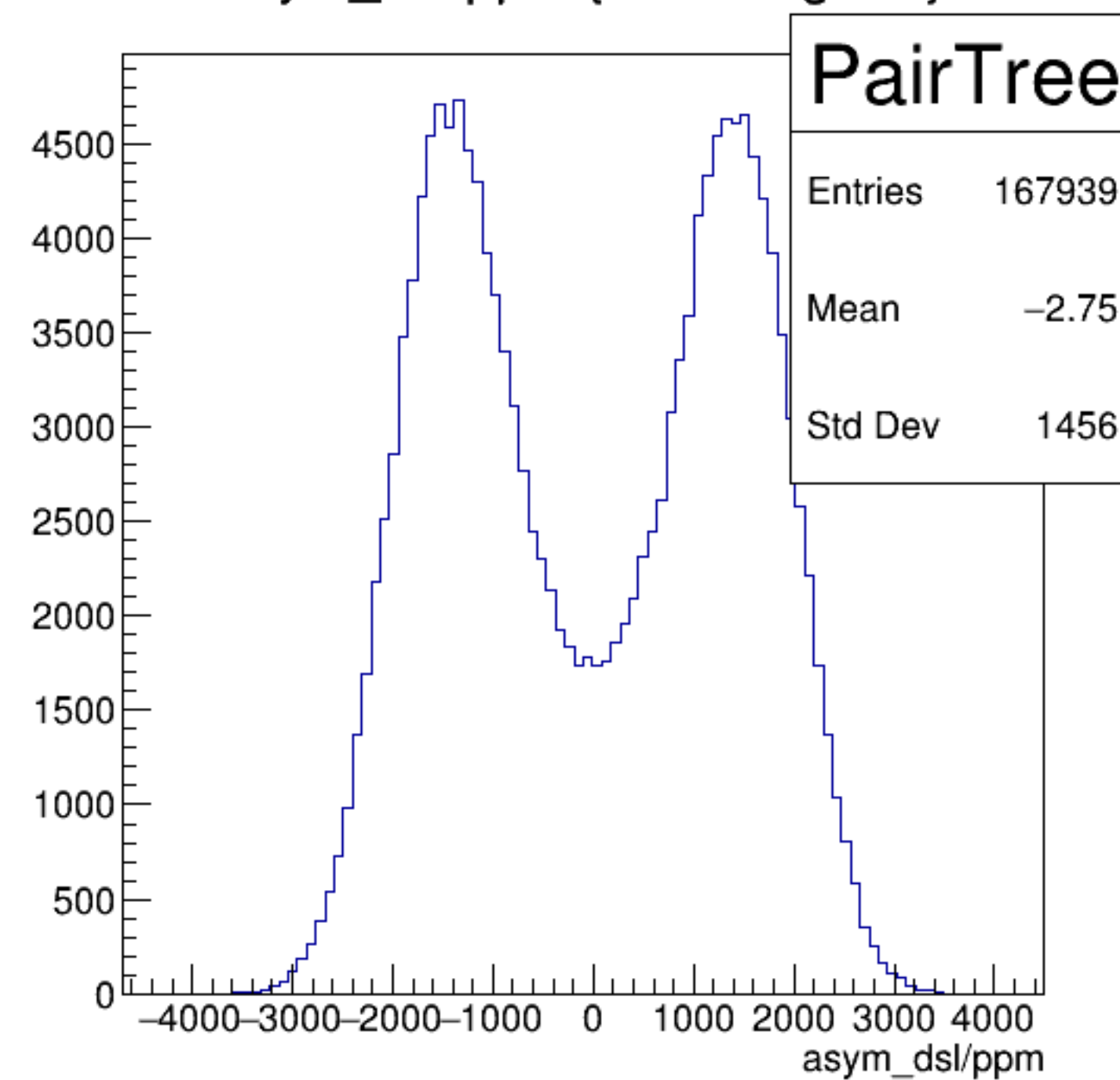
yield_dsl {ErrorFlag==0}

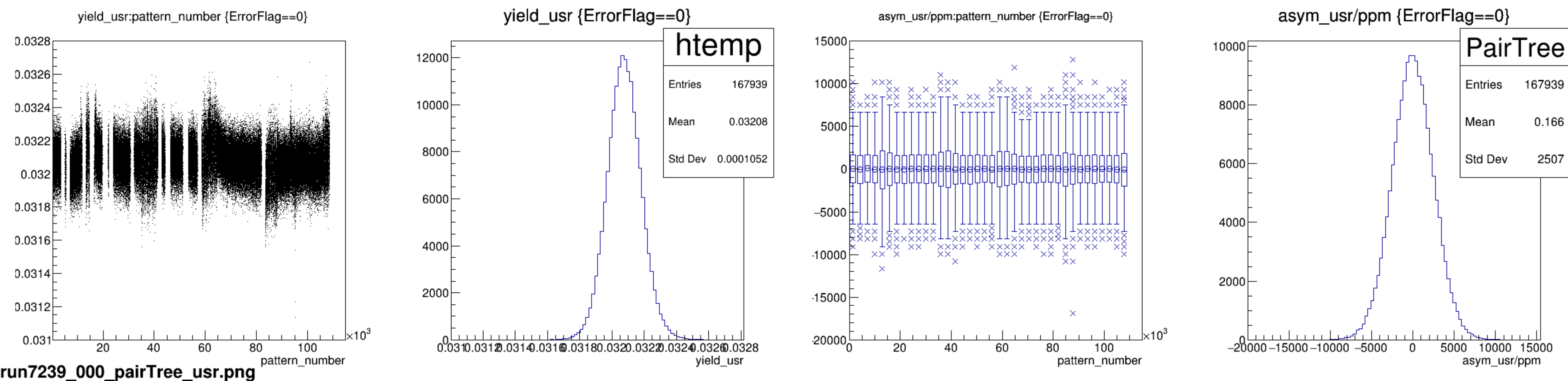


asym_dsl/ppm:pattern_number {ErrorFlag==0}

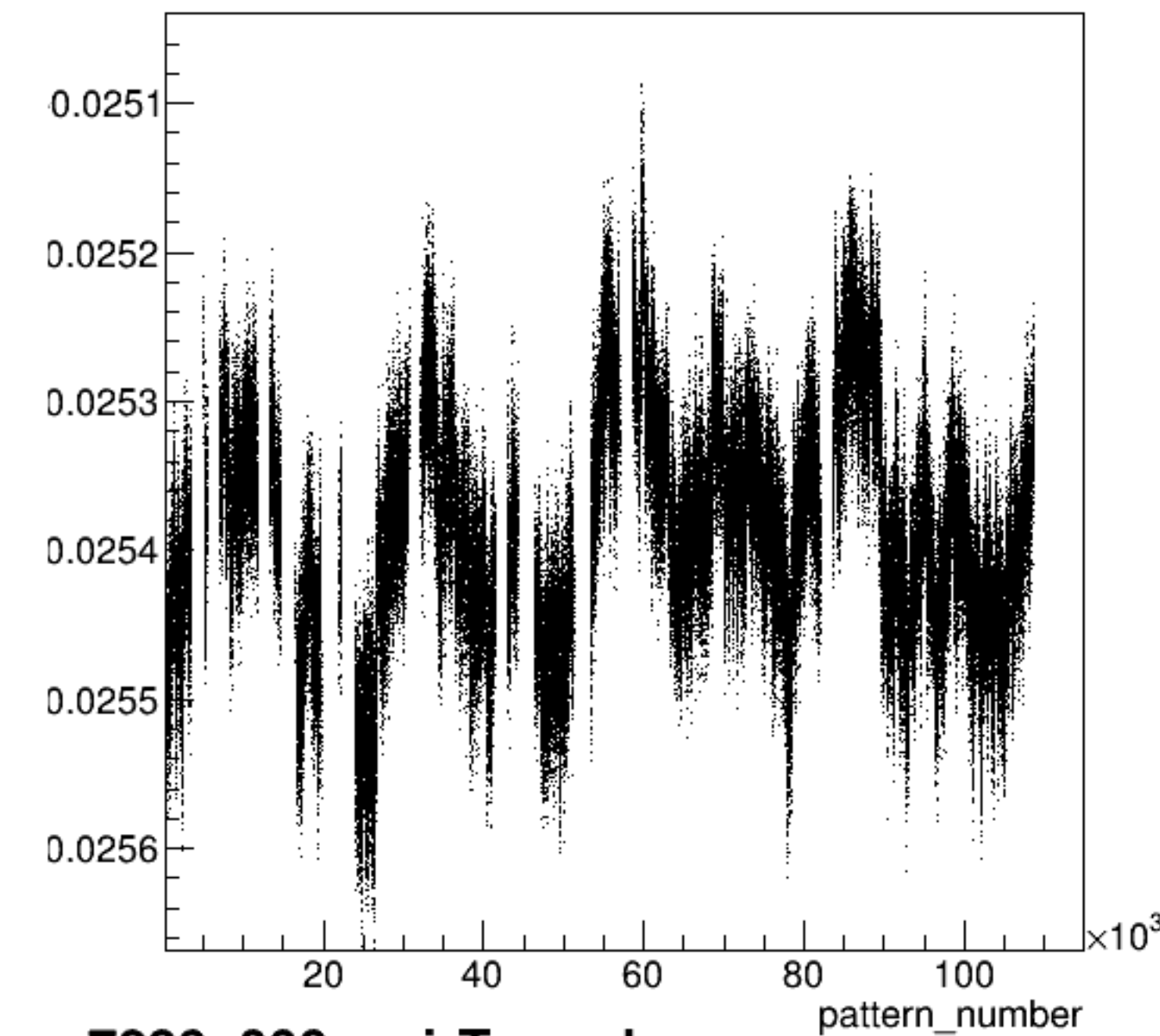


asym_dsl/ppm {ErrorFlag==0}



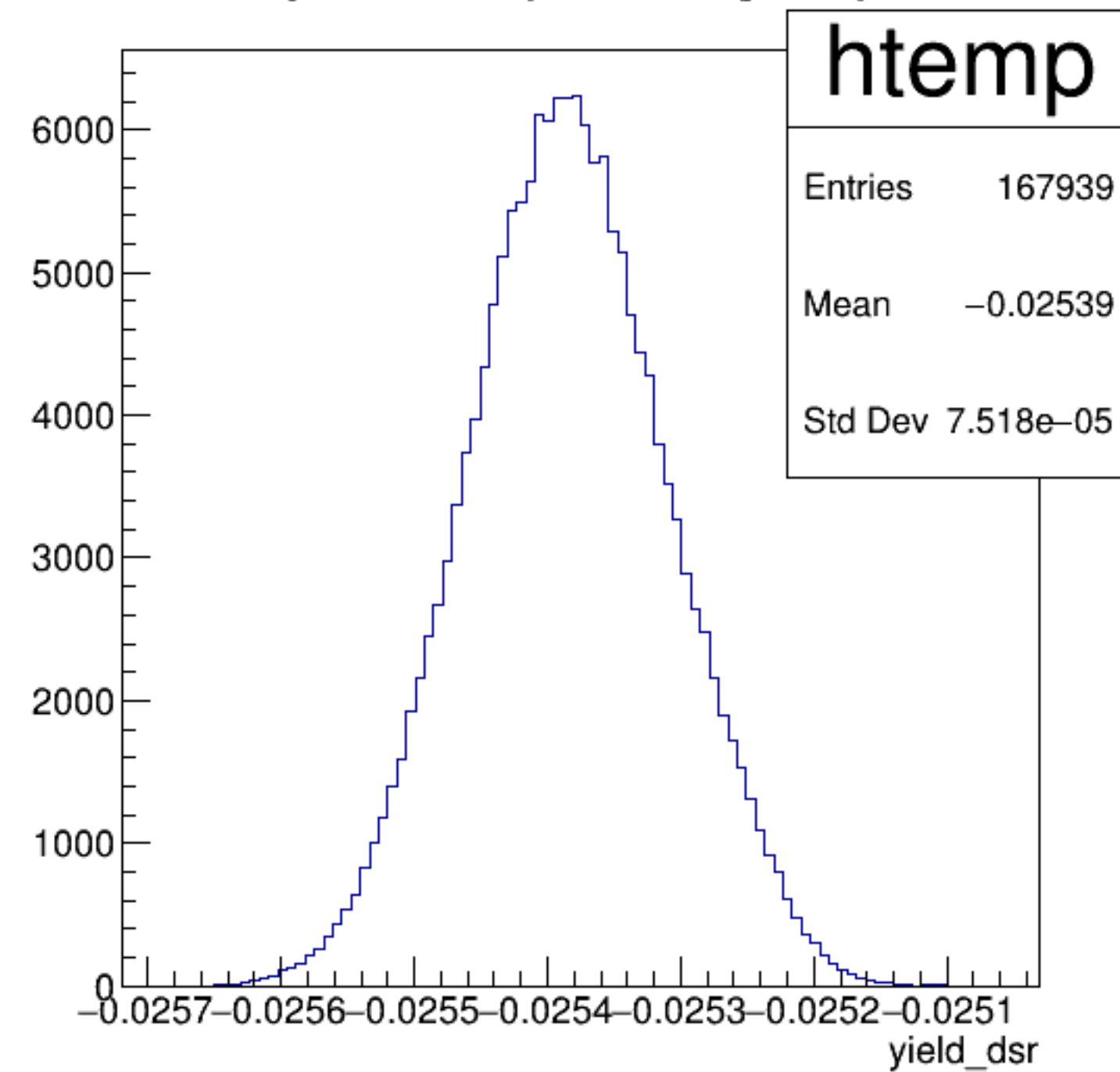


yield_dsr:pattern_number {ErrorFlag==0}

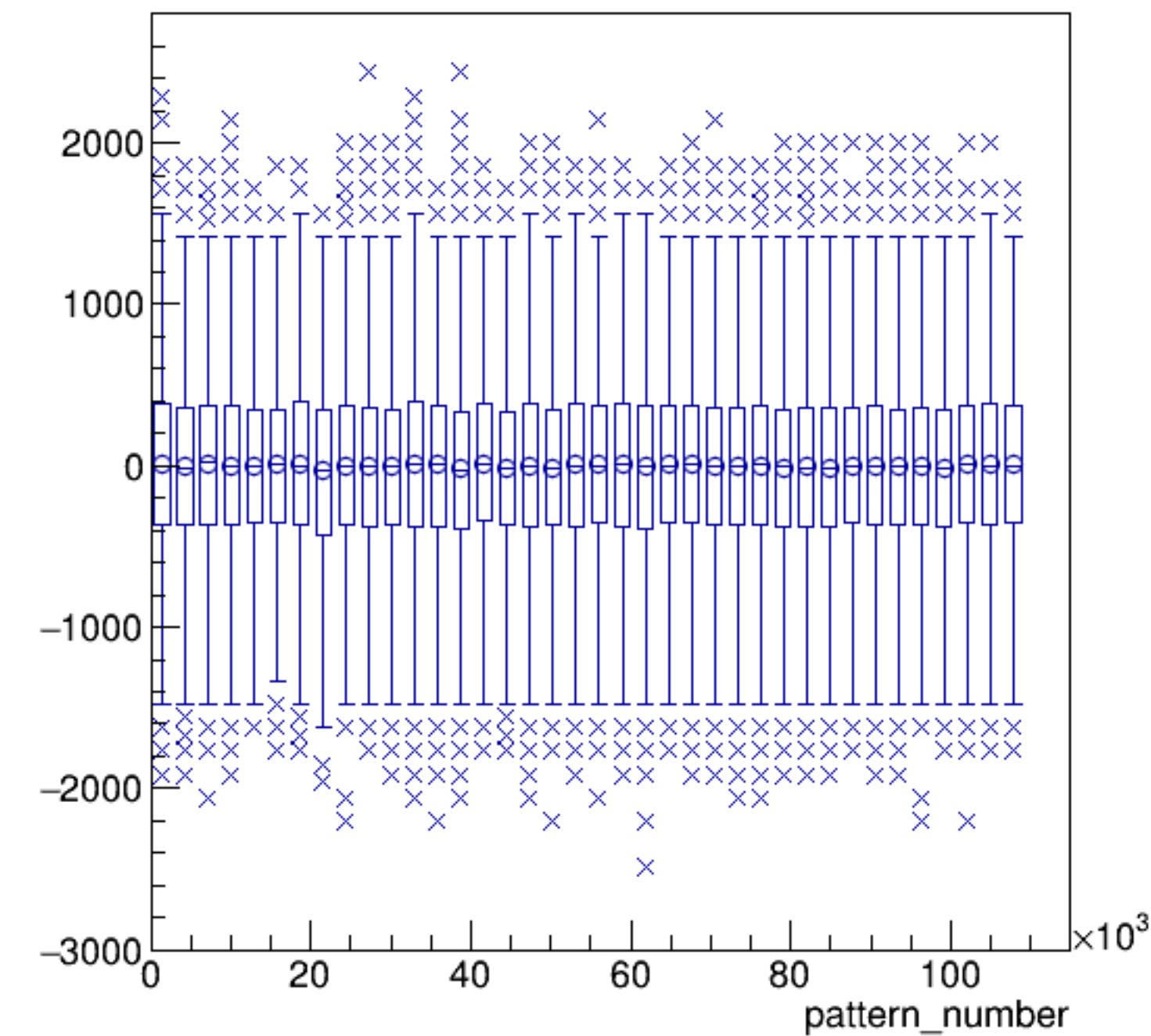


run7239_000_pairTree_dsr.png

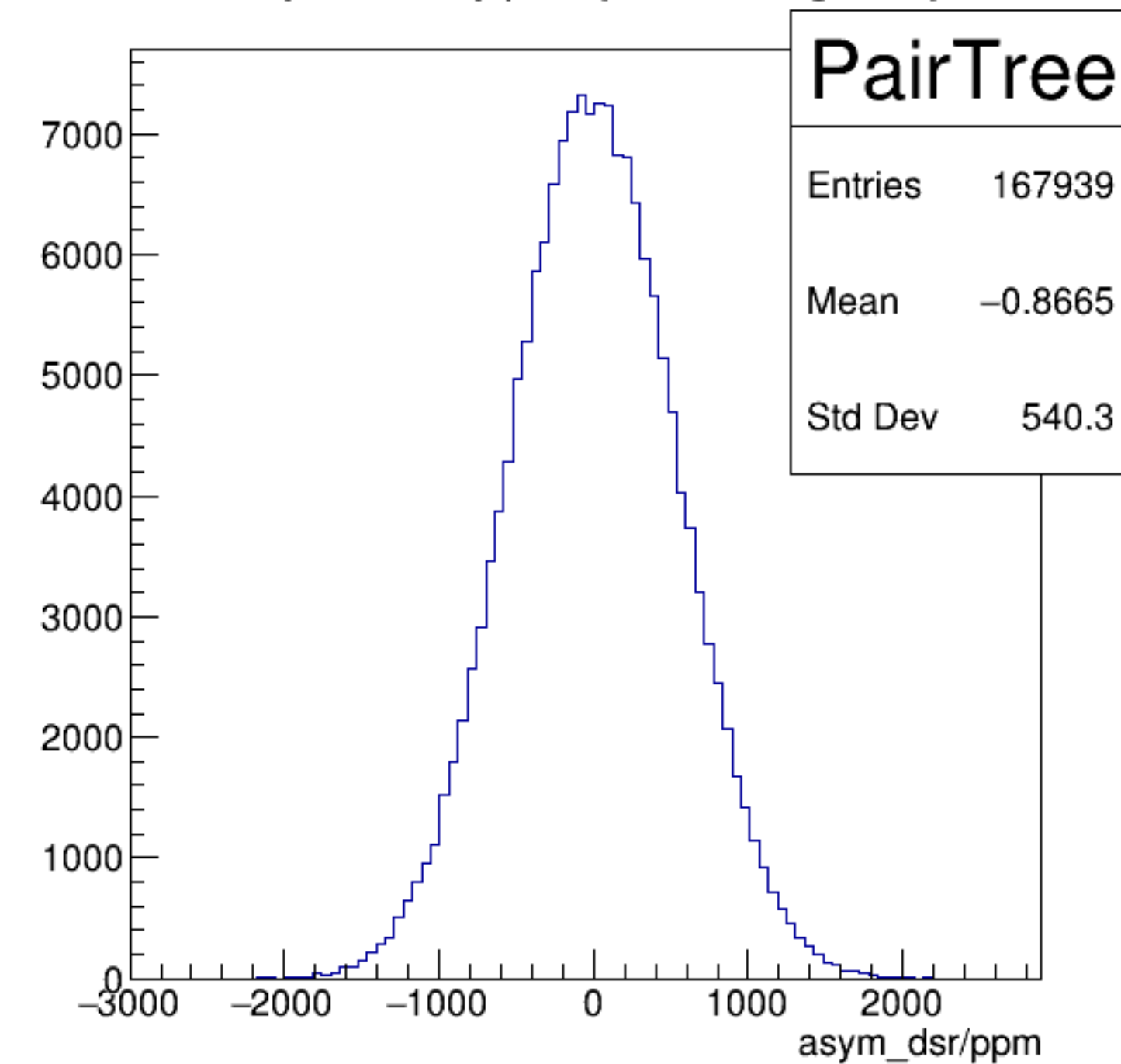
yield_dsr {ErrorFlag==0}



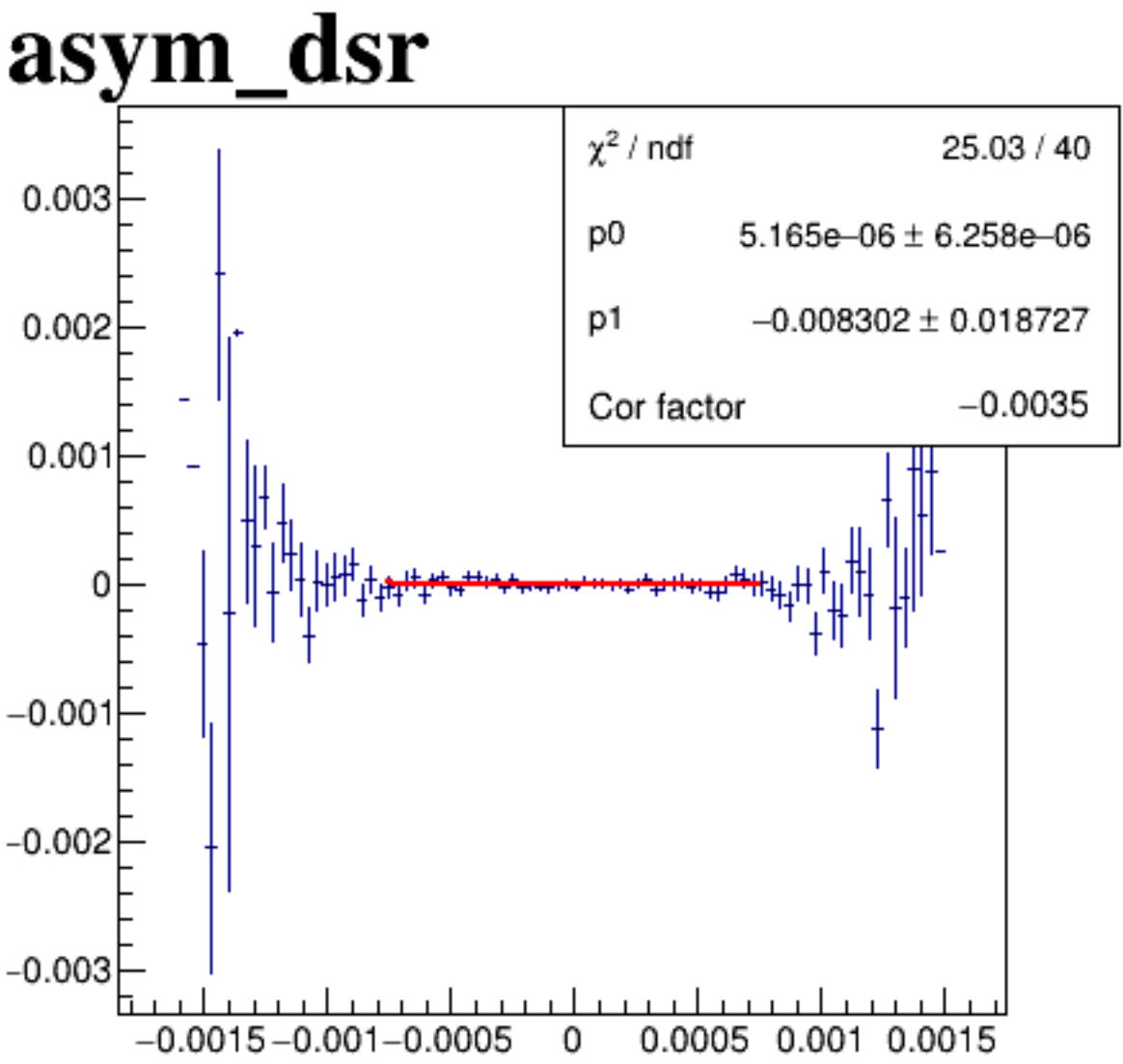
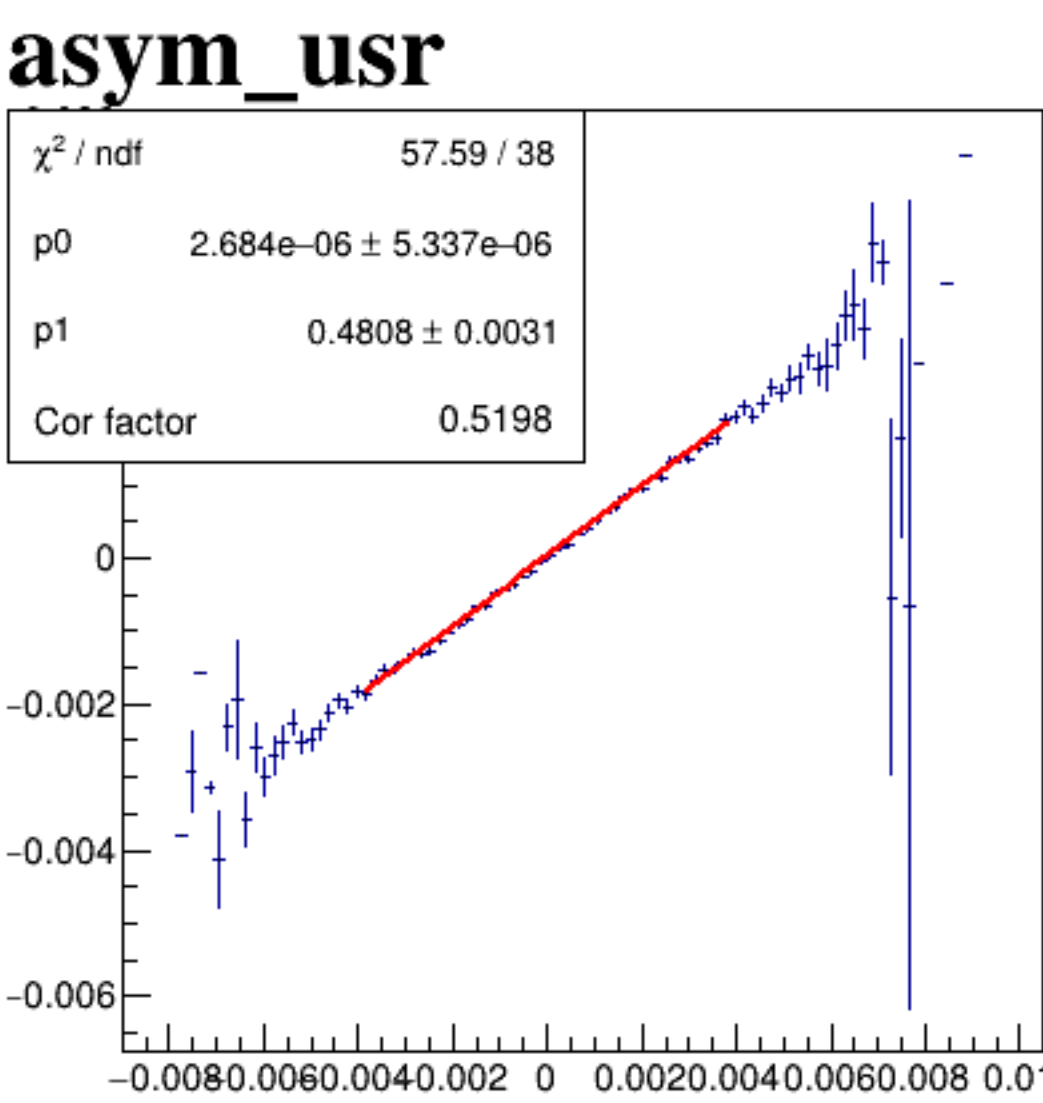
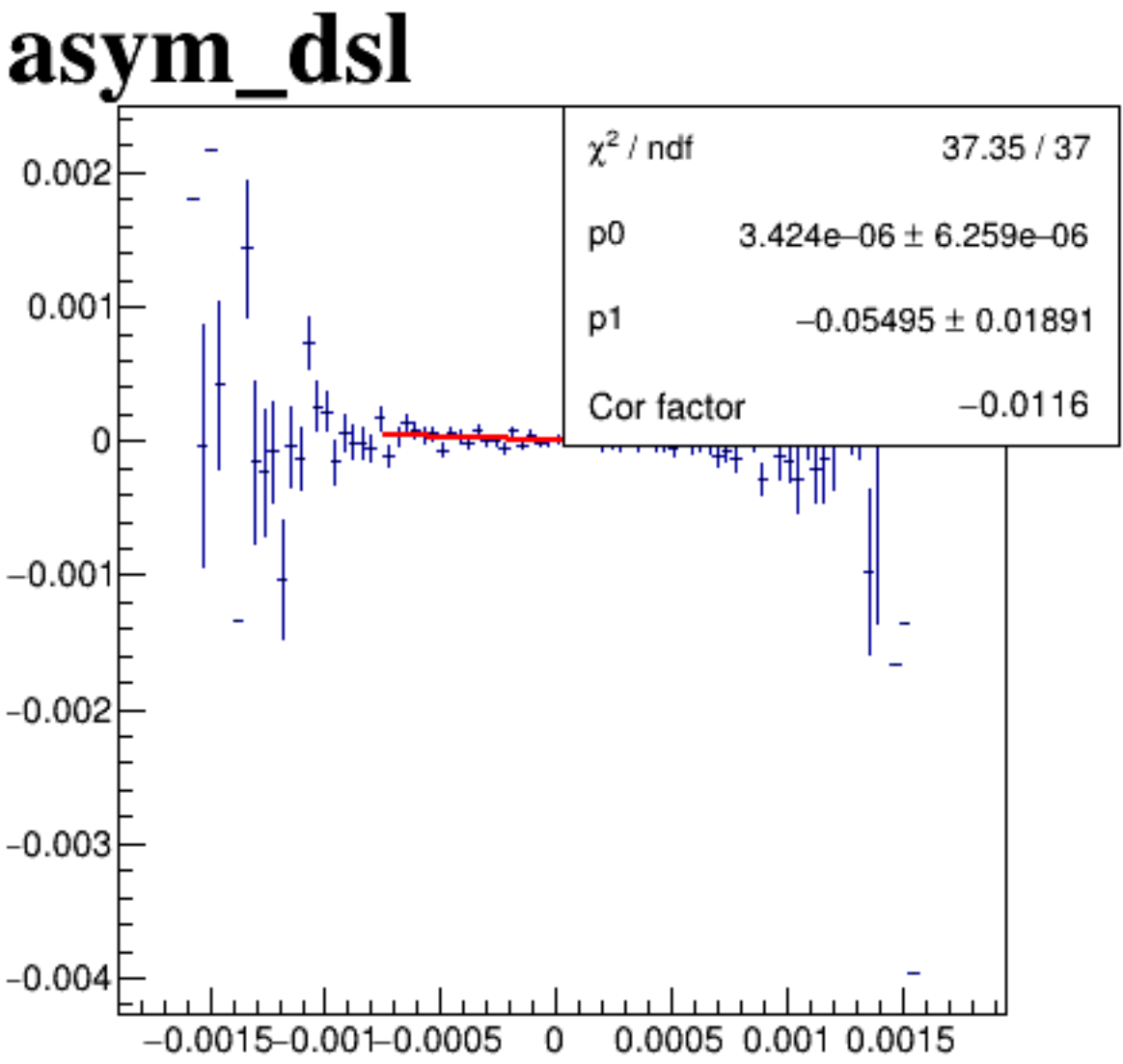
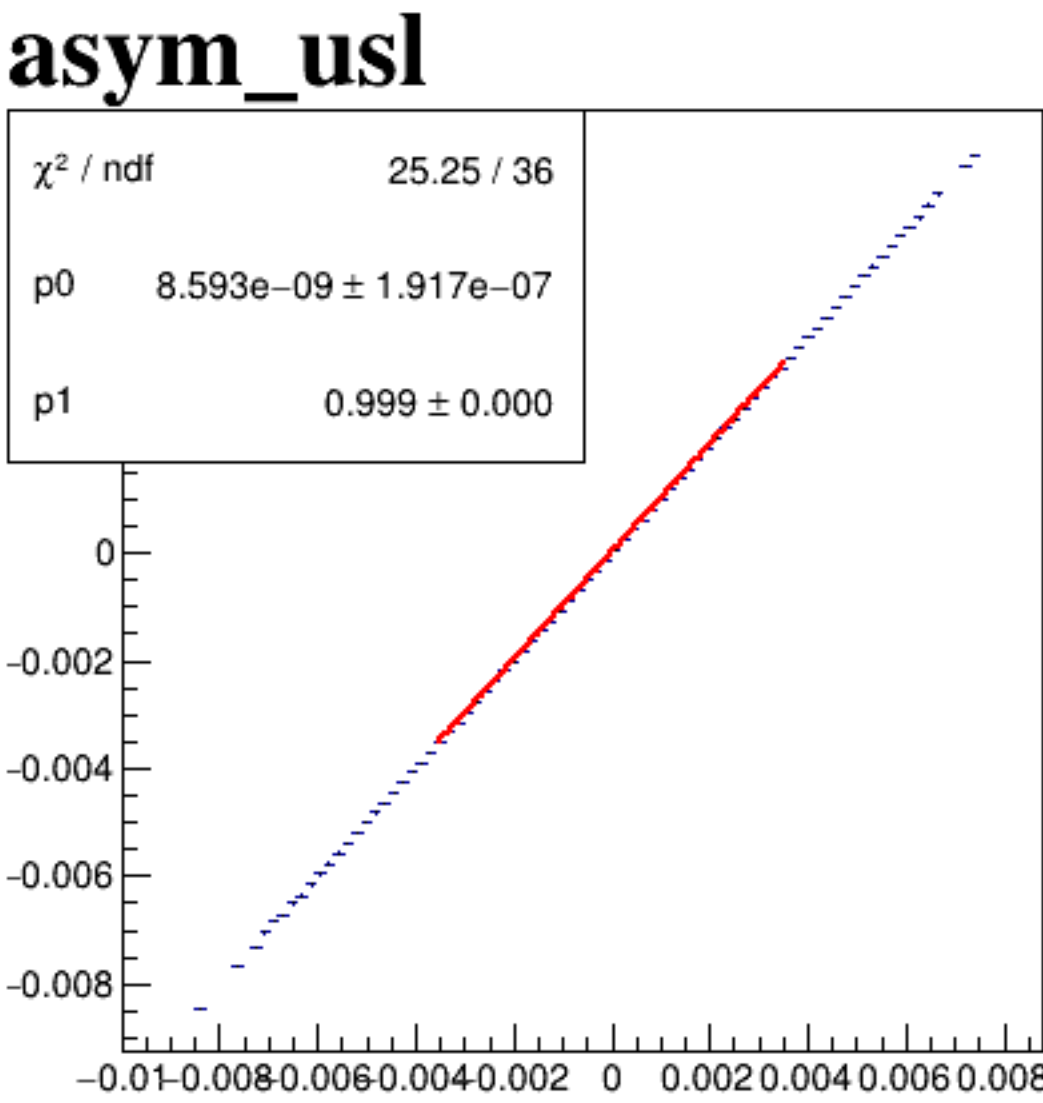
asym_dsr/ppm:pattern_number {ErrorFlag==0}



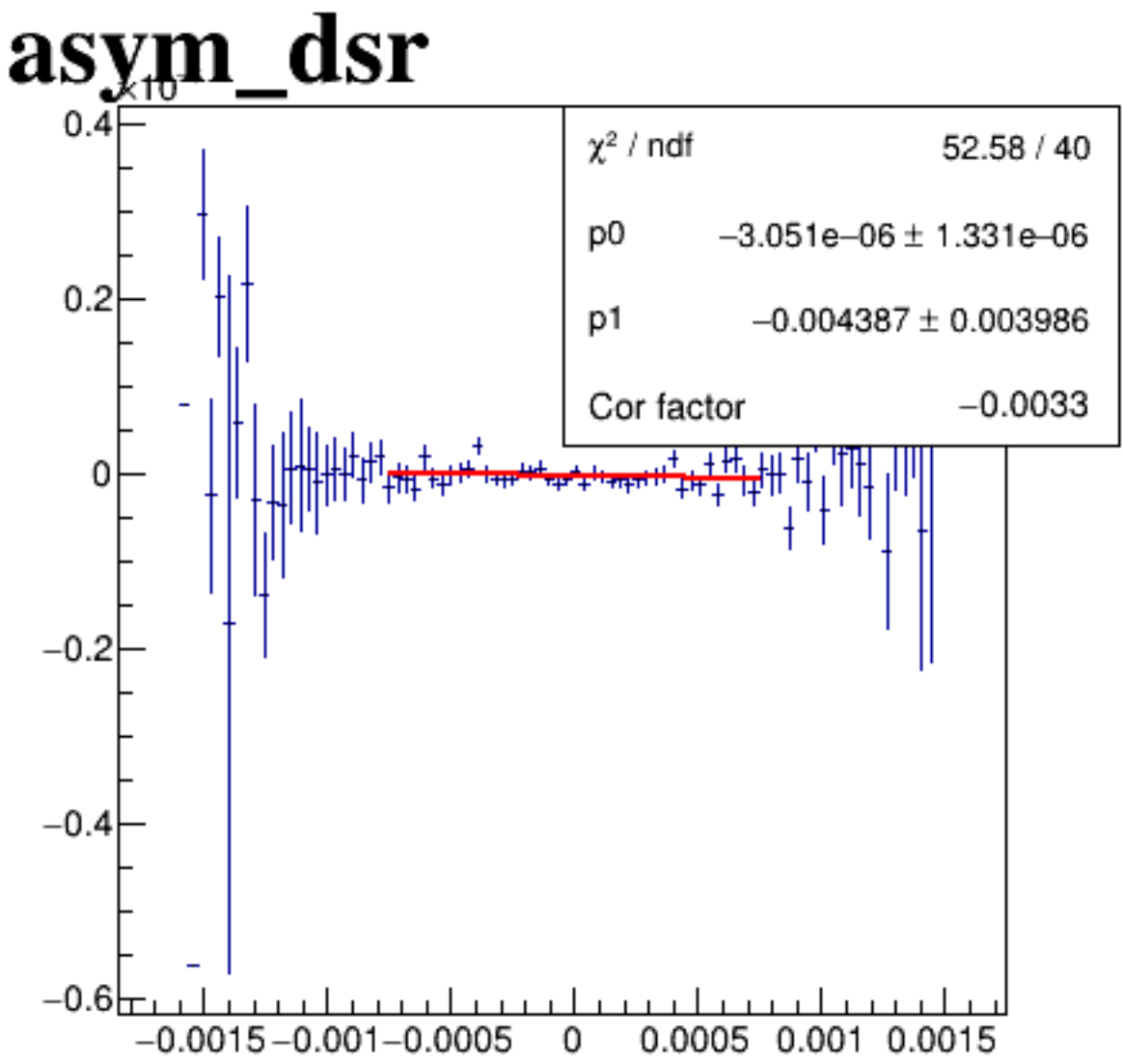
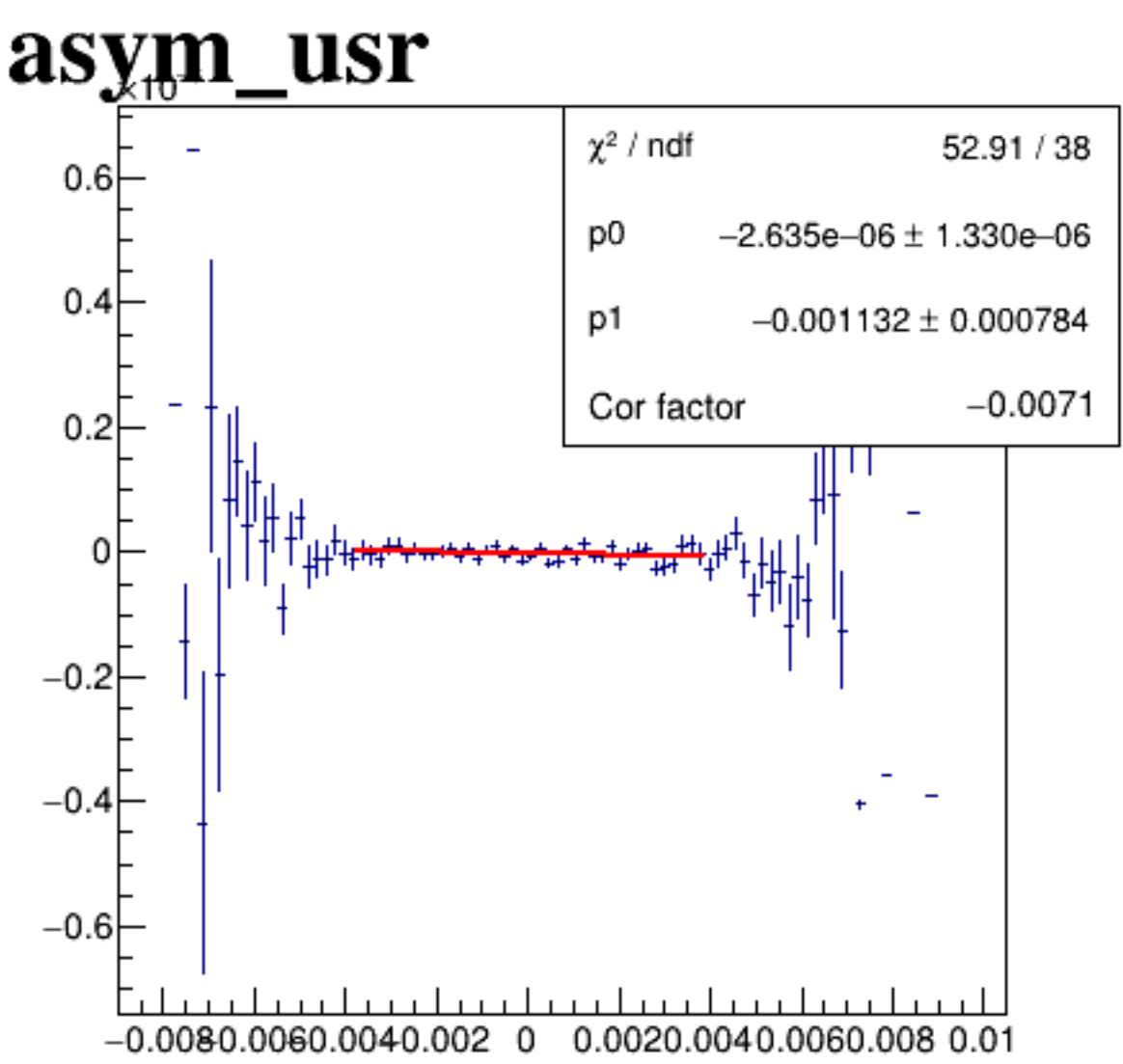
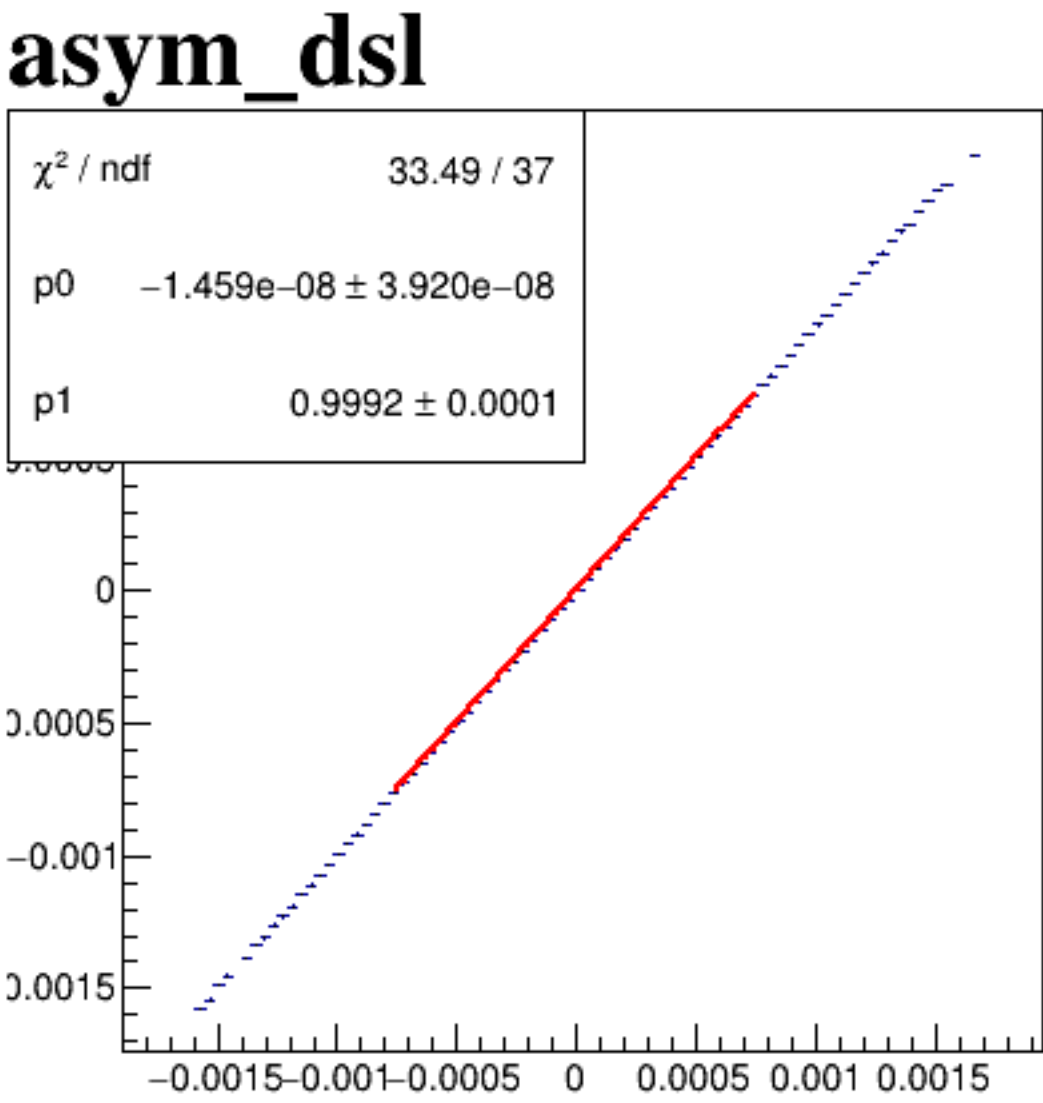
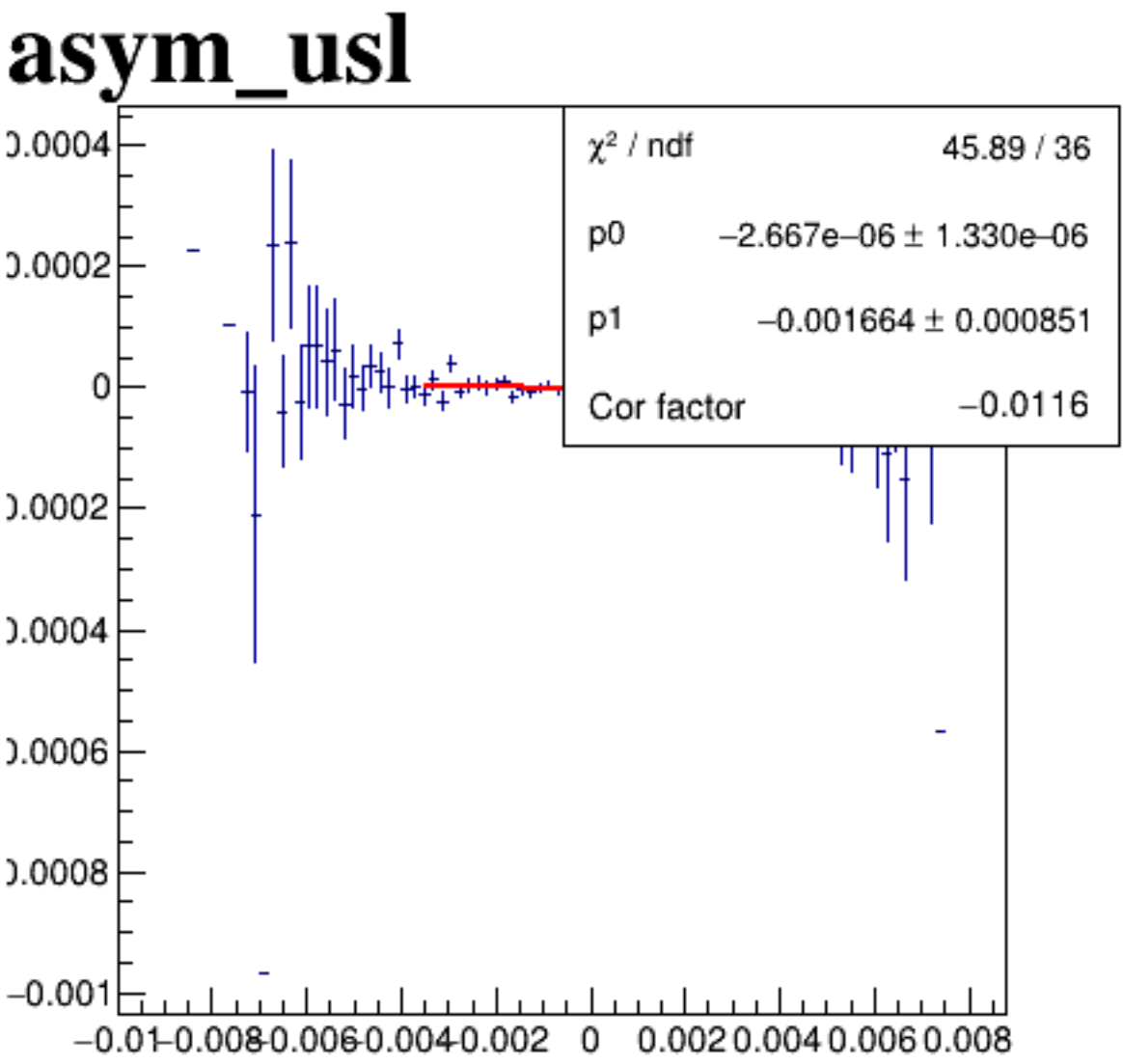
asym_dsr/ppm {ErrorFlag==0}



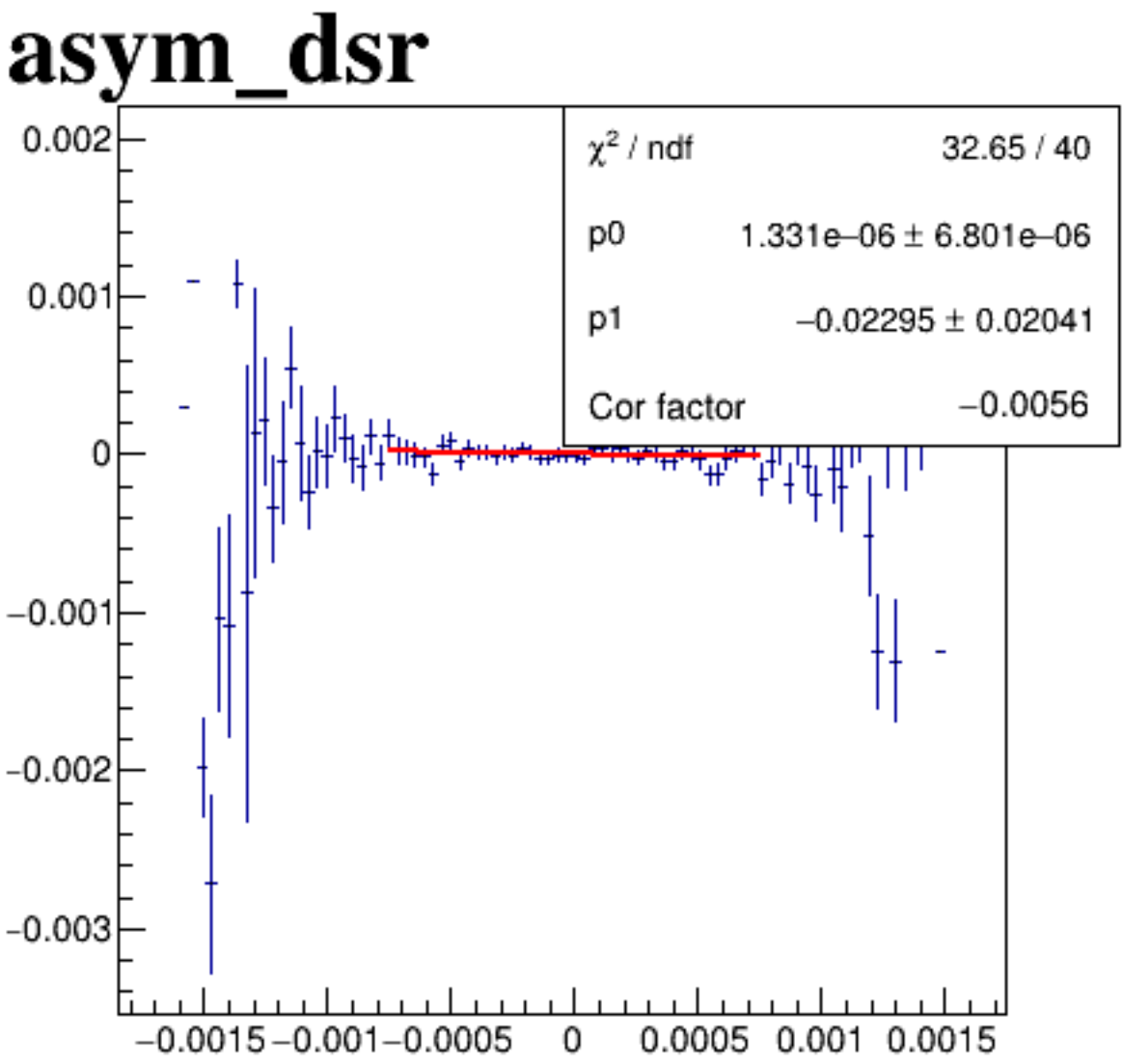
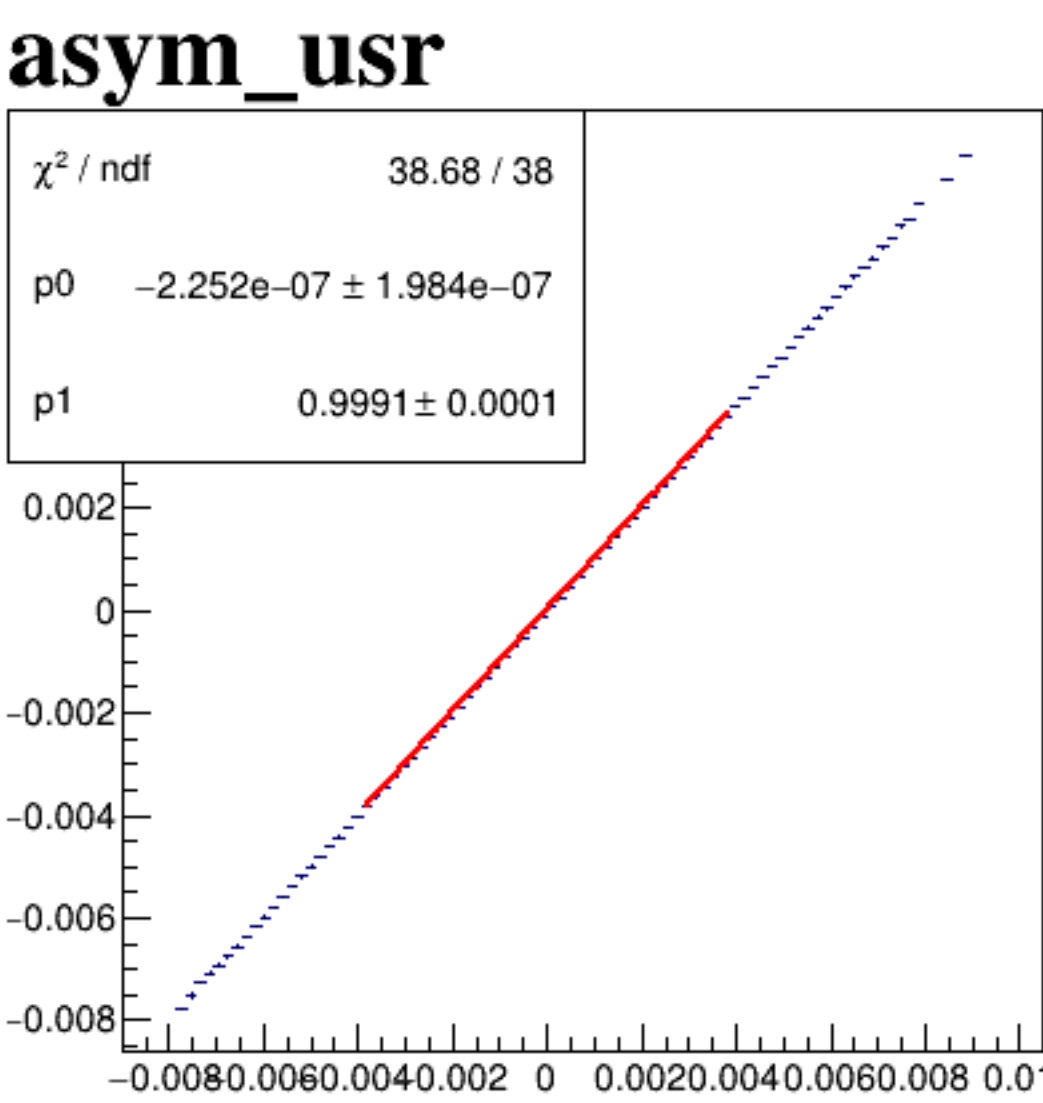
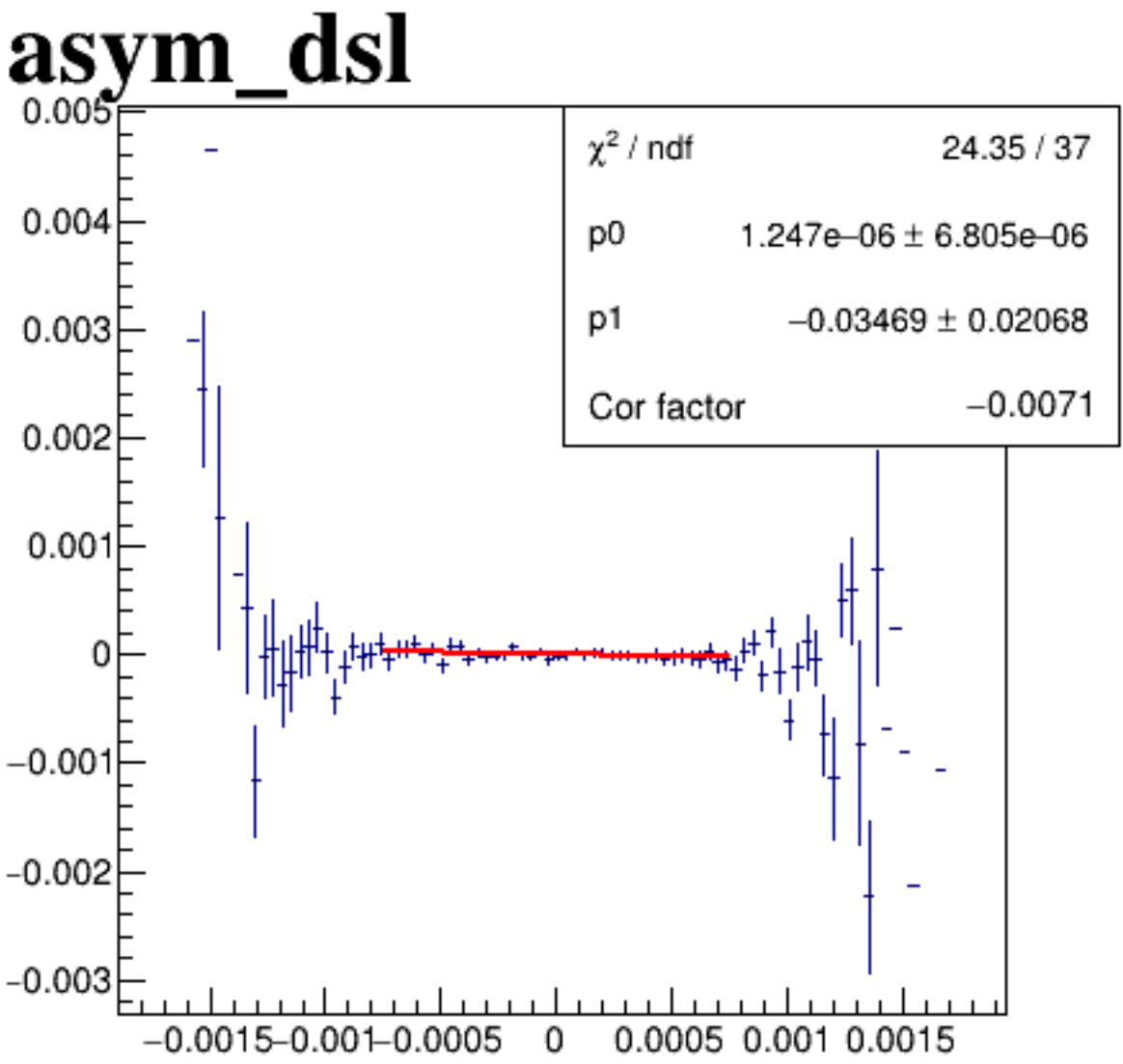
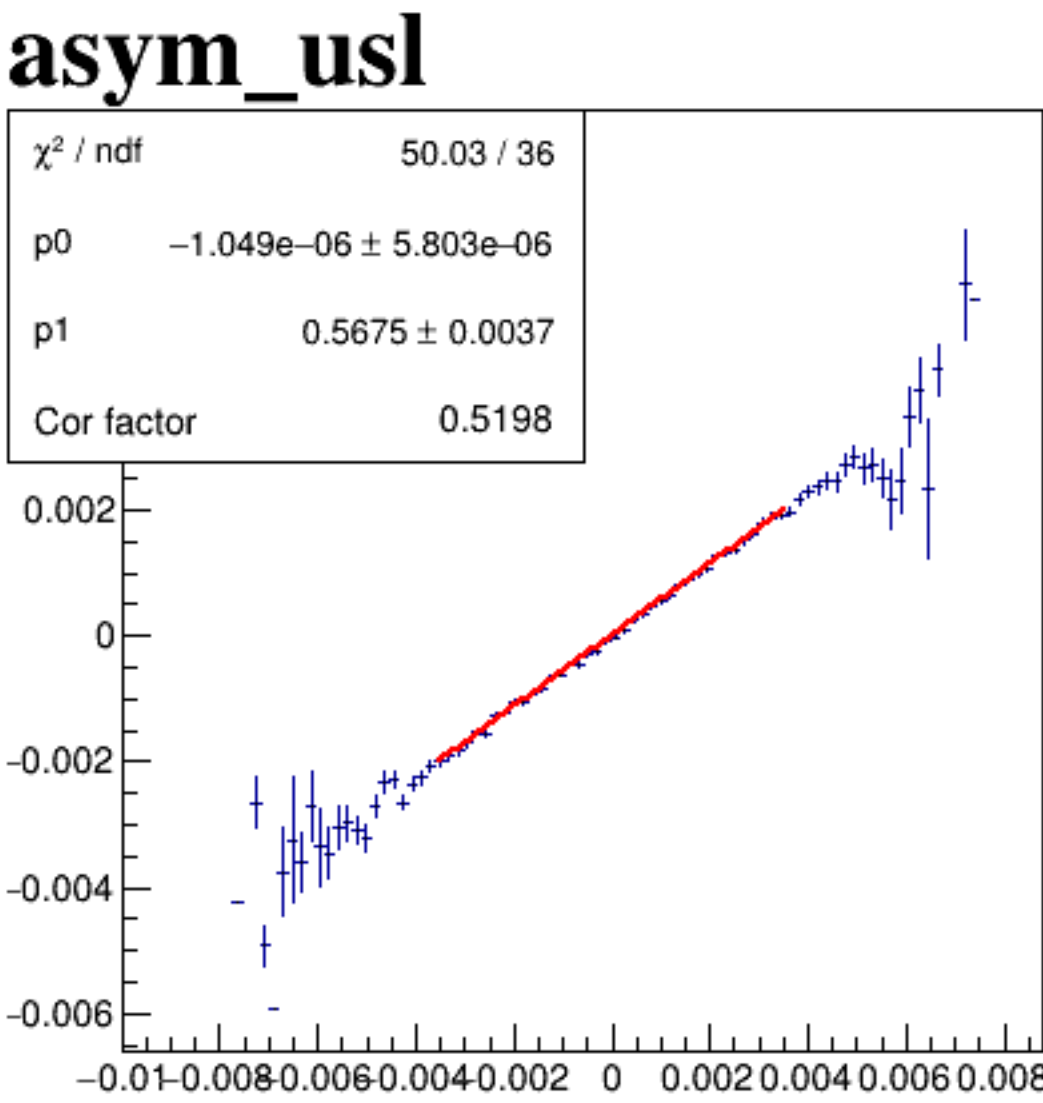
asym_usl



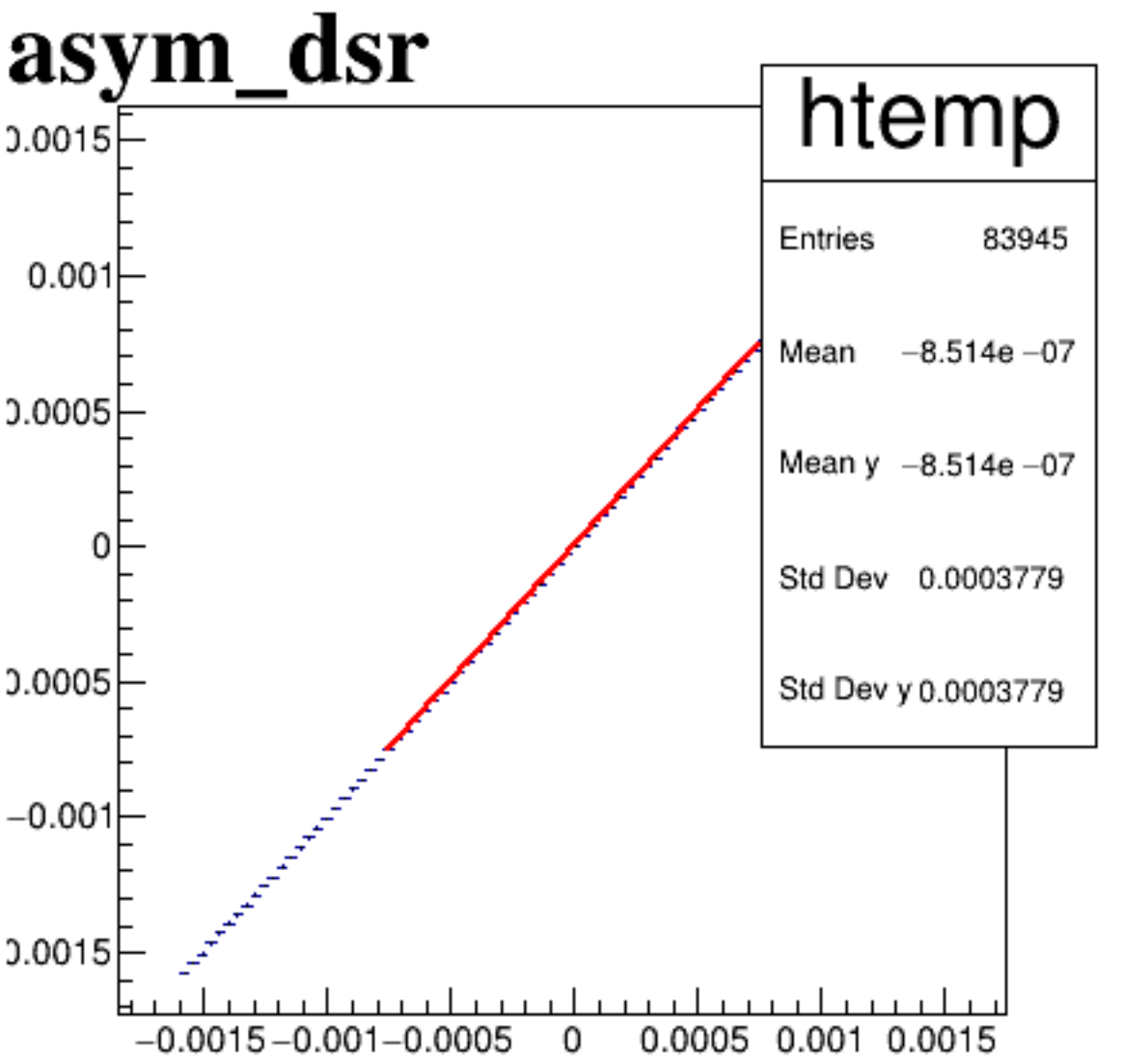
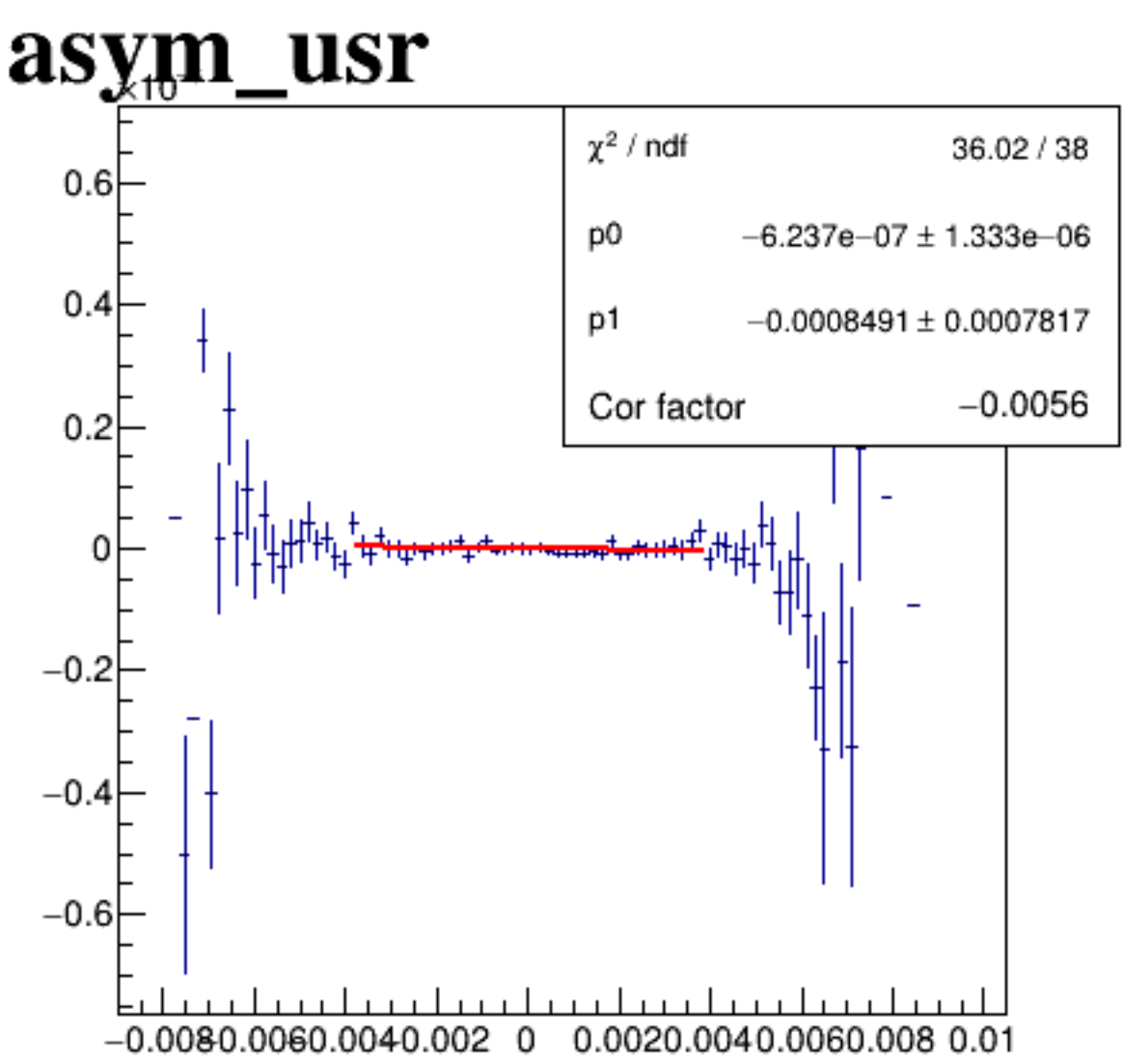
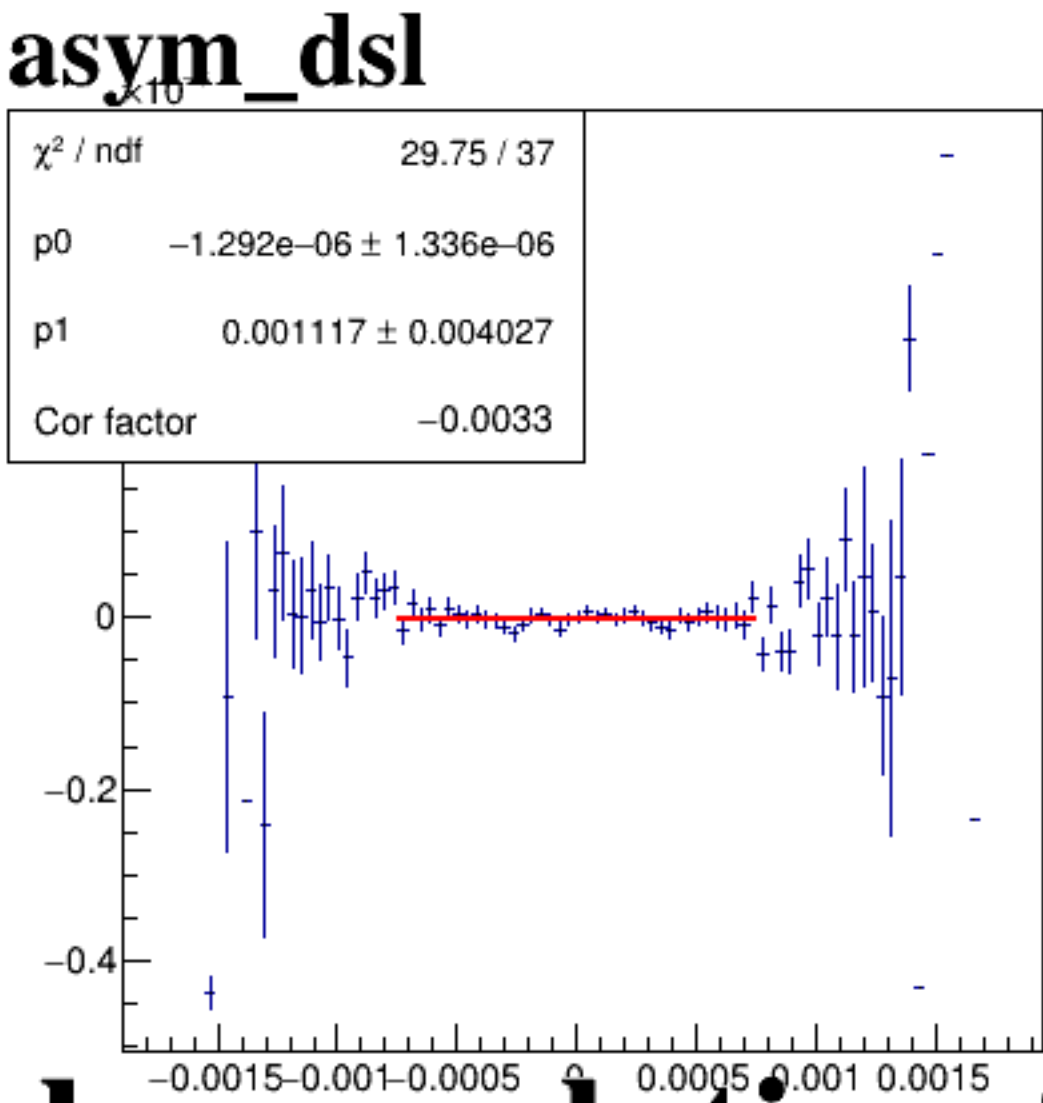
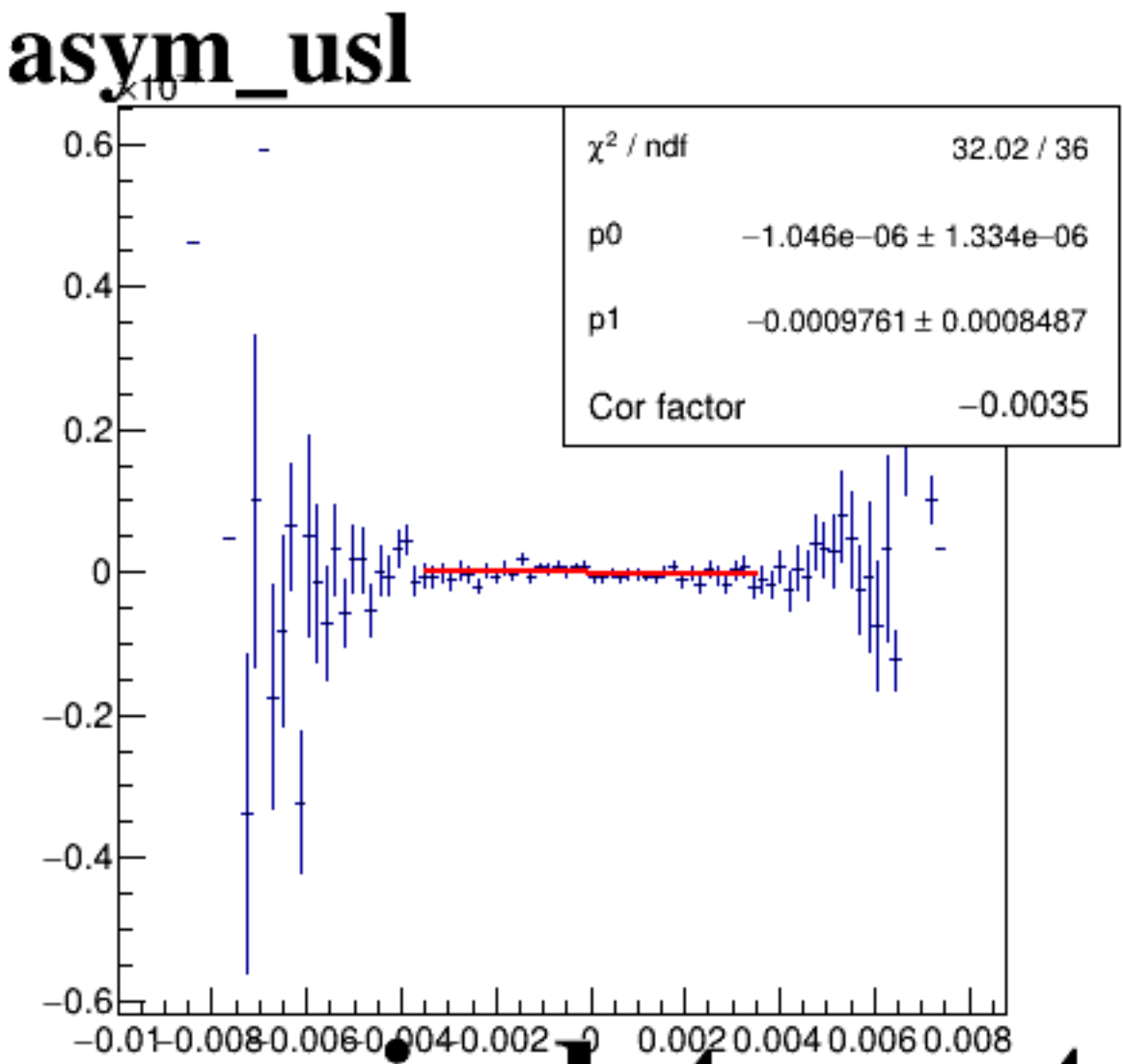
asym_dsl



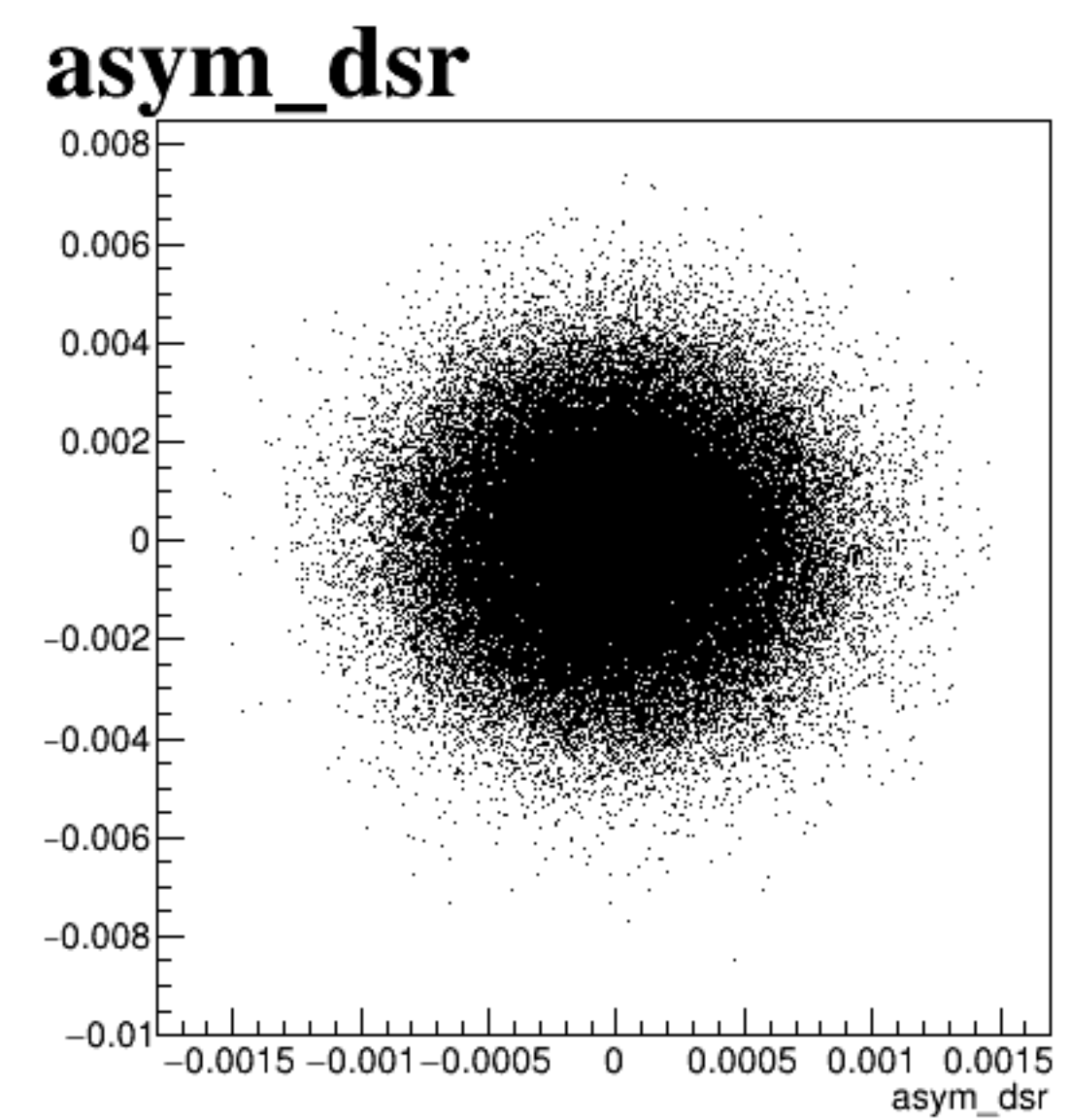
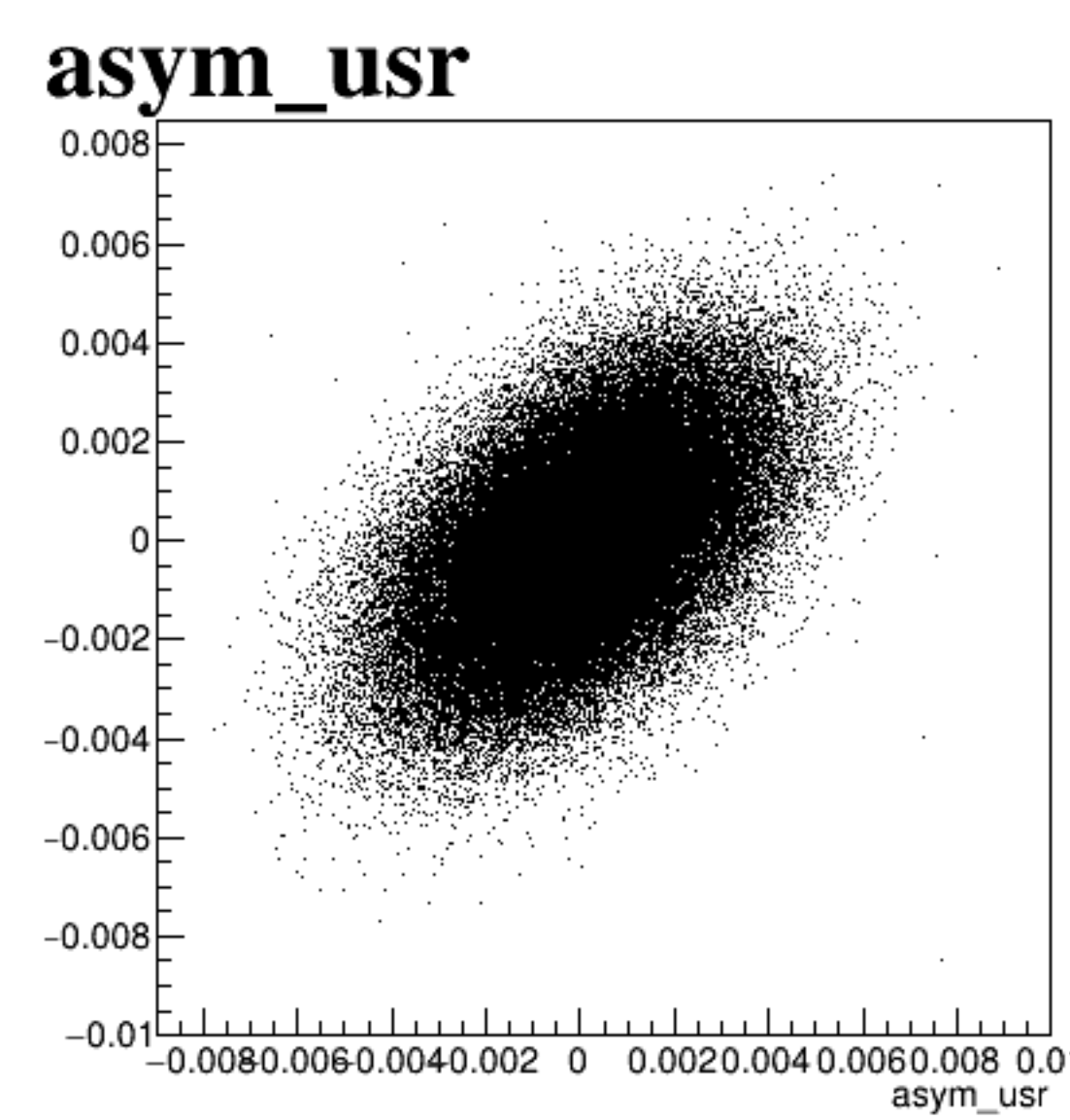
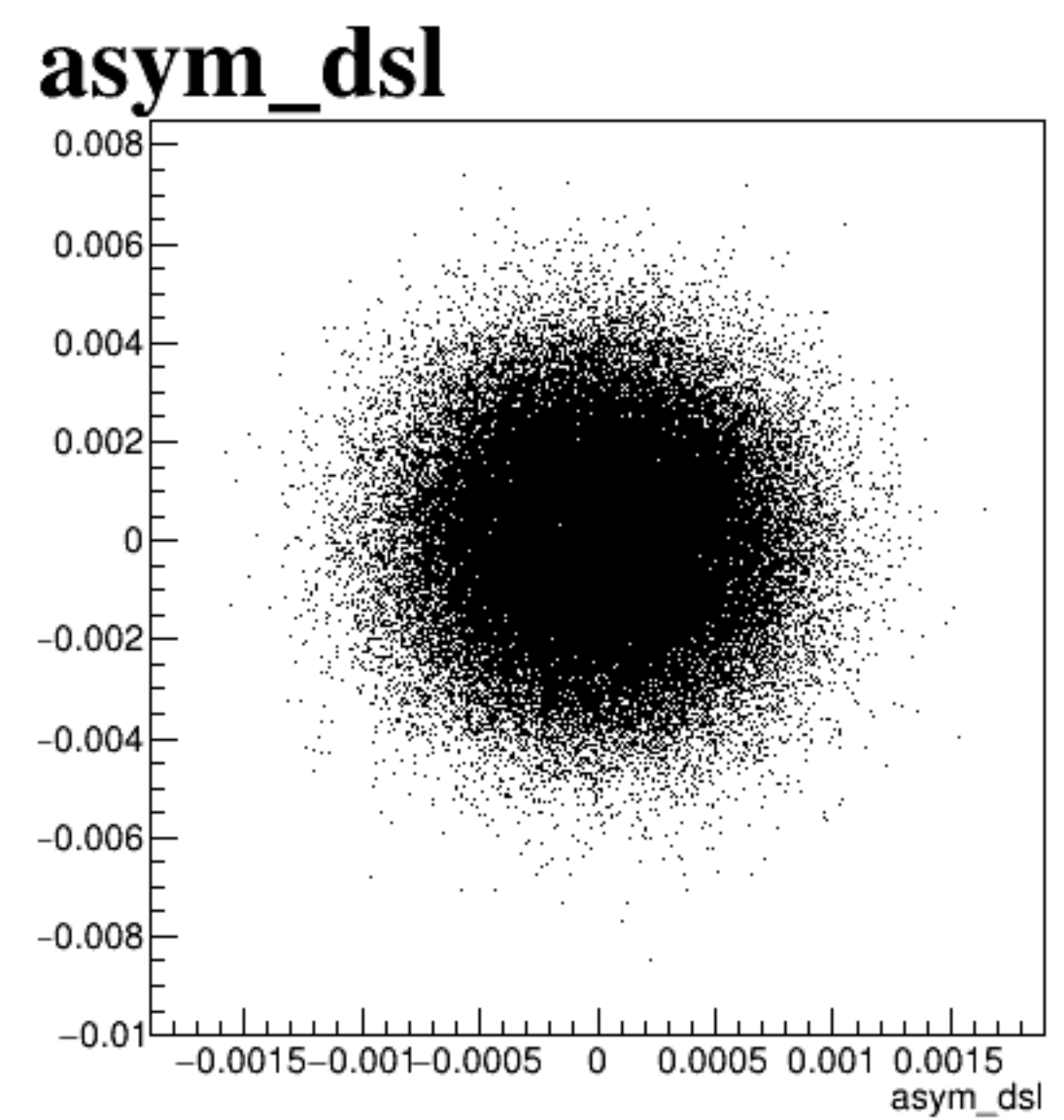
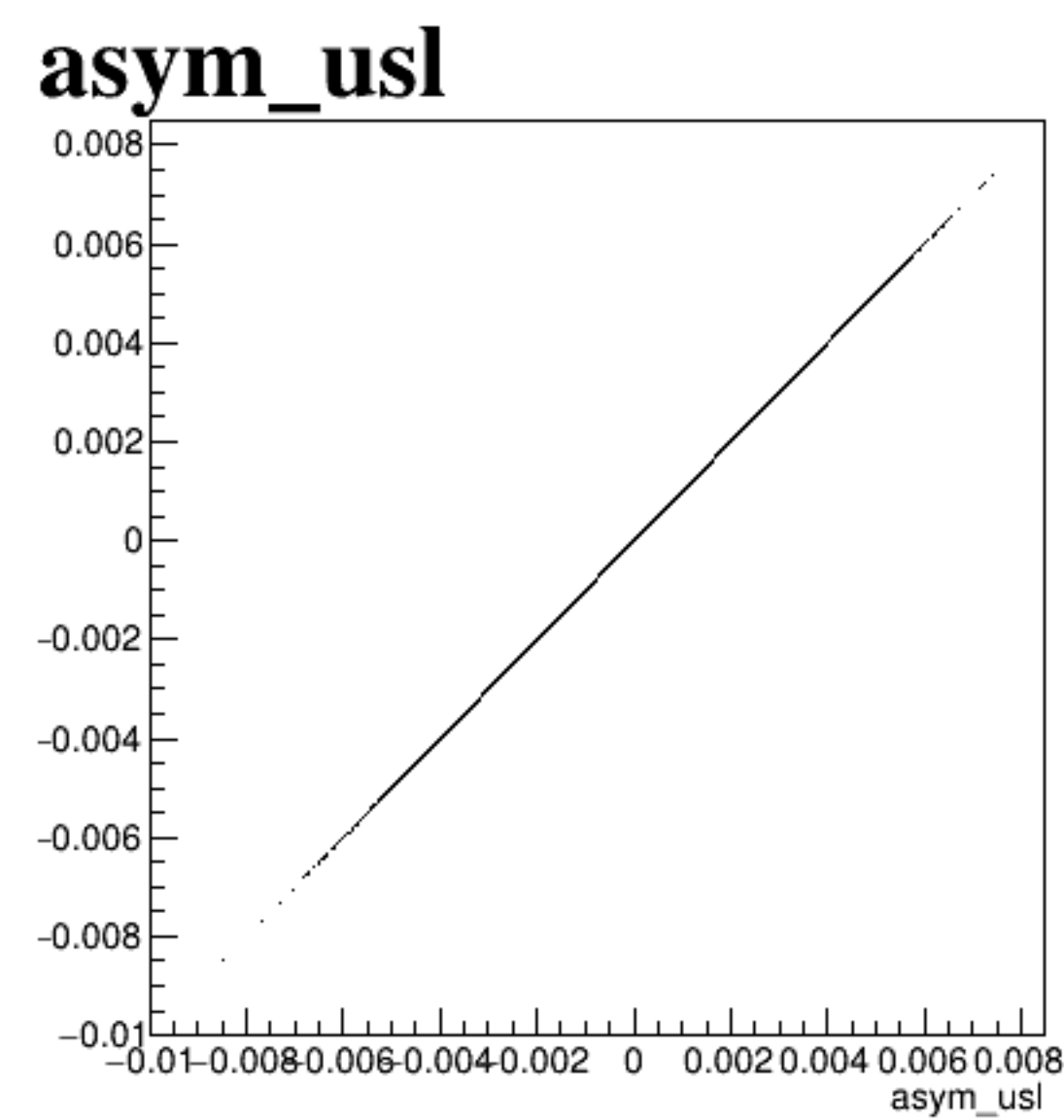
asym_usr



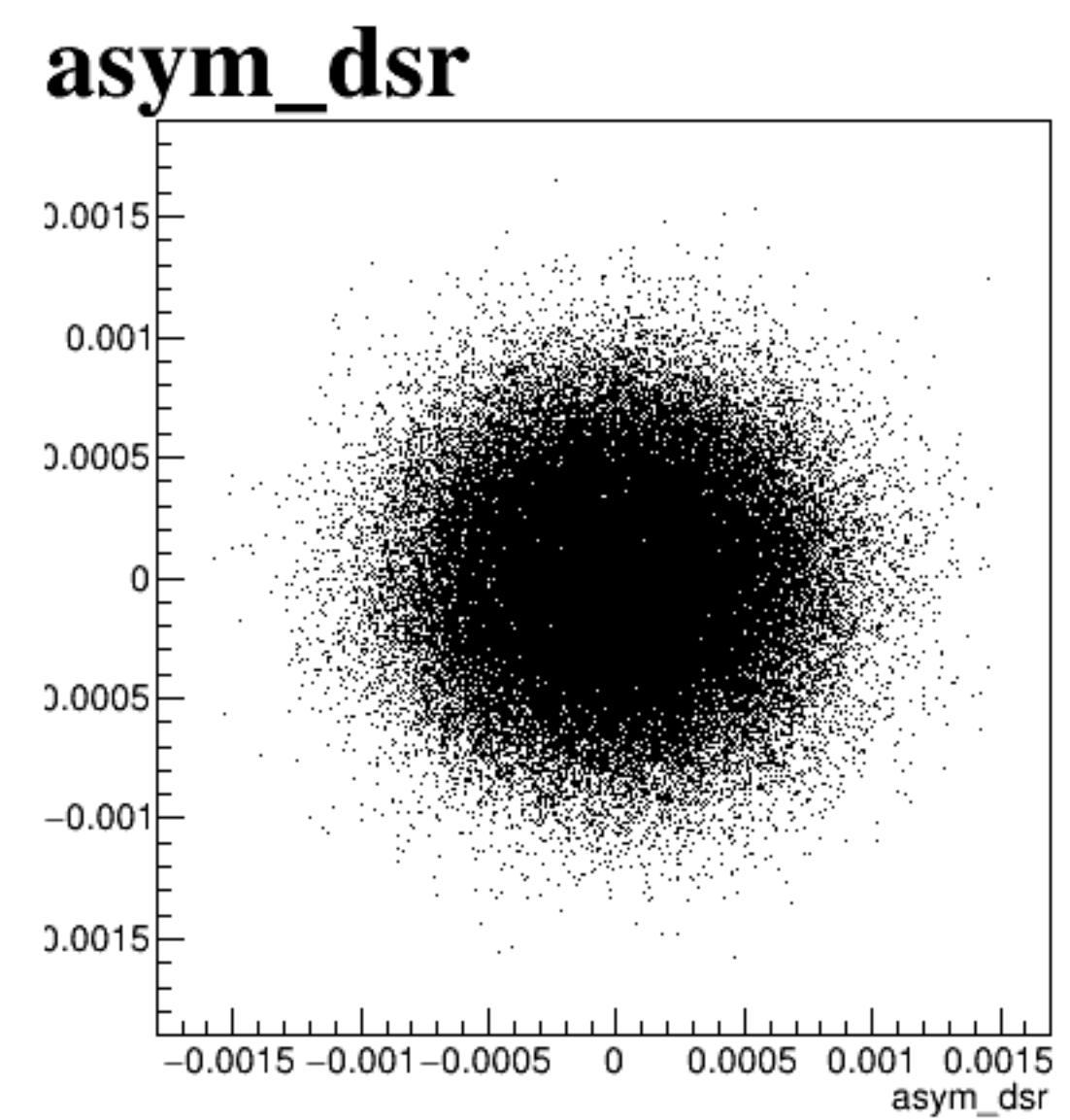
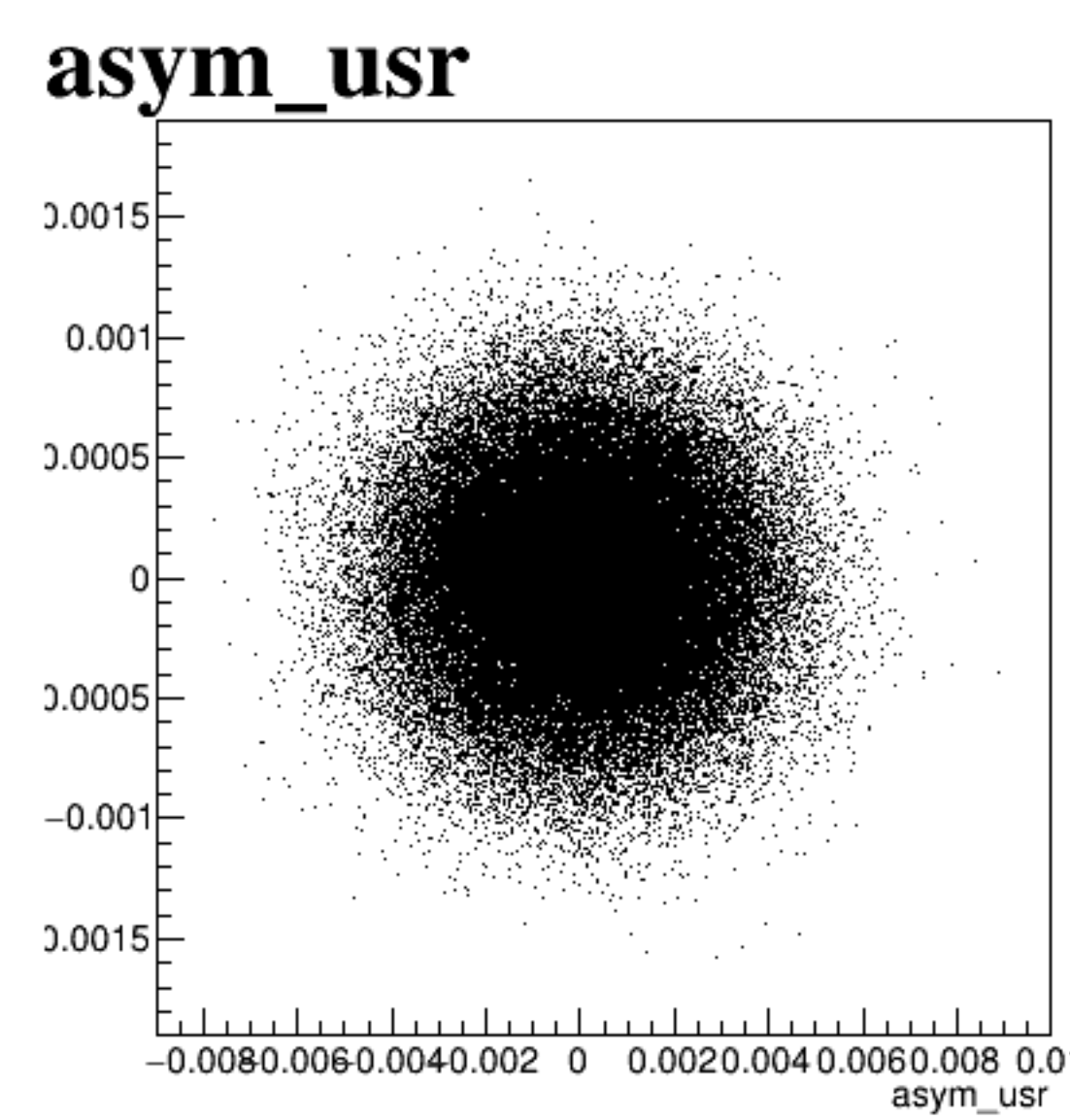
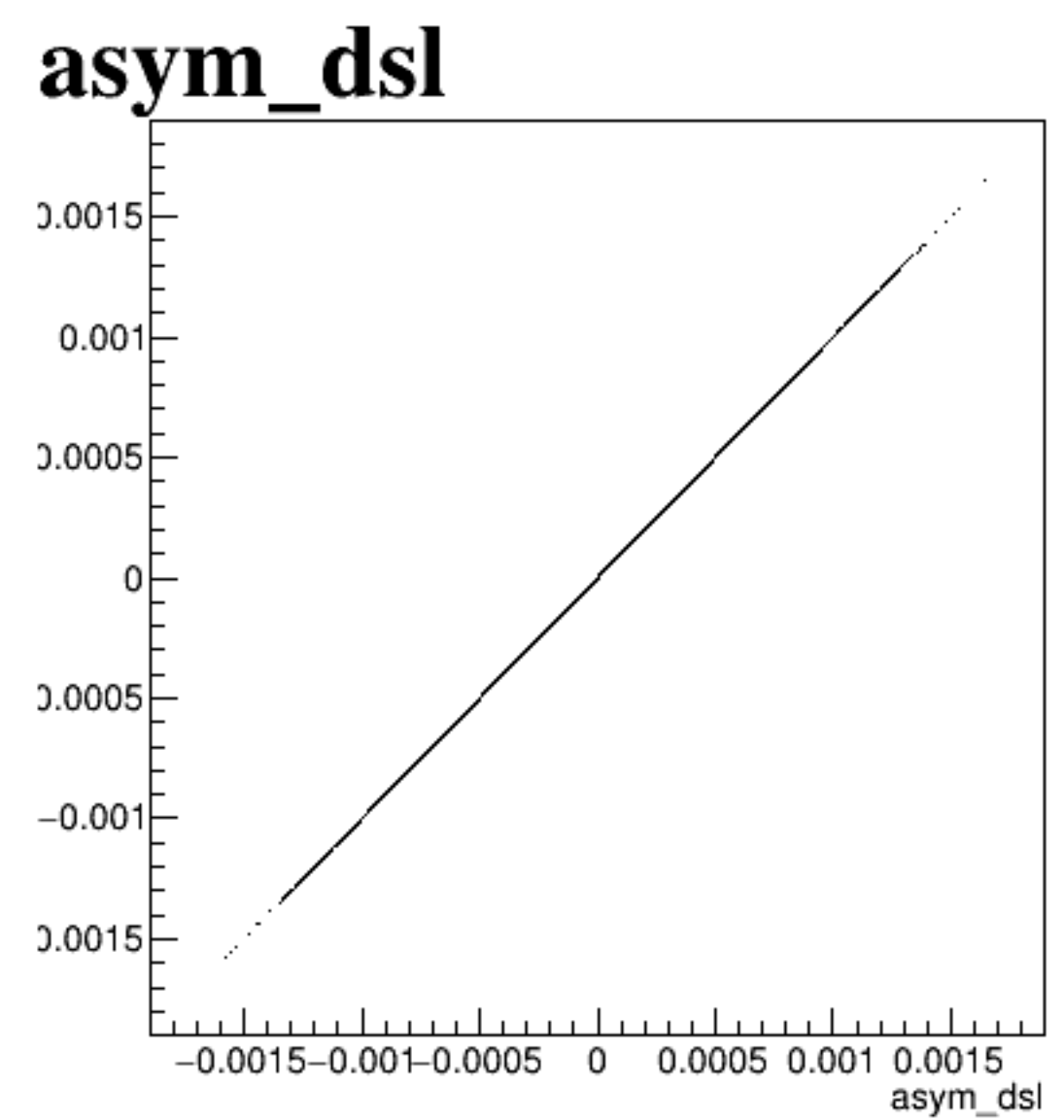
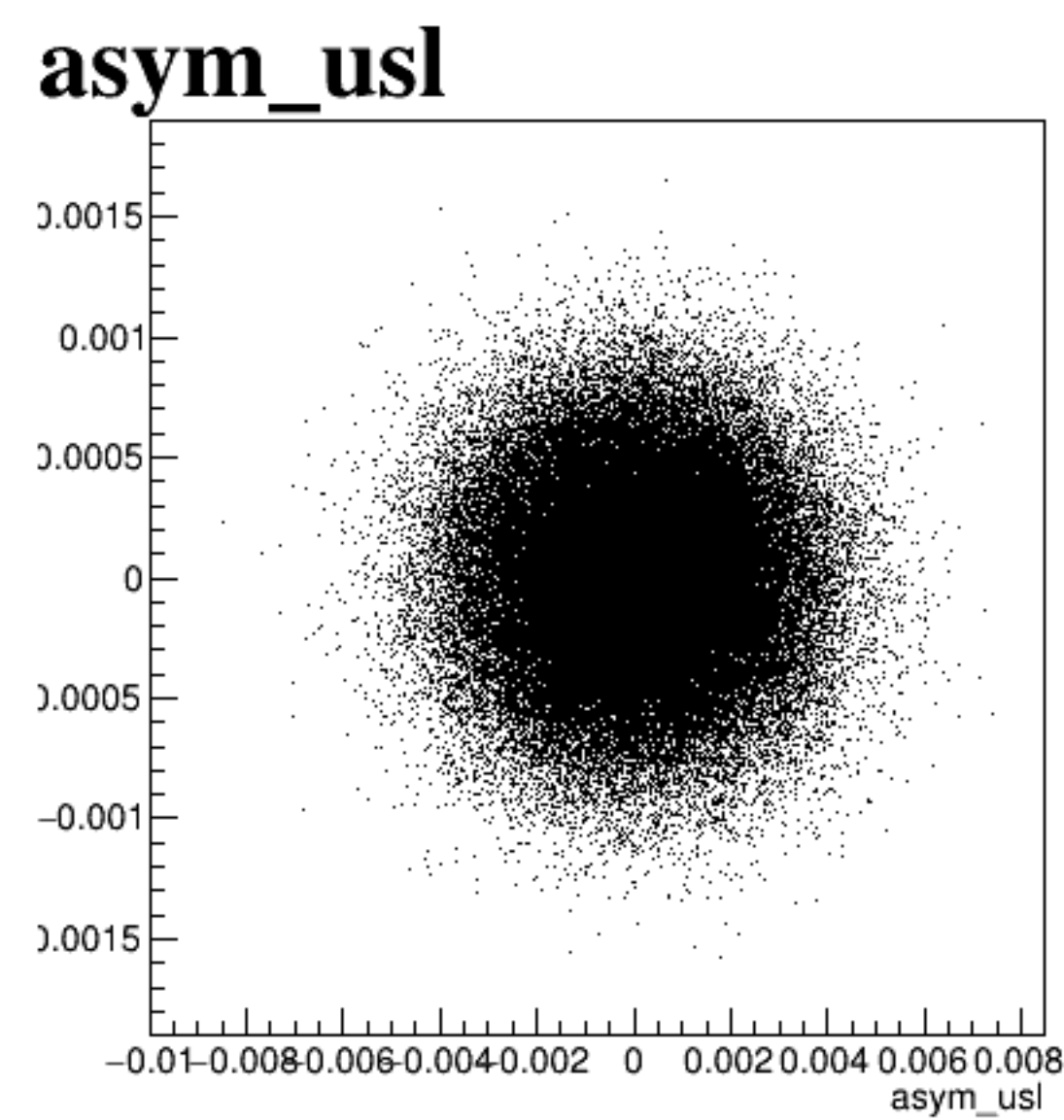
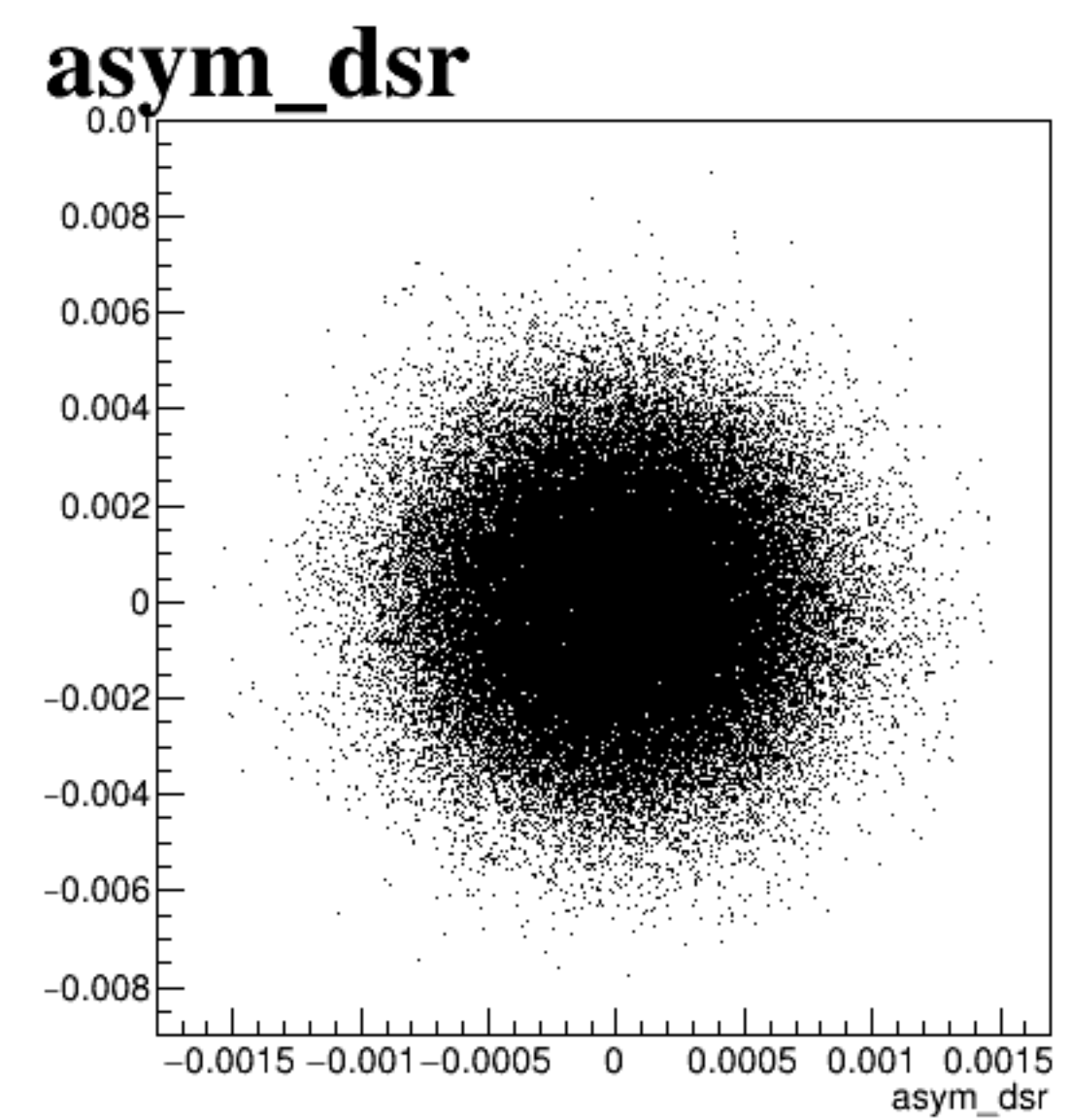
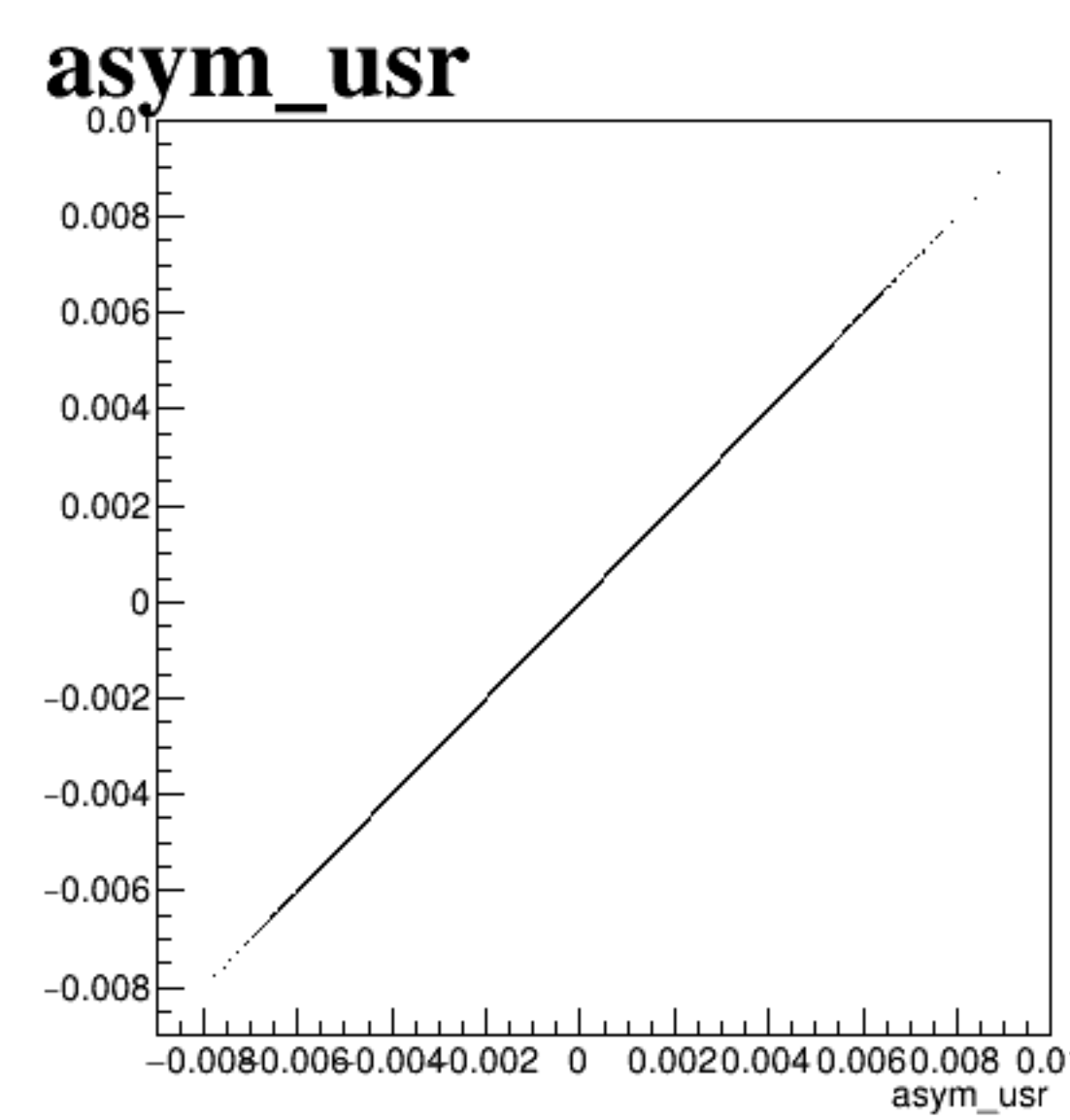
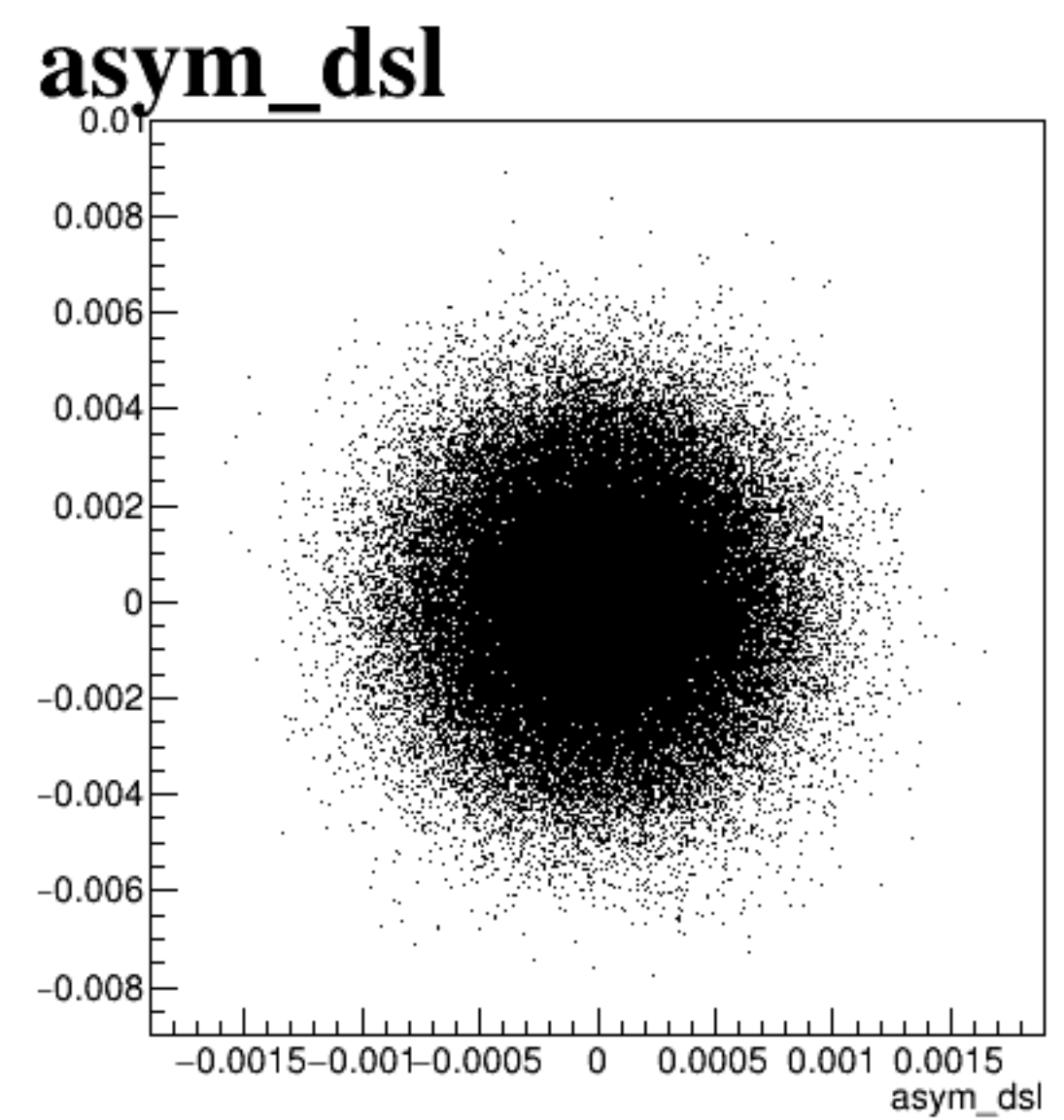
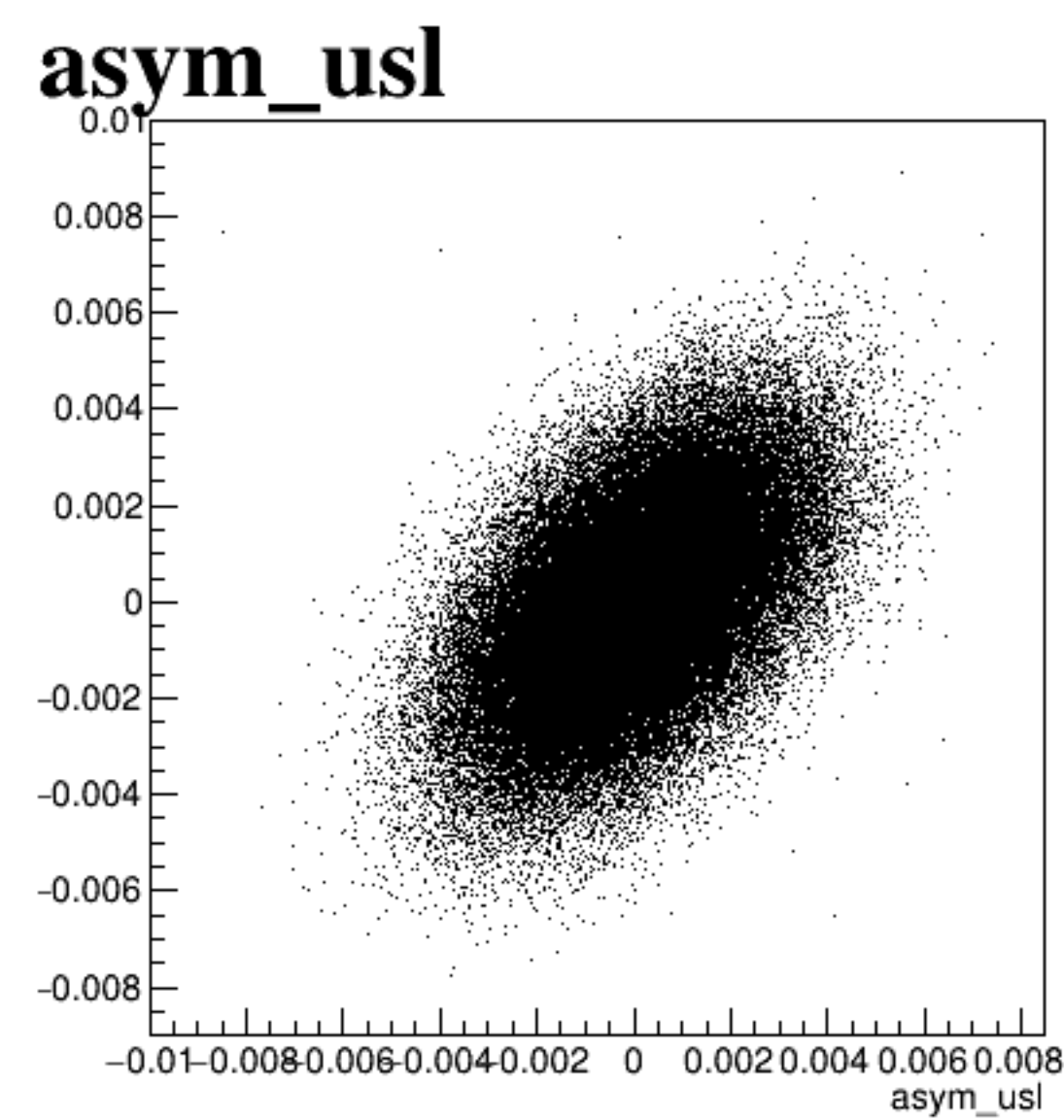
asym_dsr



asym_usl



asym_dsl

**asym_usr****asym_dsr**