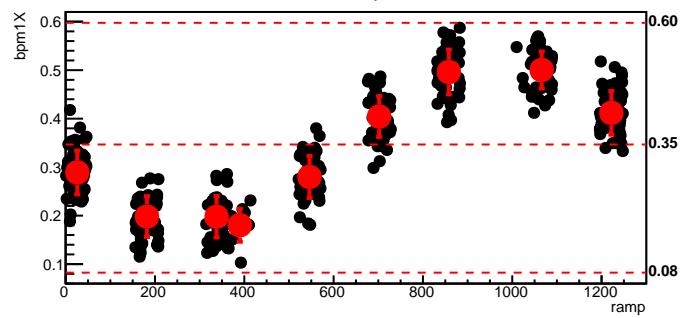
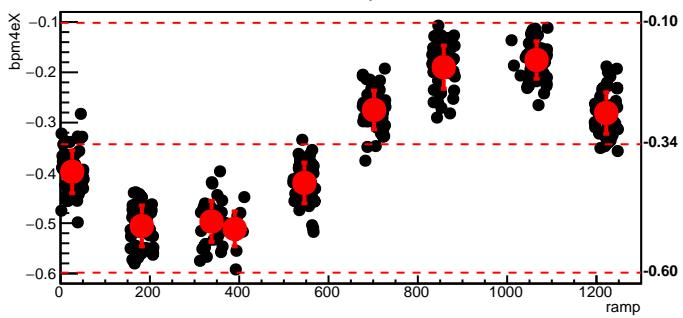


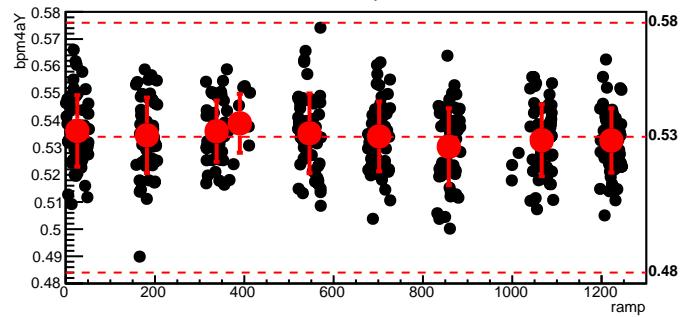
bmod Trim1,cyc=8506



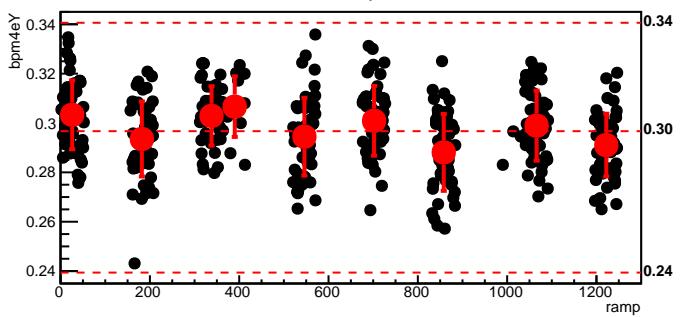
bmod Trim1,cyc=8506



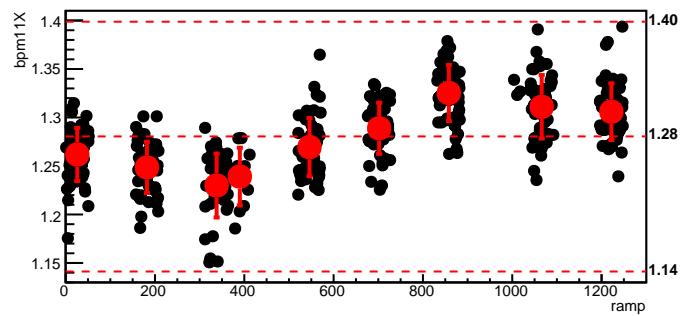
bmod Trim1,cyc=8506



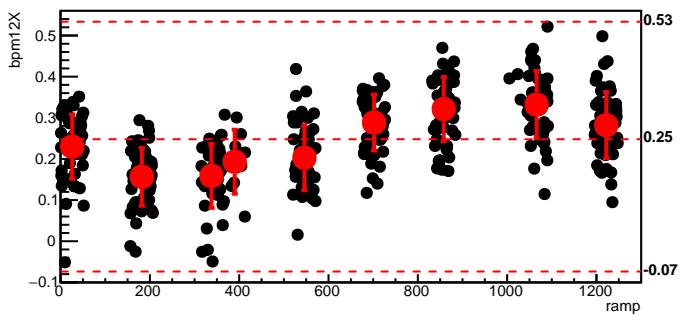
bmod Trim1,cyc=8506



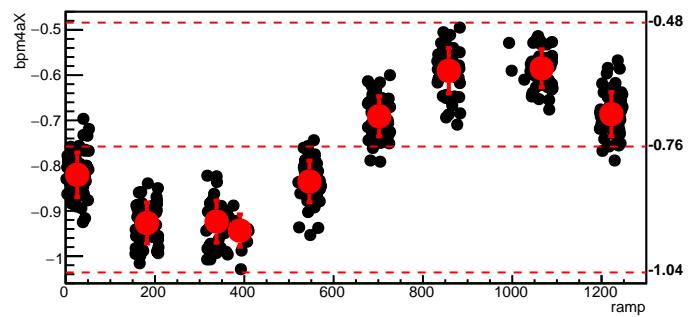
bmod Trim1,cyc=8506



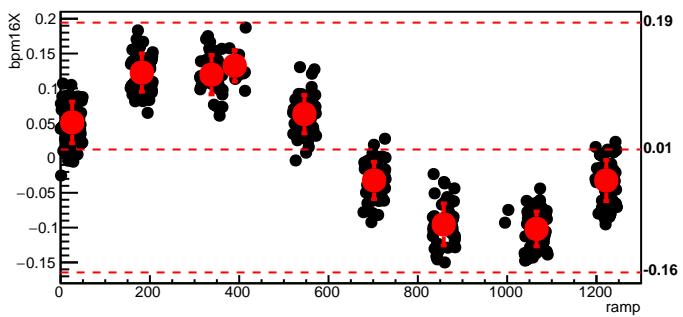
bmod Trim1,cyc=8506



bmod Trim1,cyc=8506



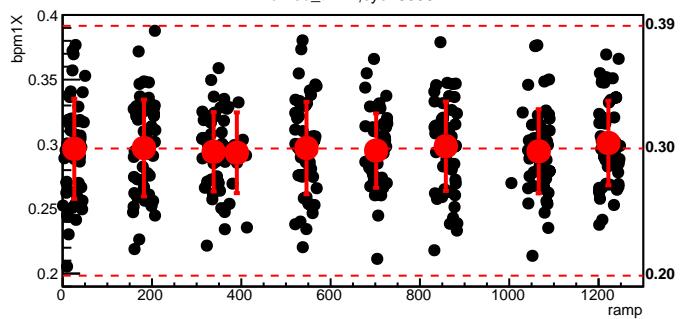
bmod Trim1,cyc=8506



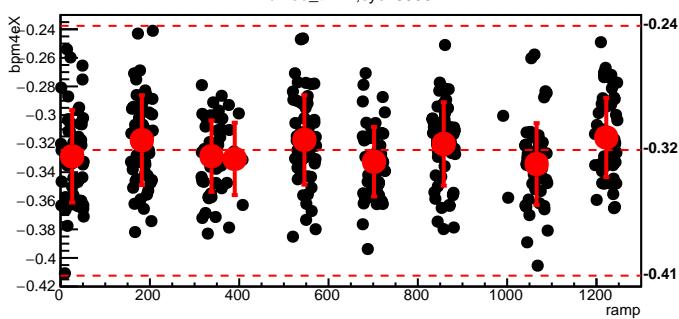




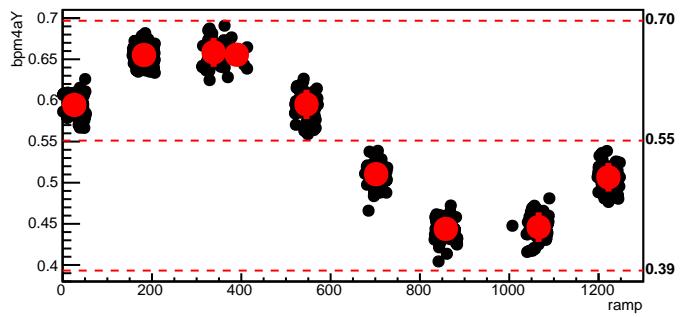
bmod Trim4,cyc=8506



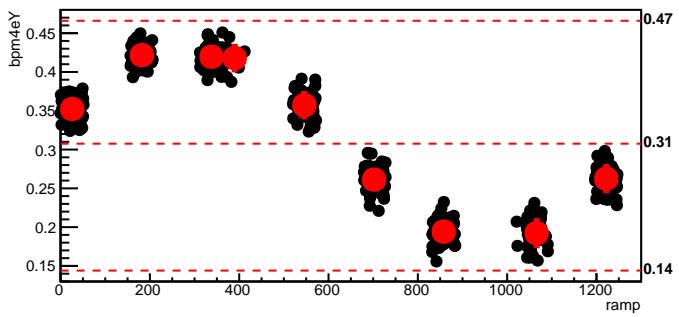
bmod Trim4,cyc=8506



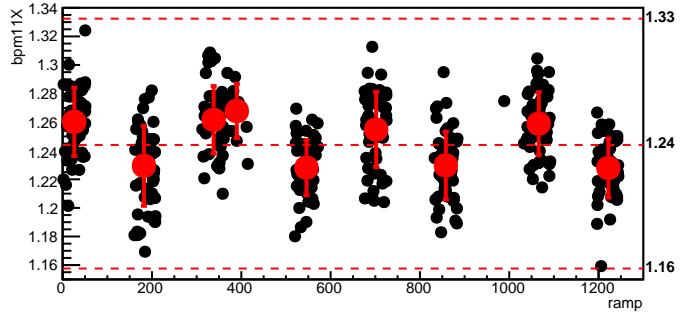
bmod Trim4,cyc=8506



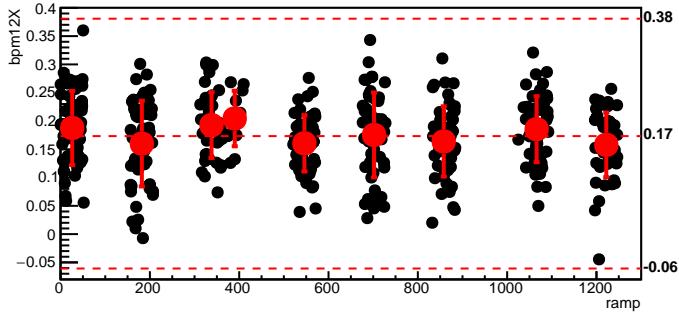
bmod Trim4,cyc=8506



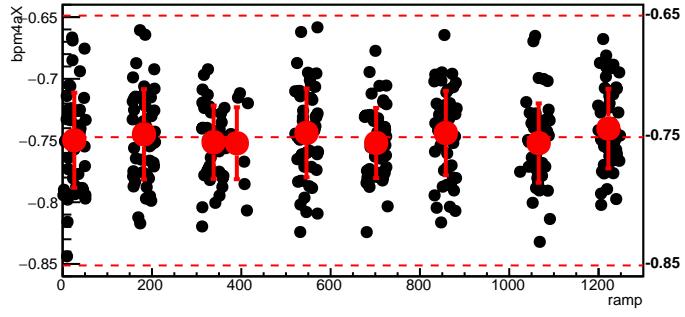
bmod Trim4,cyc=8506



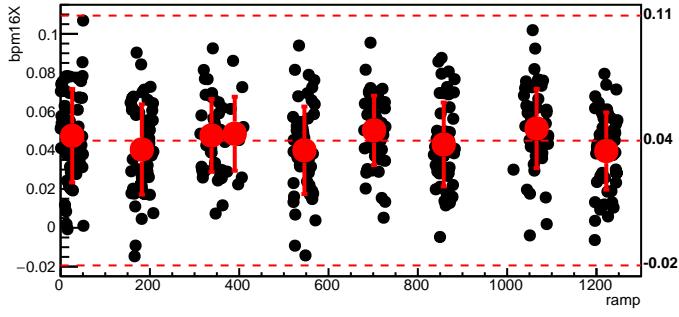
bmod Trim4,cyc=8506

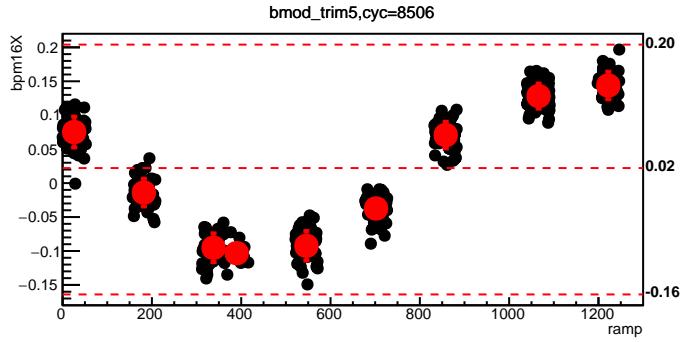
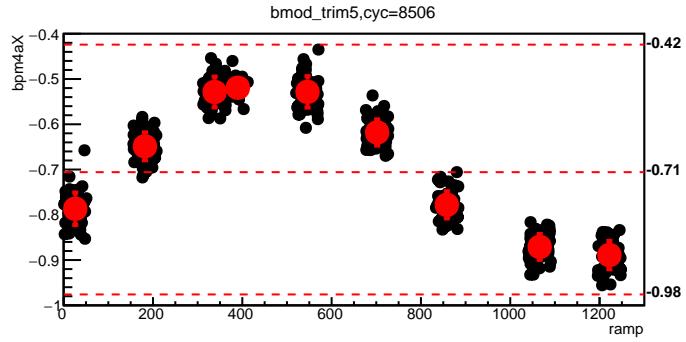
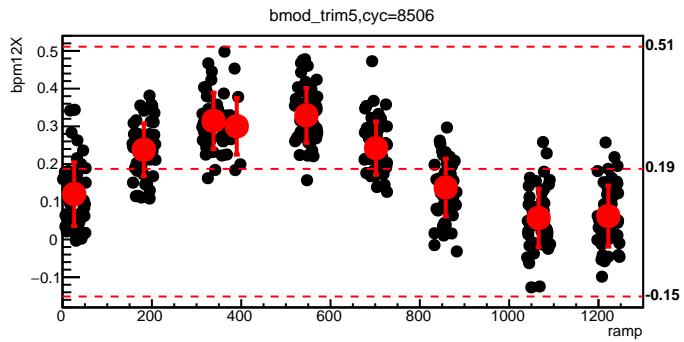
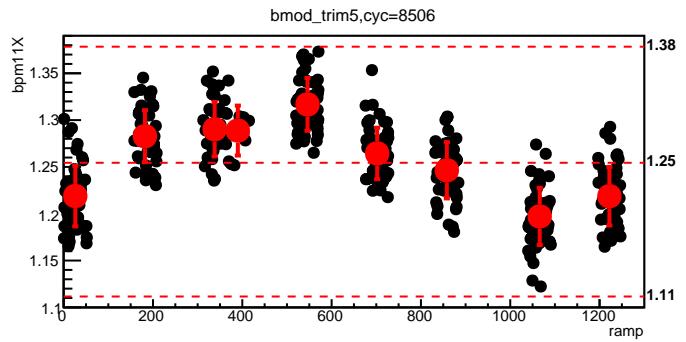
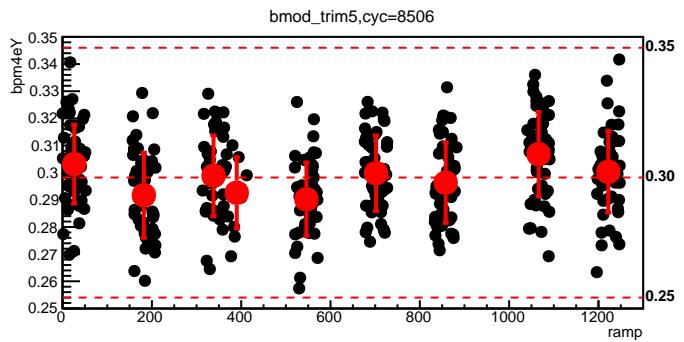
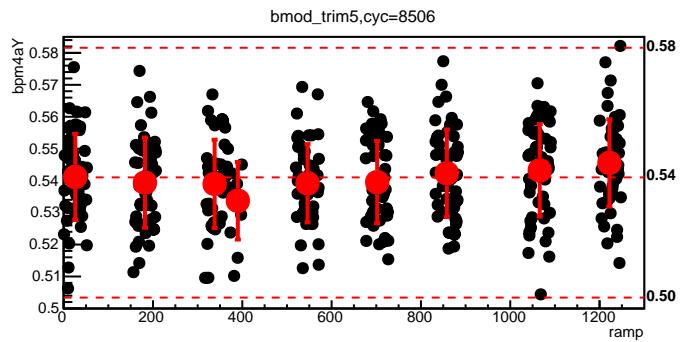
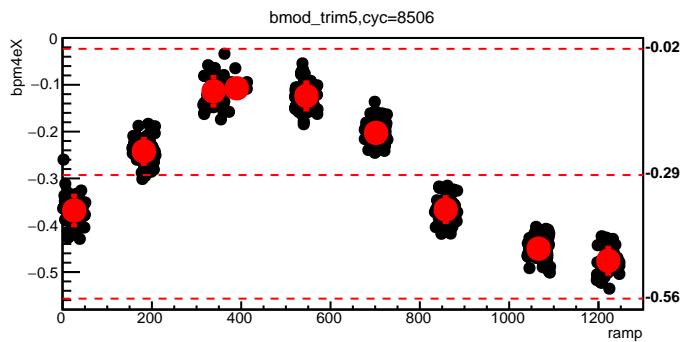
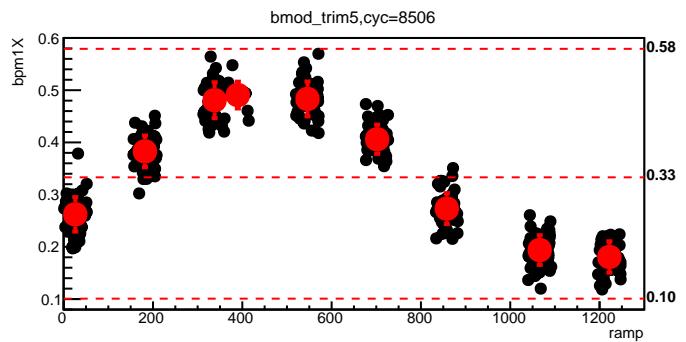


bmod Trim4,cyc=8506

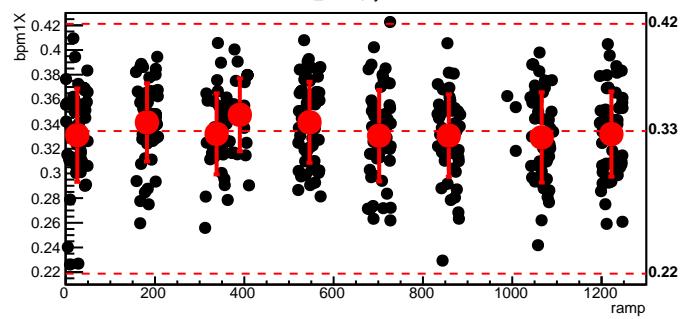


bmod Trim4,cyc=8506

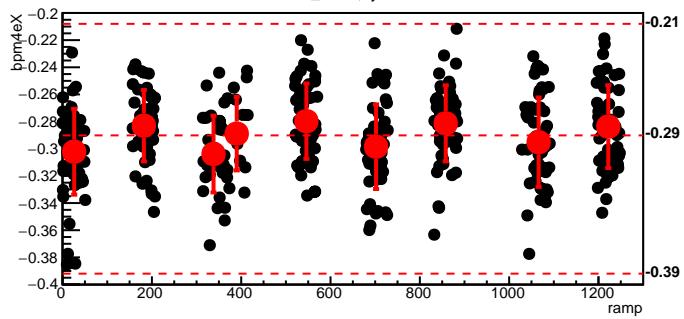




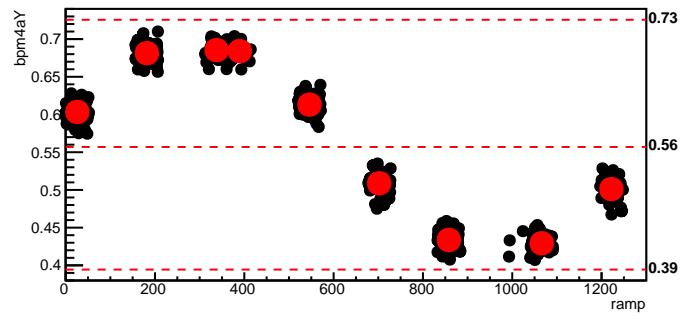
bmod Trim6,cyc=8506



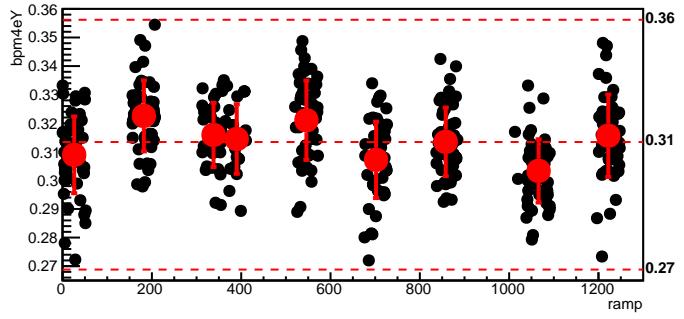
bmod Trim6,cyc=8506



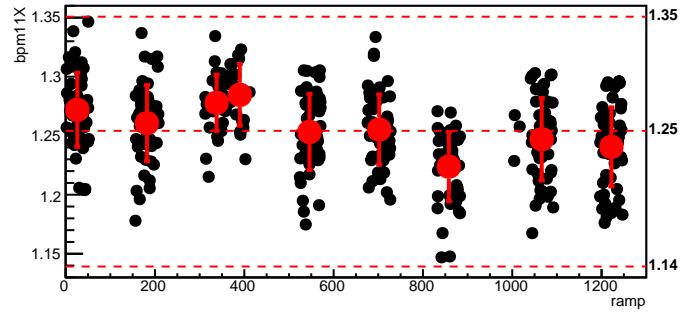
bmod Trim6,cyc=8506



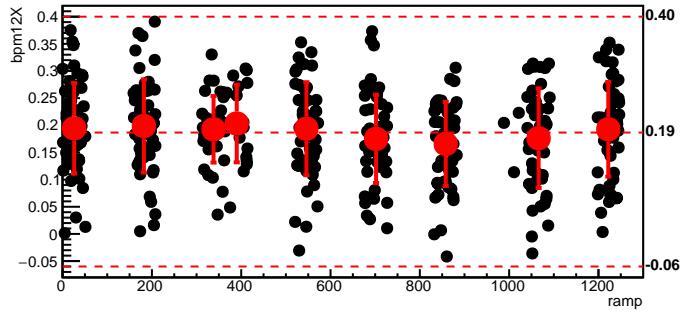
bmod Trim6,cyc=8506



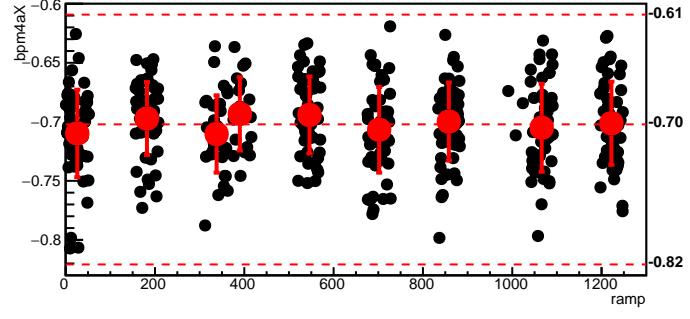
bmod Trim6,cyc=8506



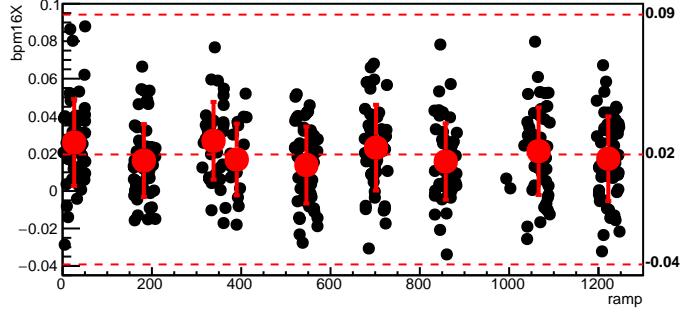
bmod Trim6,cyc=8506



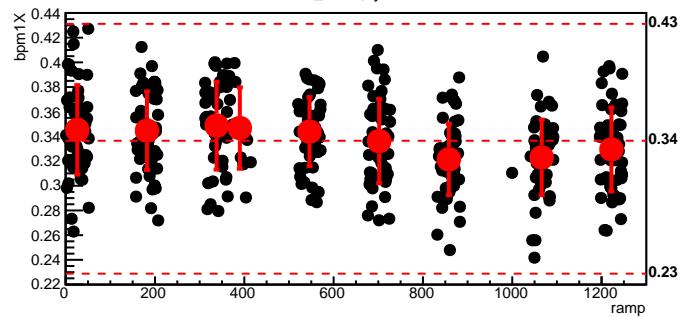
bmod Trim6,cyc=8506



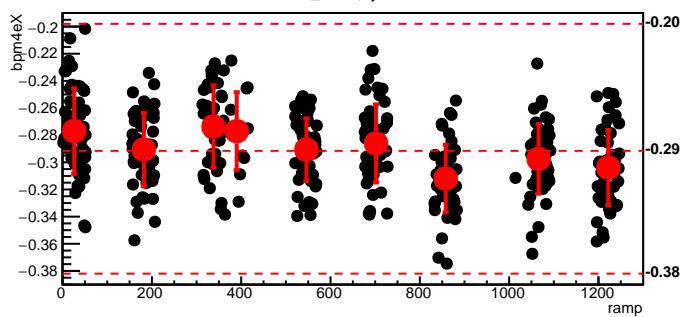
bmod Trim6,cyc=8506



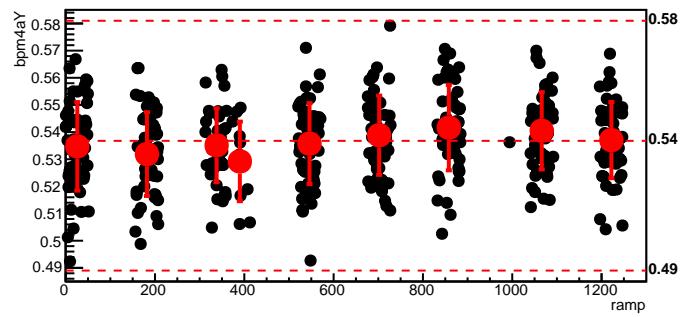
bmod Trim7,cyc=8506



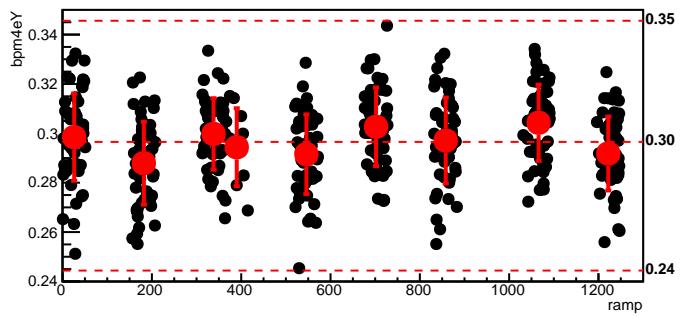
bmod Trim7,cyc=8506



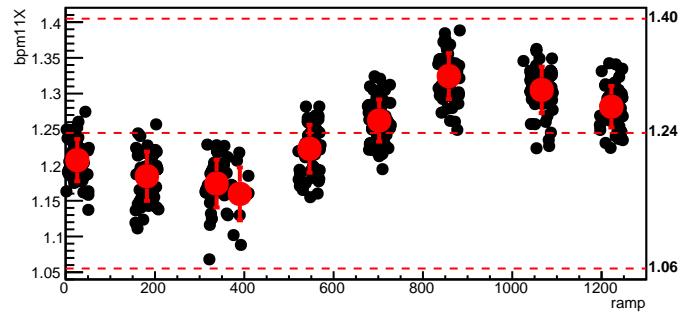
bmod Trim7,cyc=8506



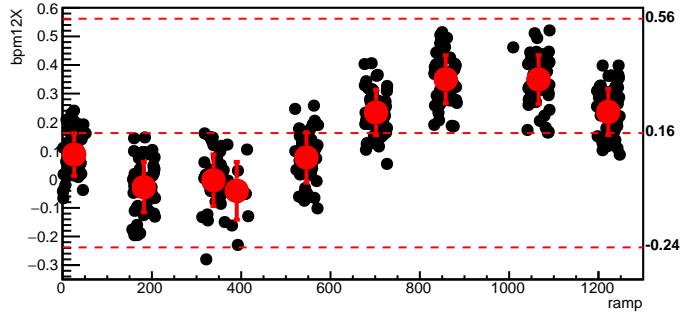
bmod Trim7,cyc=8506



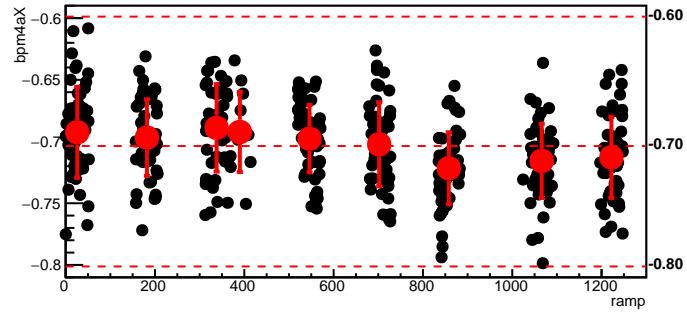
bmod Trim7,cyc=8506



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