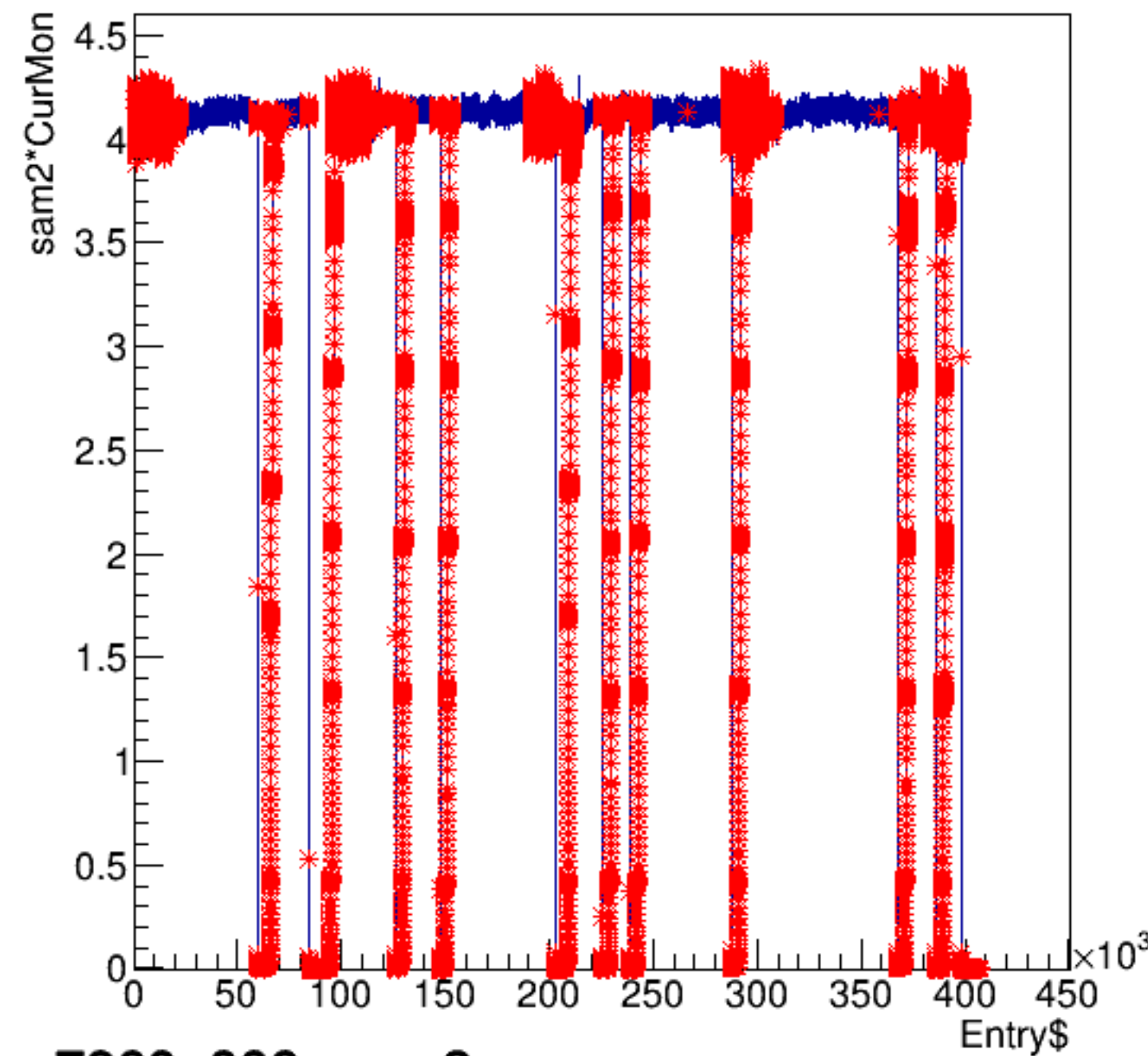
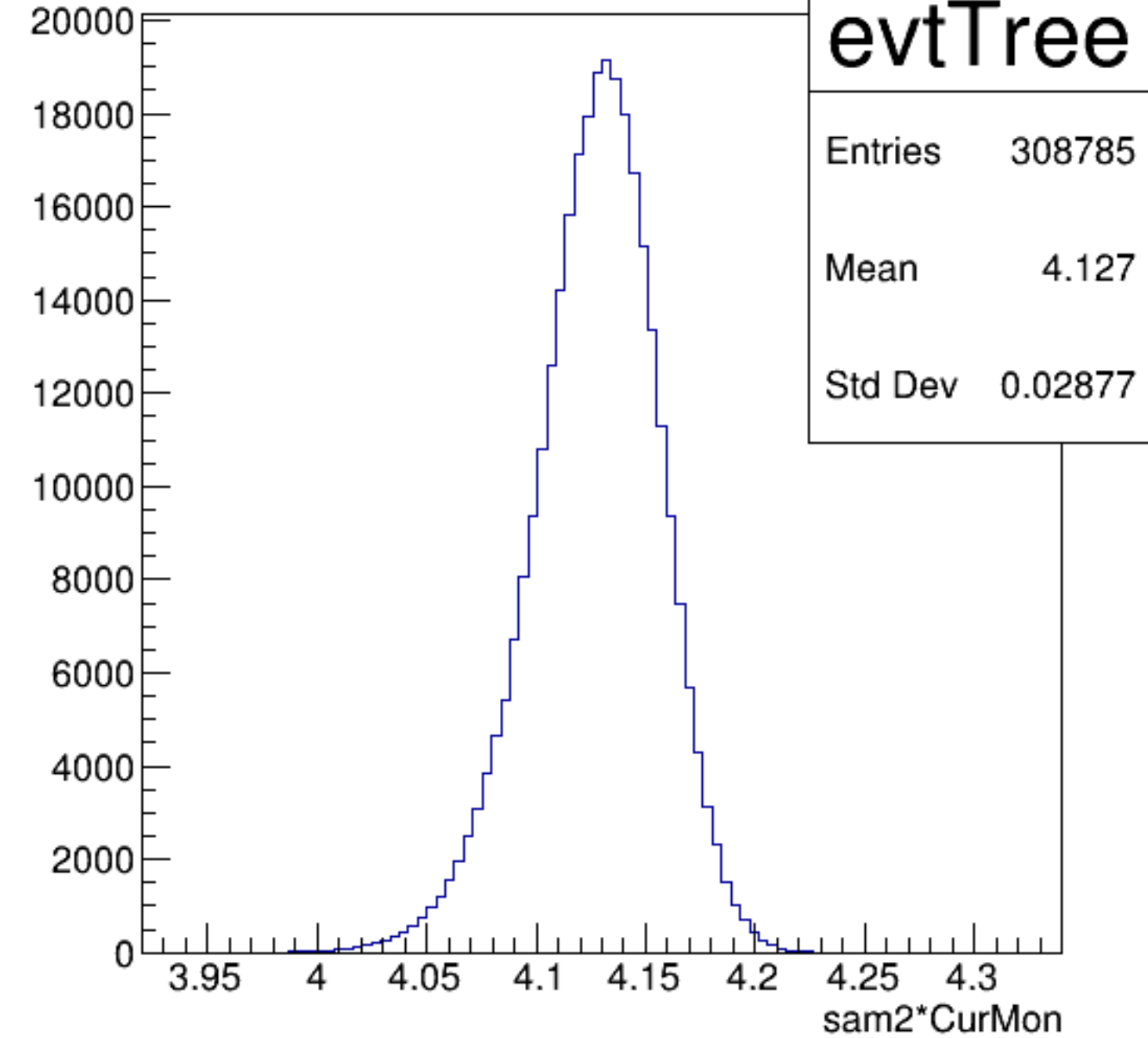


sam2*CurMon:Entry\$



run7260_000_sam2.png

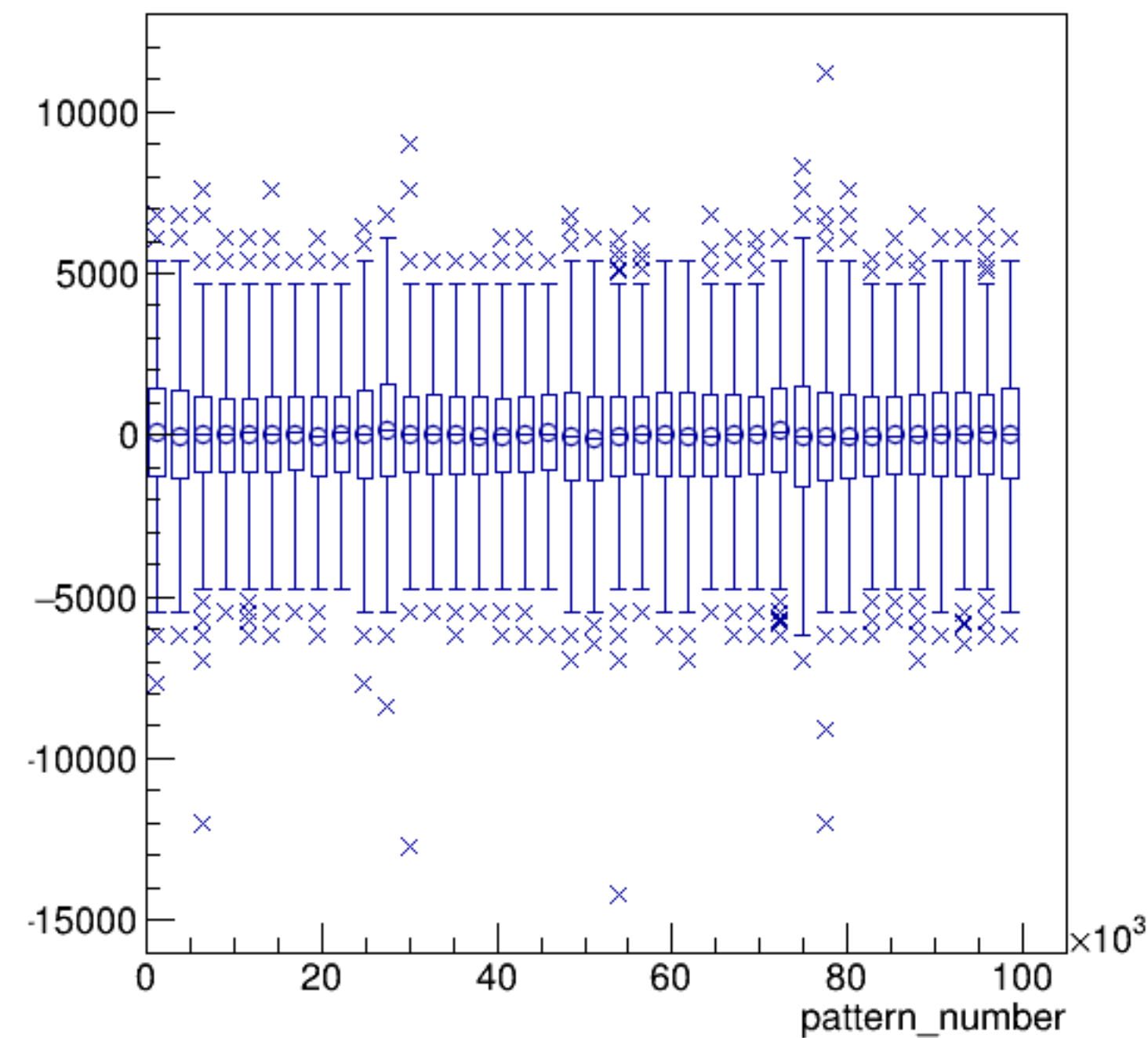
sam2*CurMon {ErrorFlag==0}



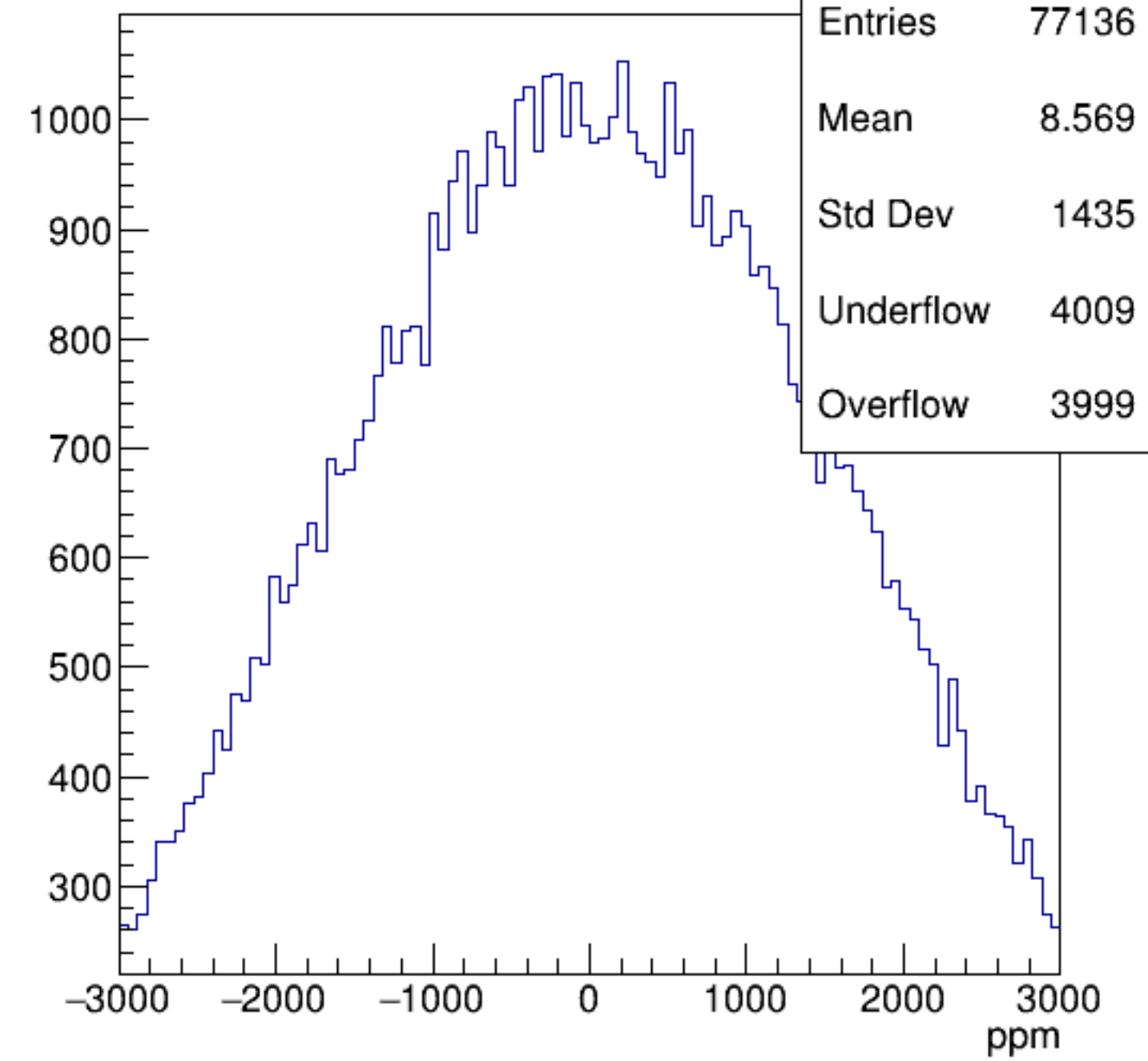
evtTree

Entries	308785
Mean	4.127
Std Dev	0.02877

asym_sam2/ppm:pattern_number {ErrorFlag==0}

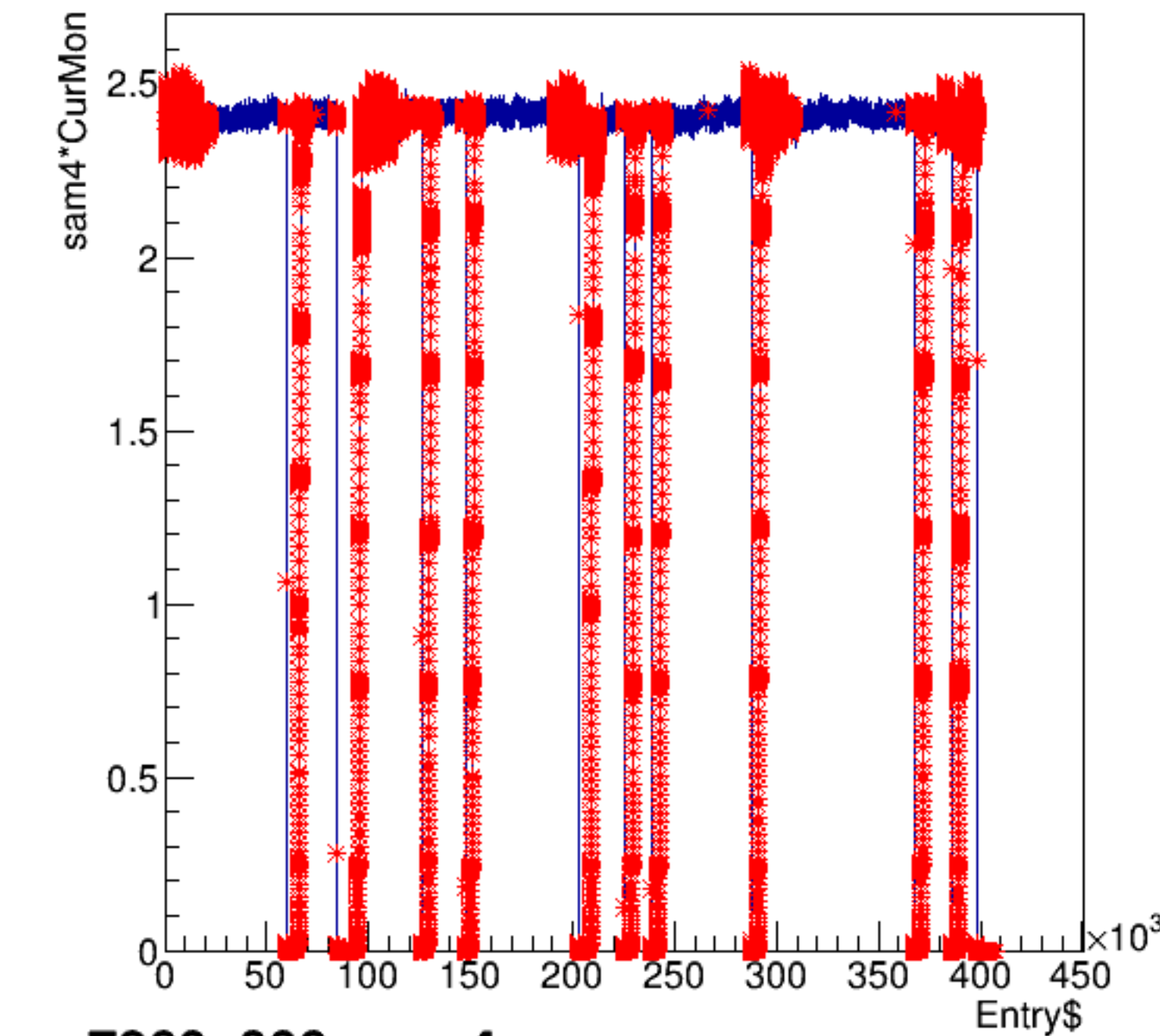


asym_sam2



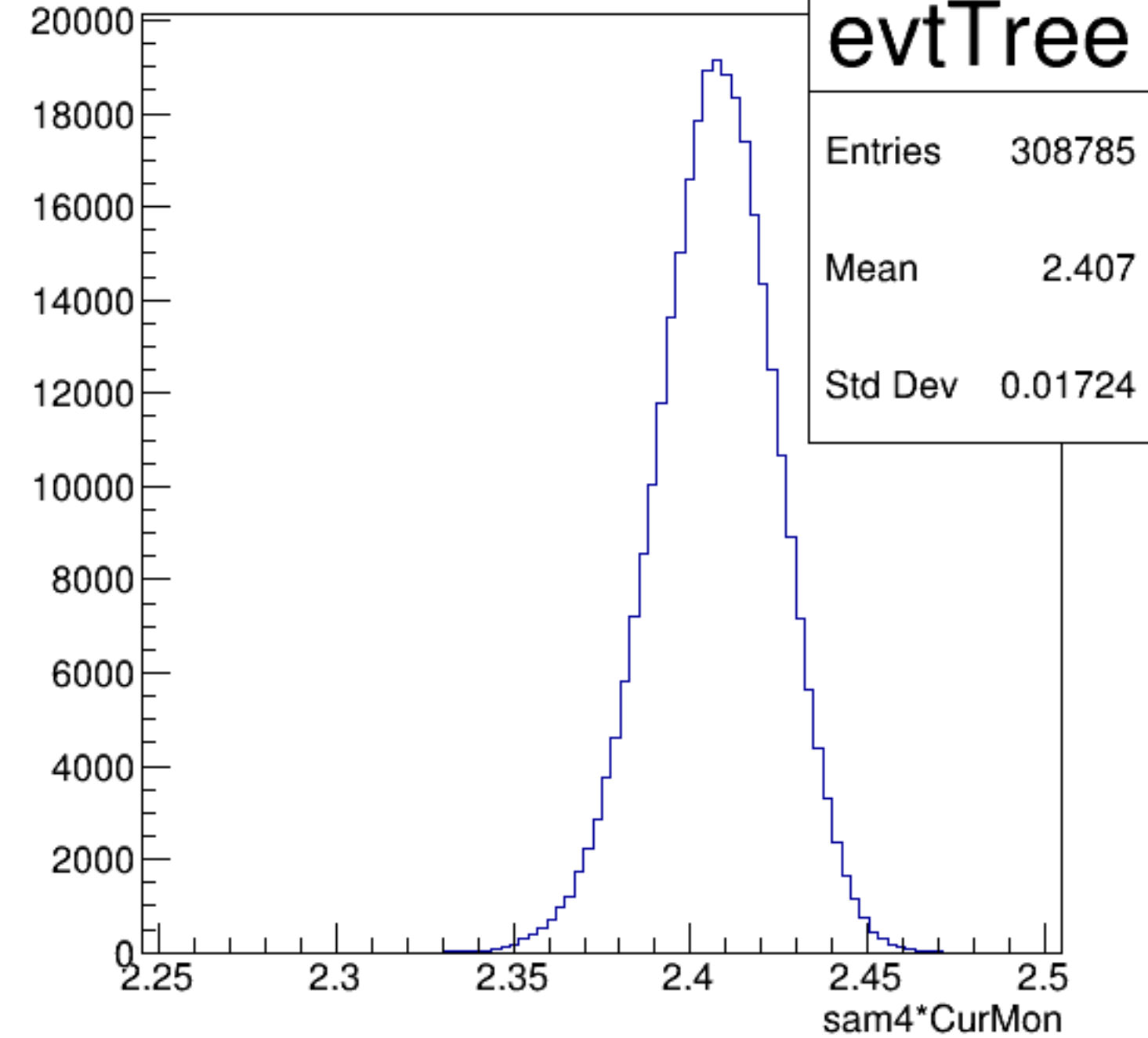
Entries	77136
Mean	8.569
Std Dev	1435
Underflow	4009
Overflow	3999

sam4*CurMon:Entry\$

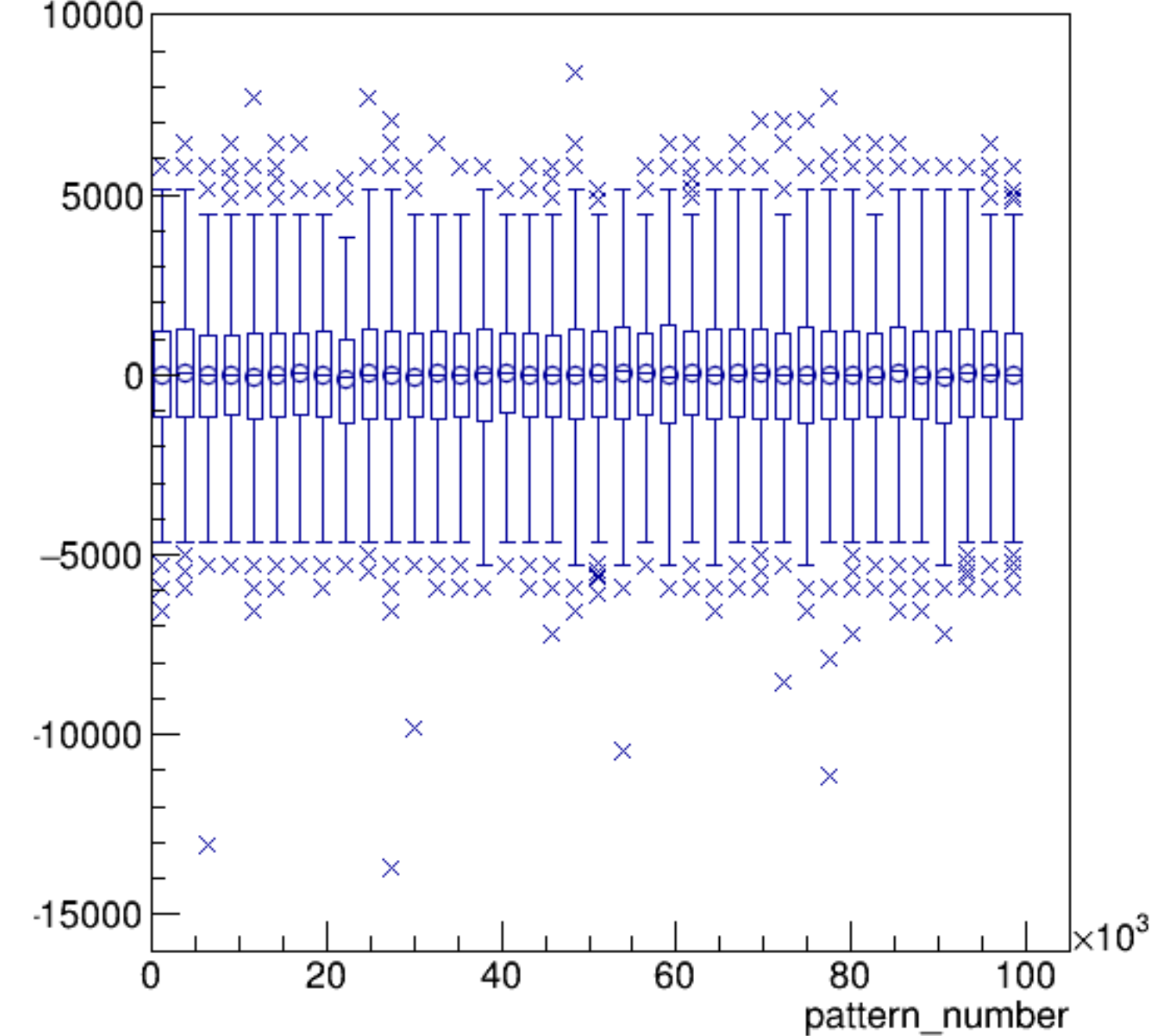


run7260_000_sam4.png

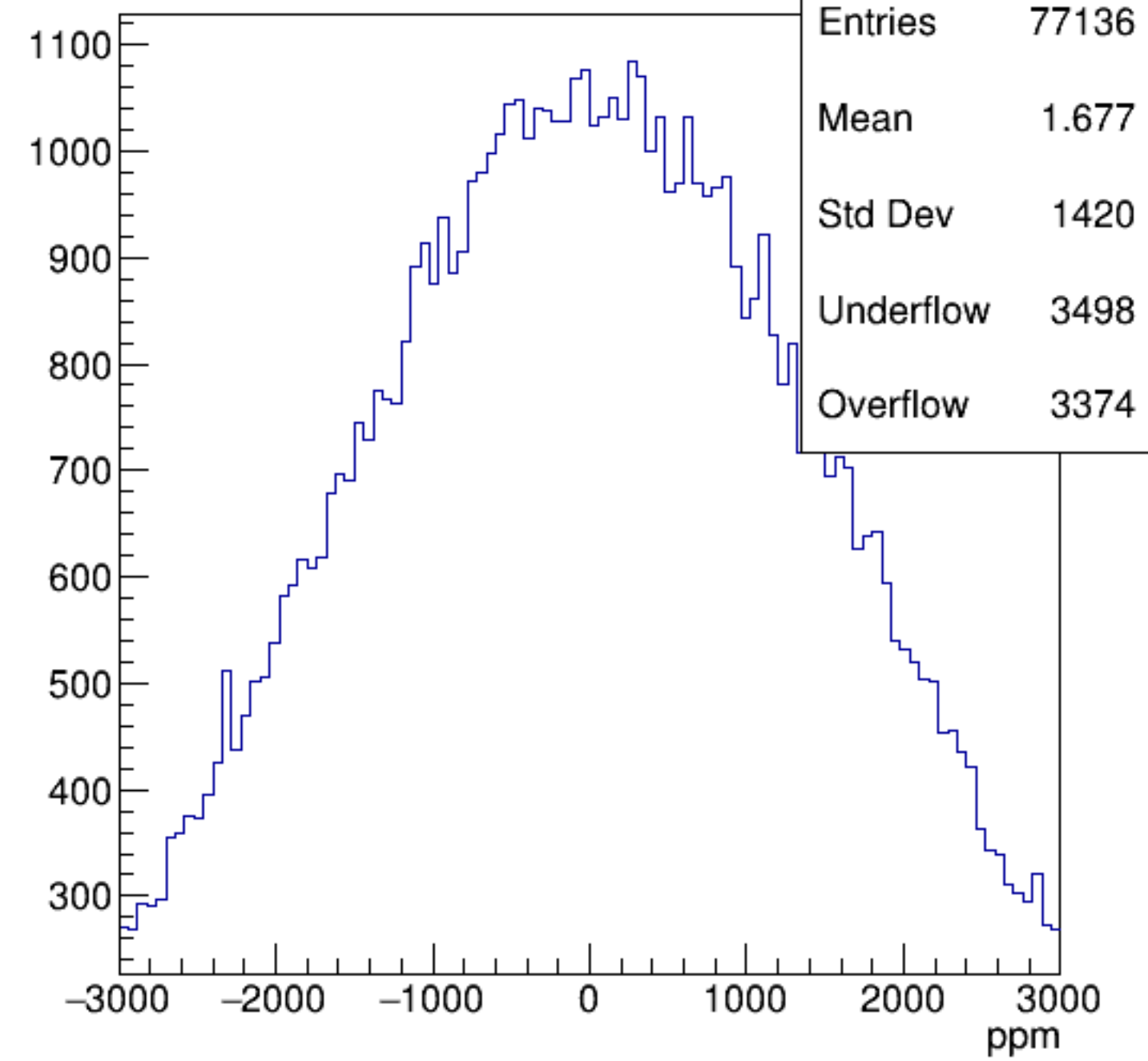
sam4*CurMon {ErrorFlag==0}



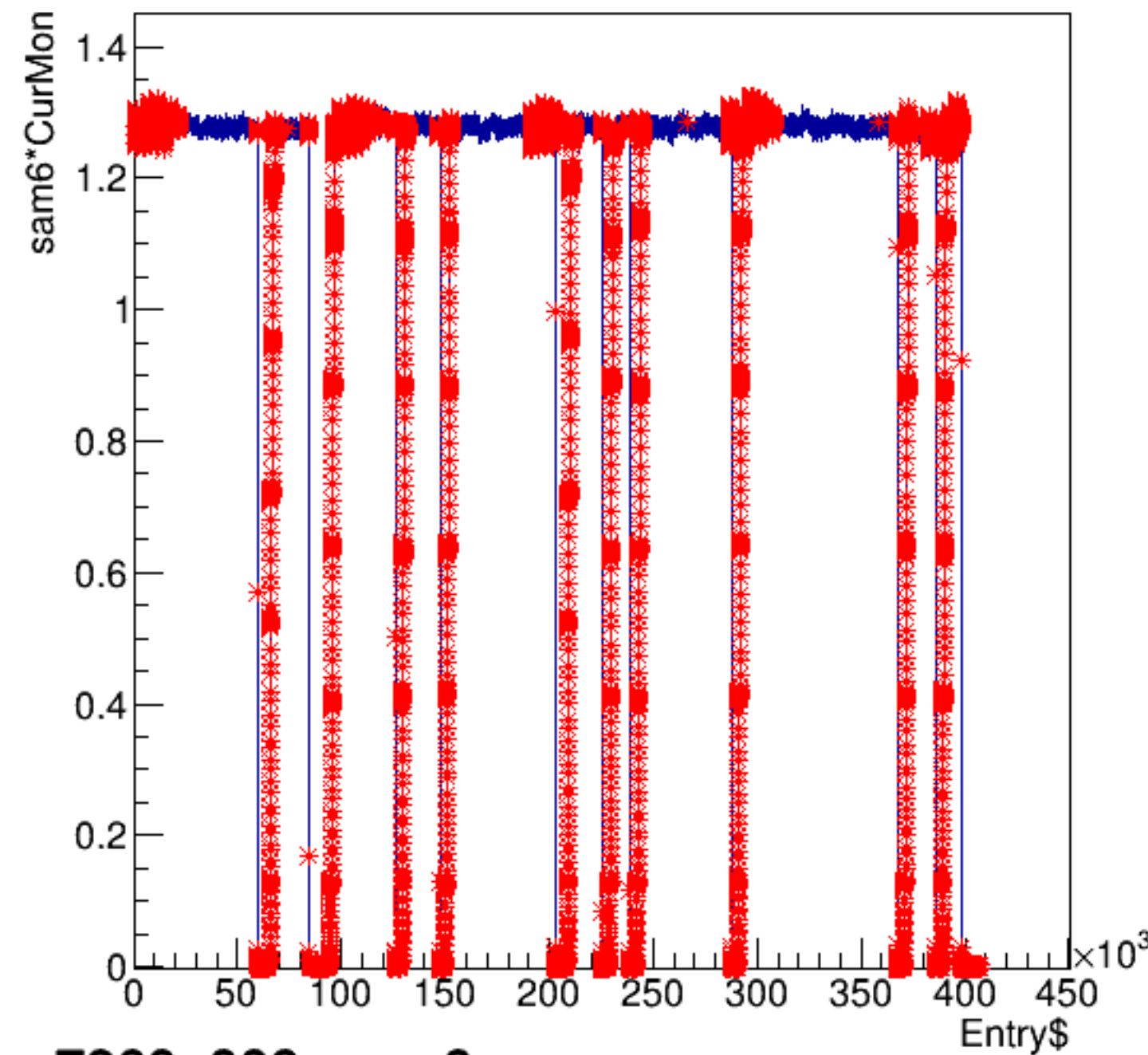
asym_sam4/ppm:pattern_number {ErrorFlag==0}



asym_sam4

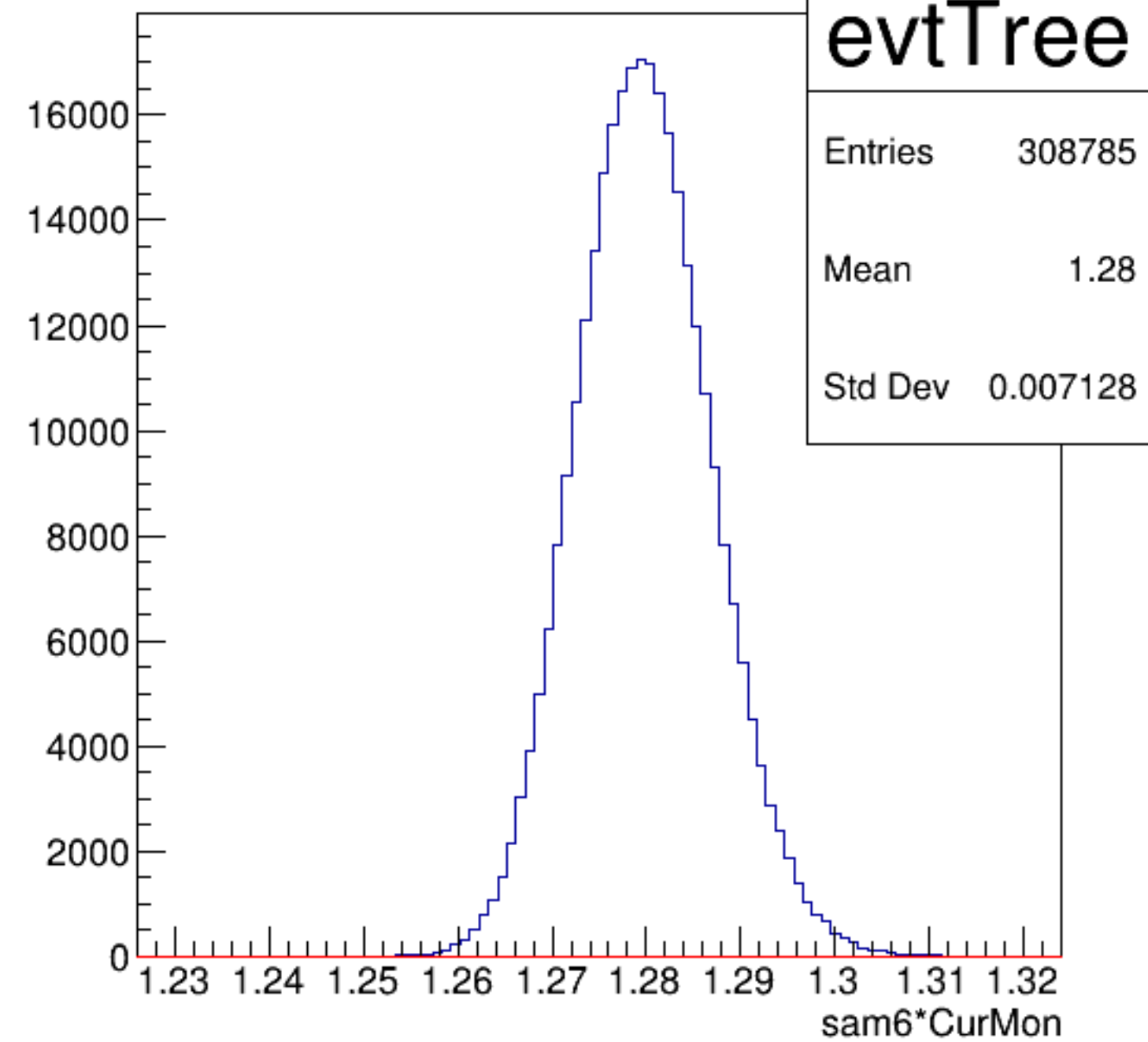


sam6*CurMon:Entry\$

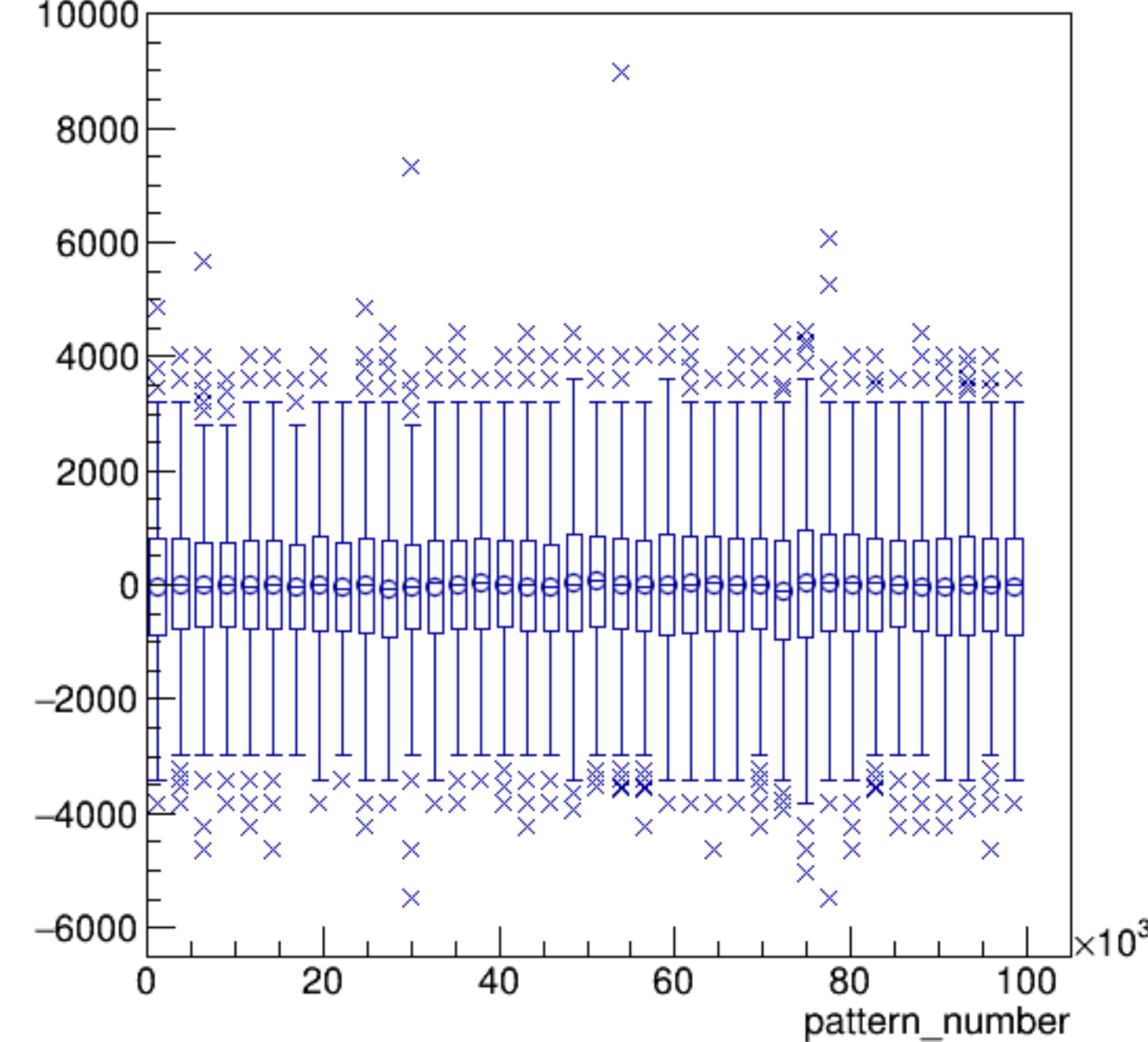


run7260_000_sam6.png

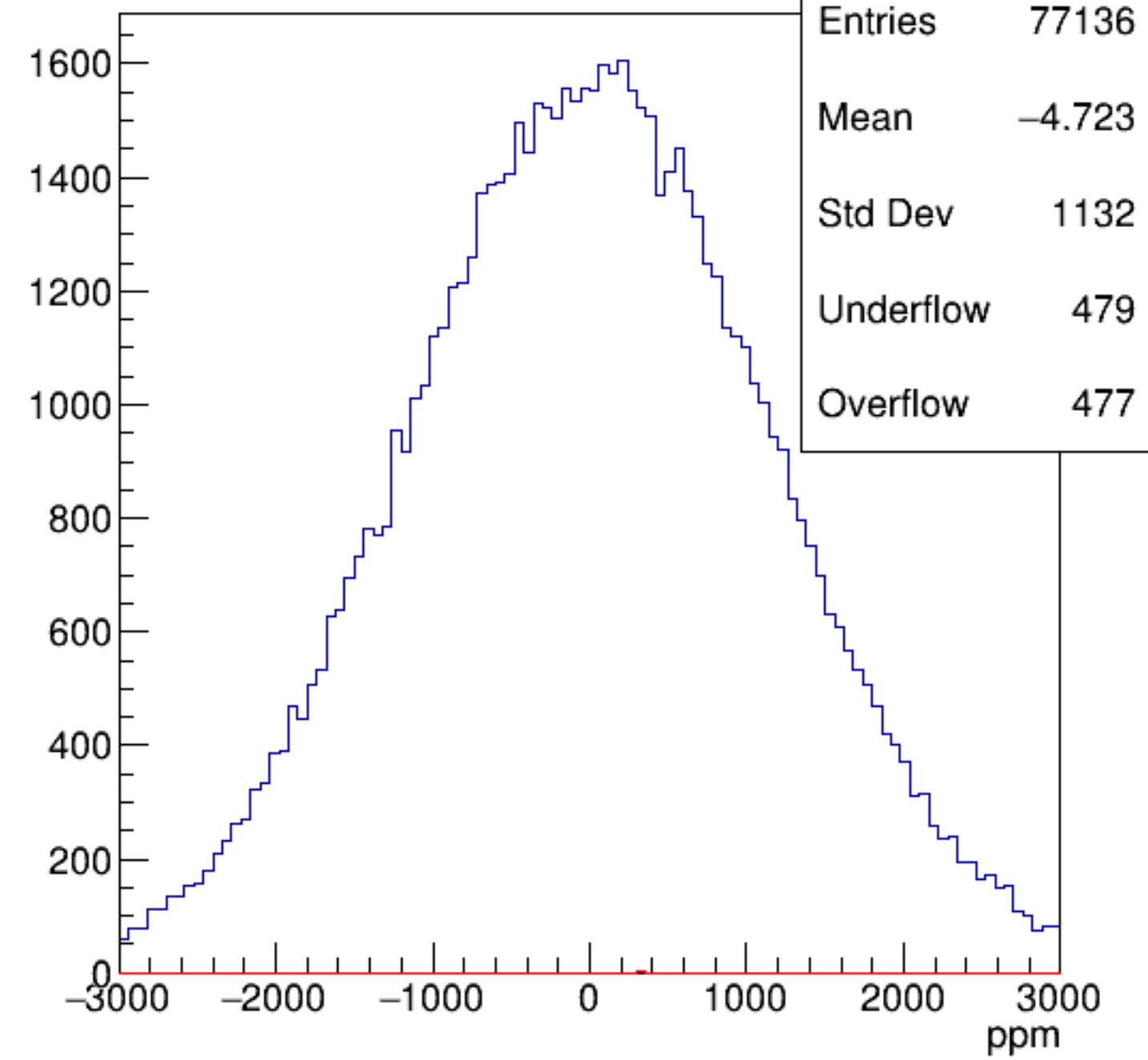
sam6*CurMon {ErrorFlag==0}



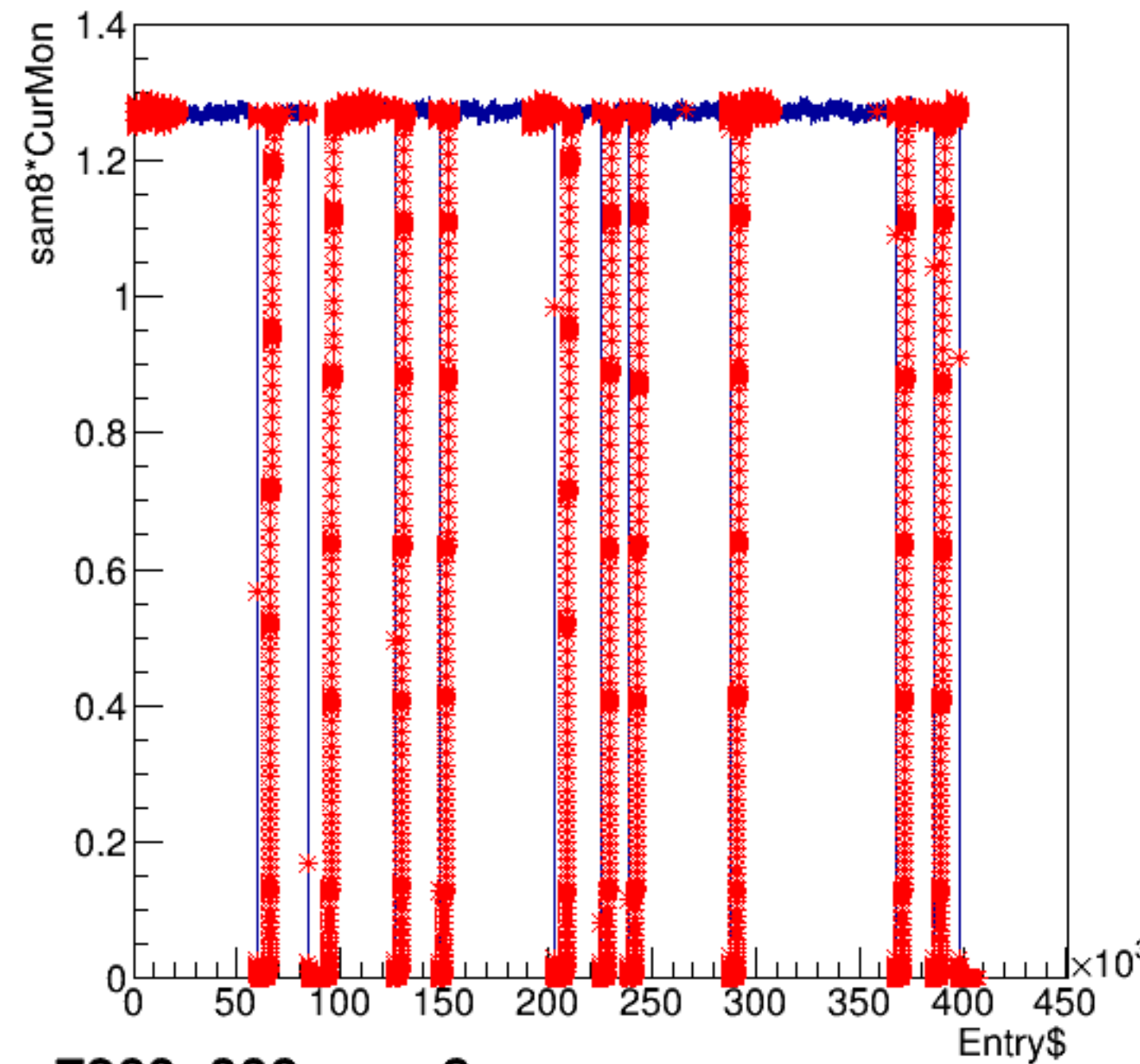
asym_sam6/ppm:pattern_number {ErrorFlag==0}



asym_sam6

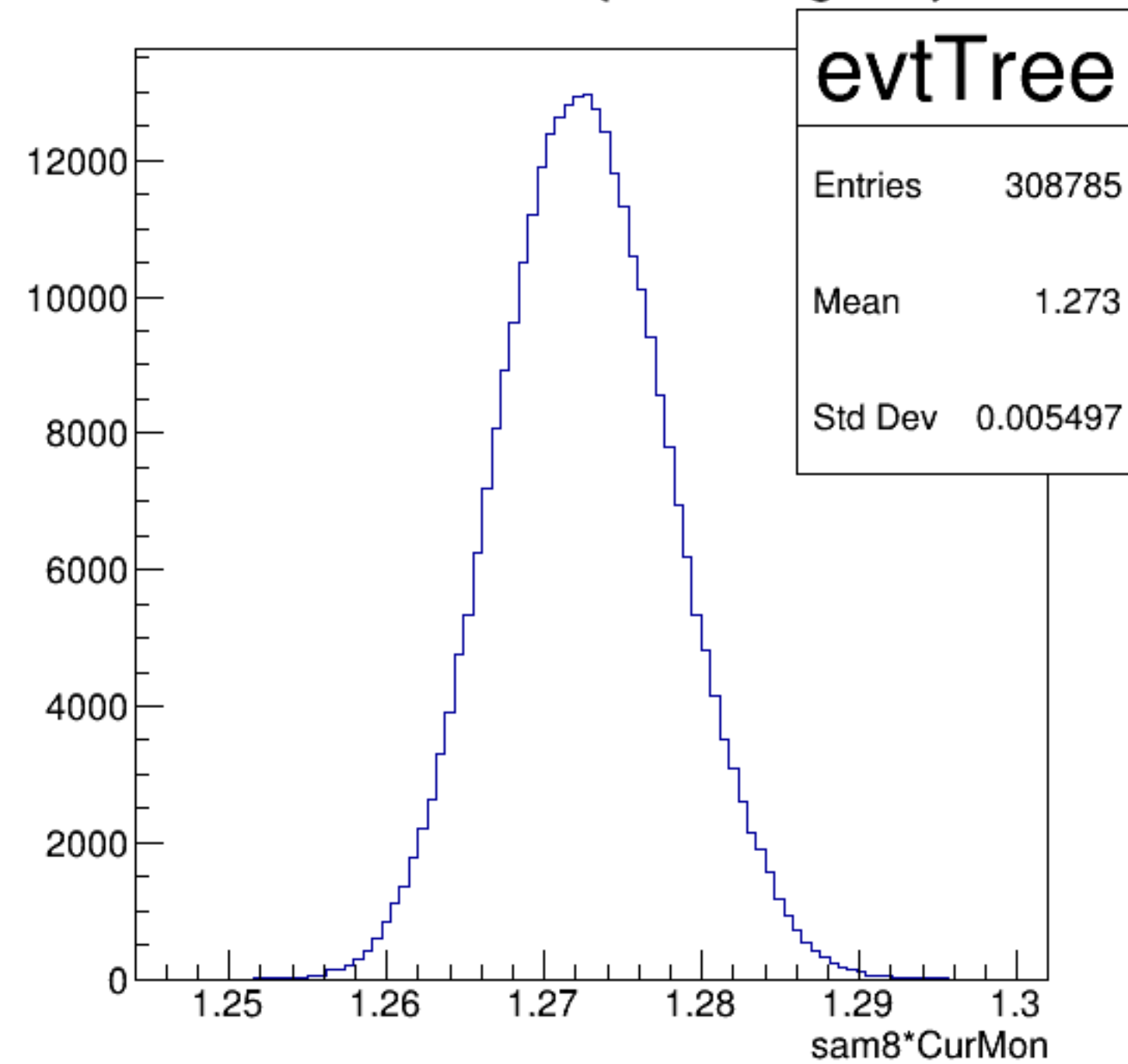


sam8*CurMon:Entry\$

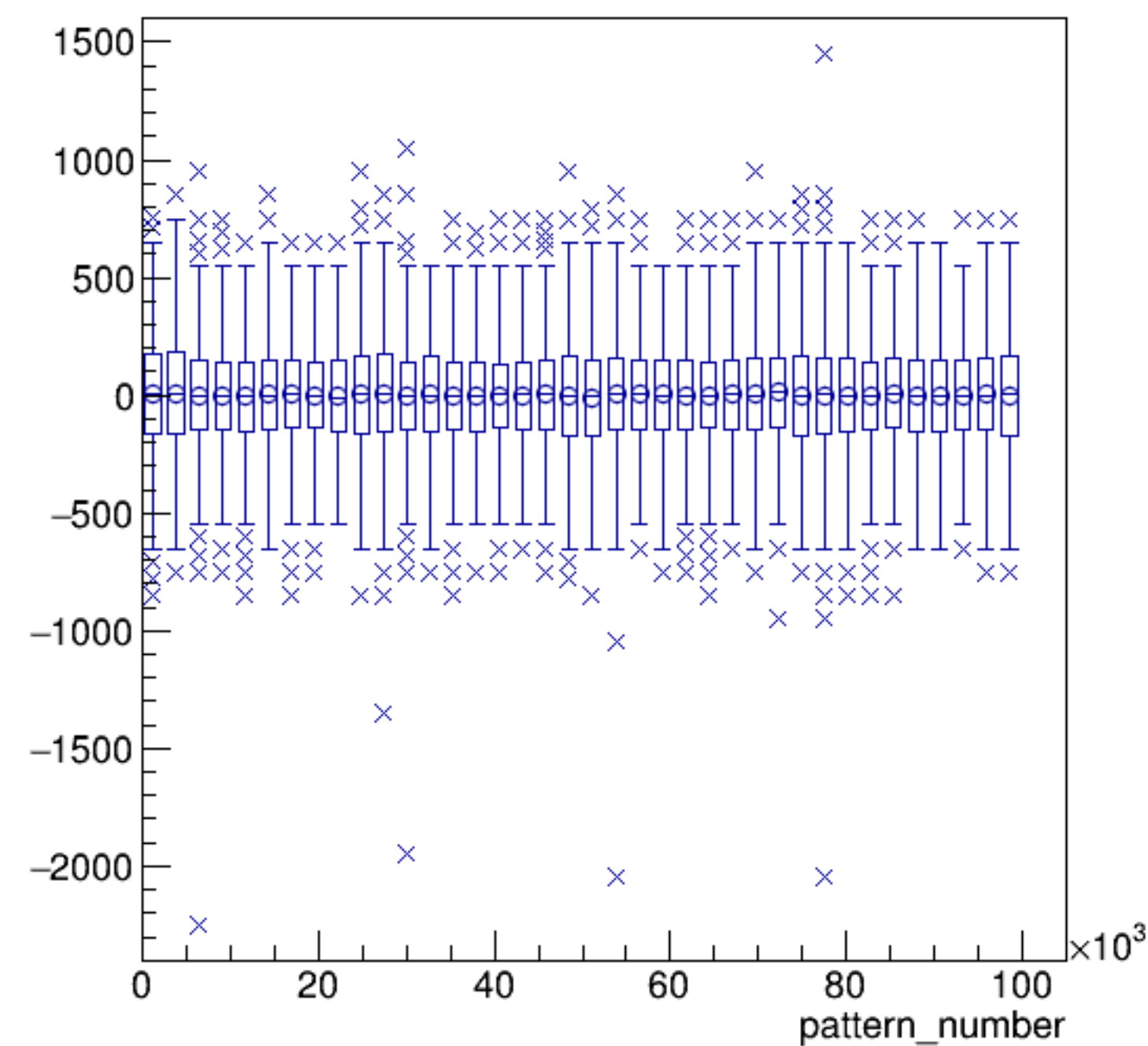


run7260_000_sam8.png

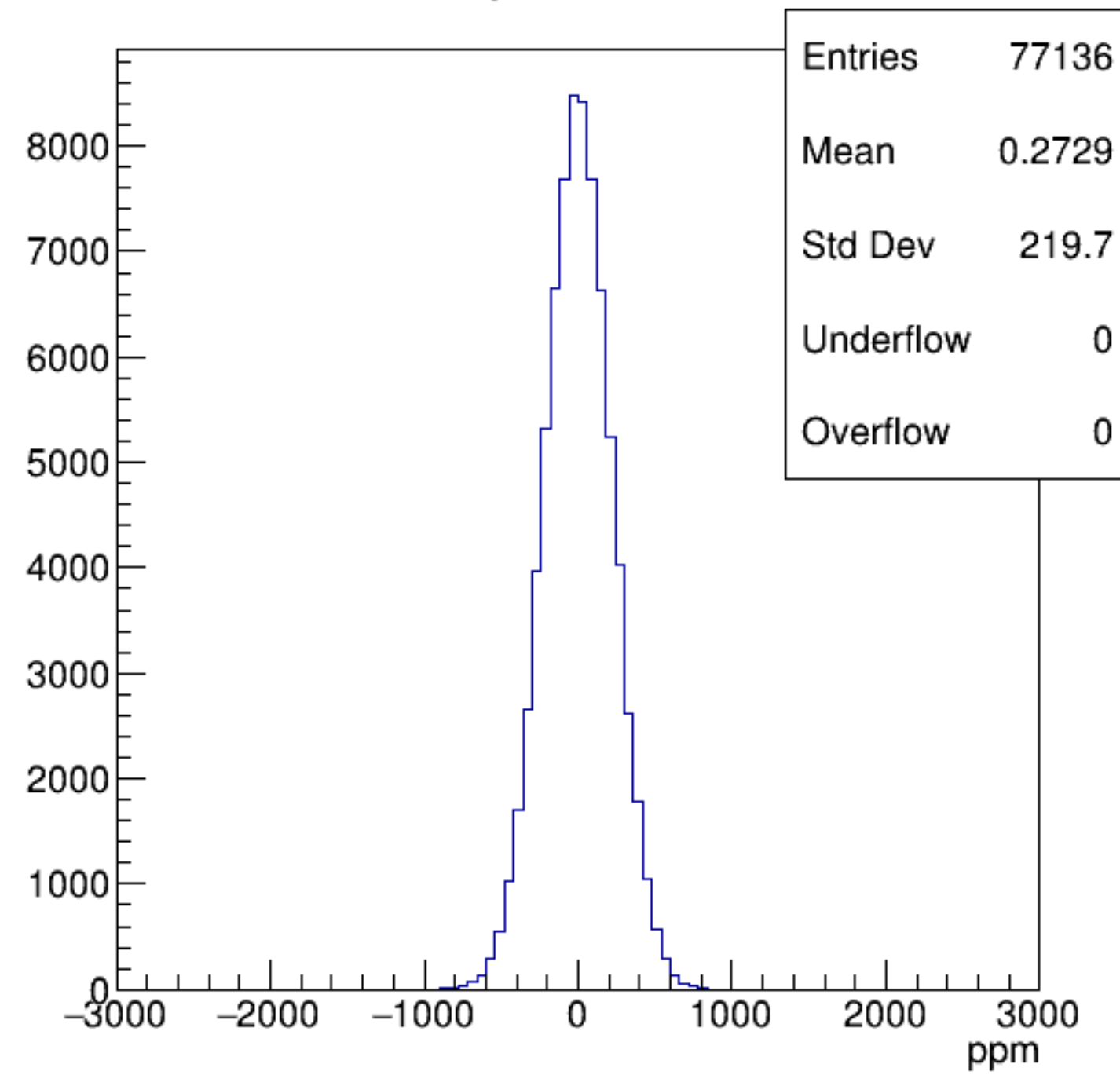
sam8*CurMon {ErrorFlag==0}



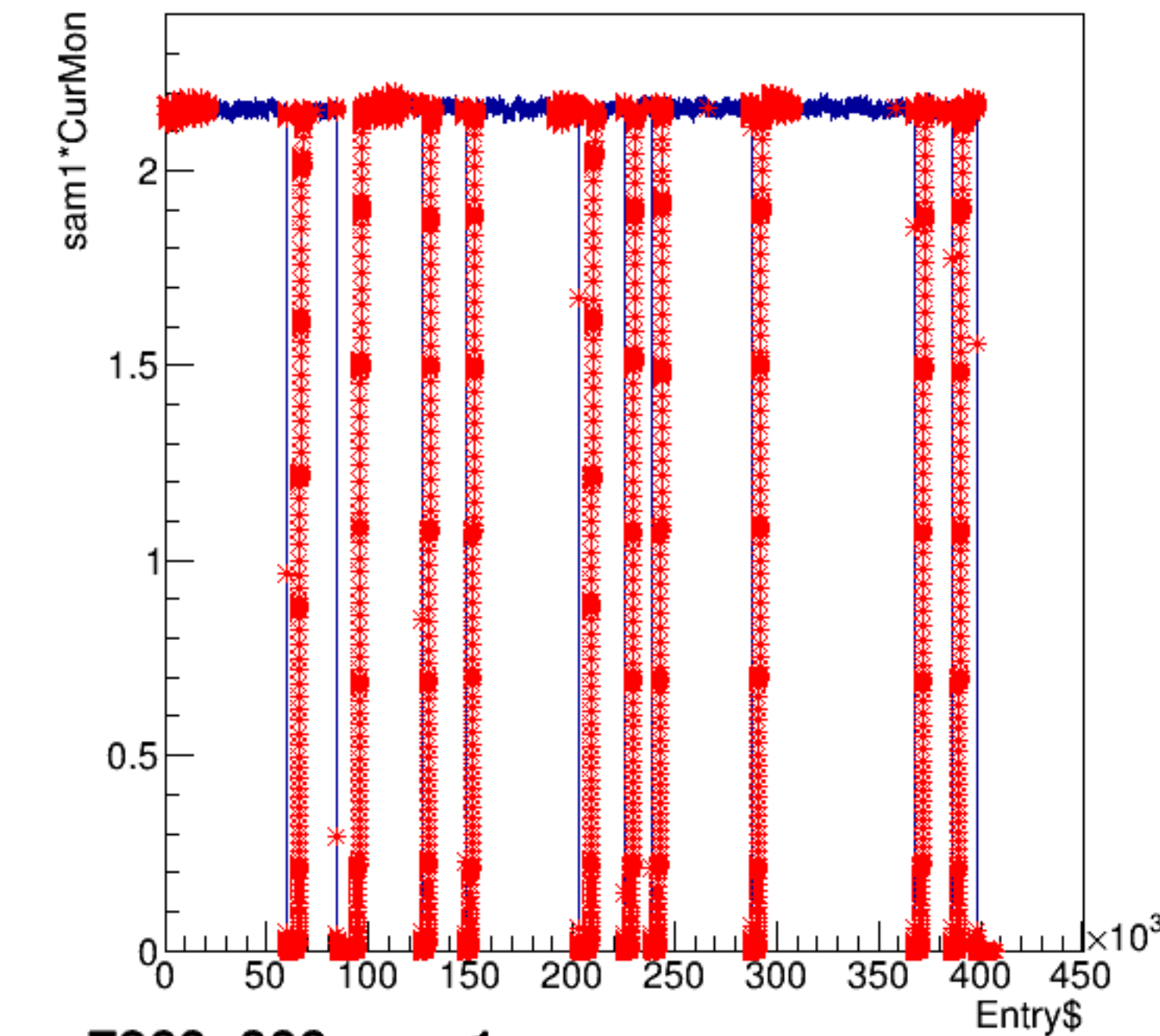
asym_sam8/ppm:pattern_number {ErrorFlag==0}



asym_sam8

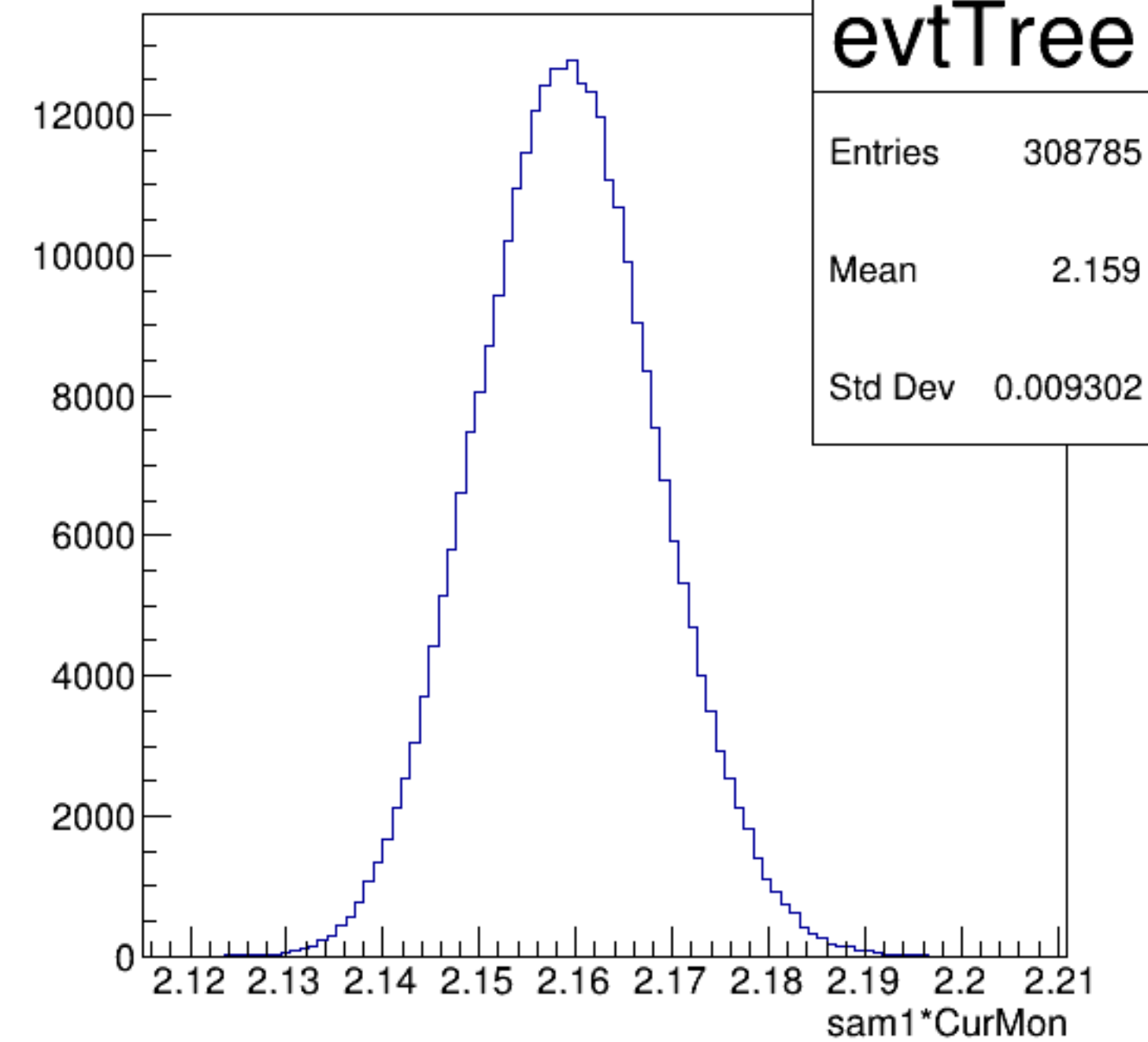


sam1*CurMon:Entry\$



run7260_000_sam1.png

sam1*CurMon {ErrorFlag==0}



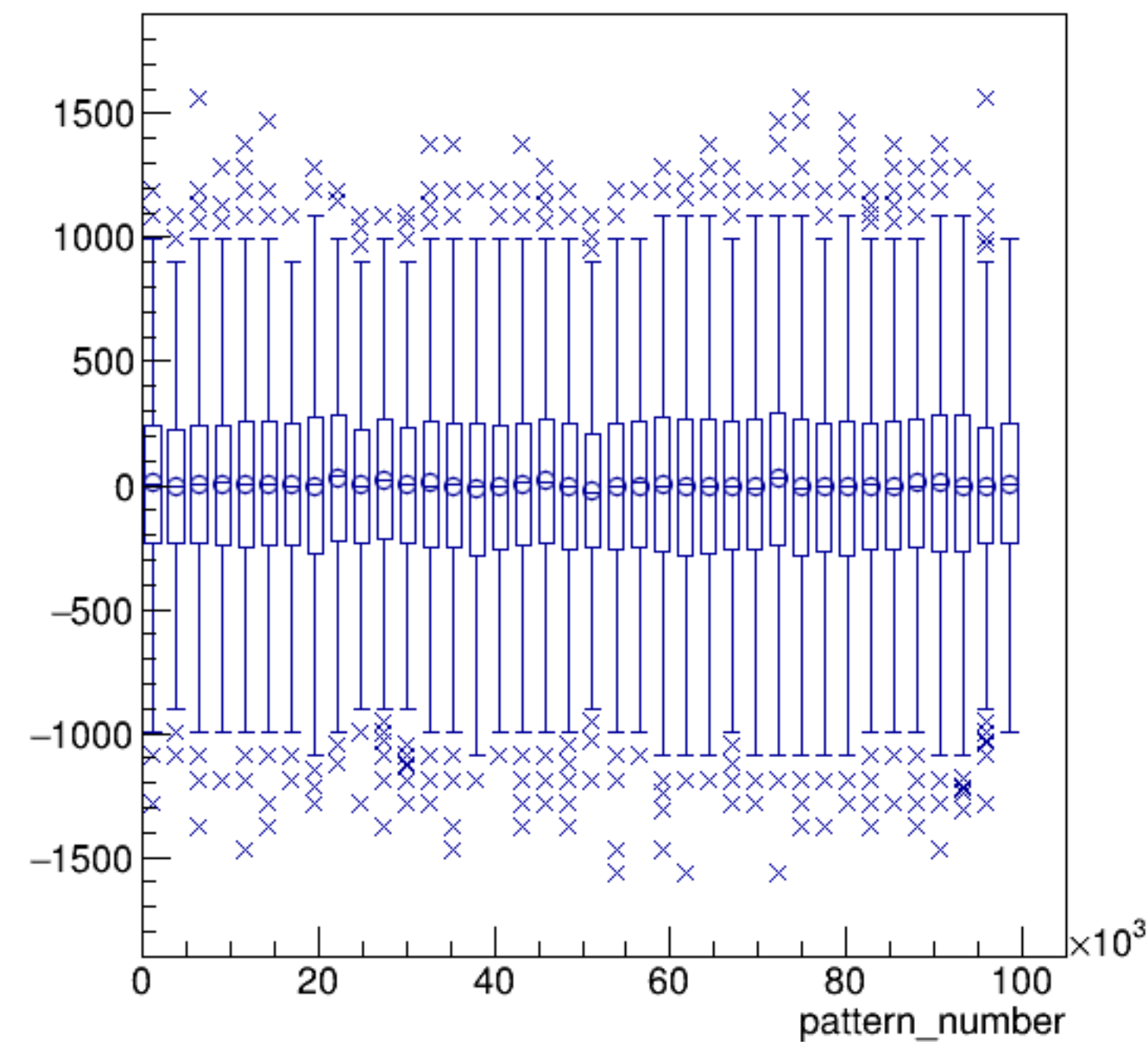
evtTree

Entries 308785

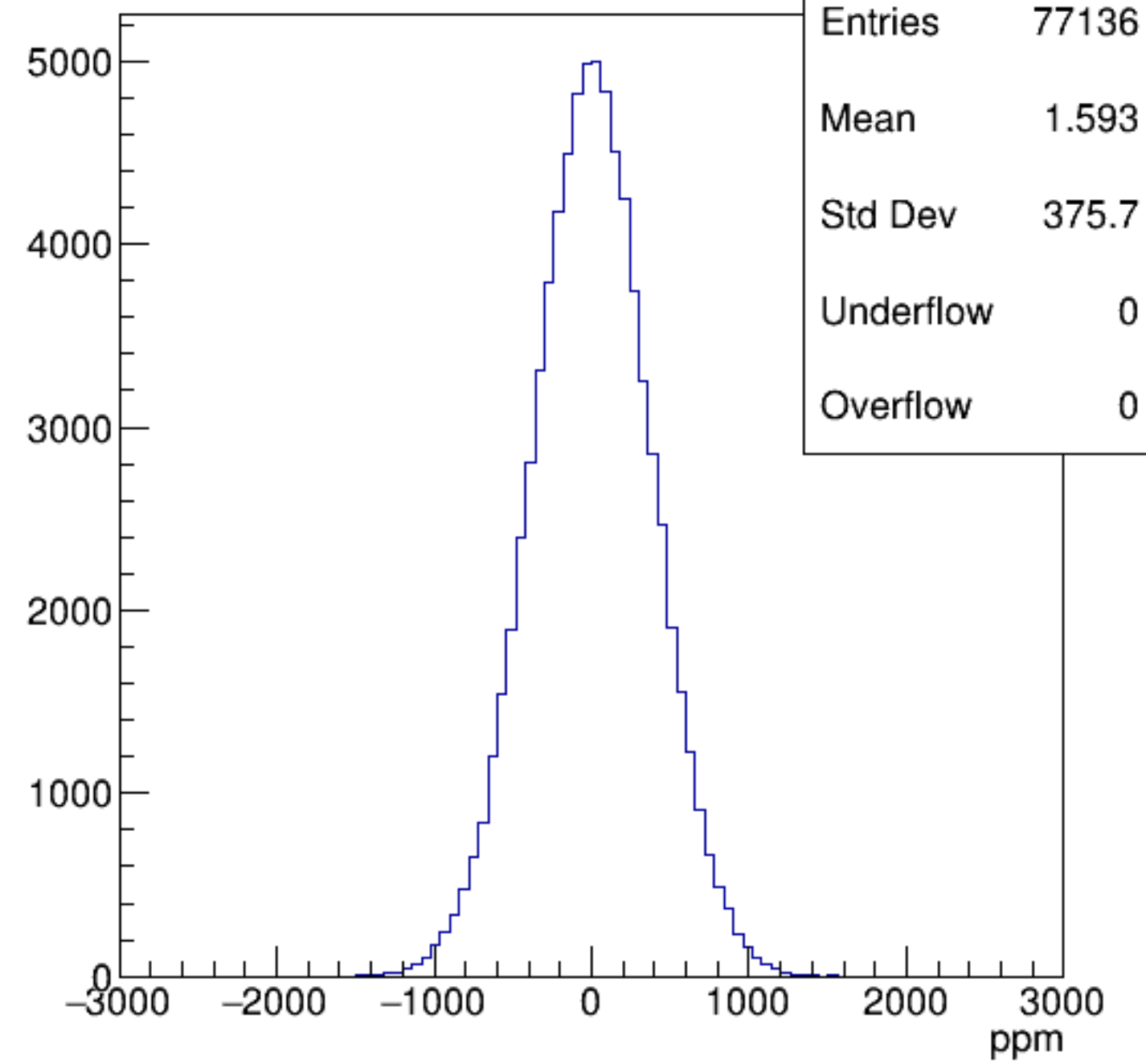
Mean 2.159

Std Dev 0.009302

asym_sam1/ppm:pattern_number {ErrorFlag==0}



asym_sam1



Entries 77136

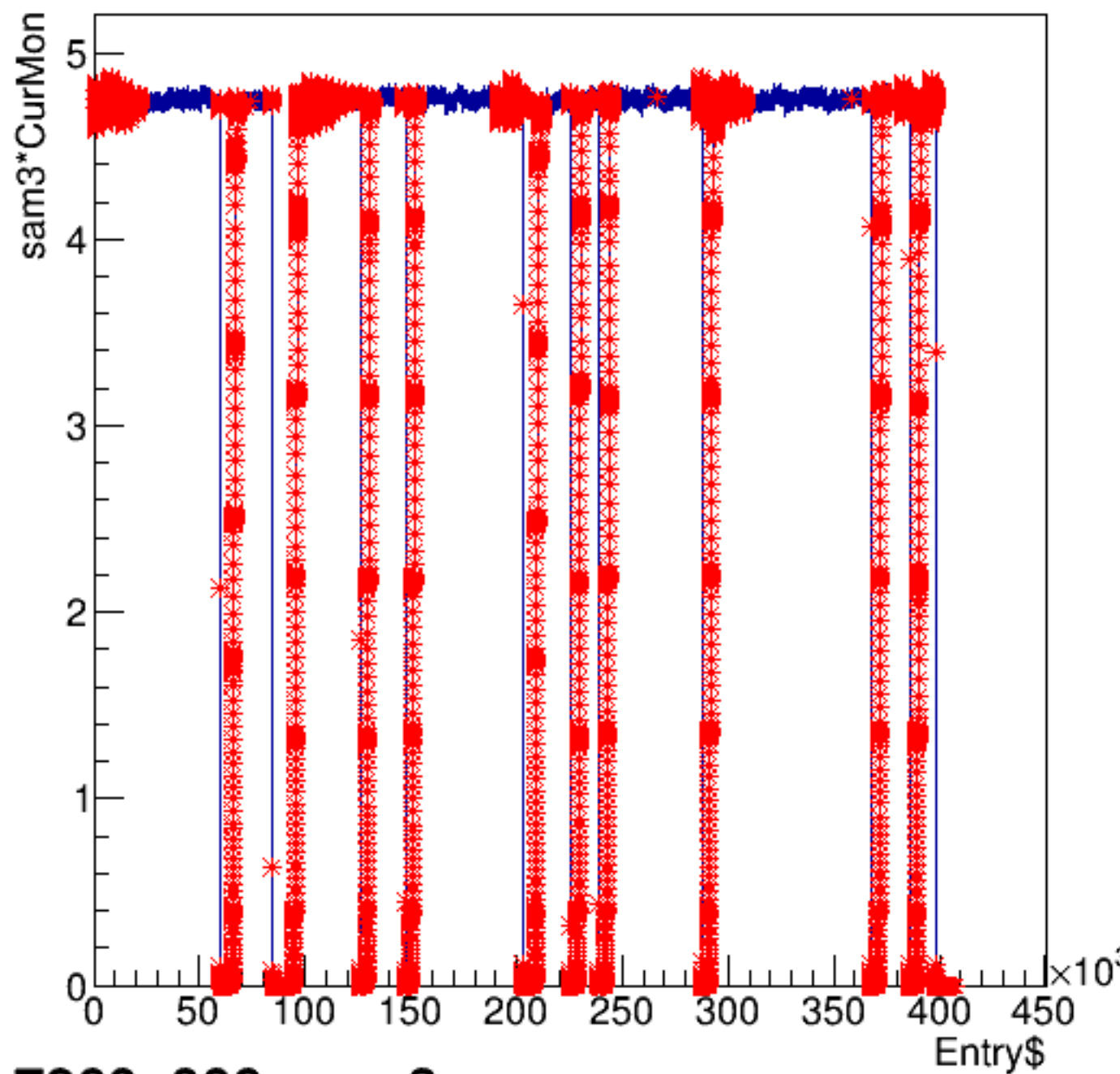
Mean 1.593

Std Dev 375.7

Underflow 0

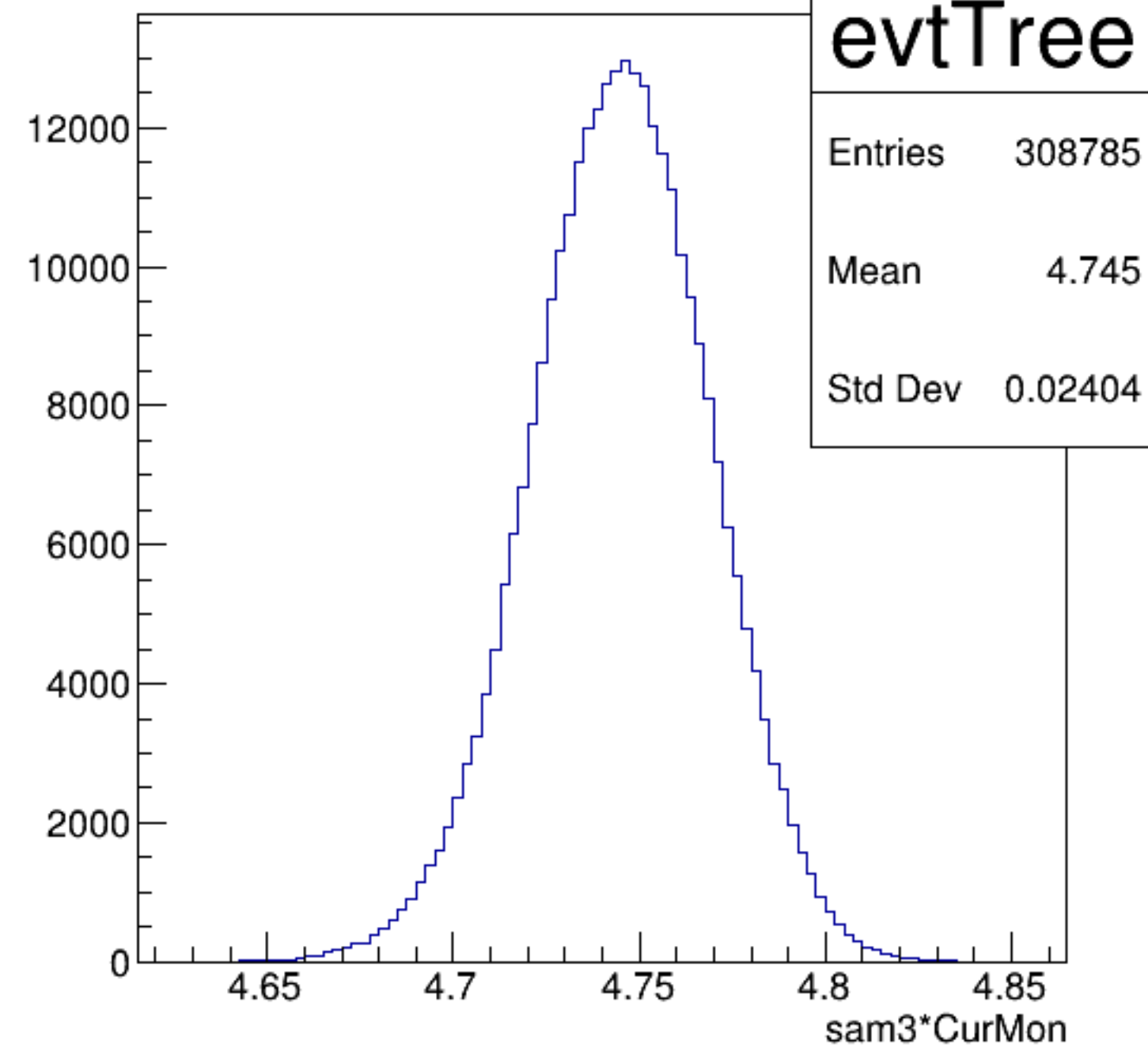
Overflow 0

sam3*CurMon:Entry\$



run7260_000_sam3.png

sam3*CurMon {ErrorFlag==0}



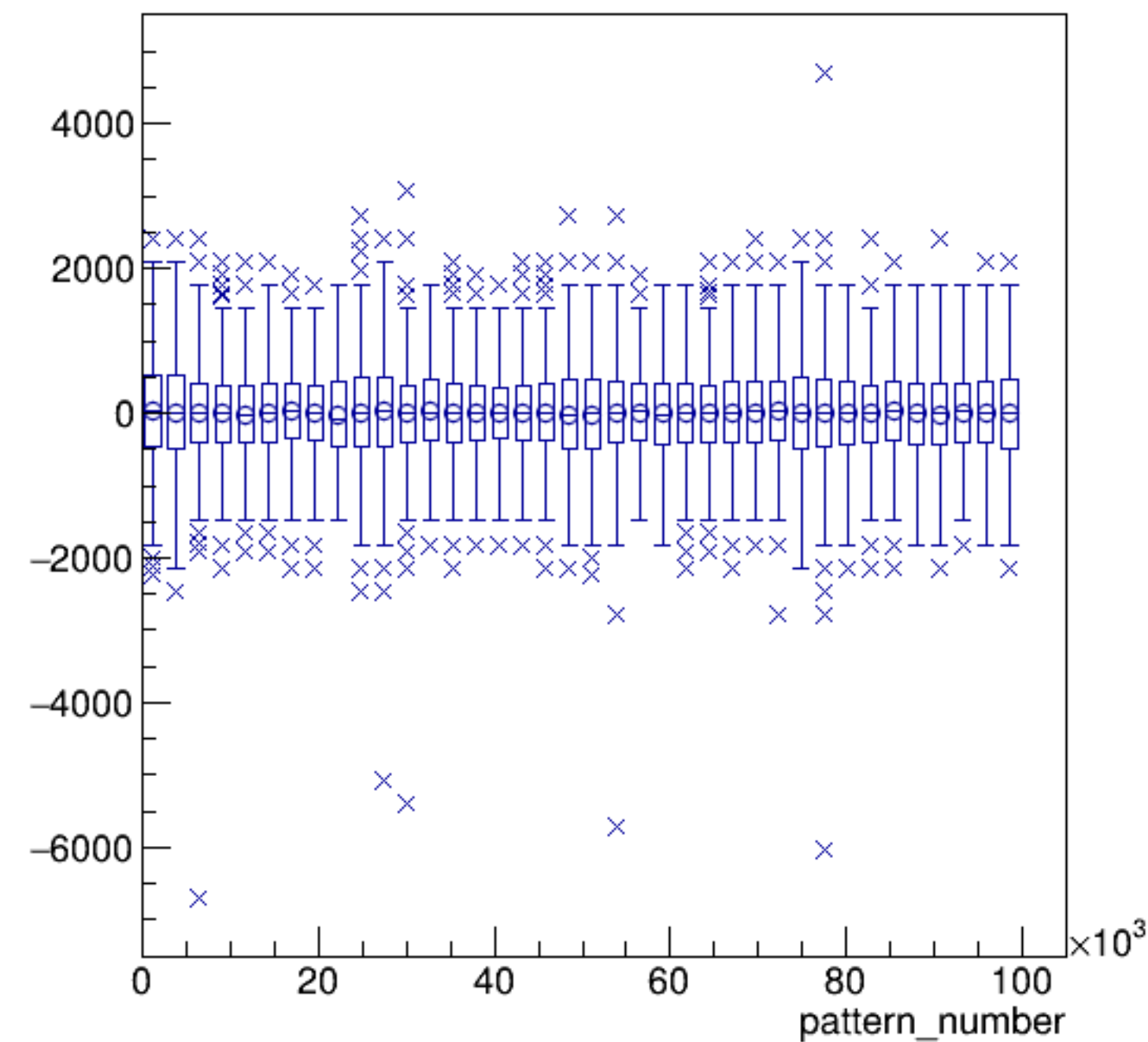
evtTree

Entries 308785

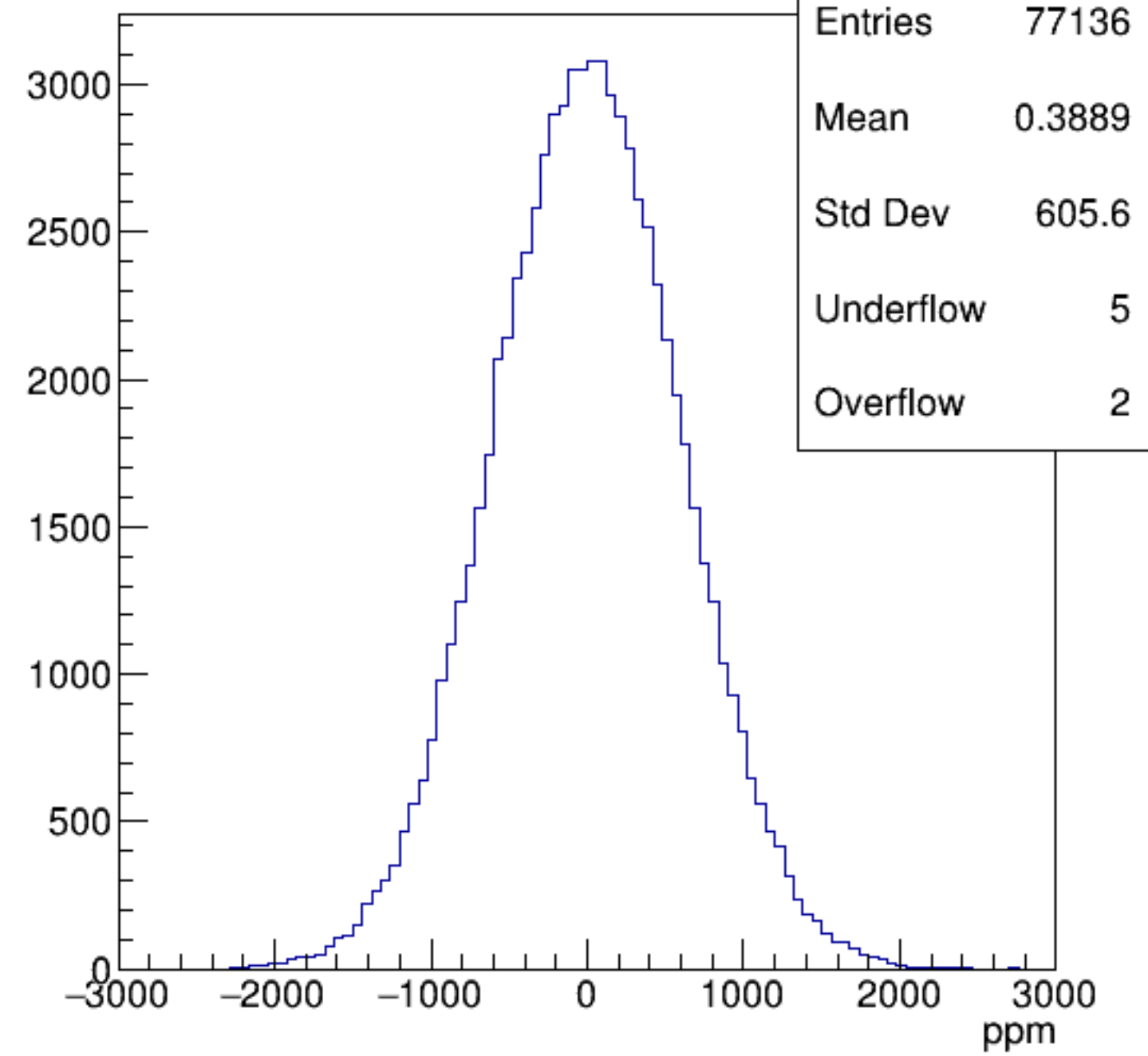
Mean 4.745

Std Dev 0.02404

asym_sam3/ppm:pattern_number {ErrorFlag==0}

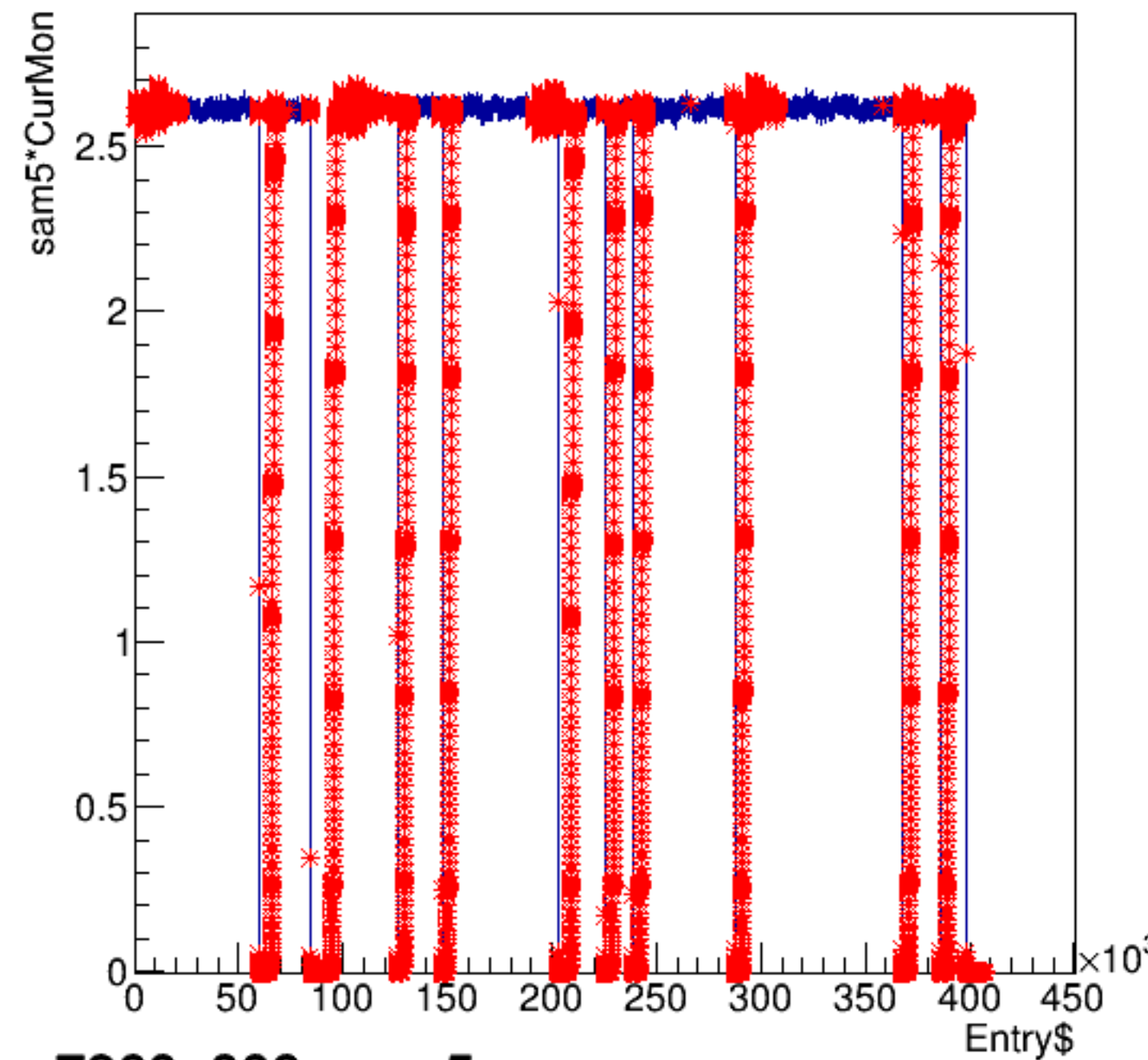


asym_sam3



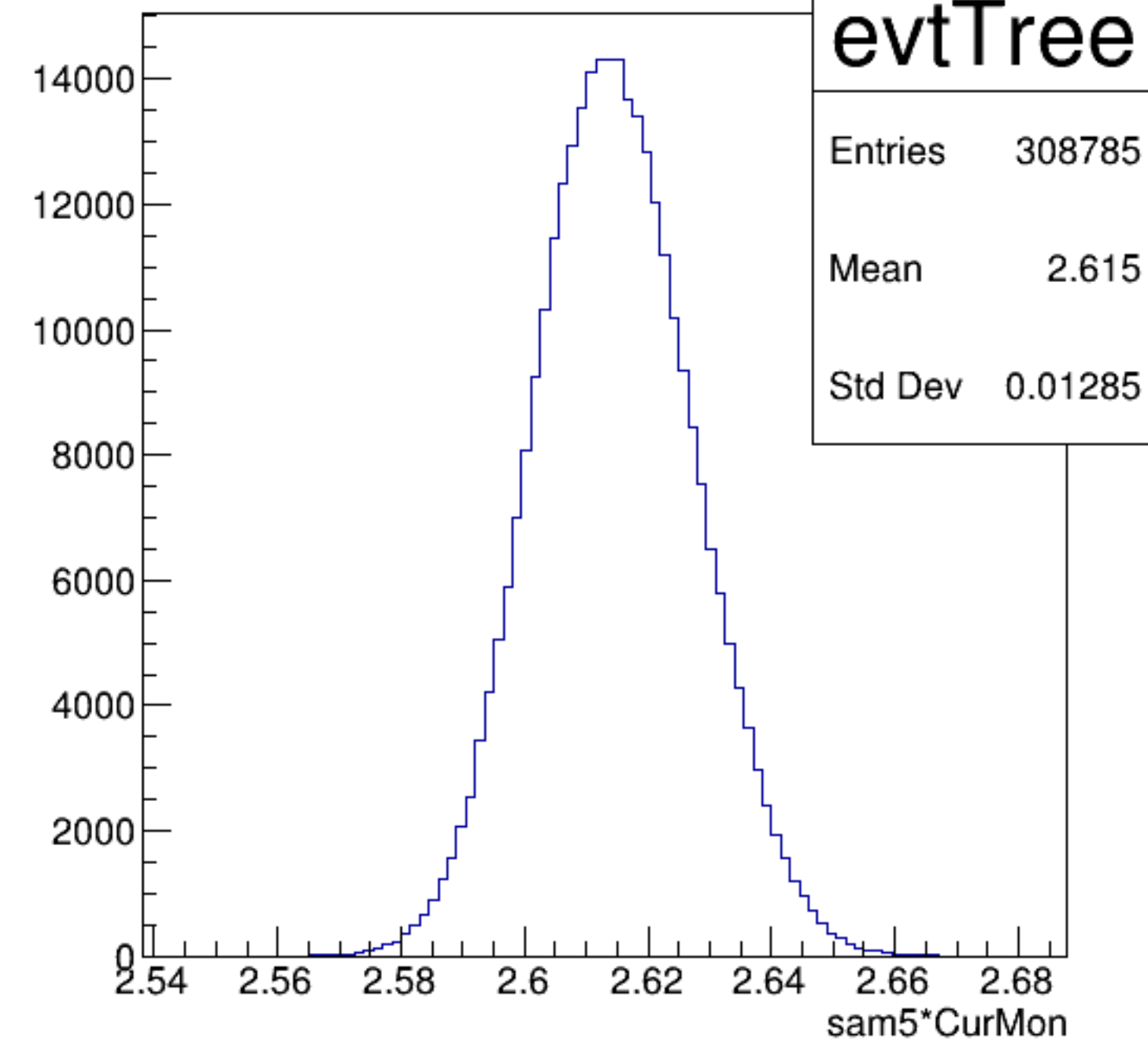
Entries	77136
Mean	0.3889
Std Dev	605.6
Underflow	5
Overflow	2

sam5*CurMon:Entry\$

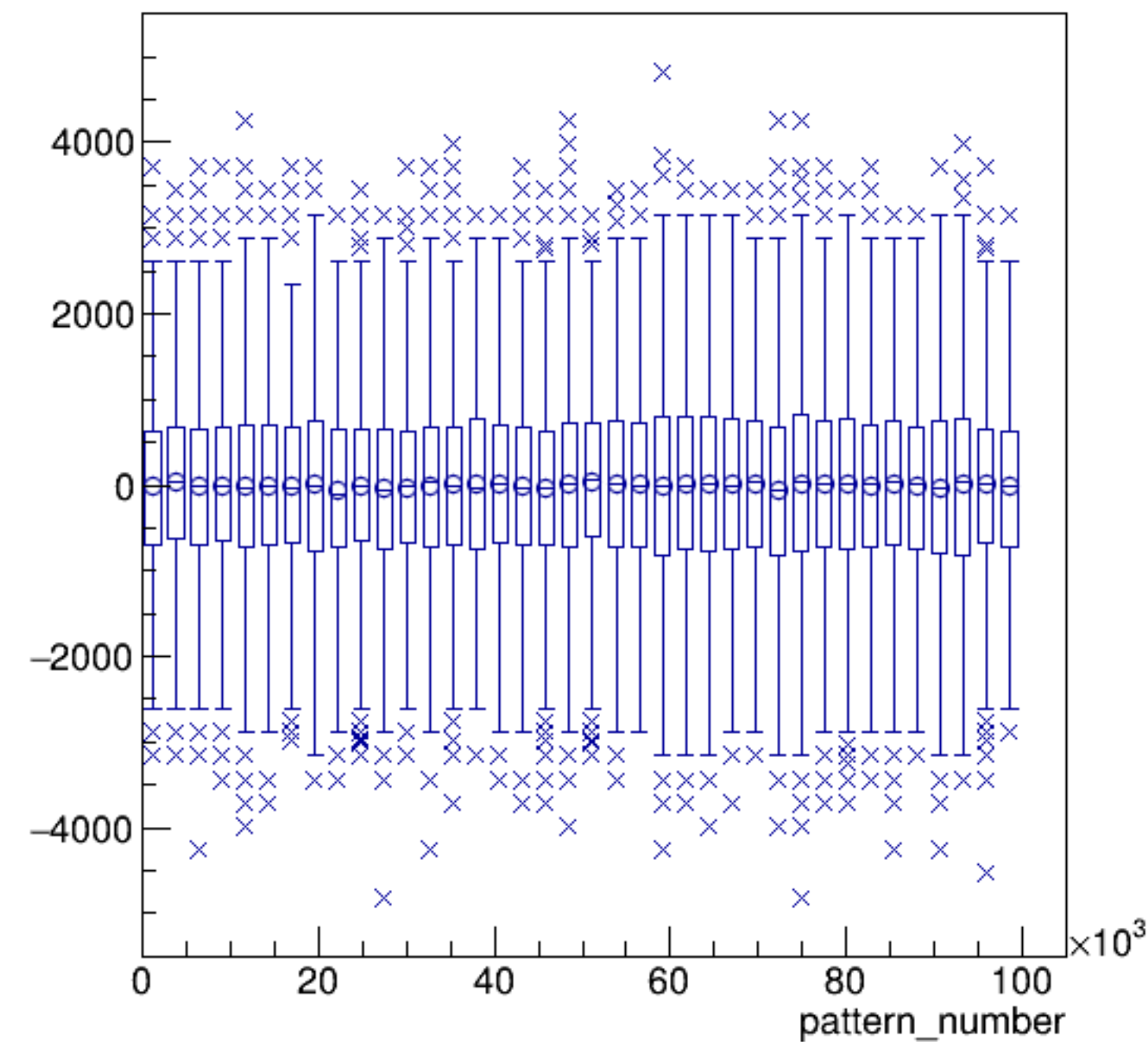


run7260_000_sam5.png

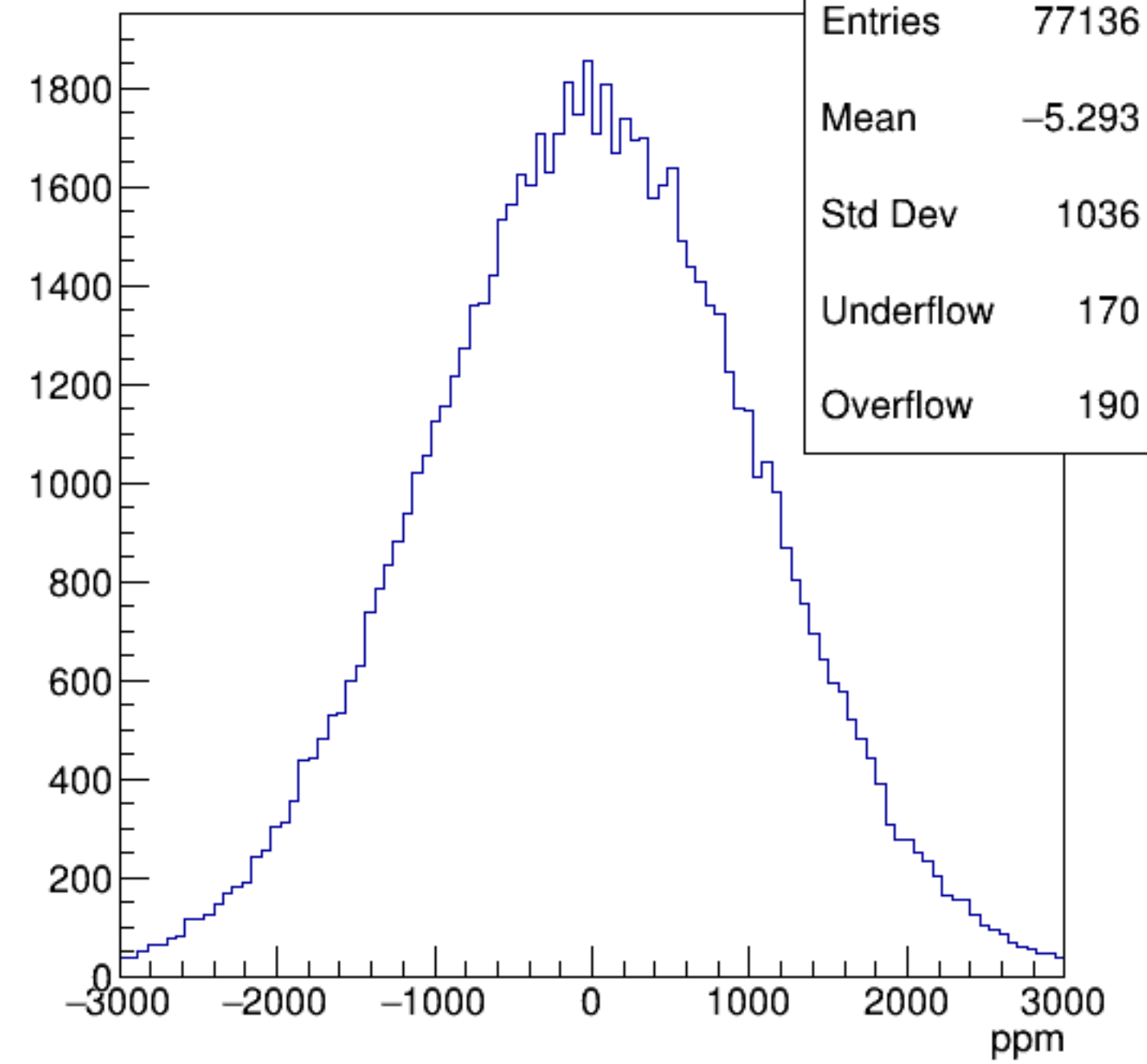
sam5*CurMon {ErrorFlag==0}



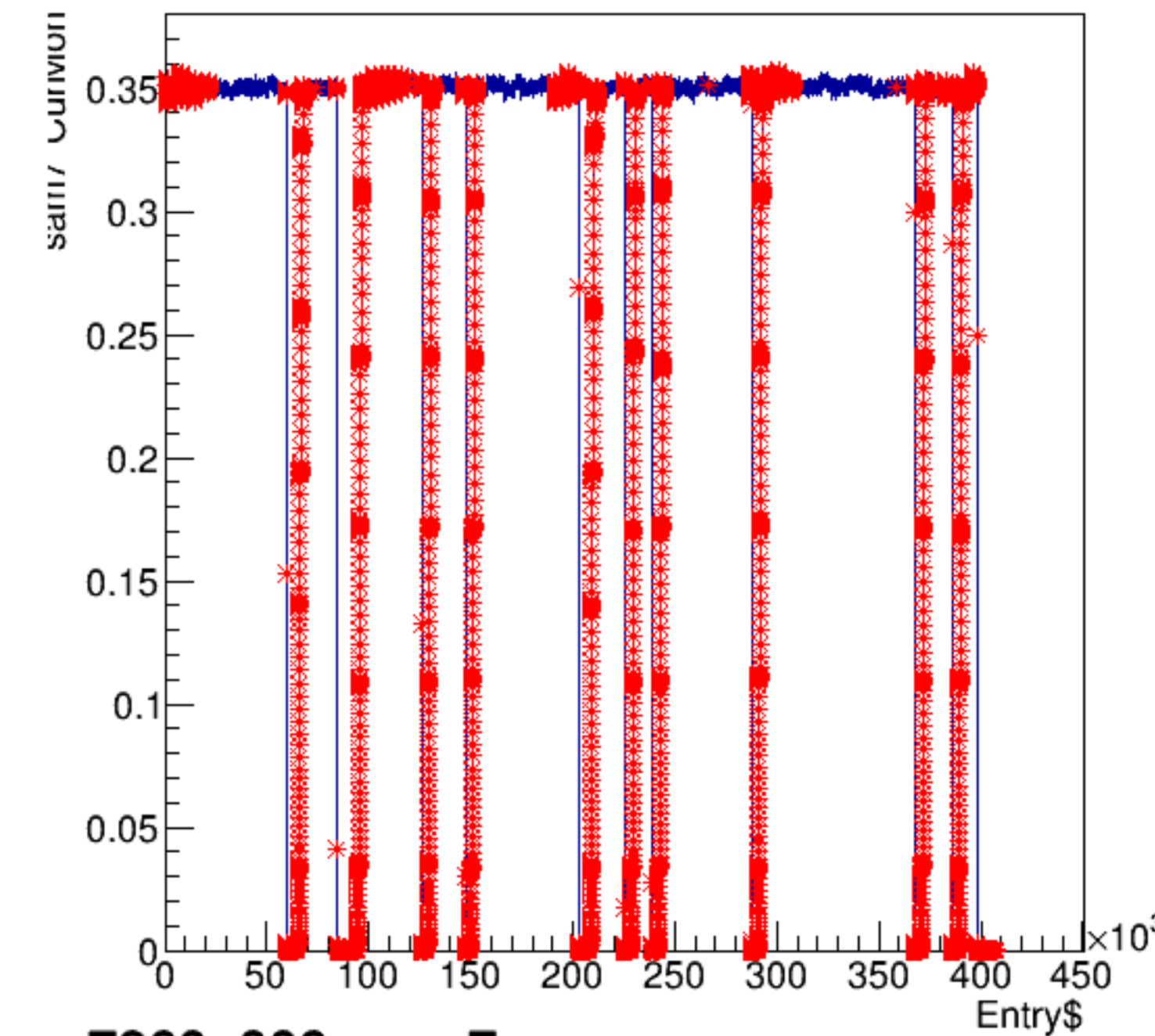
asym_sam5/ppm:pattern_number {ErrorFlag==0}



asym_sam5

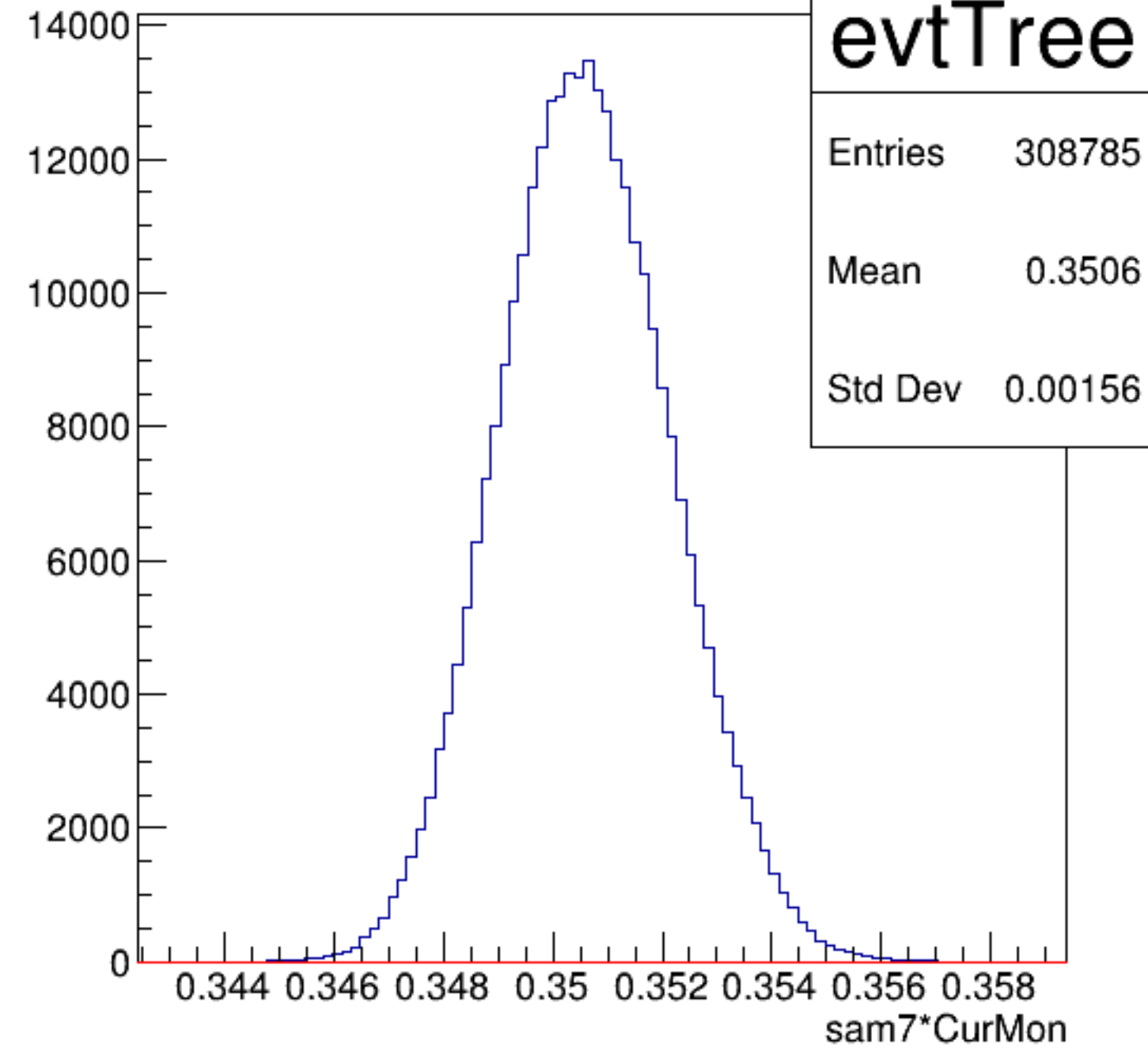


sam7*CurMon:Entry\$



run7260_000_sam7.png

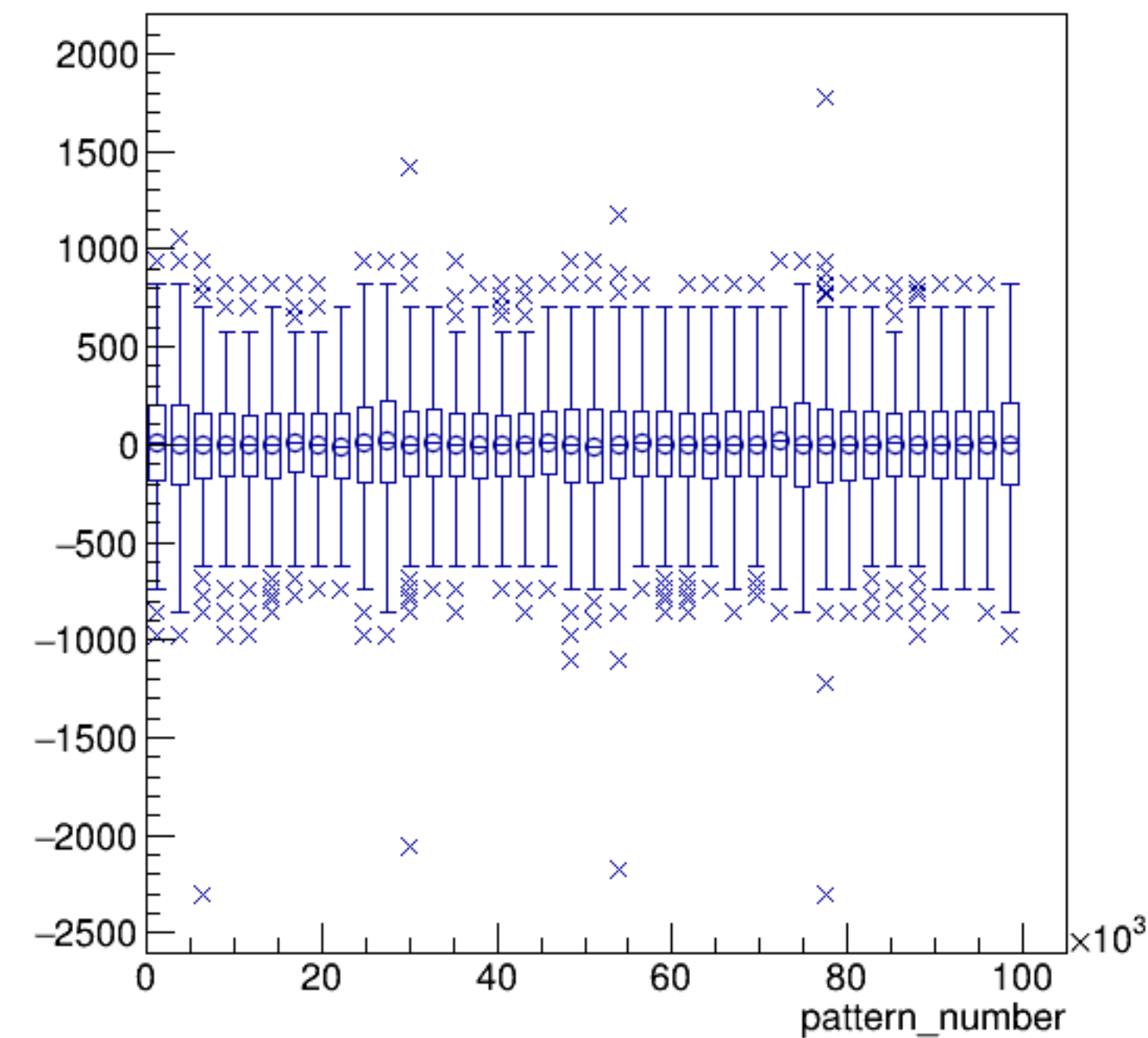
sam7*CurMon {ErrorFlag==0}



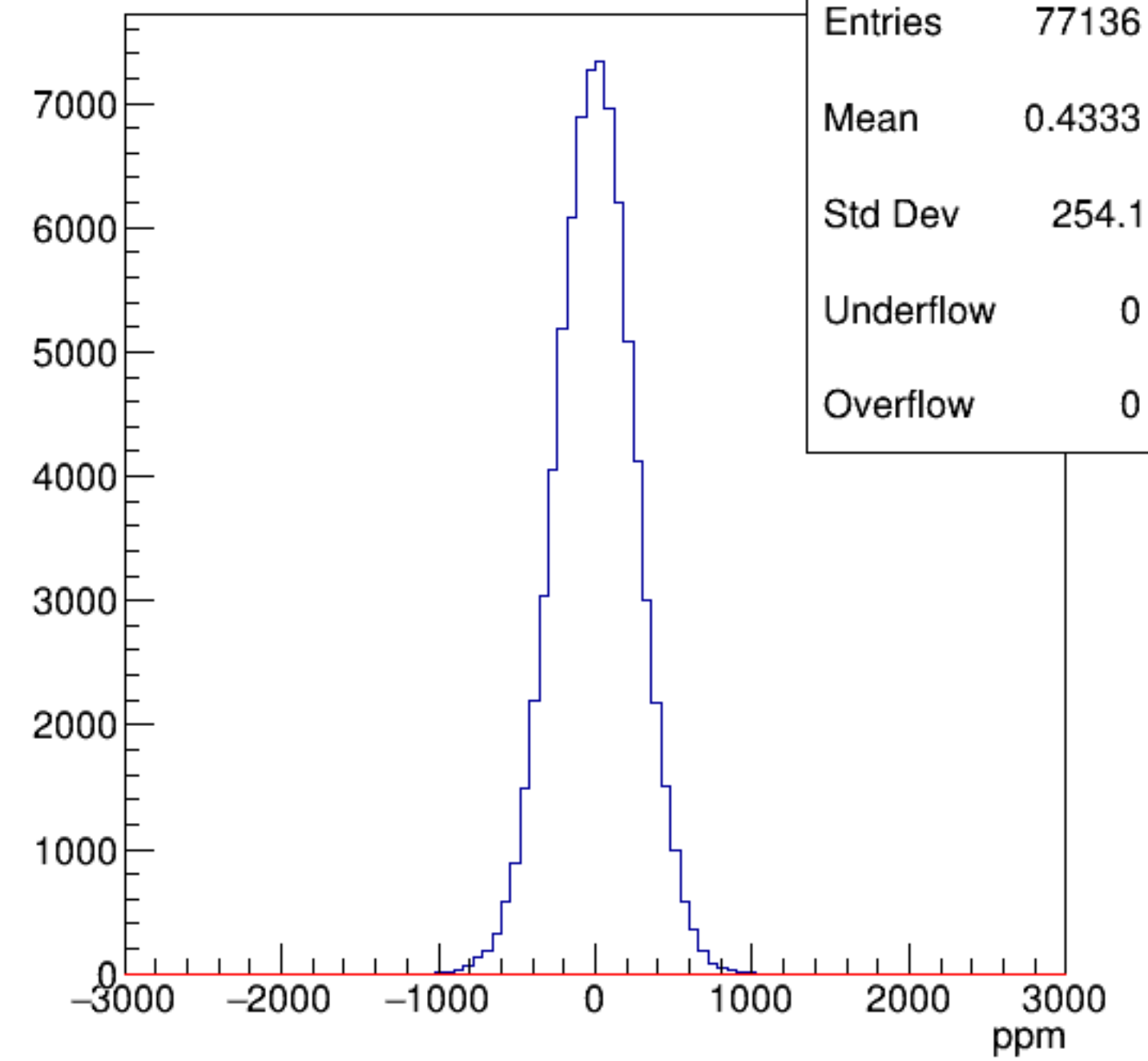
evtTree

Entries	308785
Mean	0.3506
Std Dev	0.00156

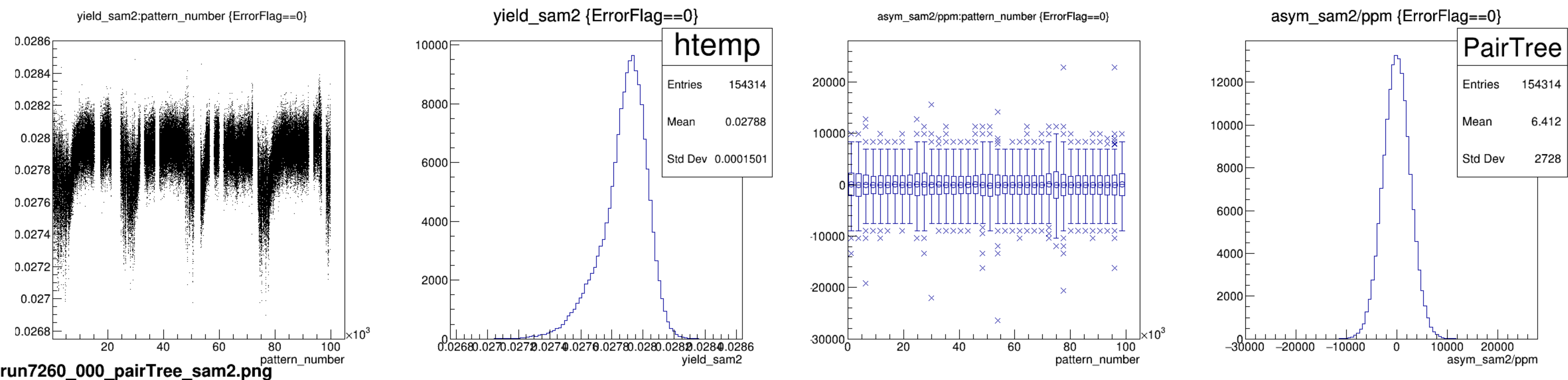
asym_sam7/ppm:pattern_number {ErrorFlag==0}



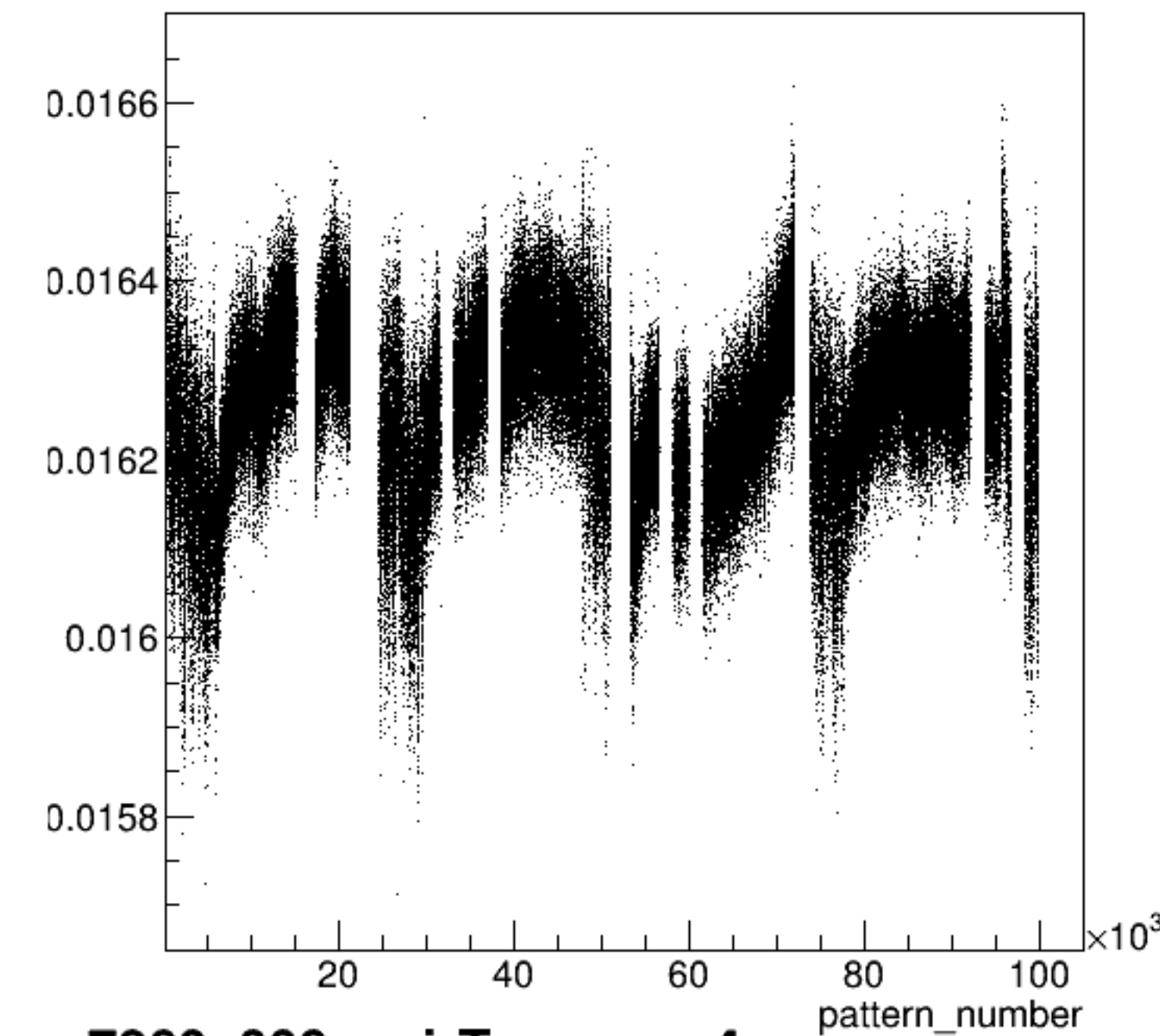
asym_sam7



Entries	77136
Mean	0.4333
Std Dev	254.1
Underflow	0
Overflow	0

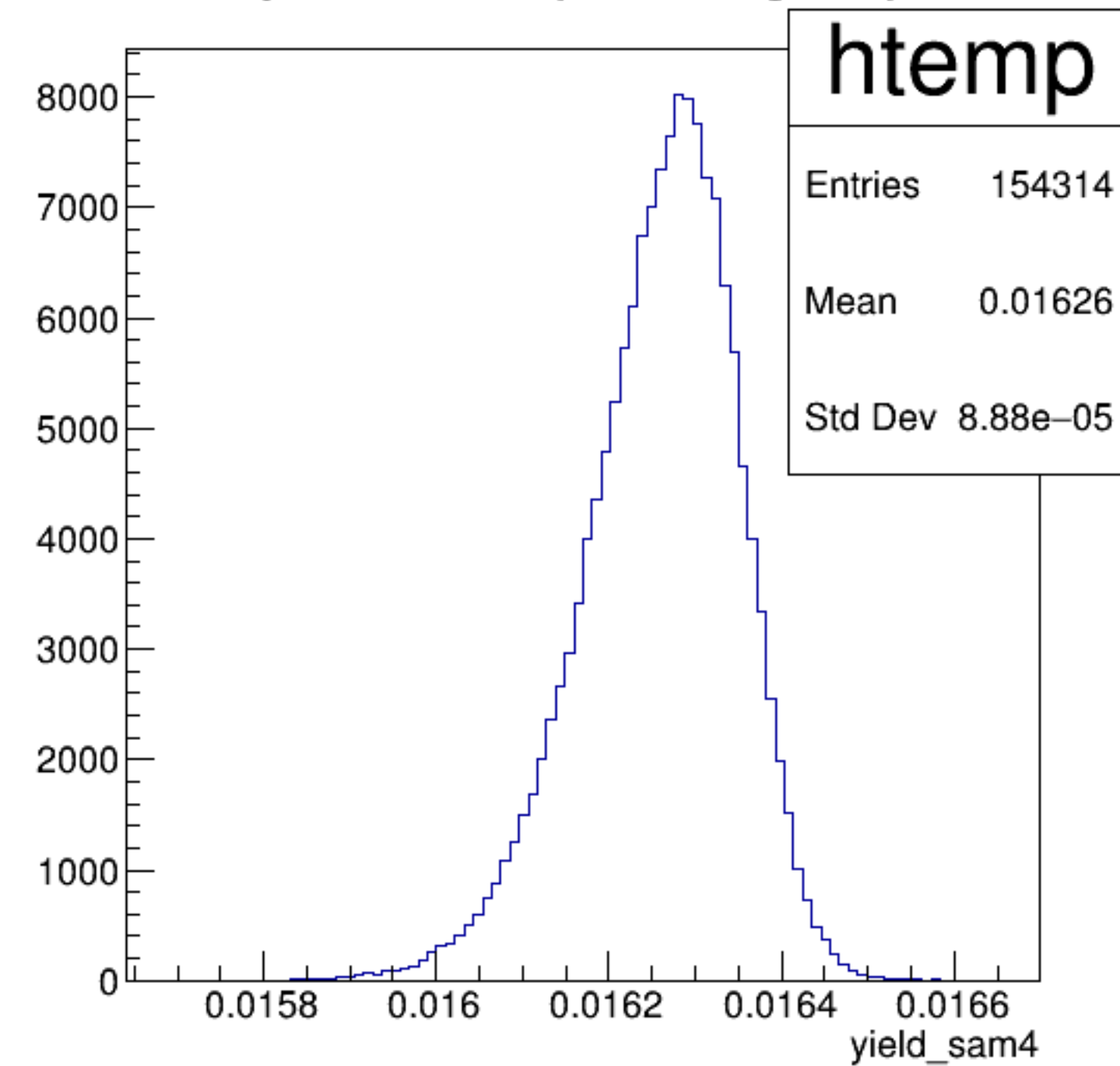


yield_sam4:pattern_number {ErrorFlag==0}

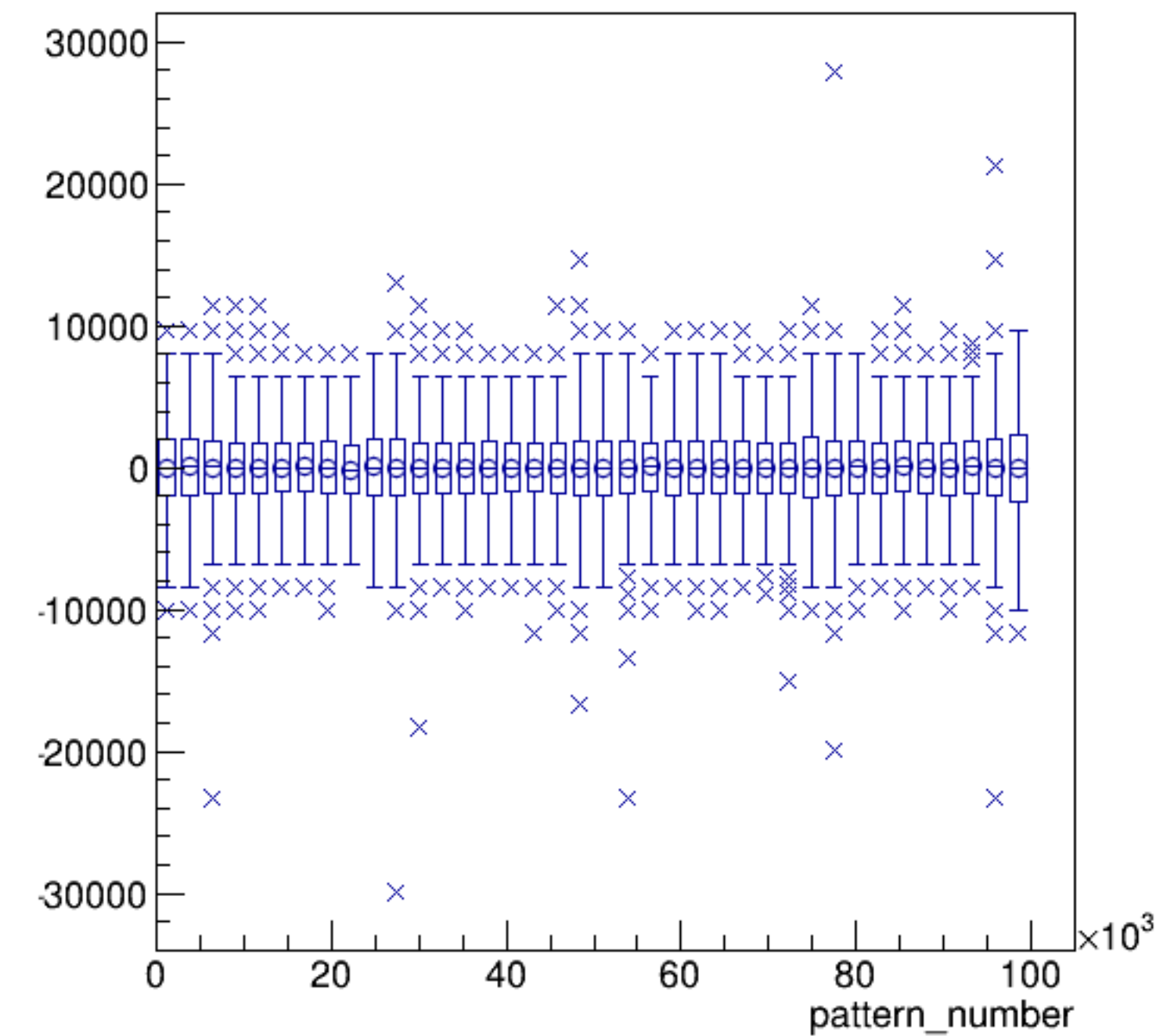


run7260_000_pairTree_sam4.png

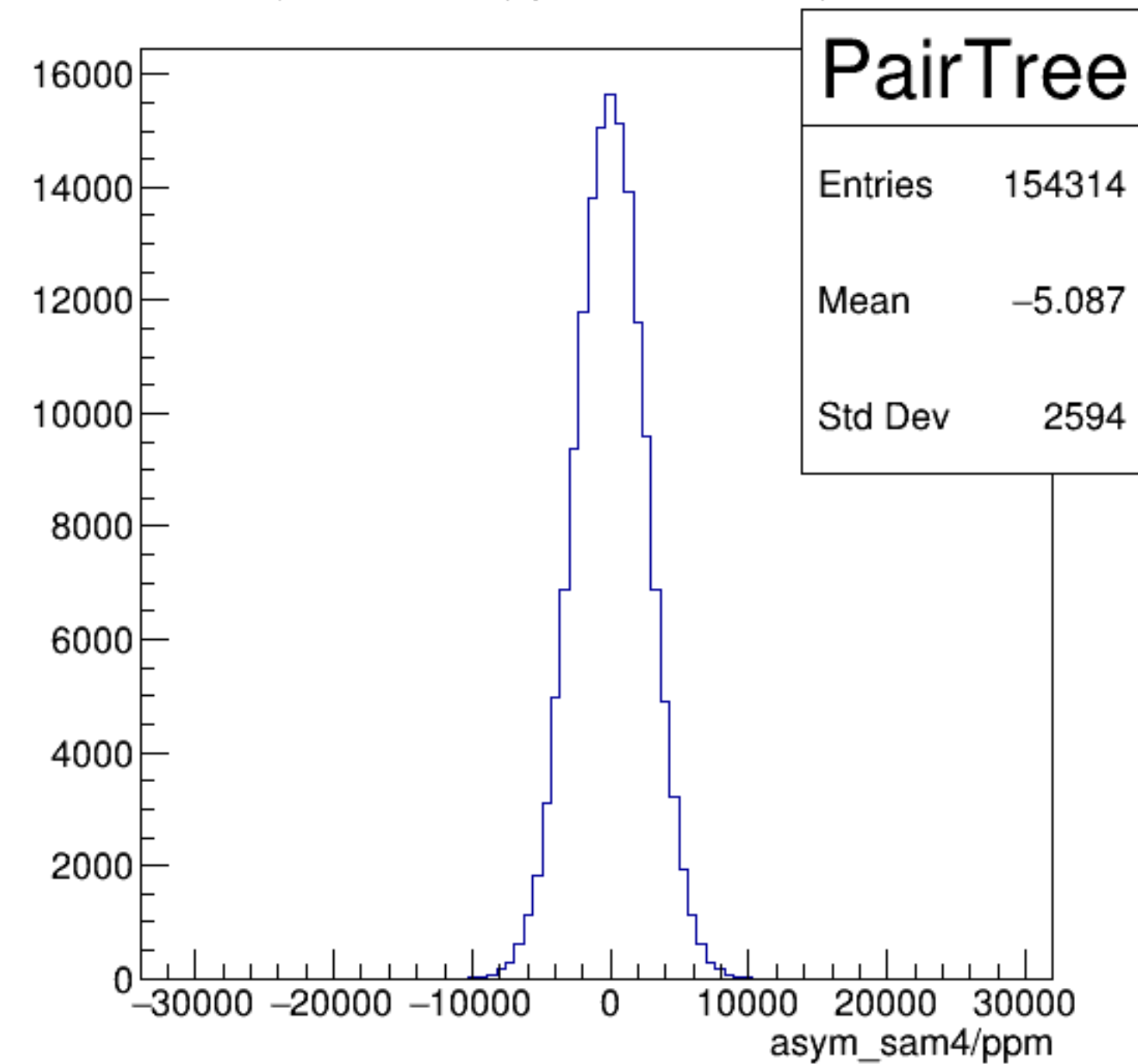
yield_sam4 {ErrorFlag==0}

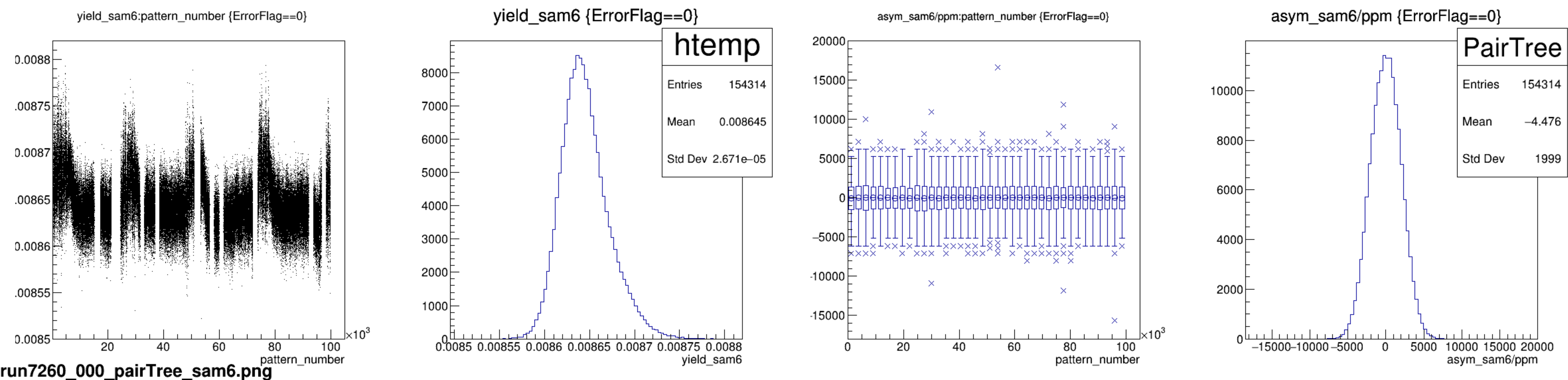


asym_sam4/ppm:pattern_number {ErrorFlag==0}

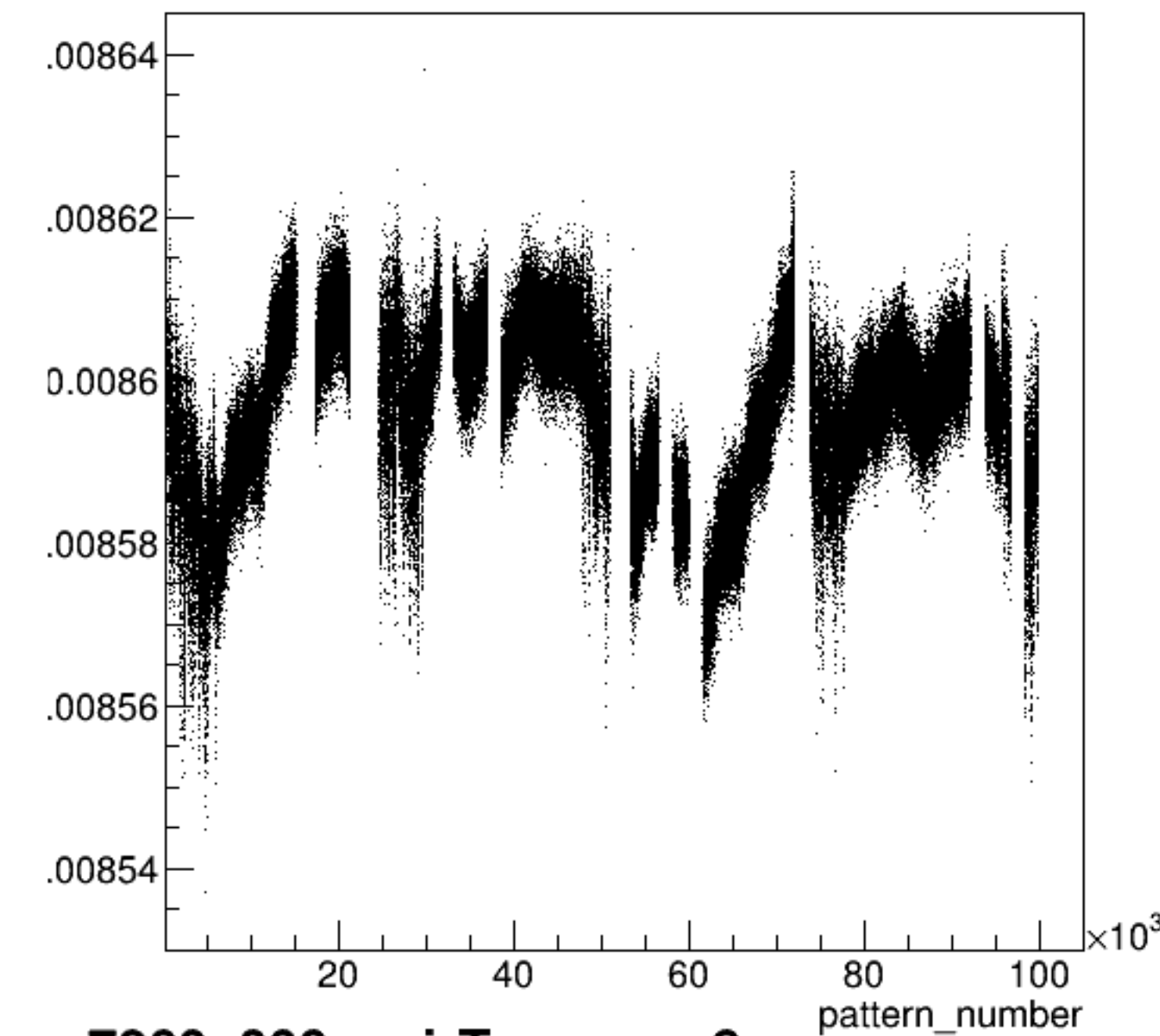


asym_sam4/ppm {ErrorFlag==0}



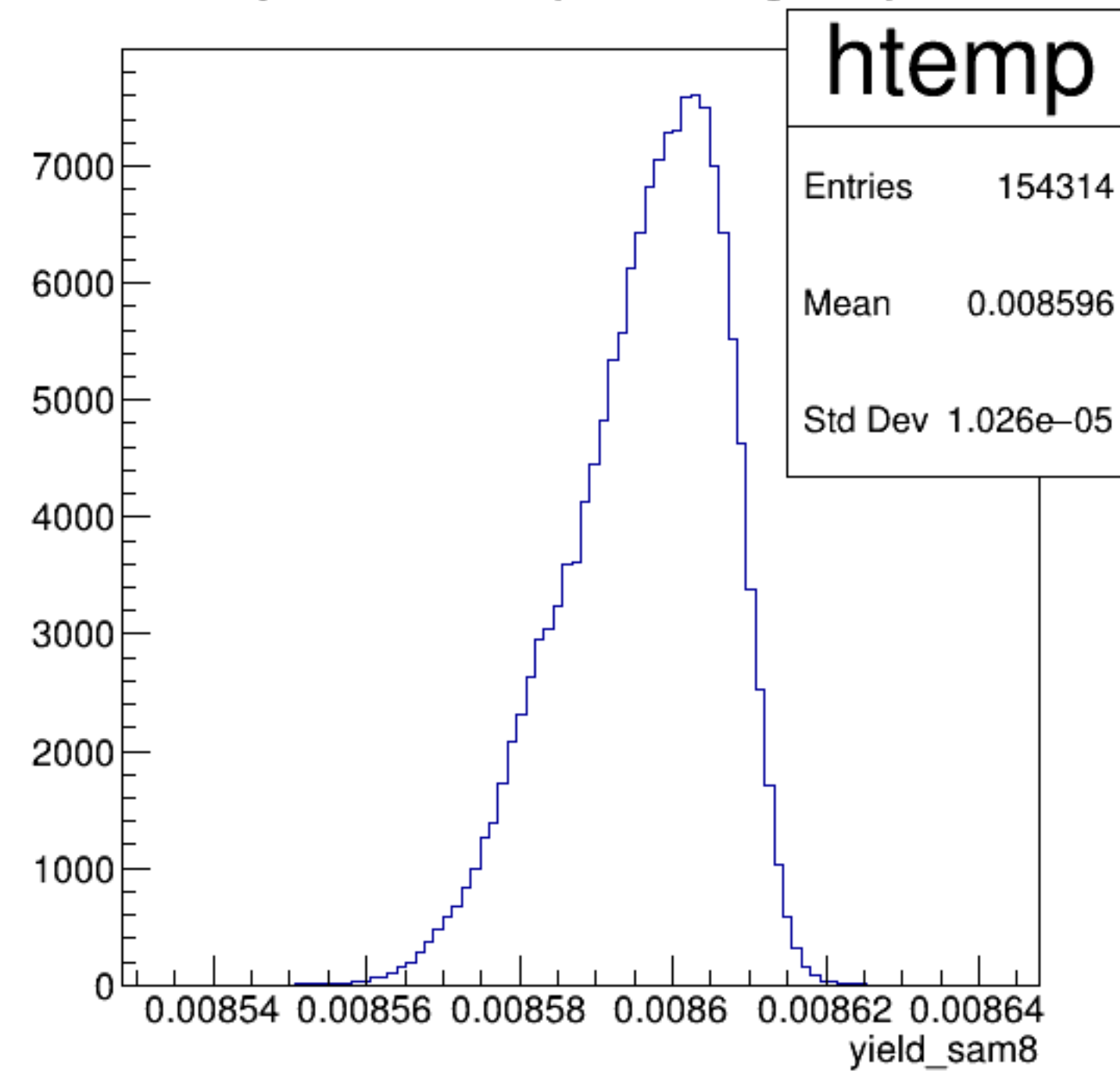


yield_sam8:pattern_number {ErrorFlag==0}

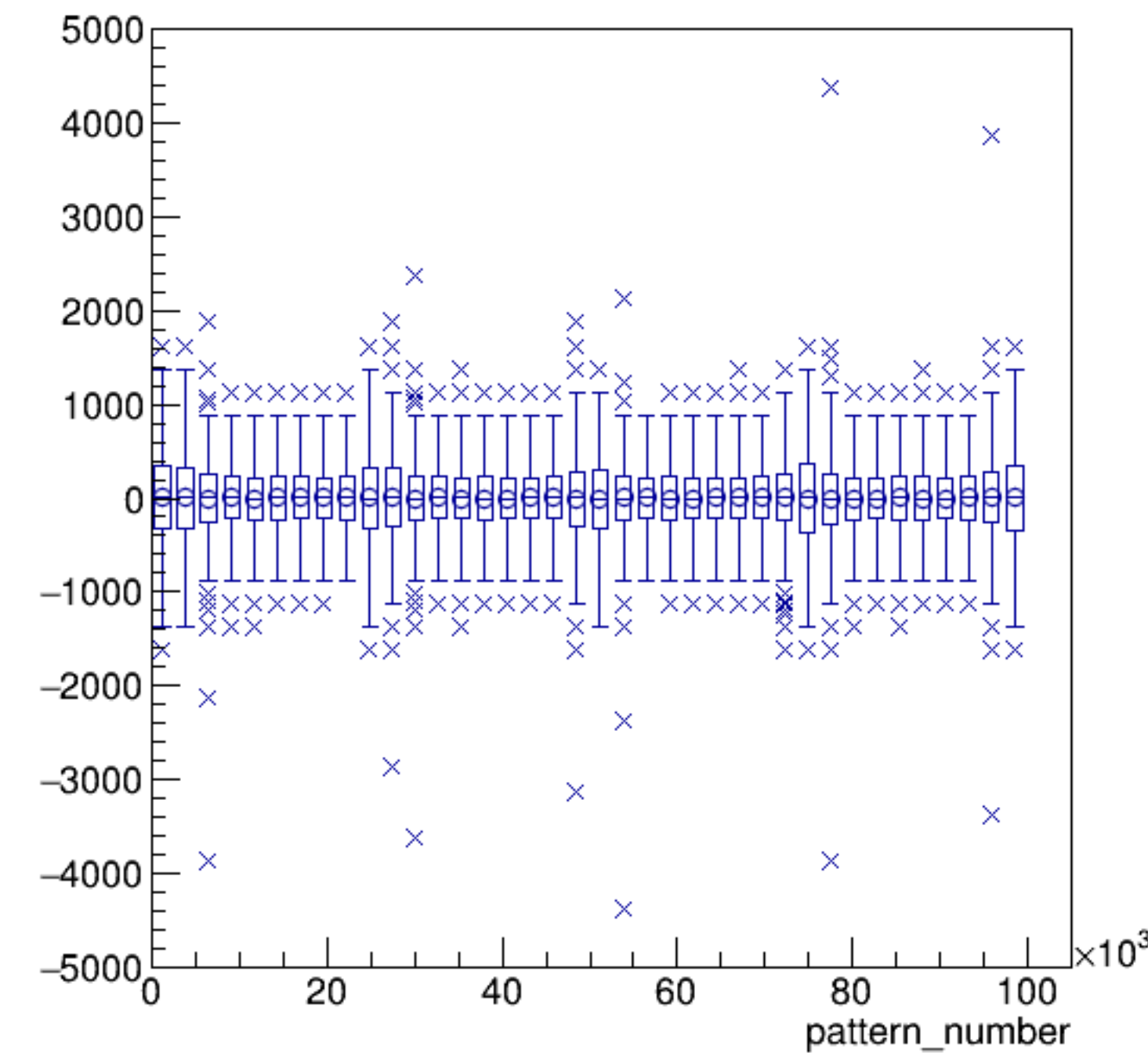


run7260_000_pairTree_sam8.png

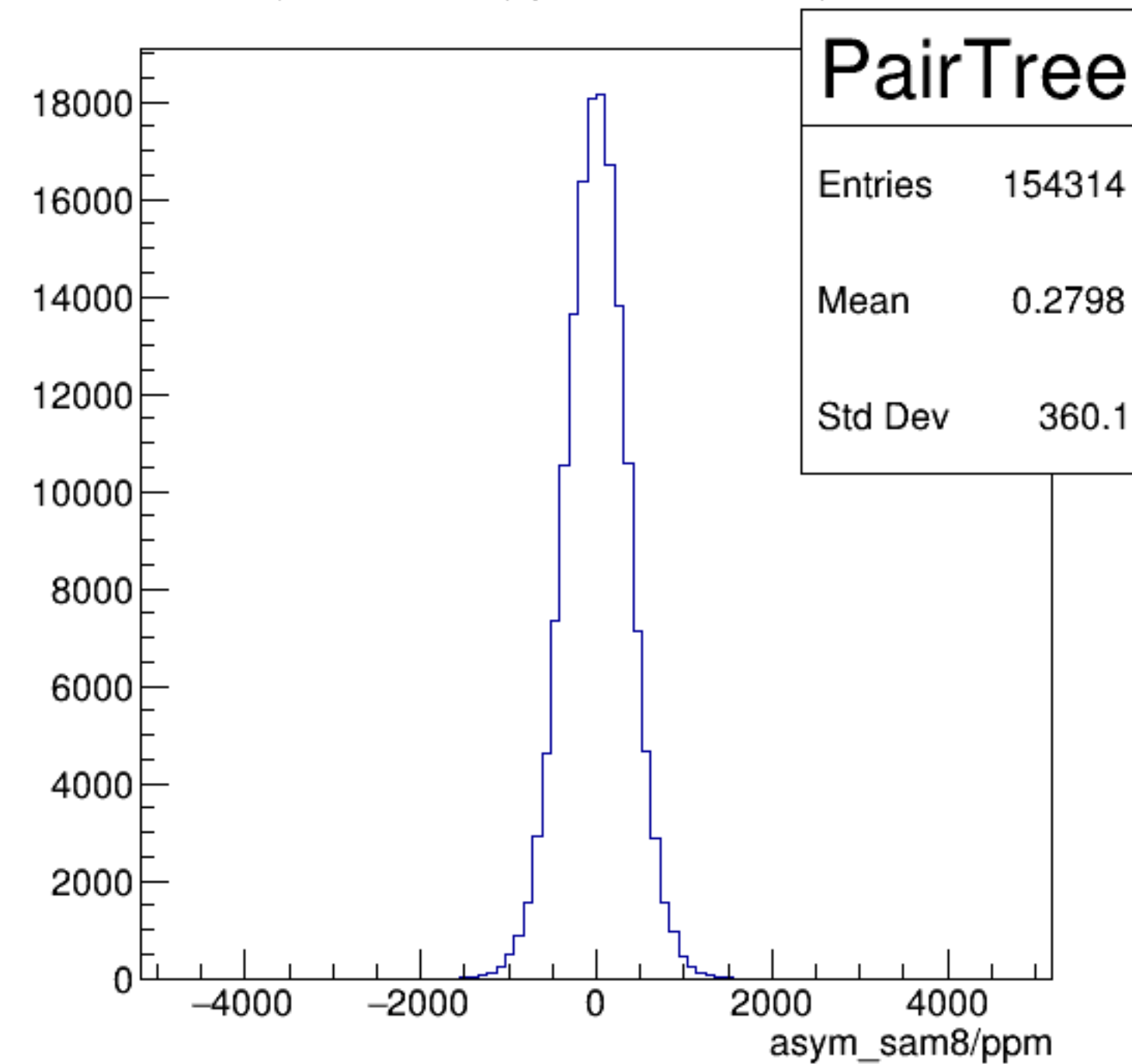
yield_sam8 {ErrorFlag==0}



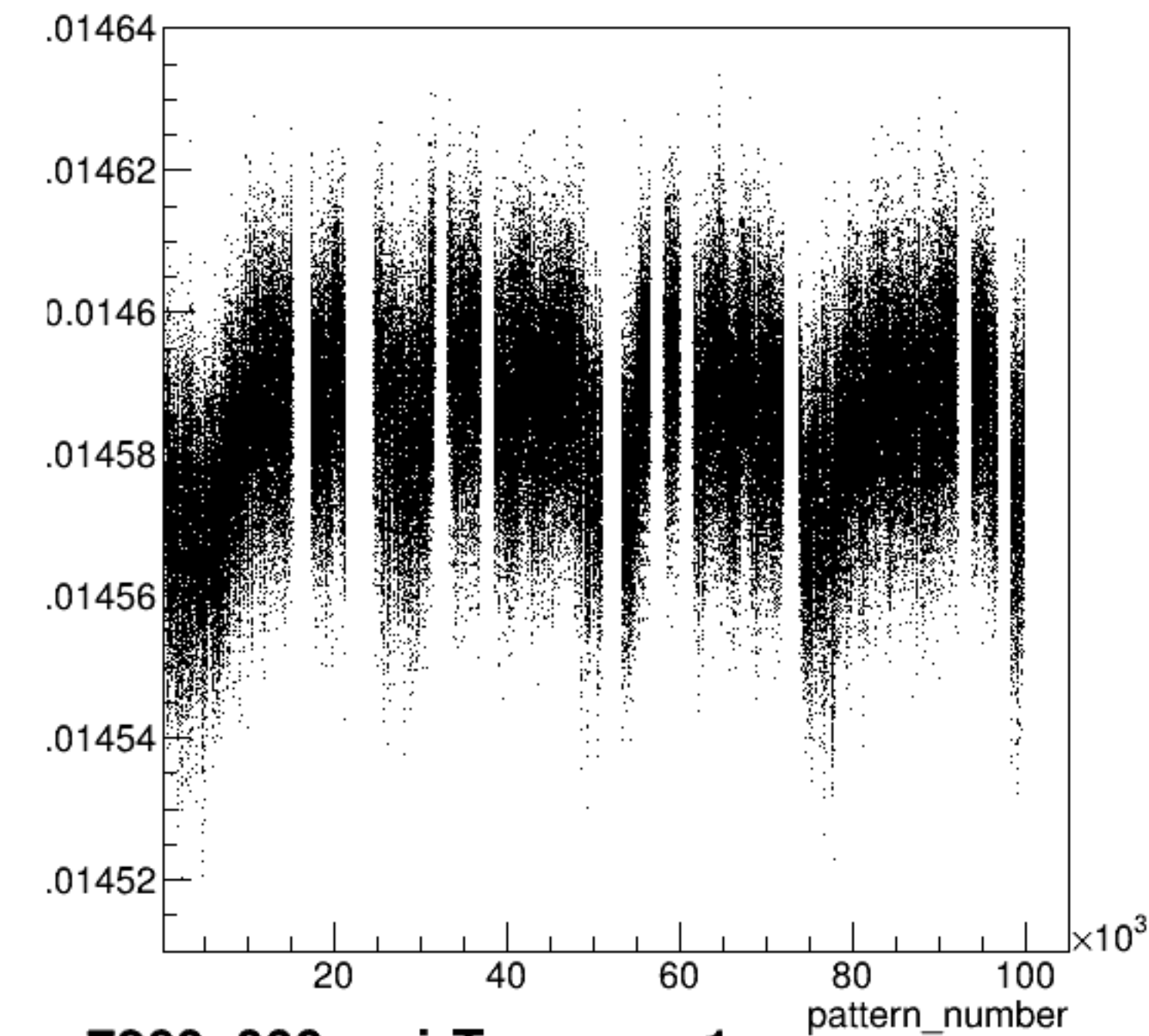
asym_sam8/ppm:pattern_number {ErrorFlag==0}



asym_sam8/ppm {ErrorFlag==0}

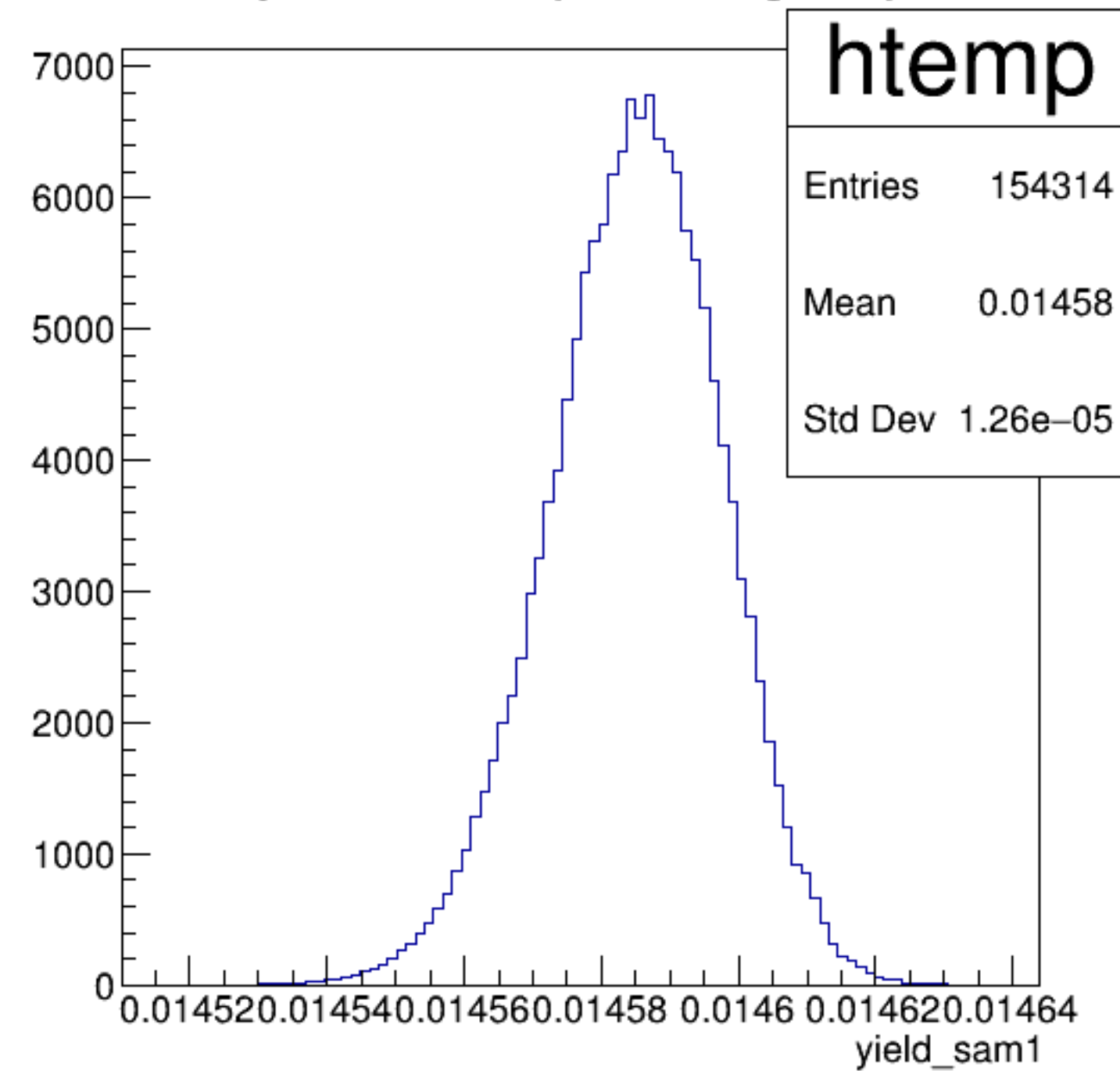


yield_sam1:pattern_number {ErrorFlag==0}

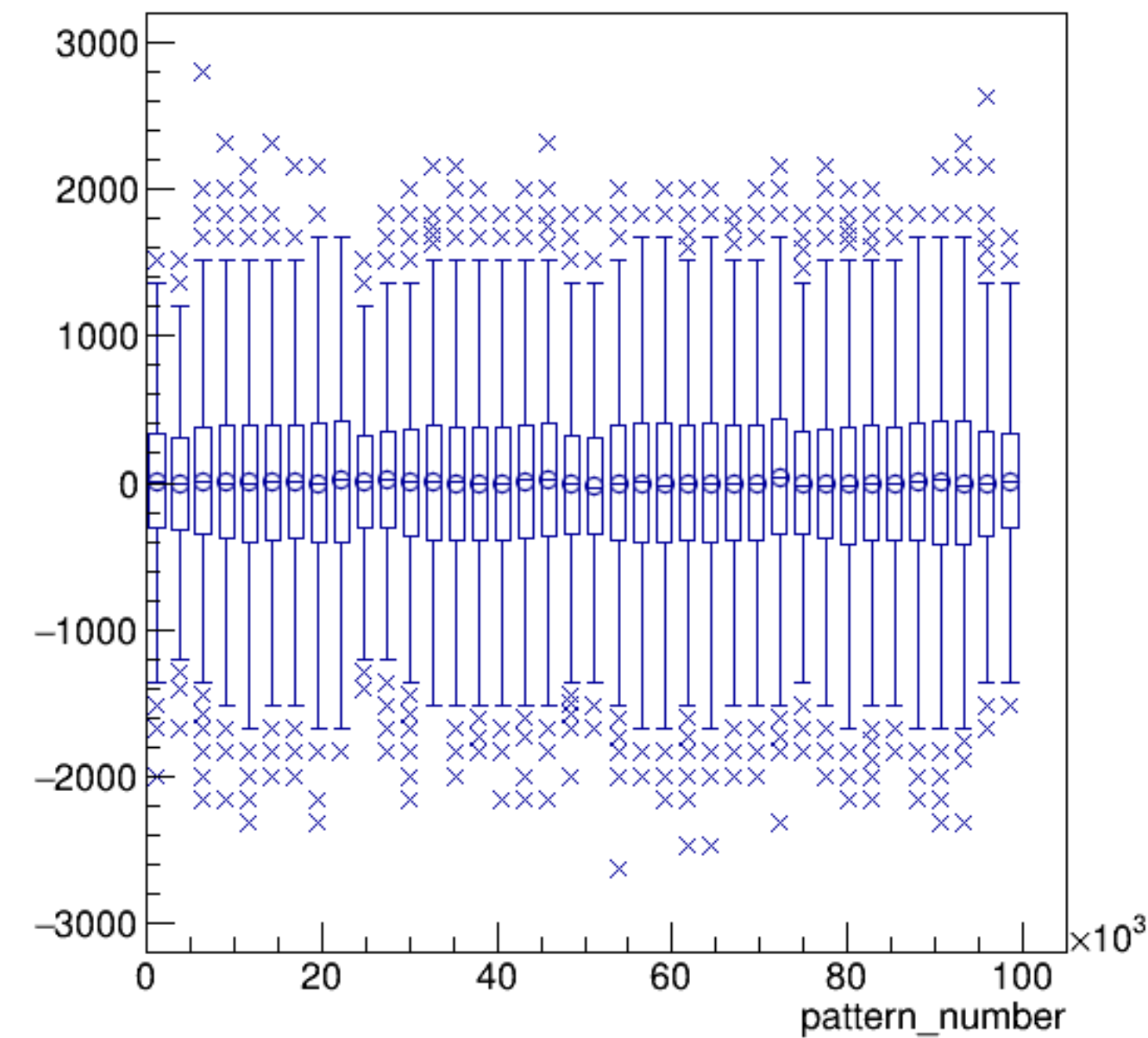


run7260_000_pairTree_sam1.png

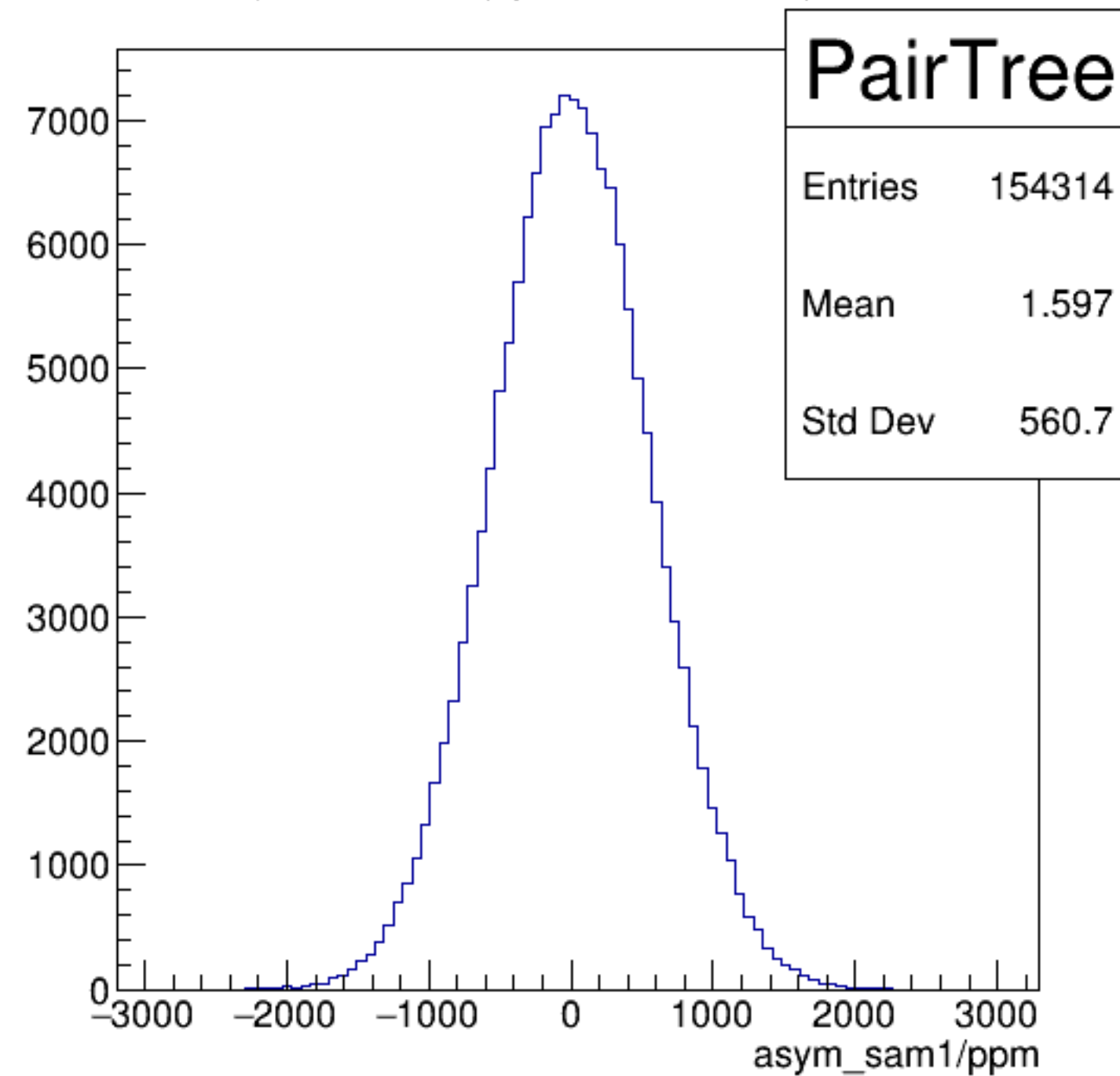
yield_sam1 {ErrorFlag==0}

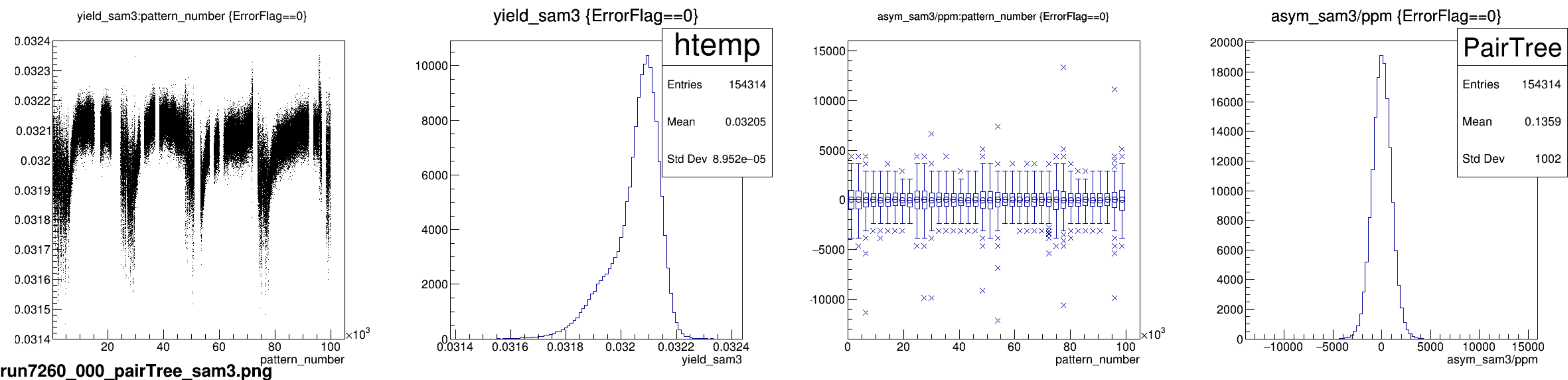


asym_sam1/ppm:pattern_number {ErrorFlag==0}

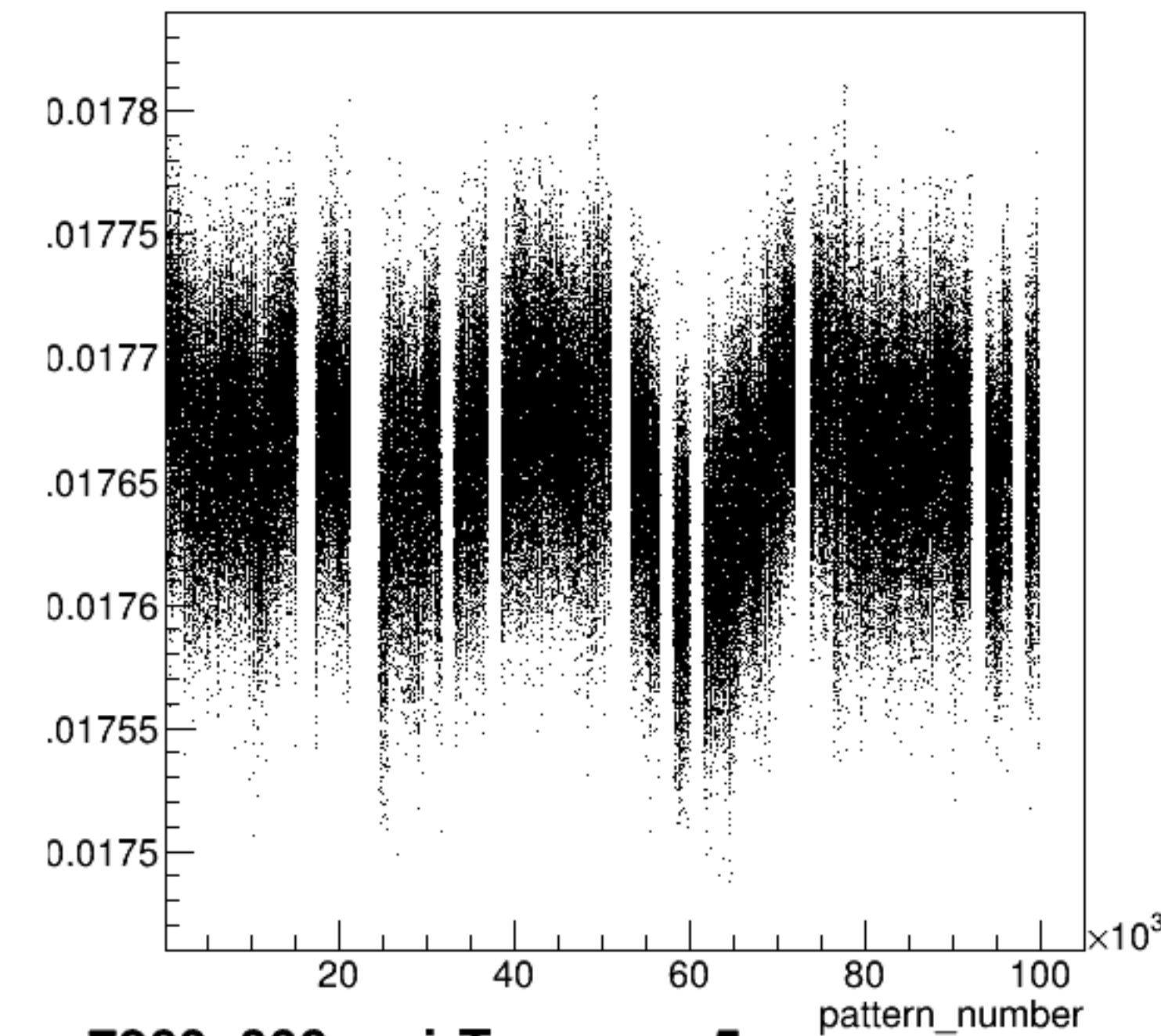


asym_sam1/ppm {ErrorFlag==0}



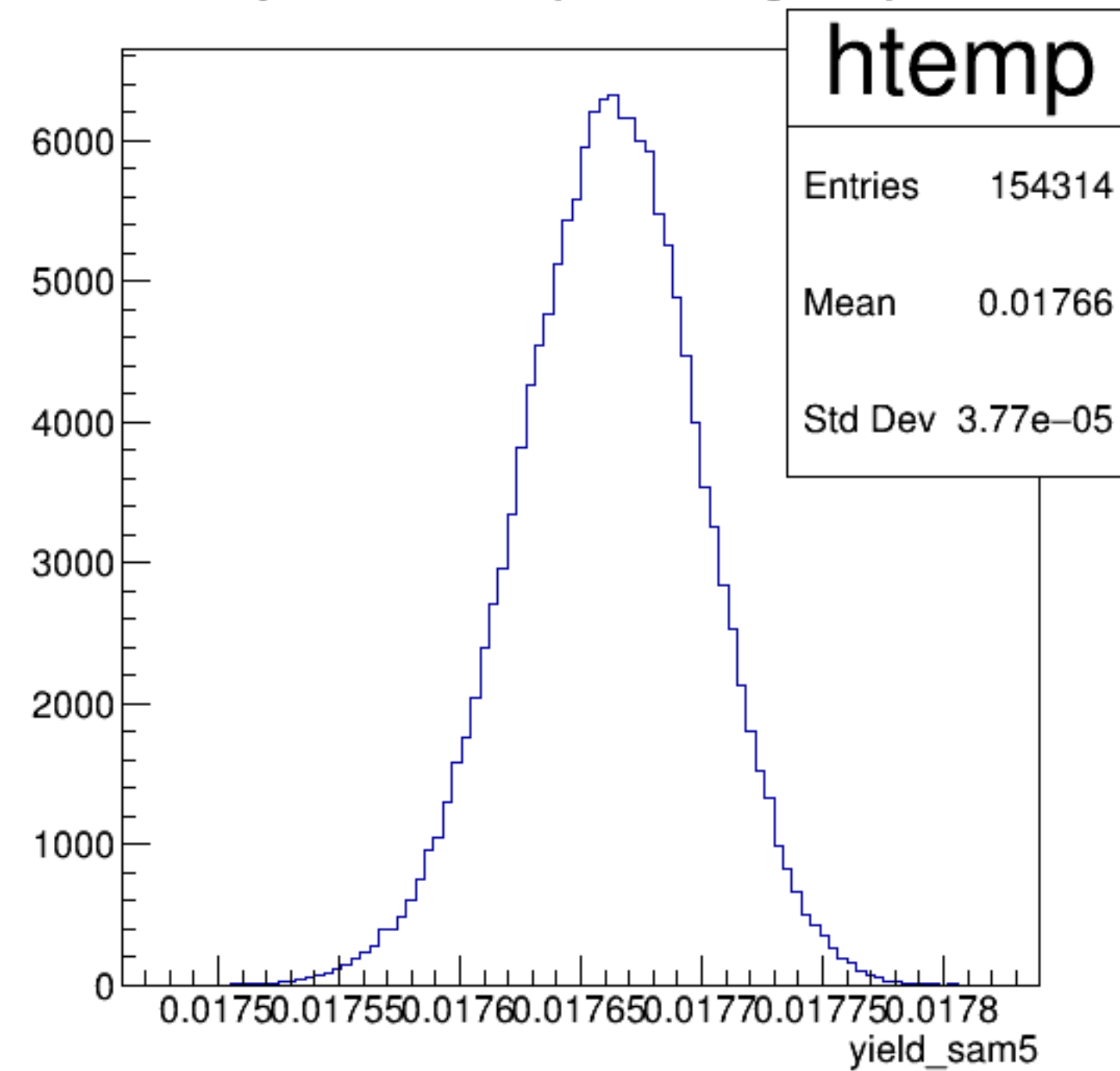


yield_sam5:pattern_number {ErrorFlag==0}

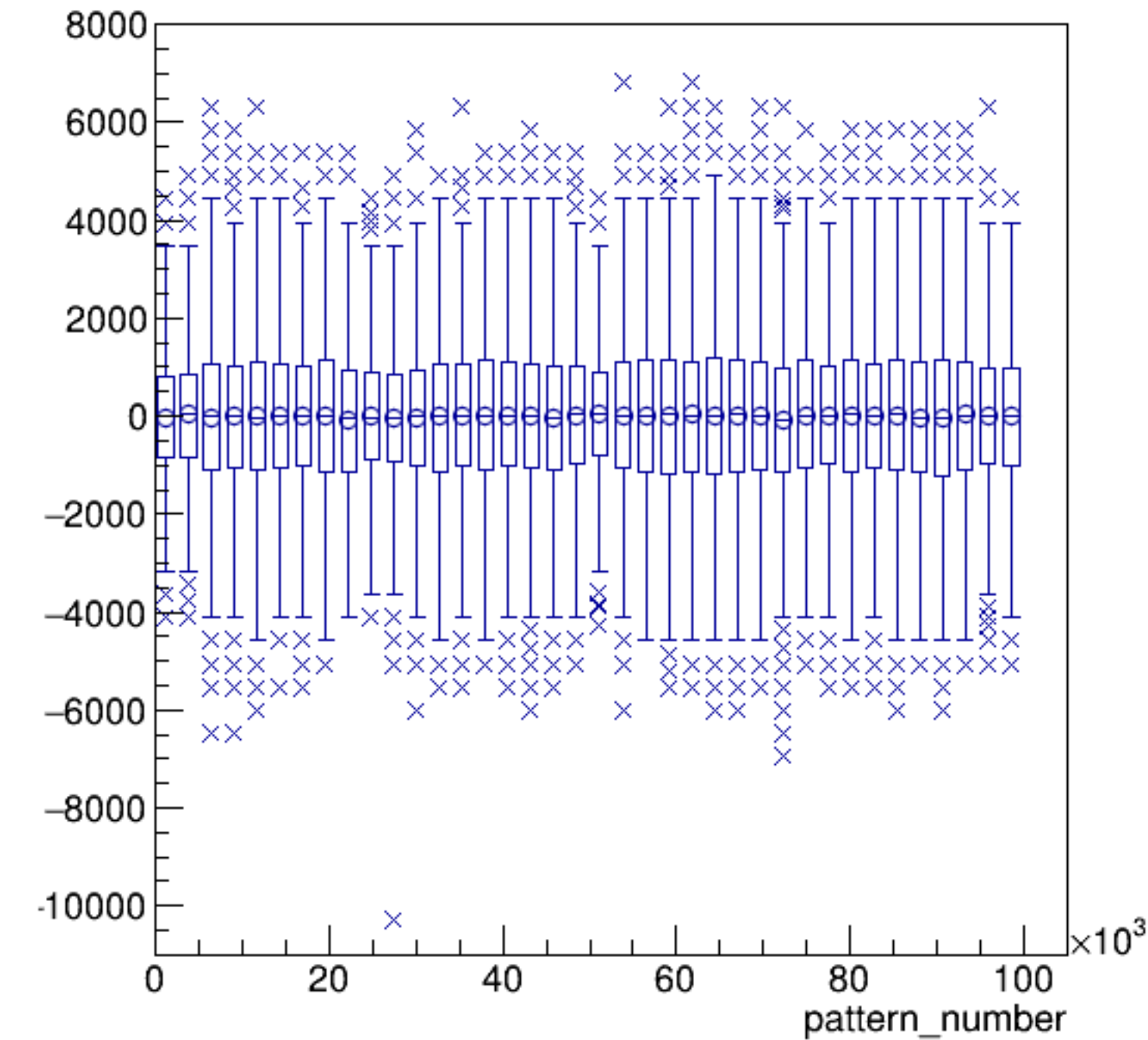


run7260_000_pairTree_sam5.png

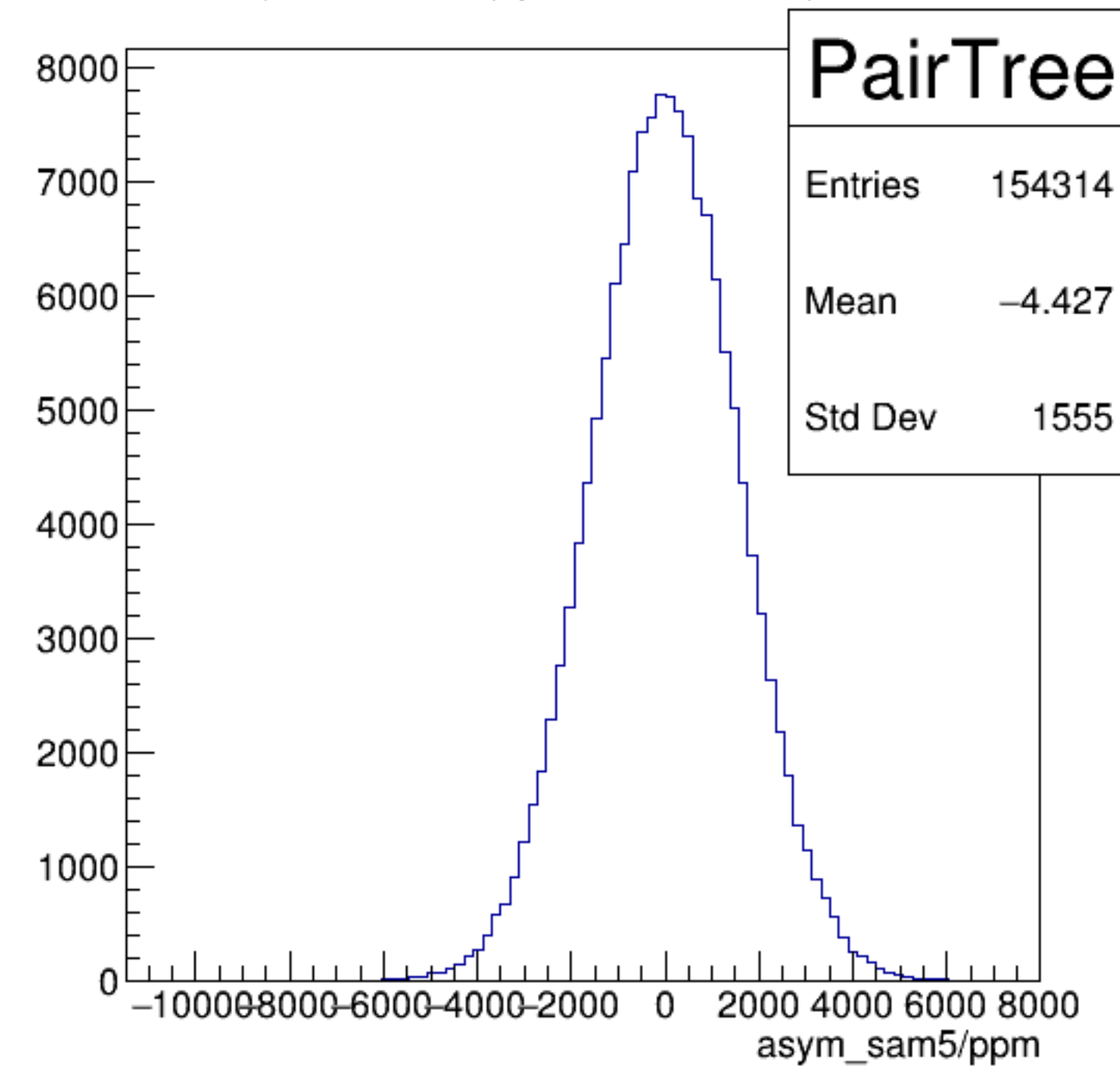
yield_sam5 {ErrorFlag==0}

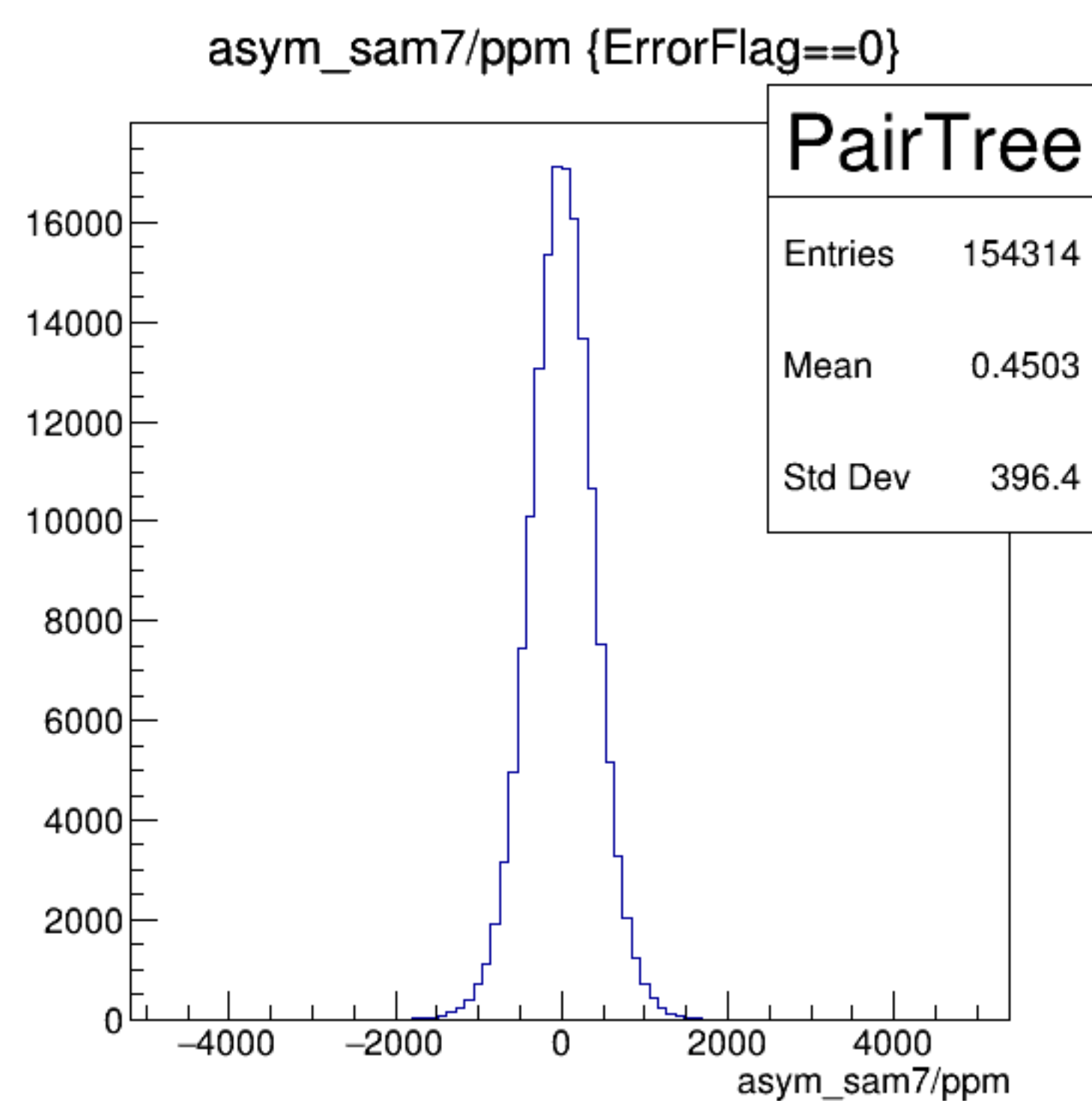
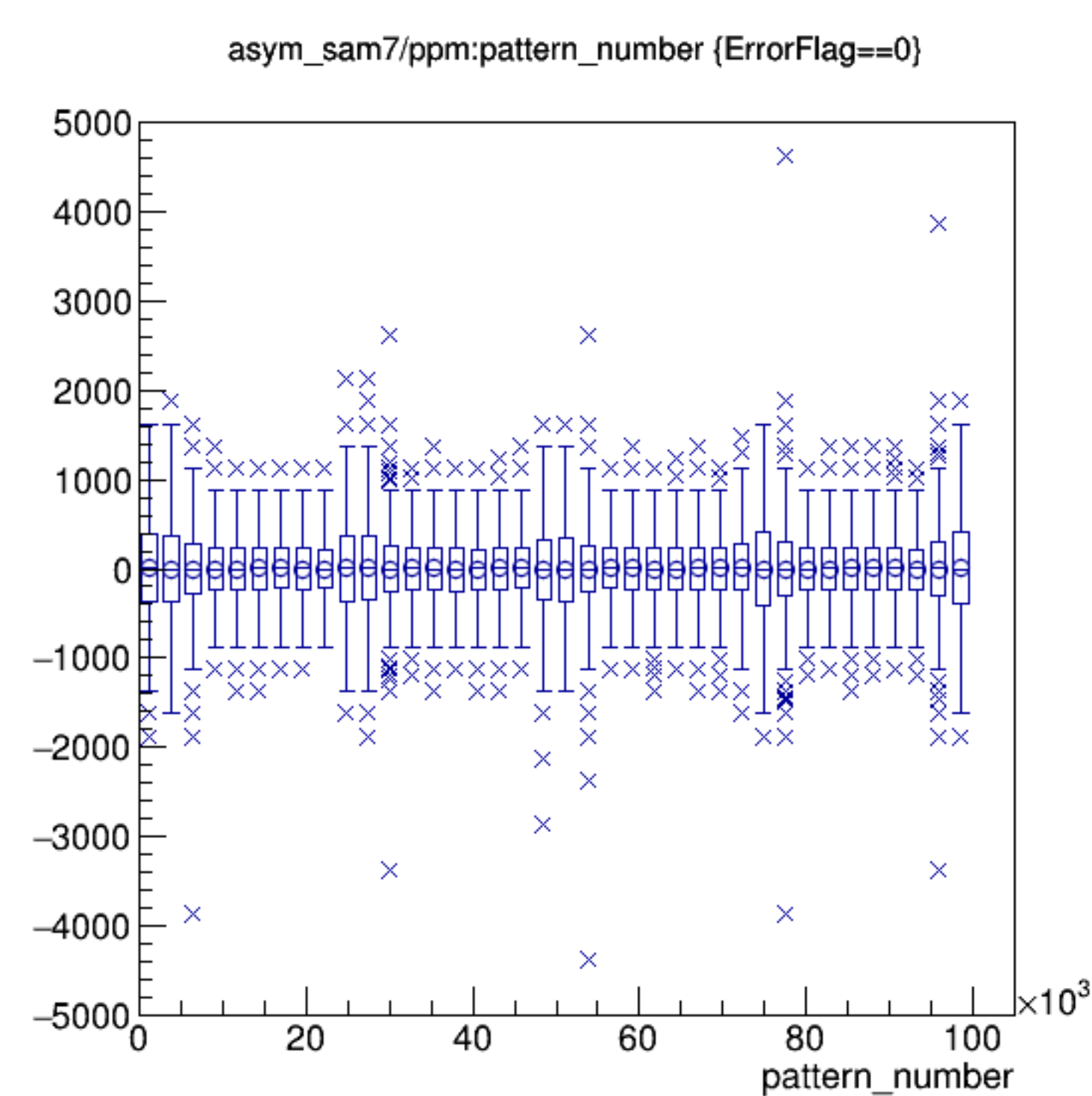
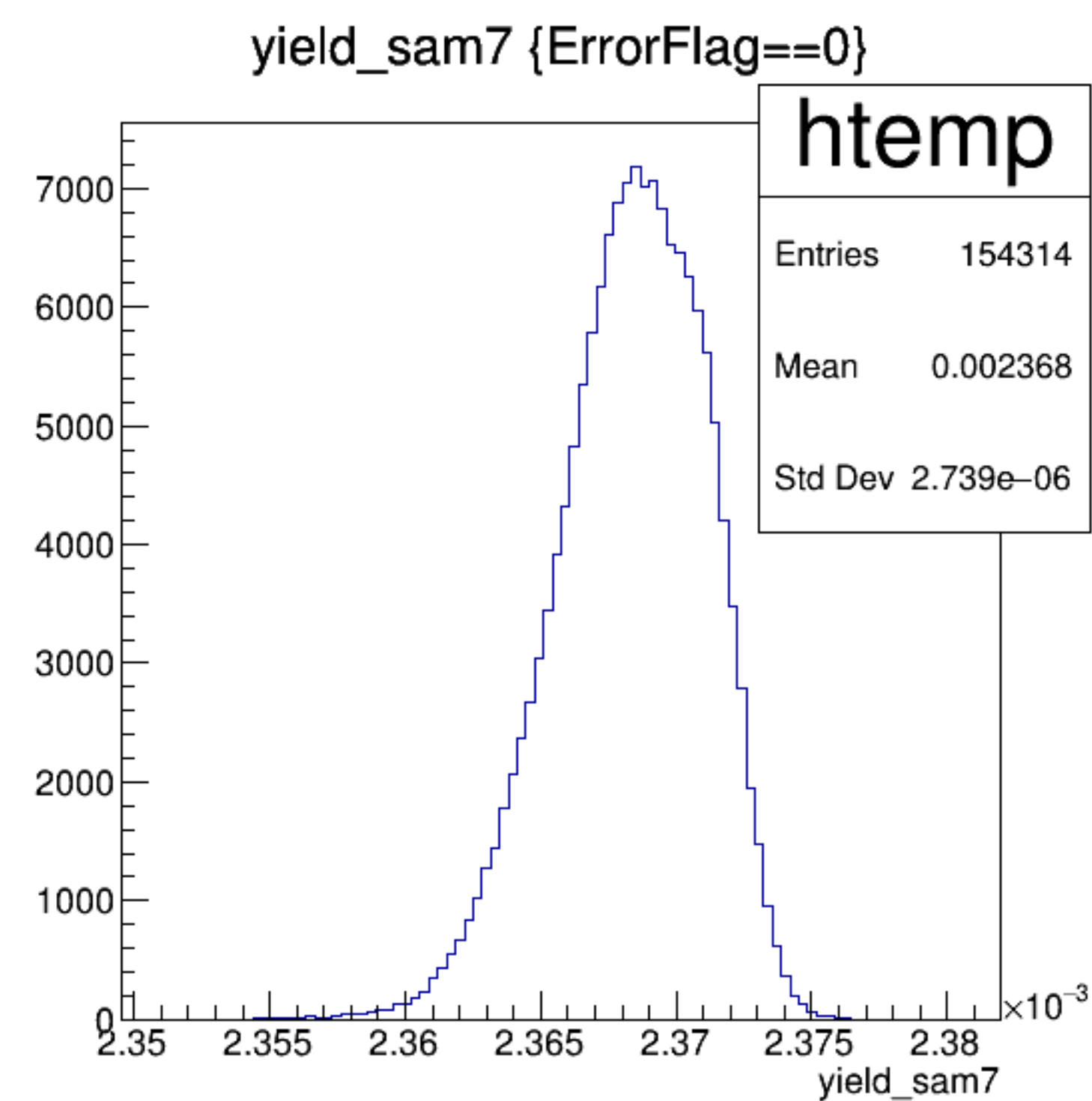
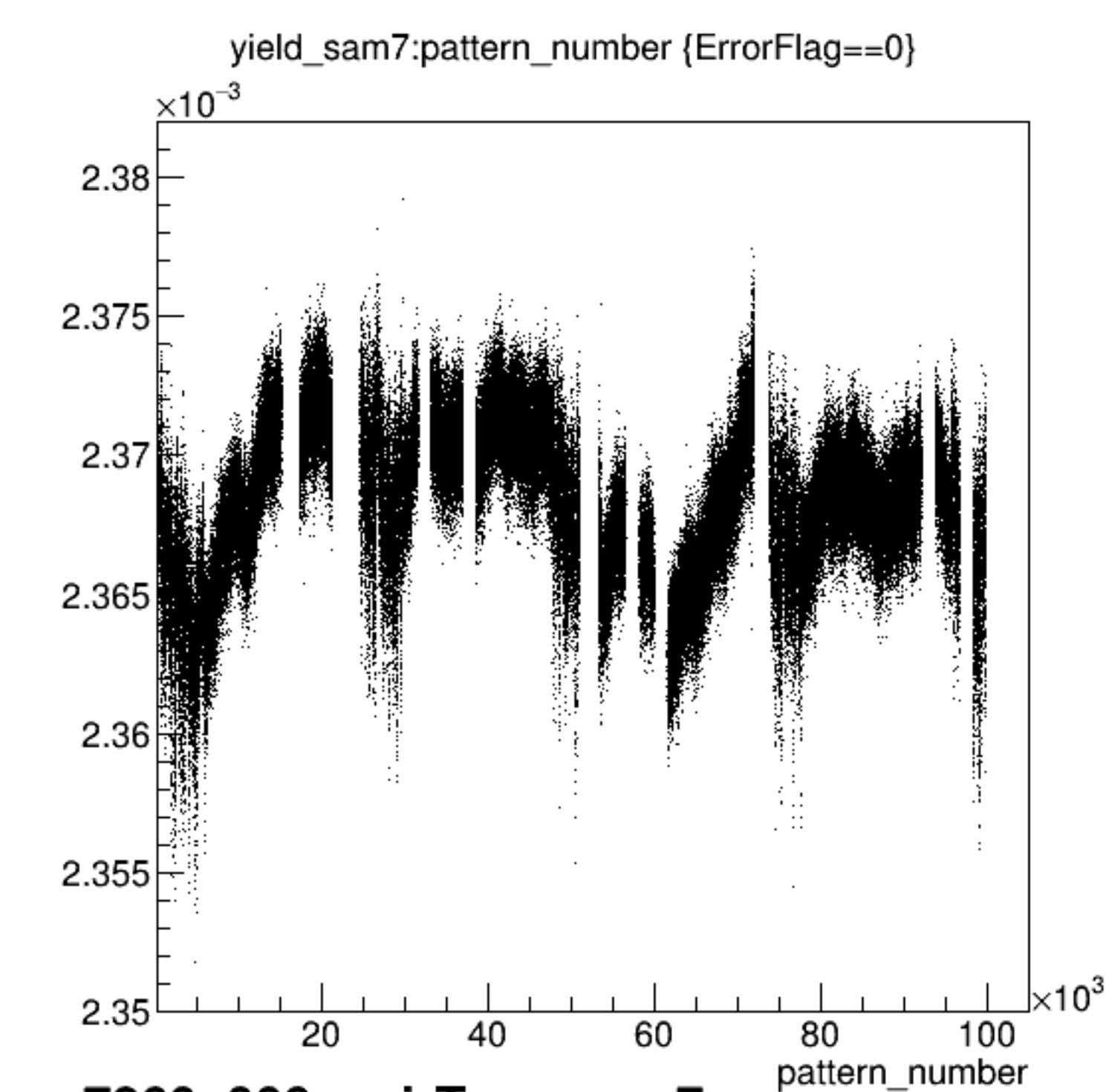


asym_sam5/ppm:pattern_number {ErrorFlag==0}

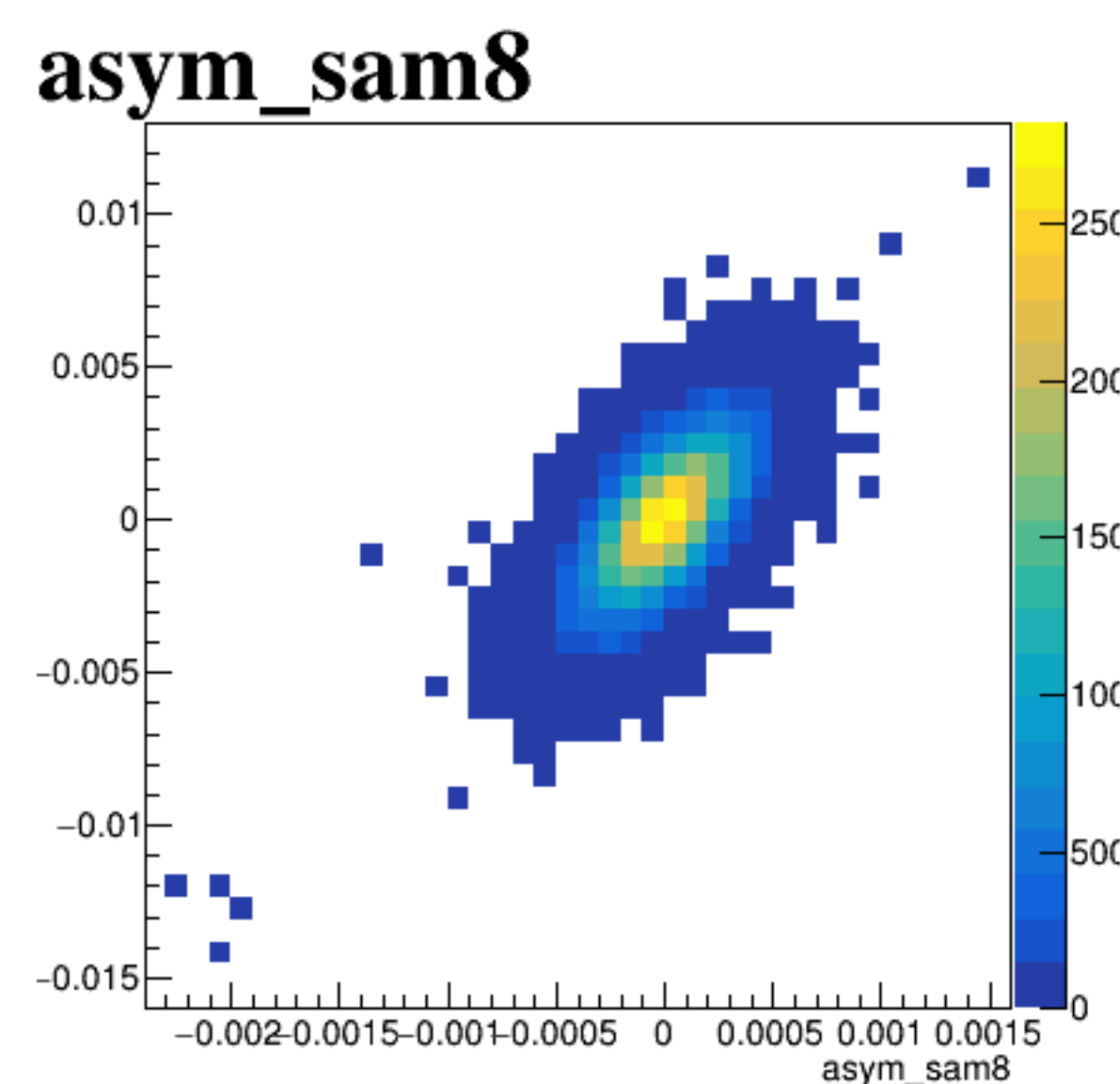
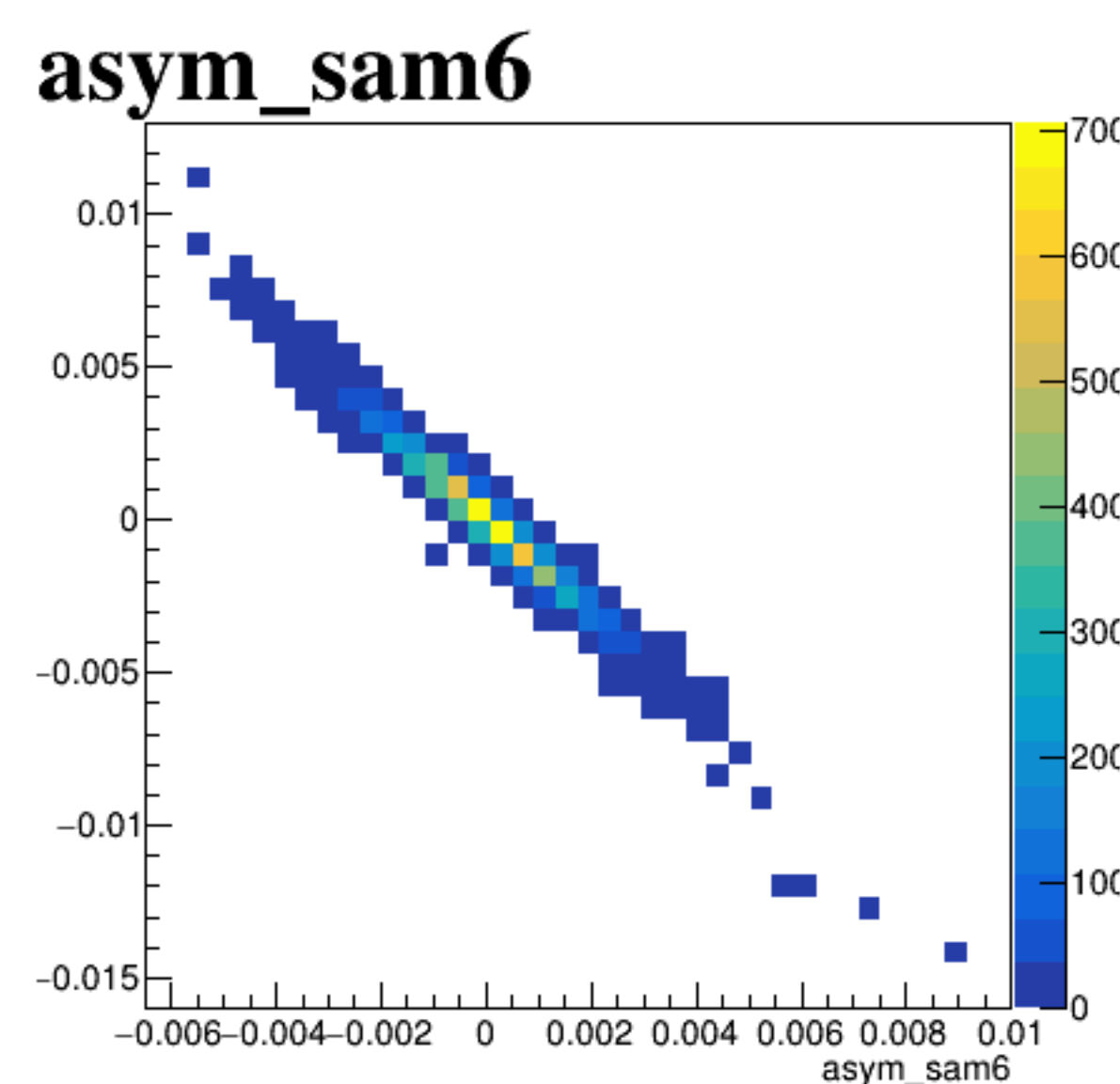
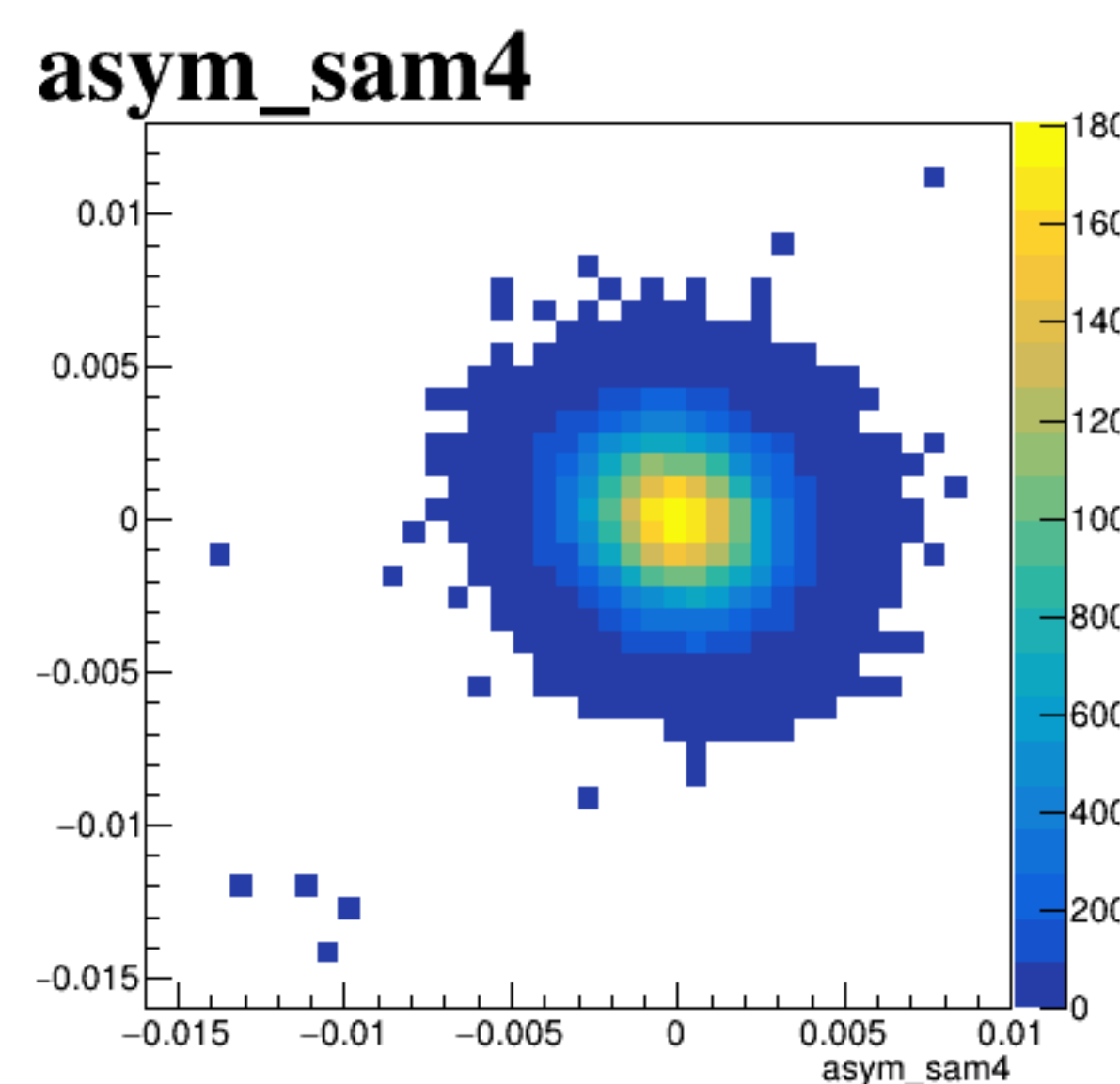
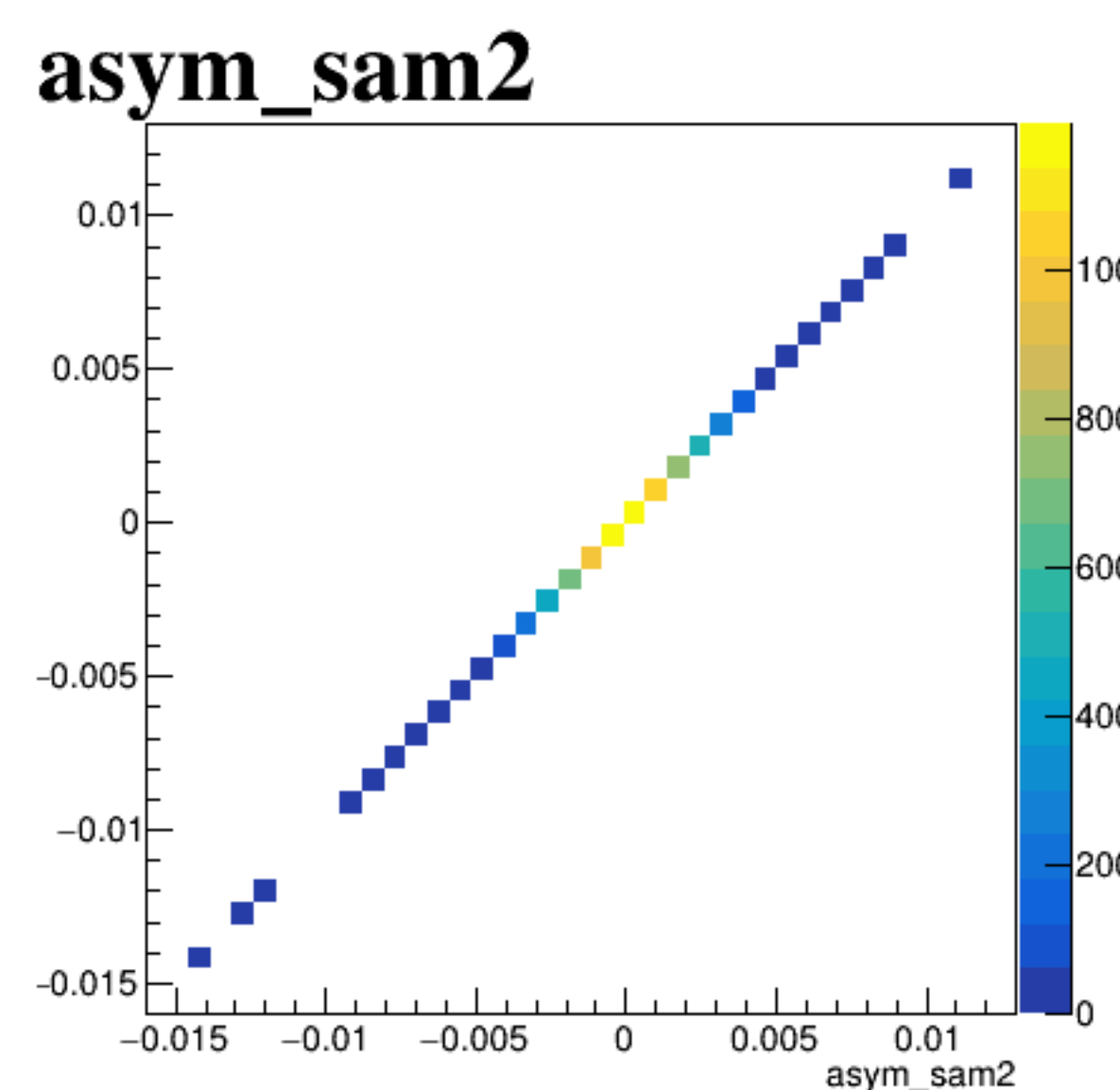


asym_sam5/ppm {ErrorFlag==0}

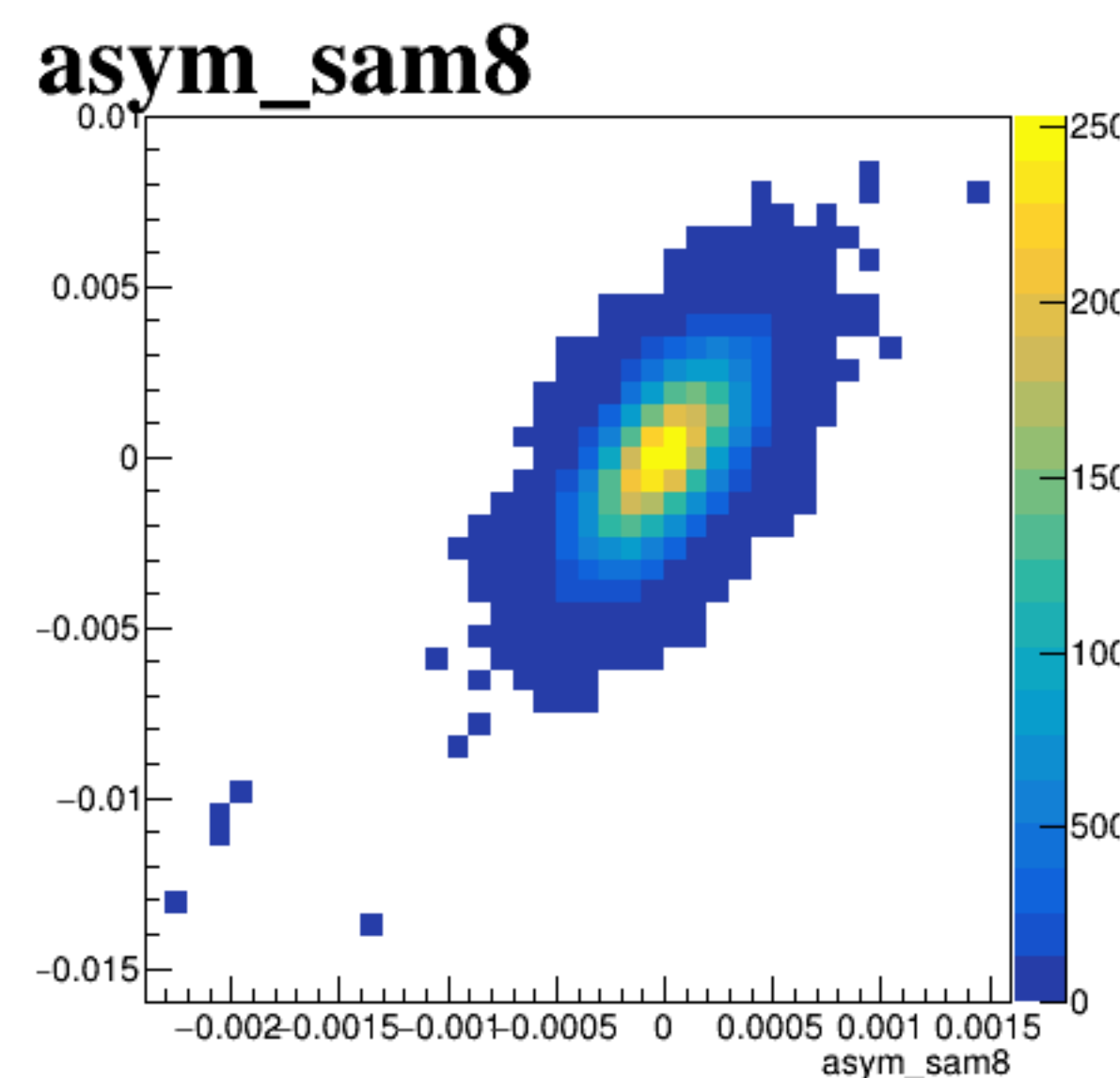
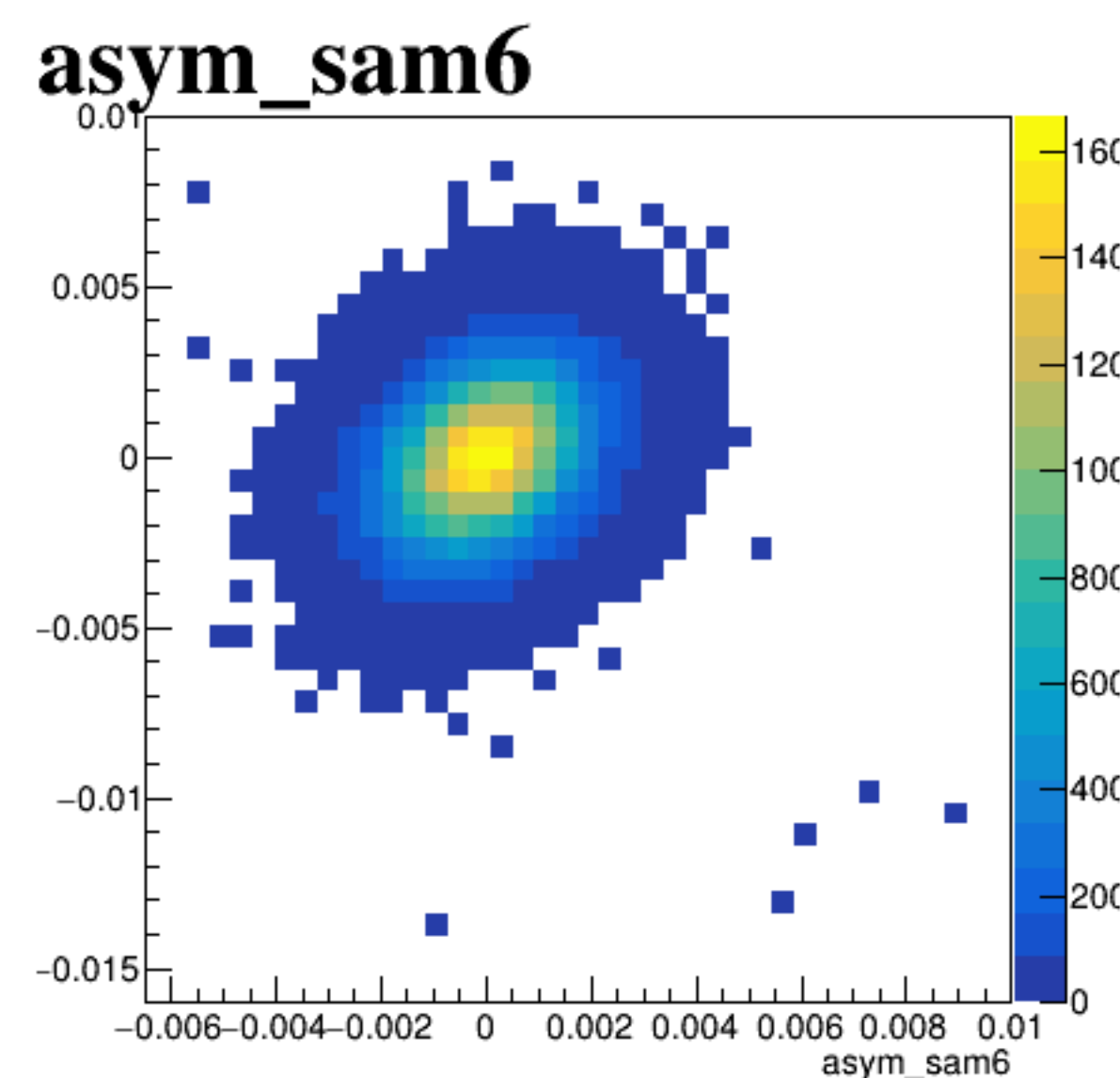
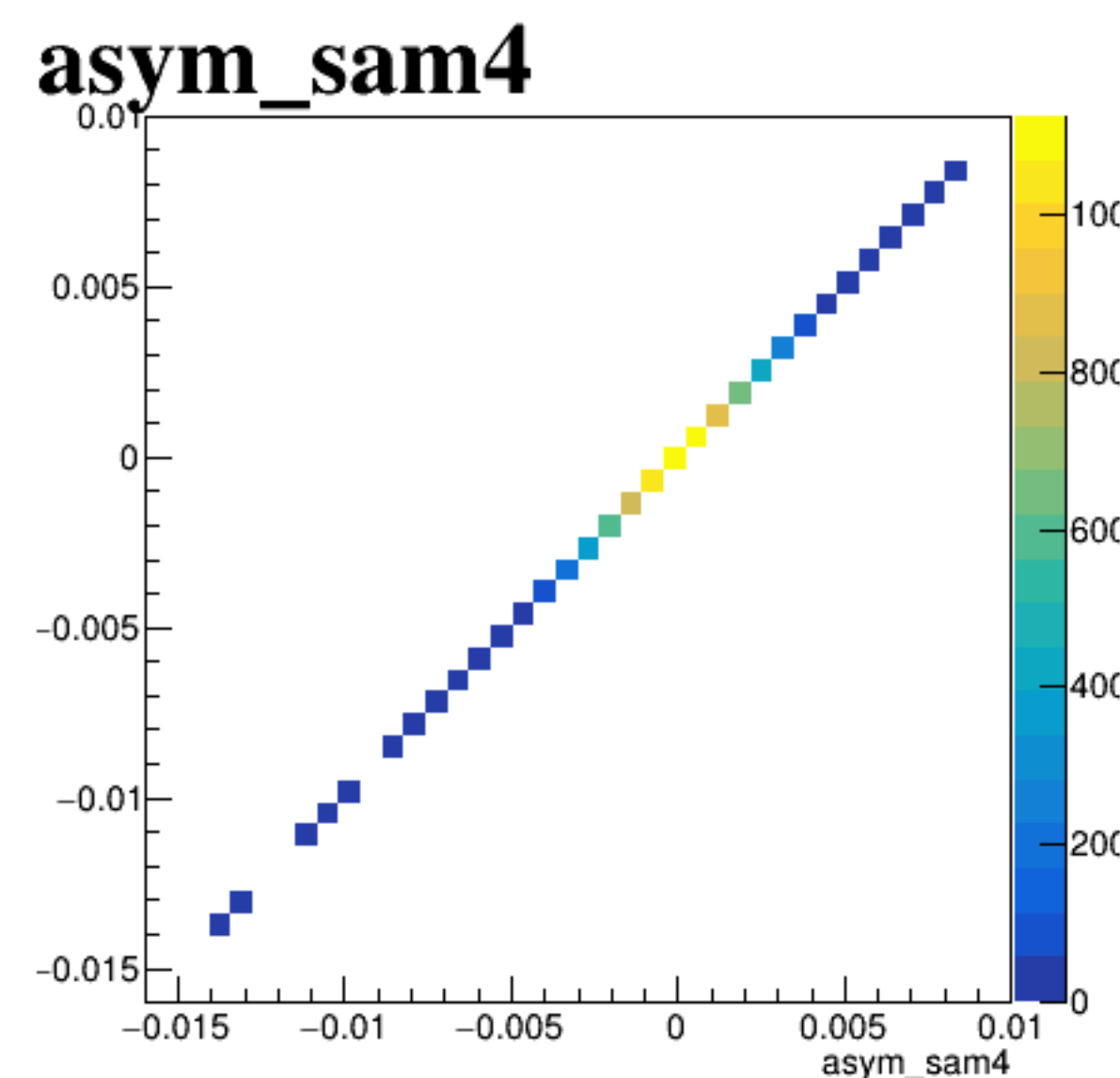
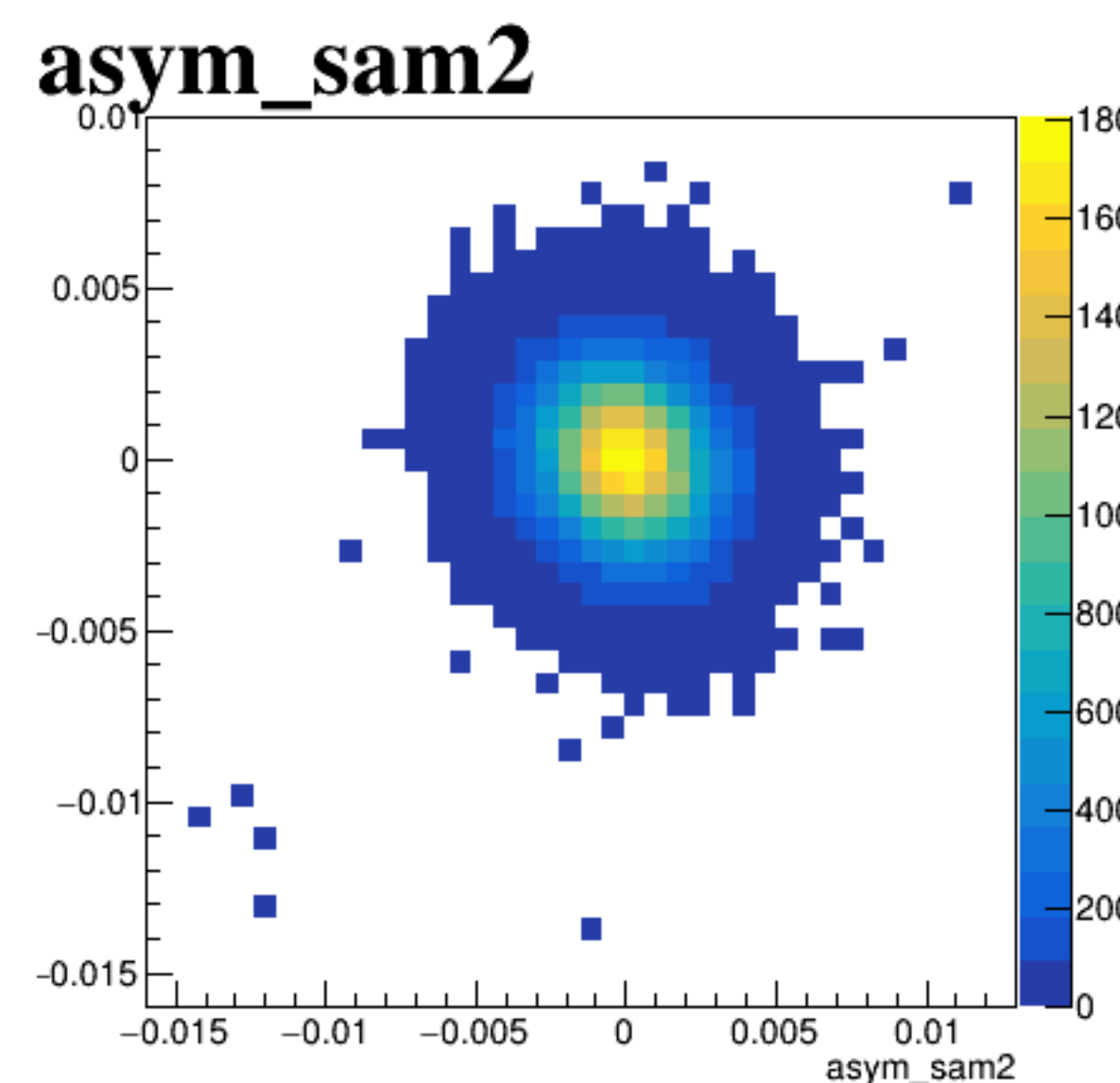




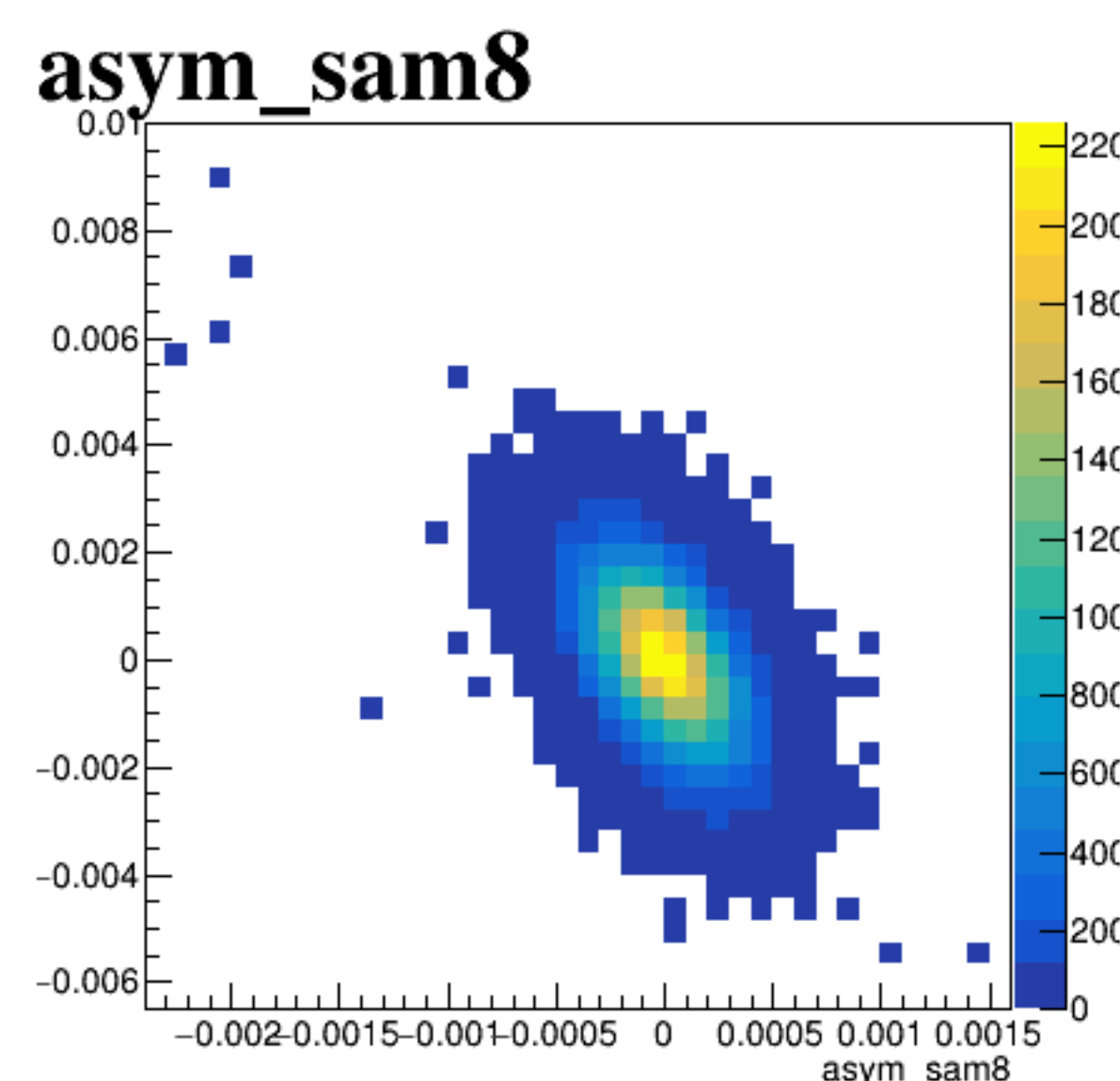
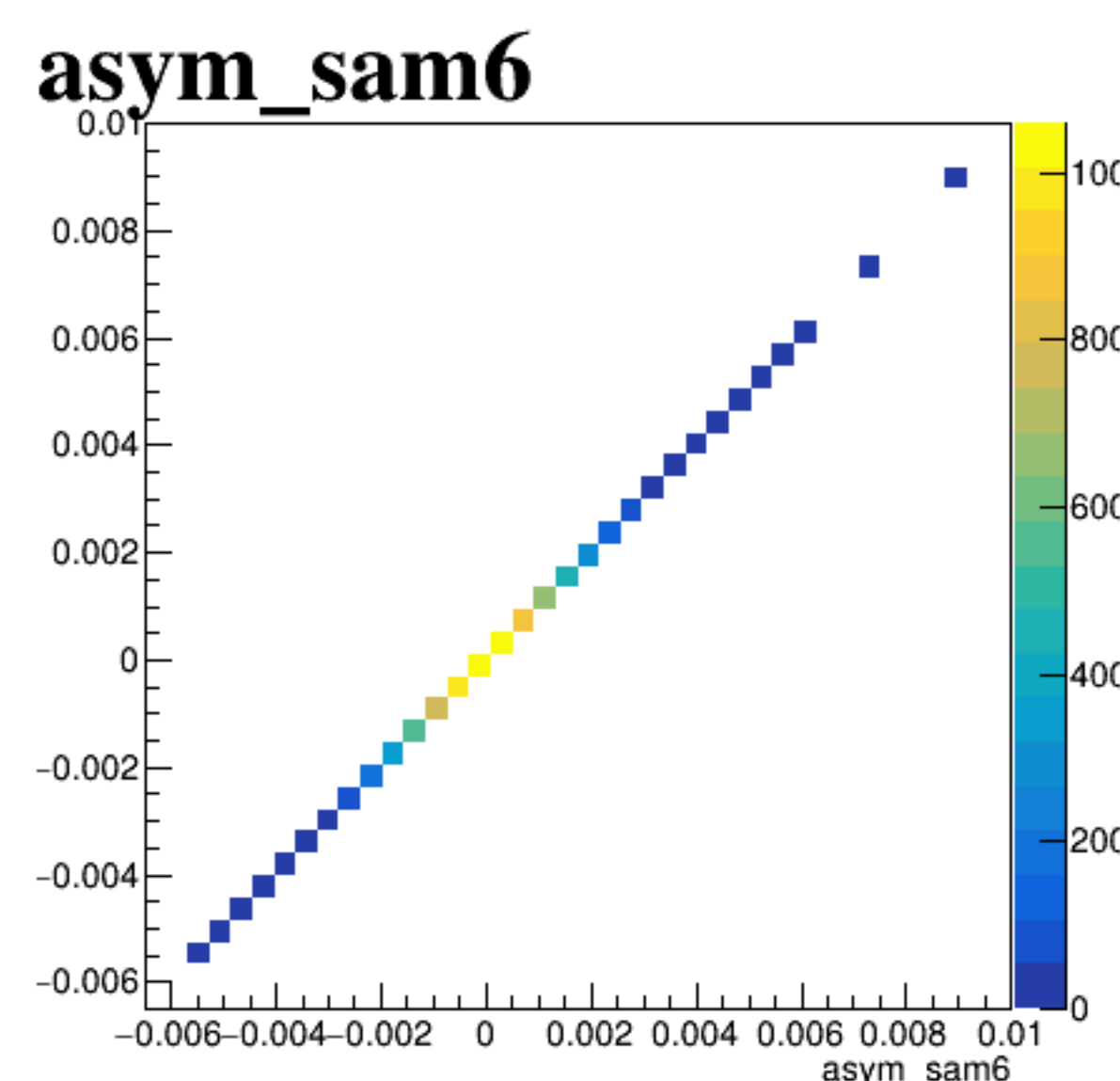
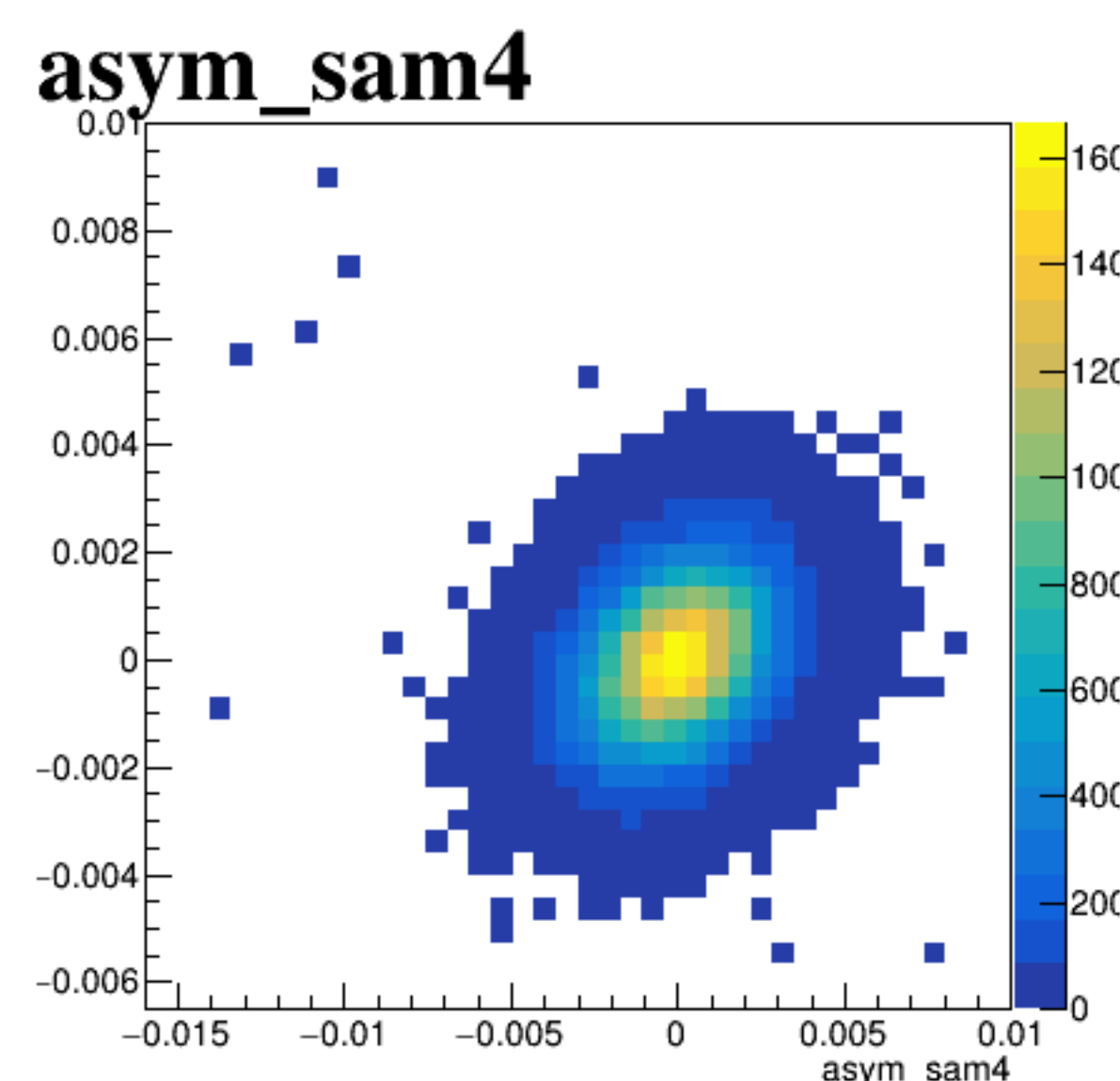
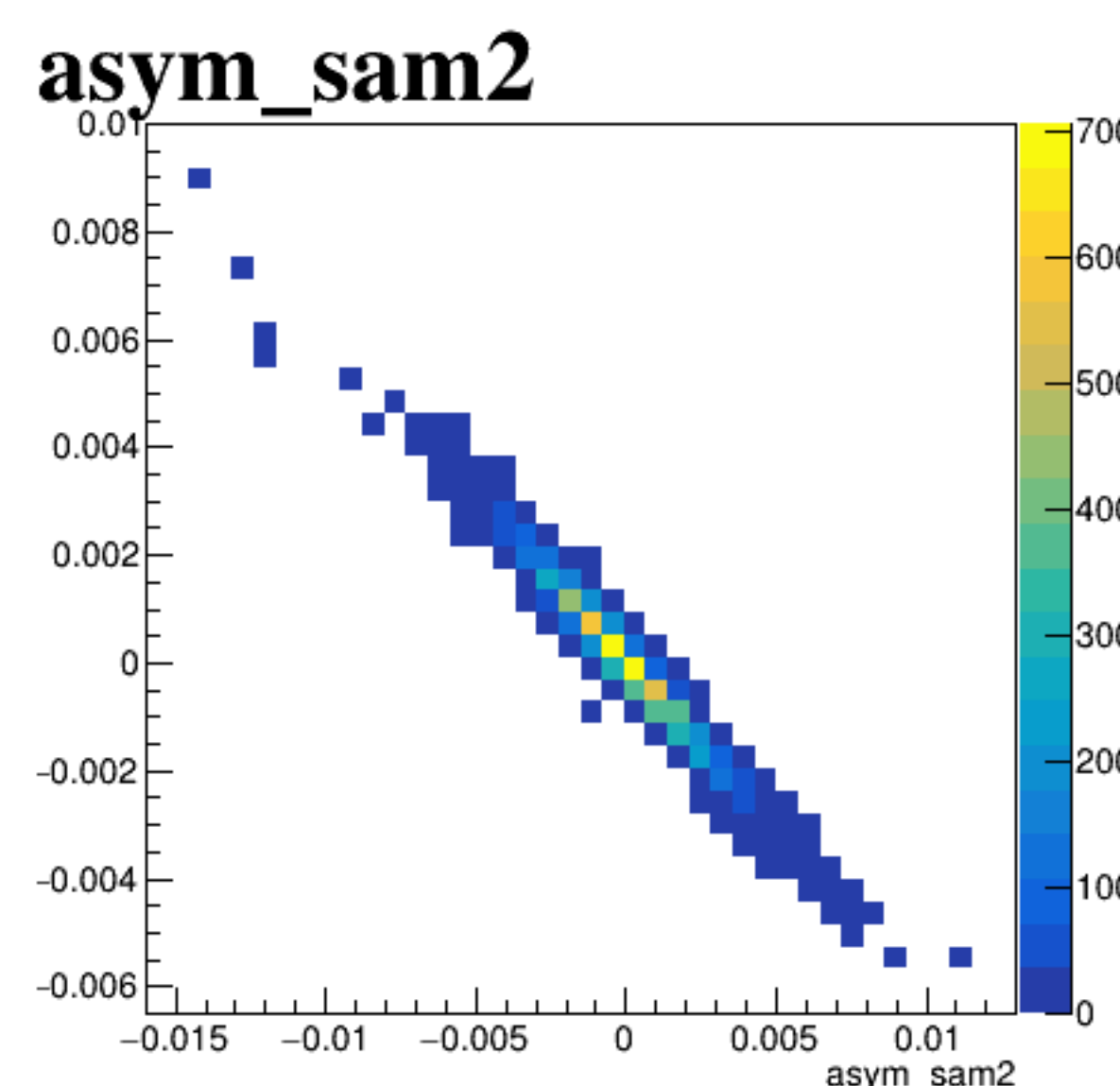
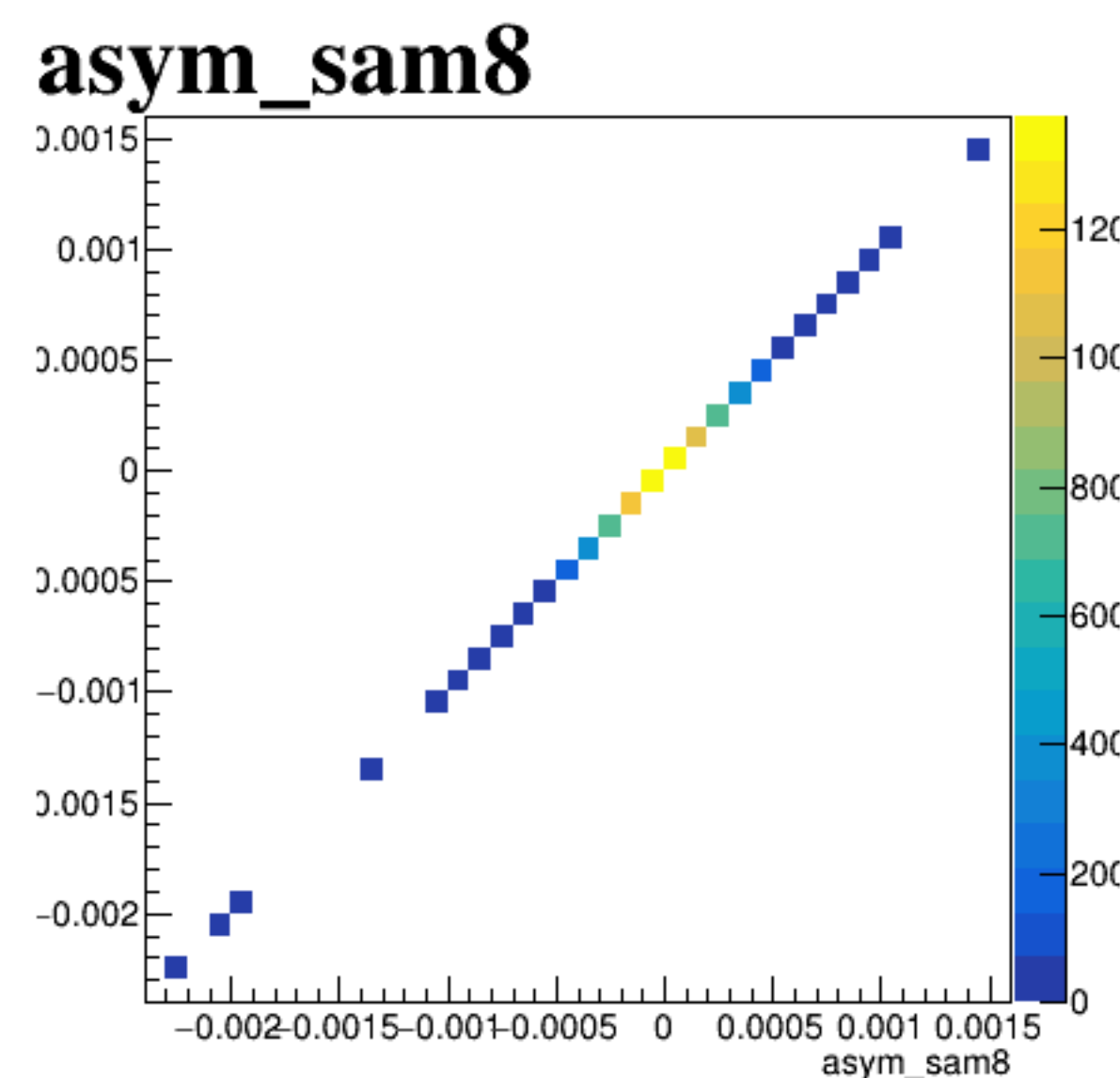
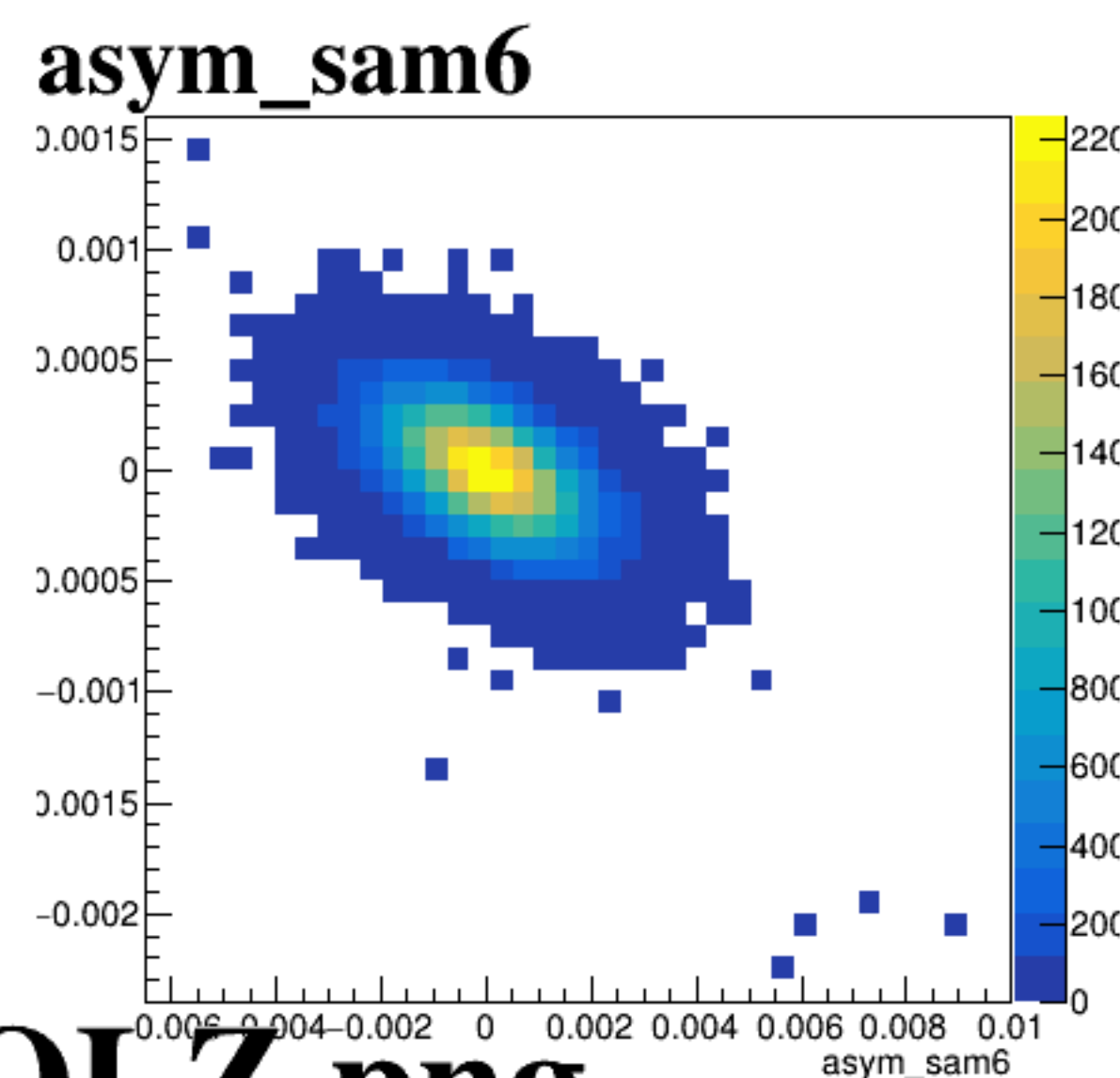
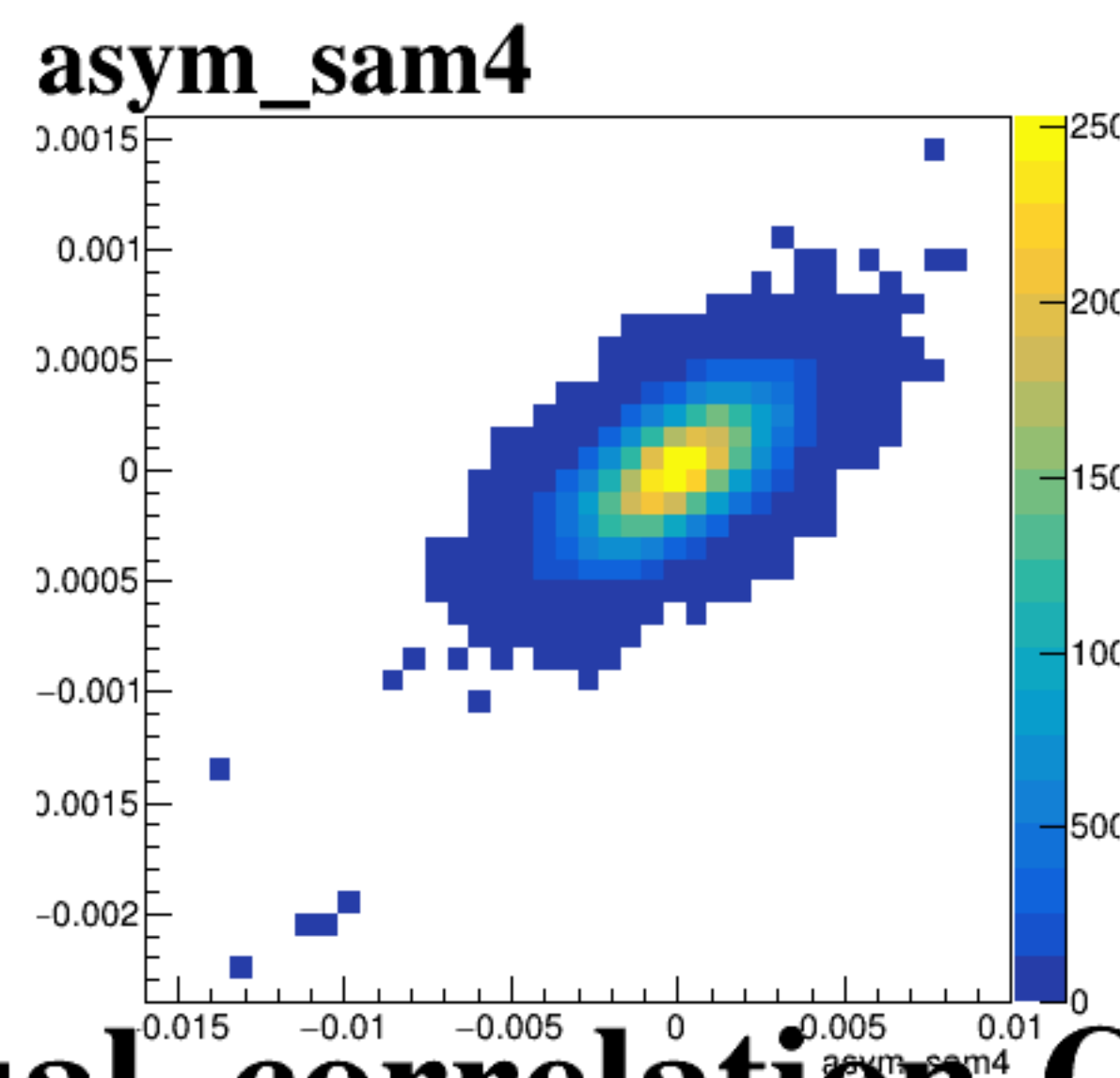
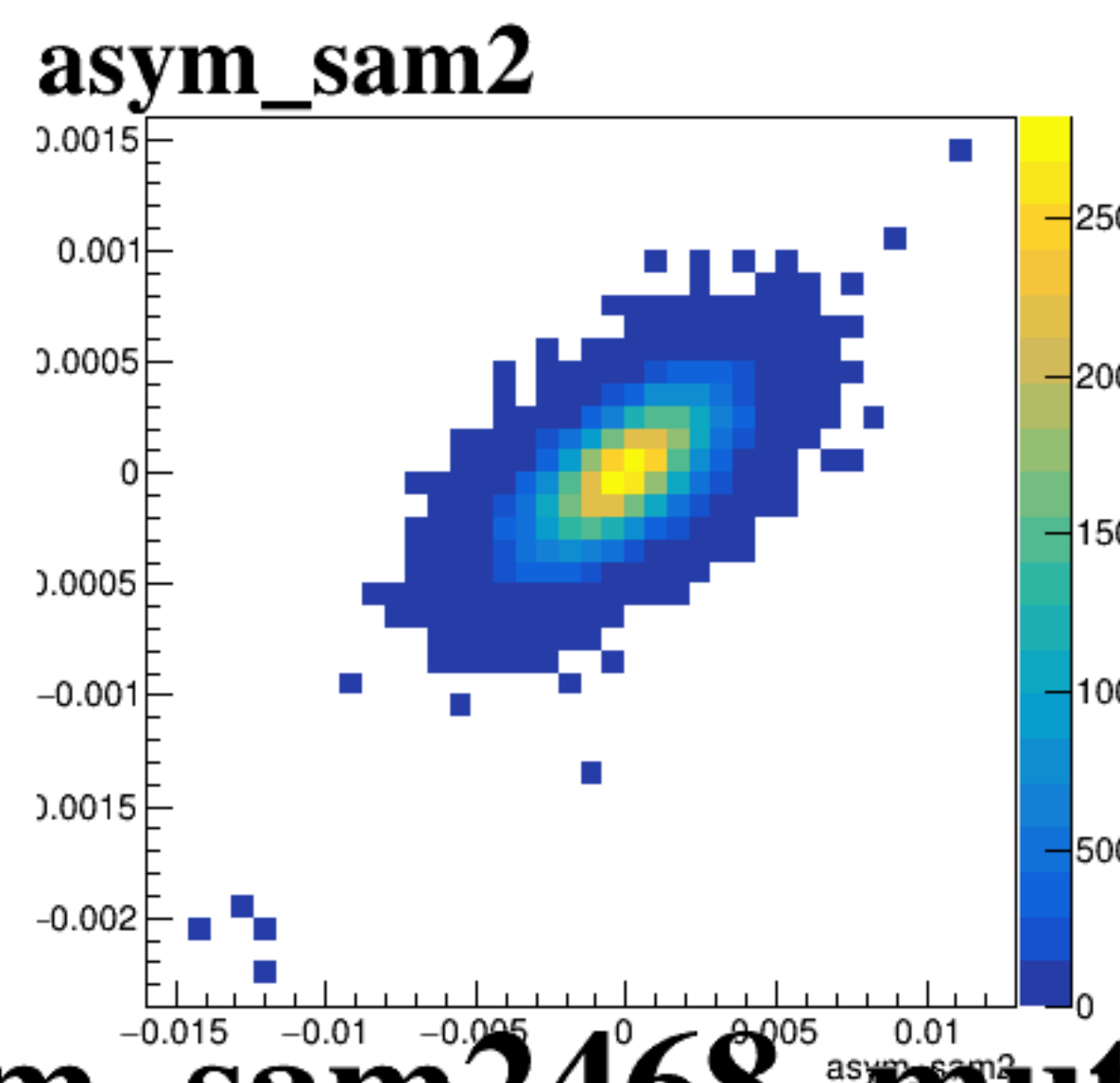
asym_sam2



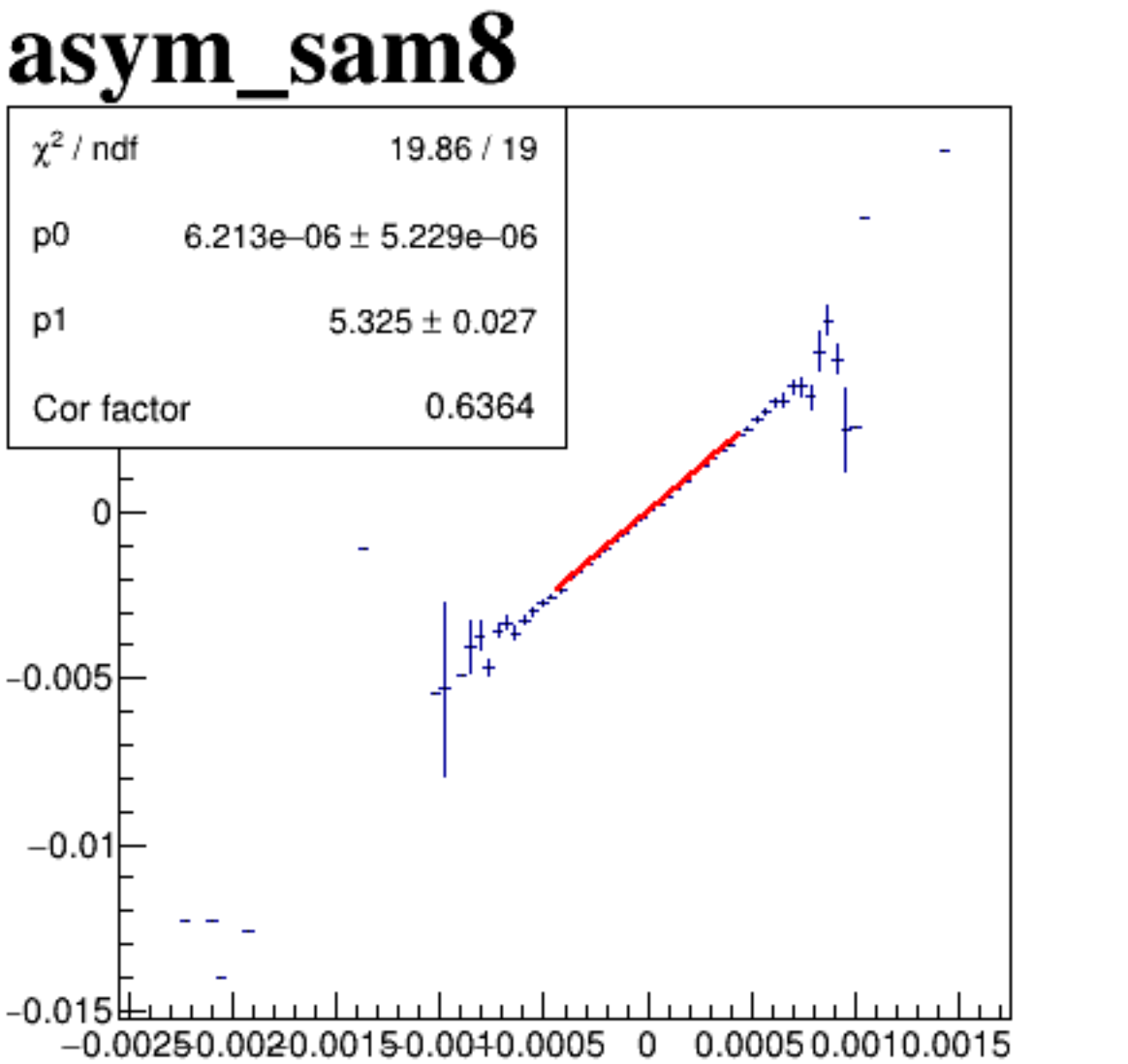
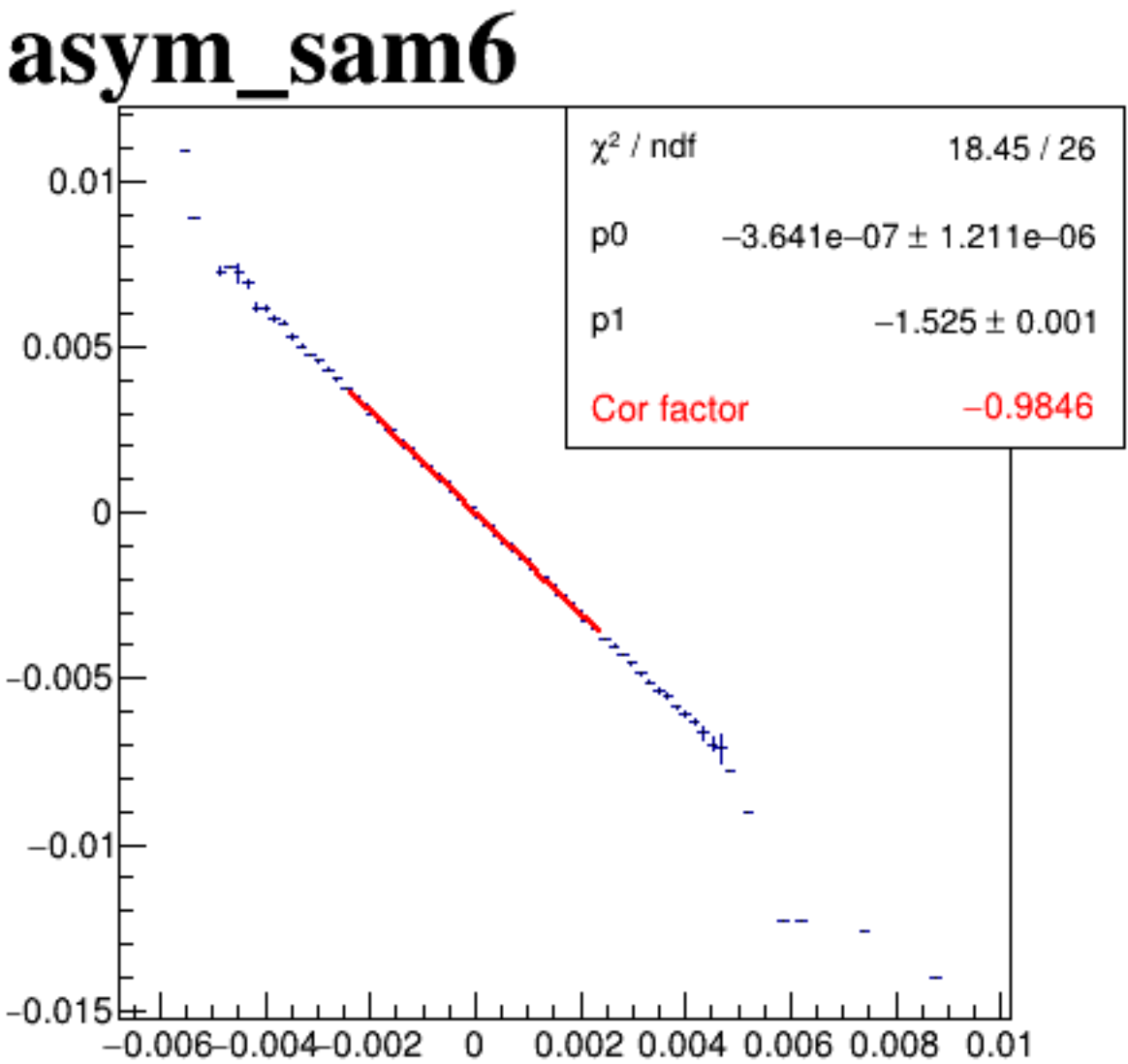
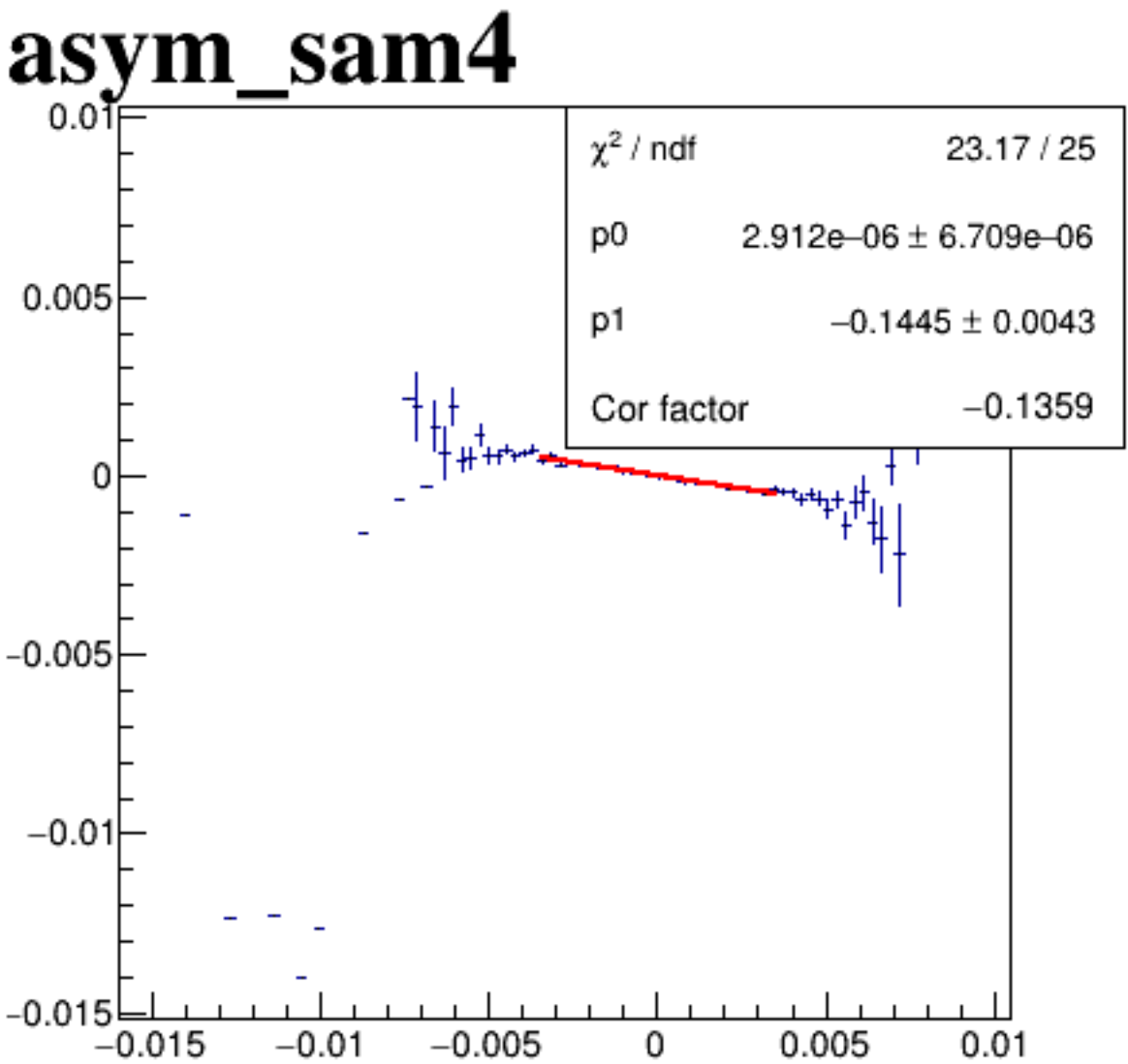
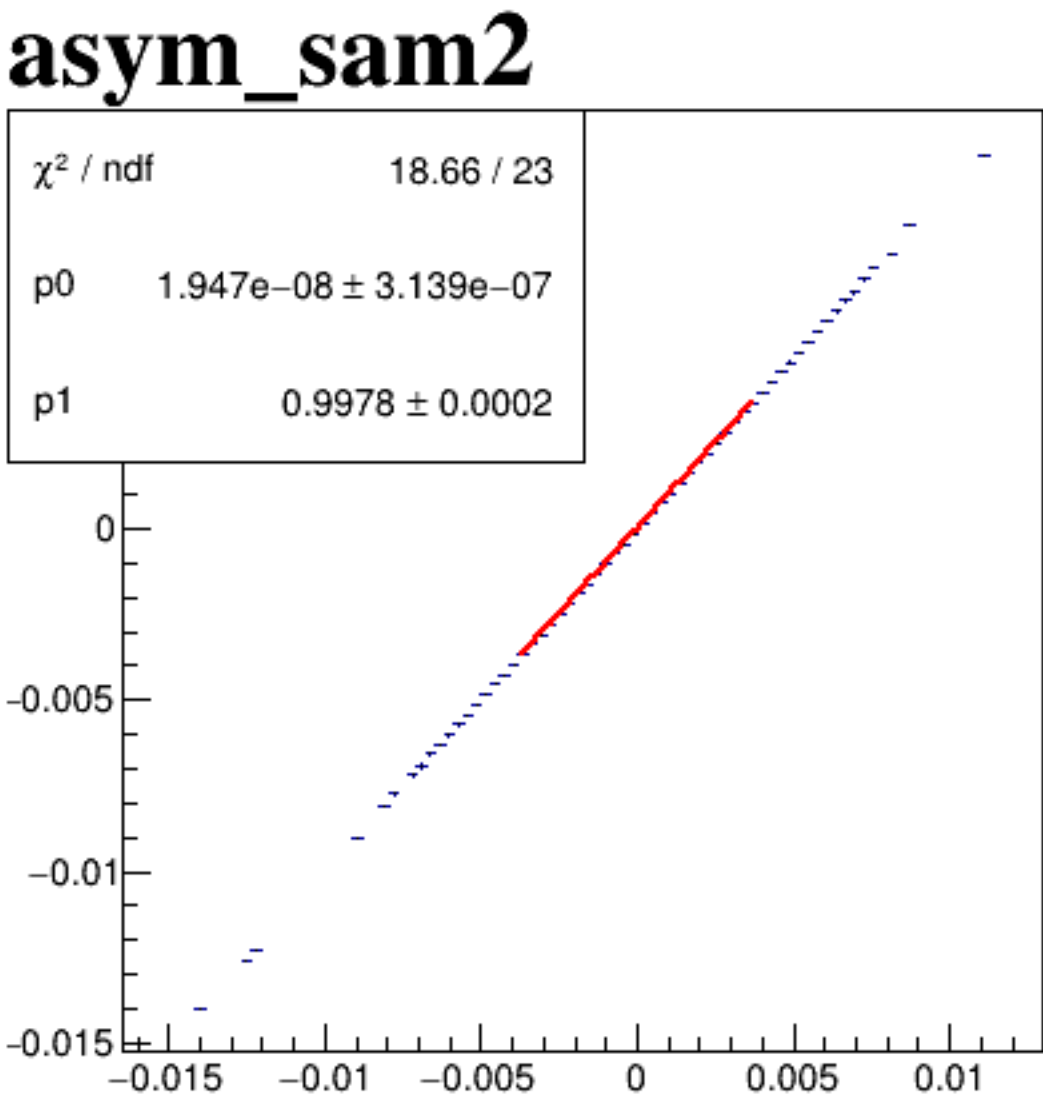
asym_sam4



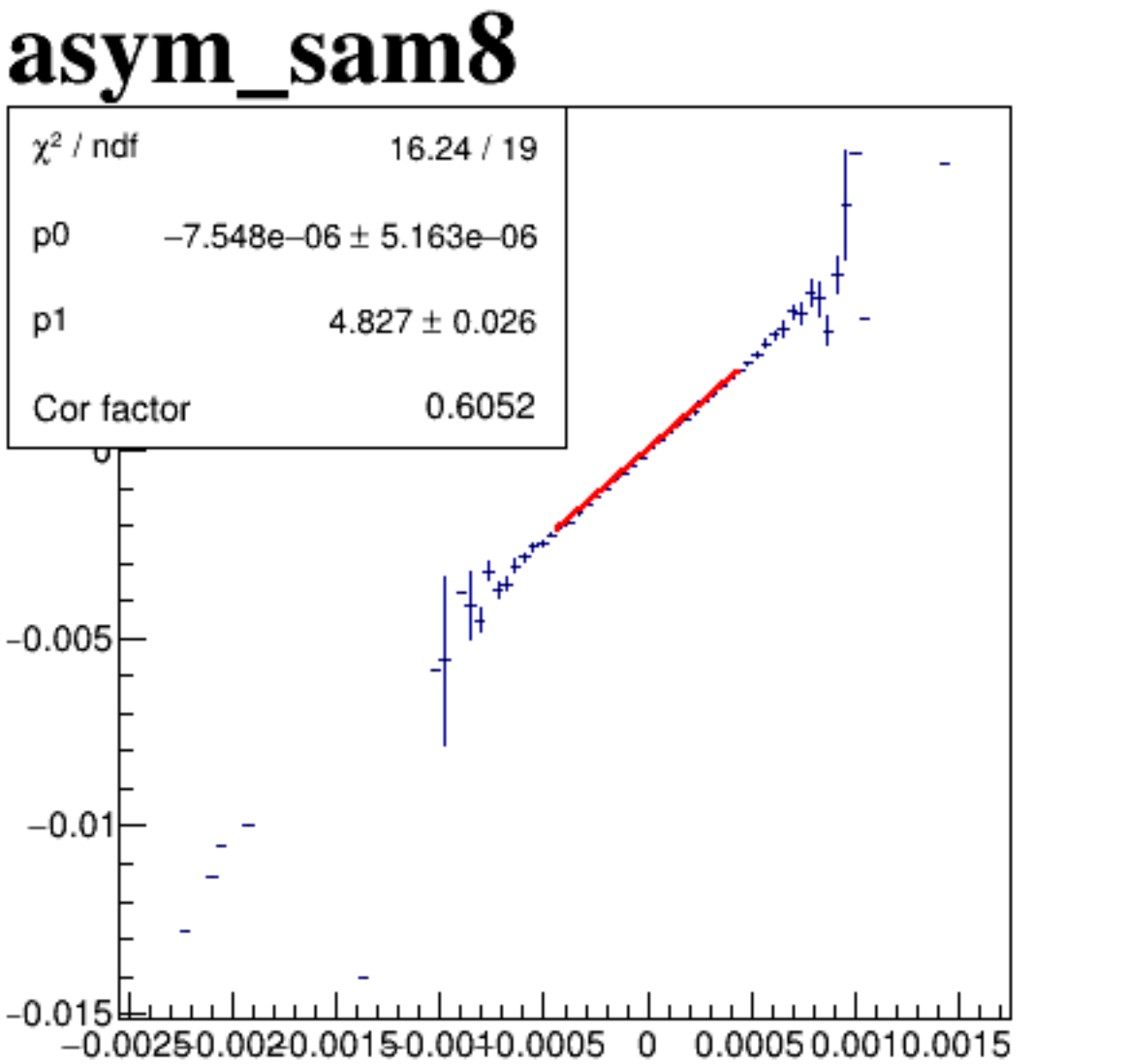
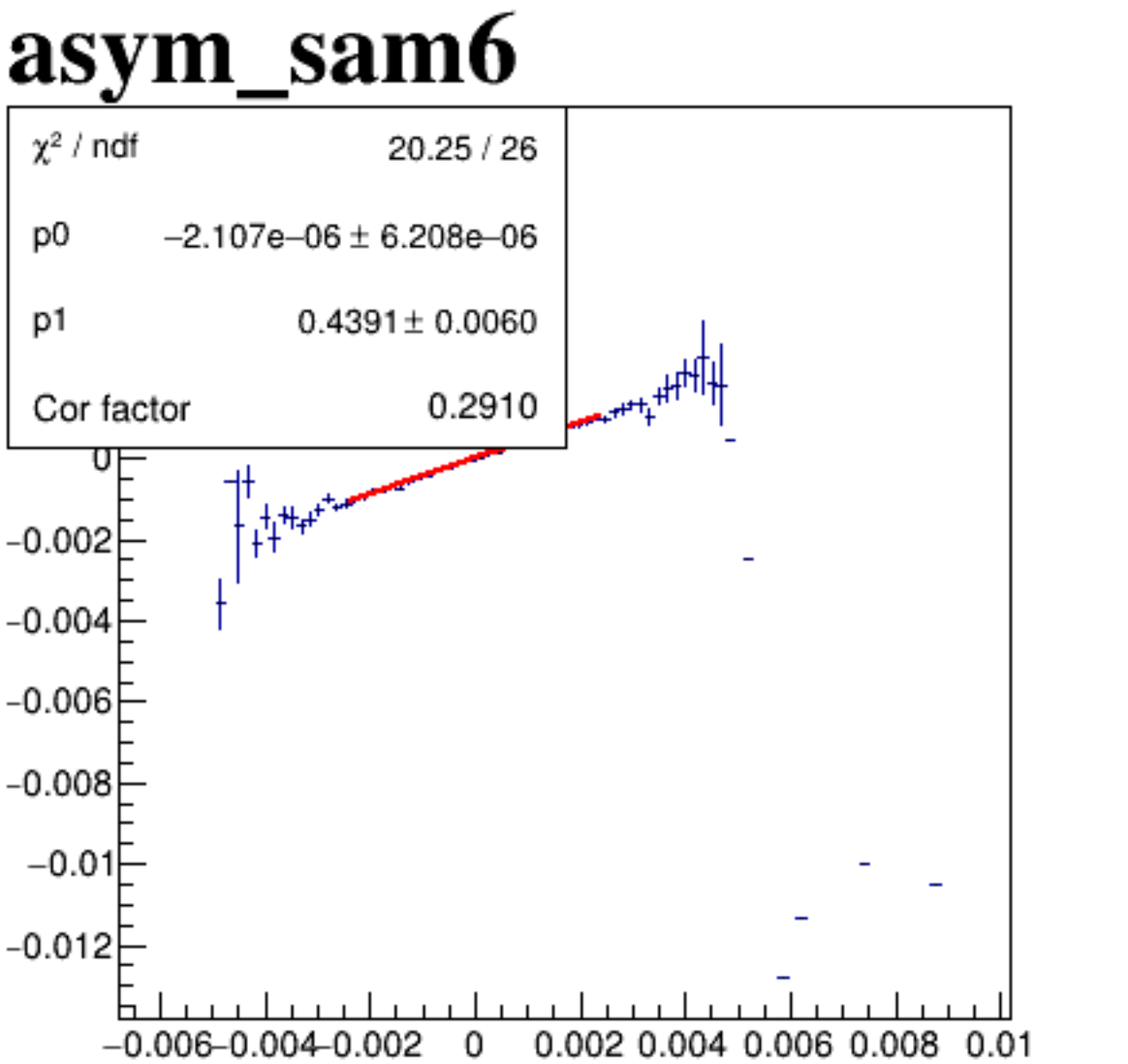
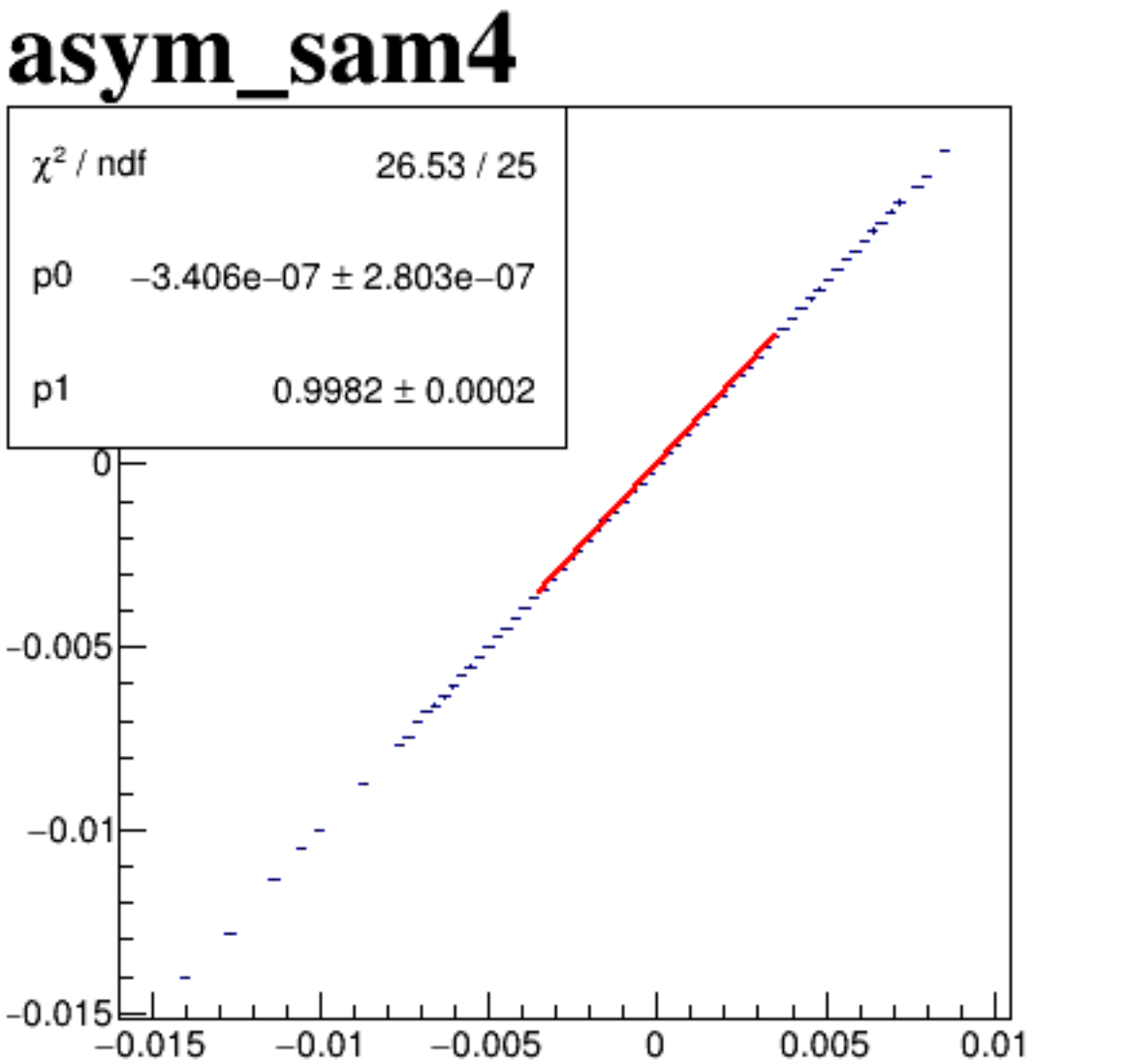
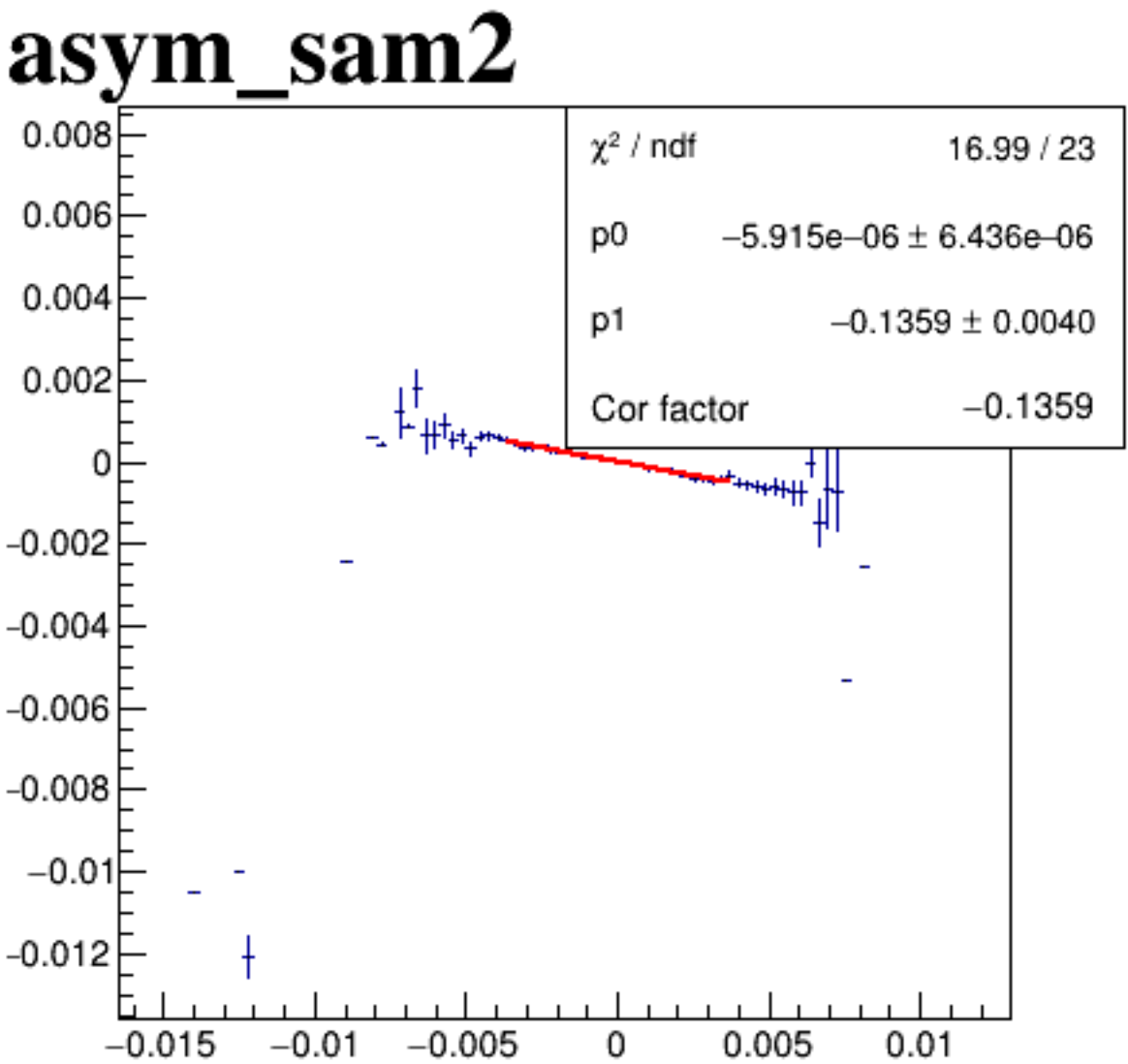
asym_sam6

**asym_sam8**

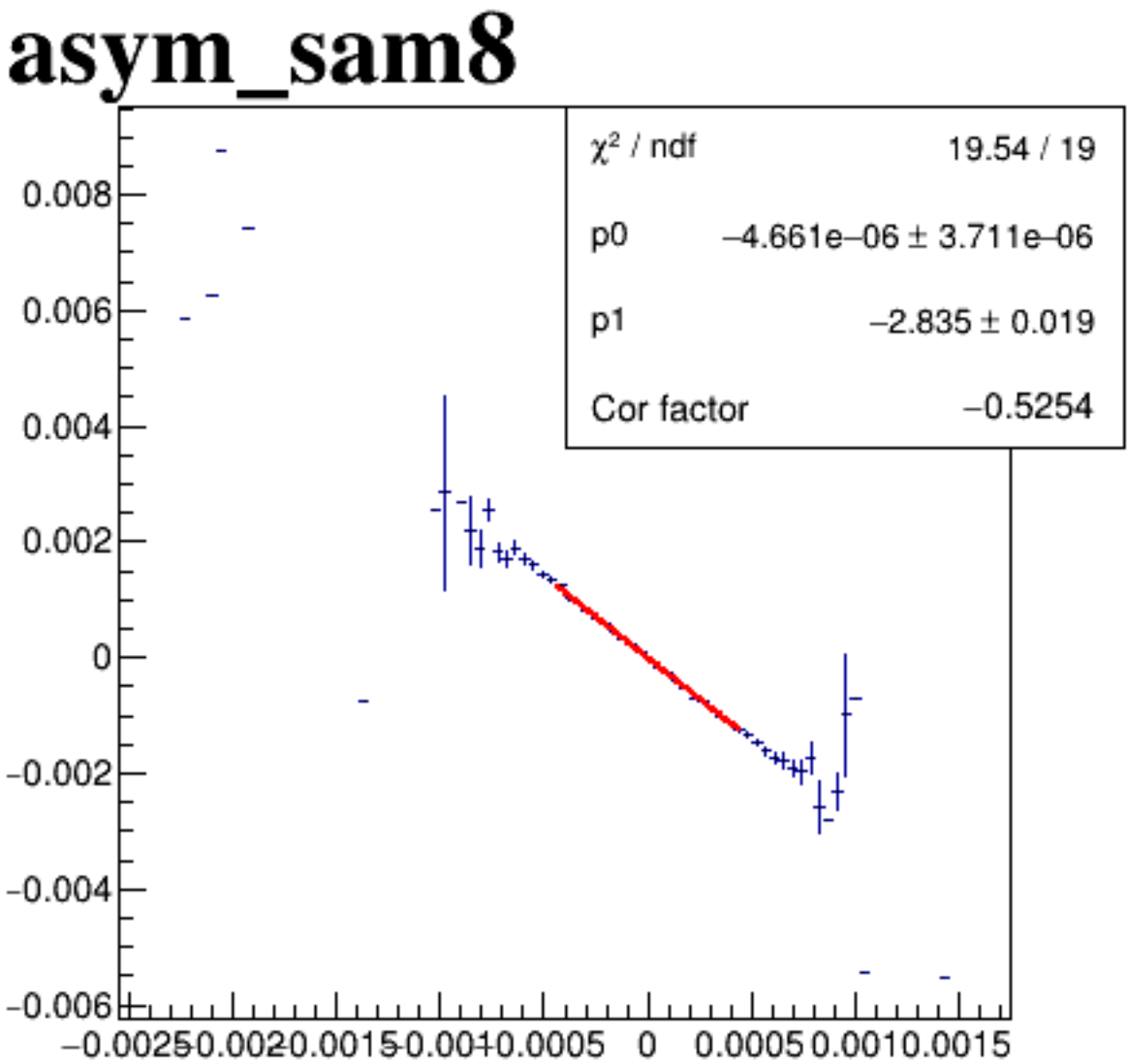
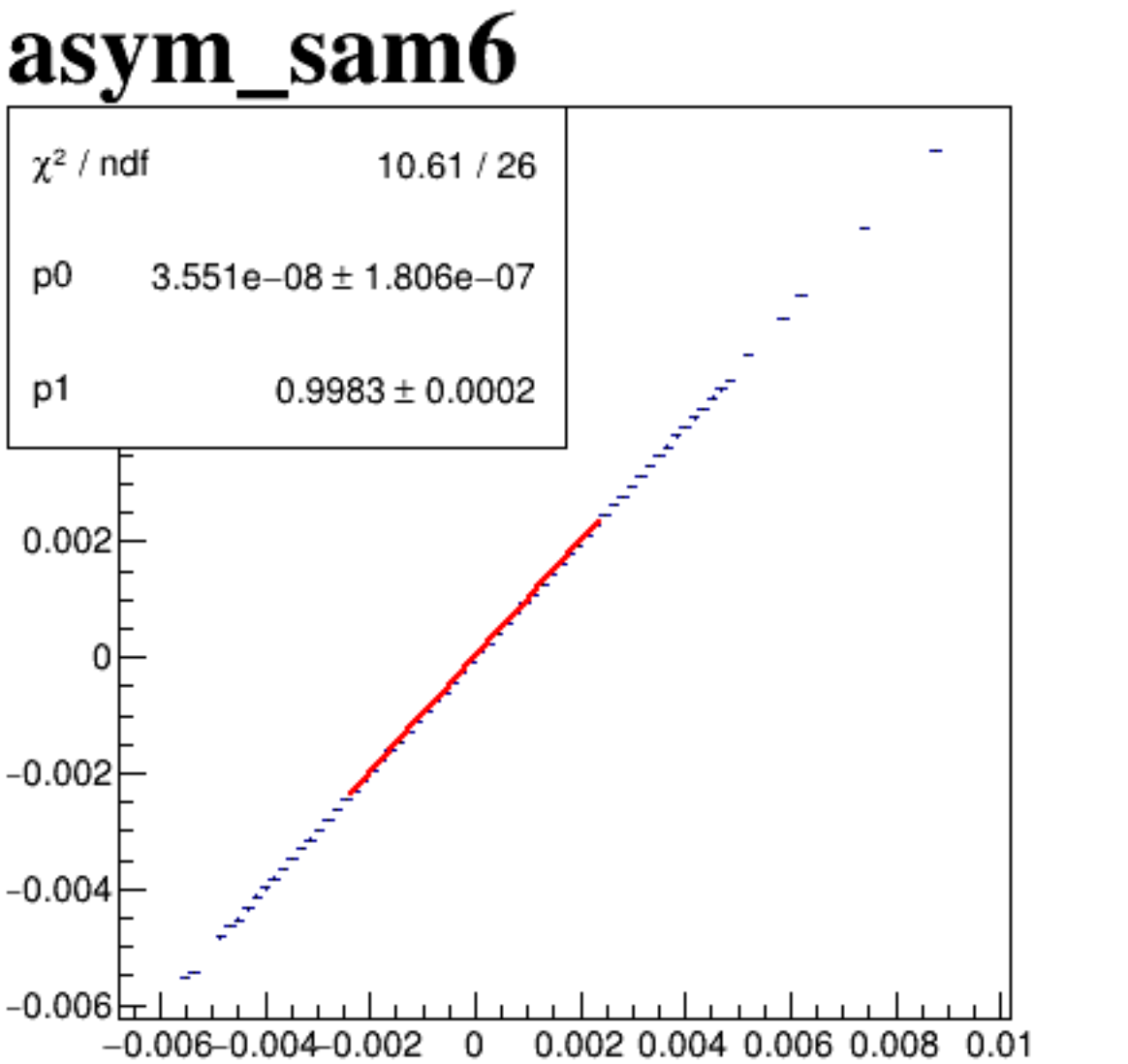
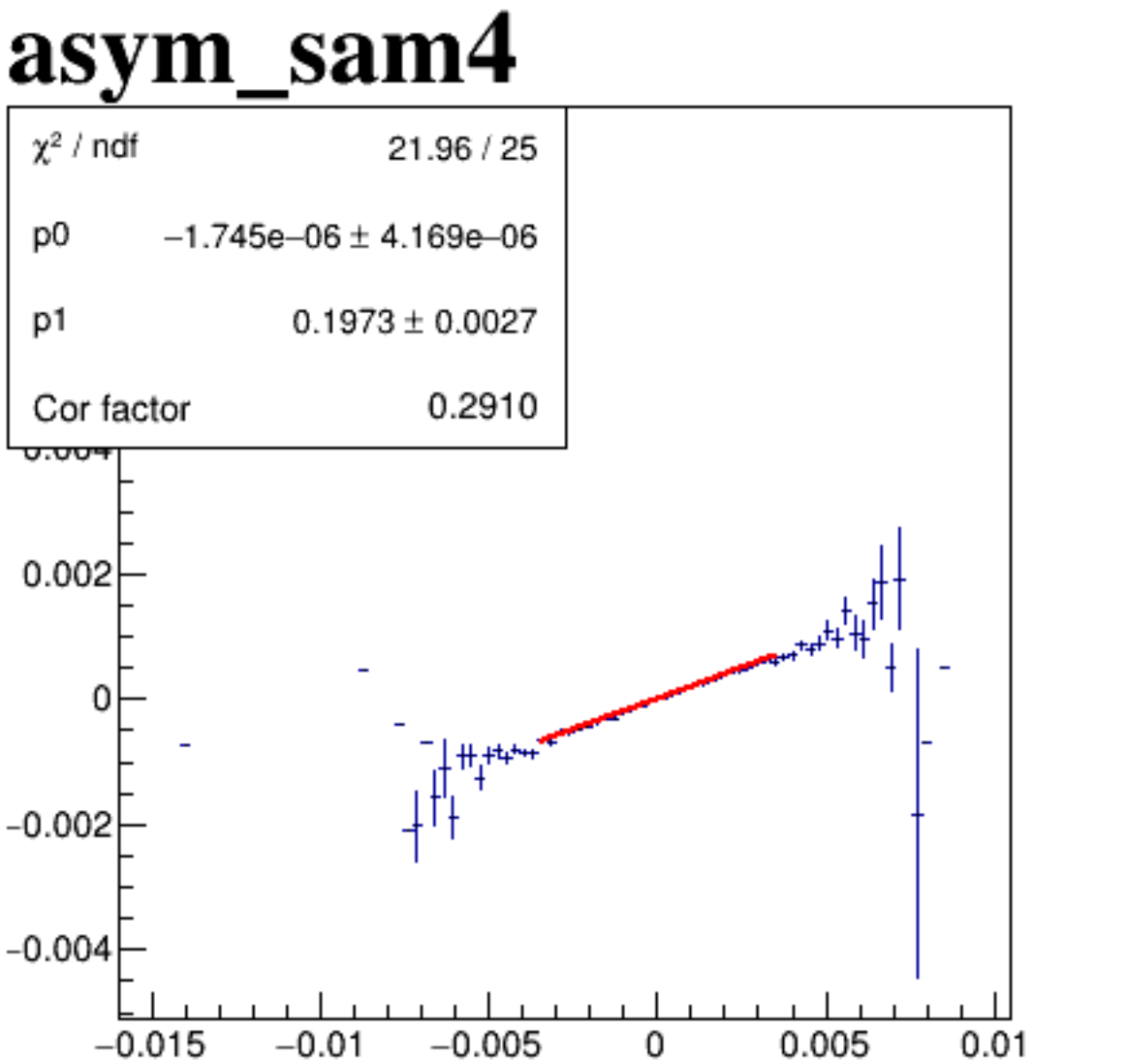
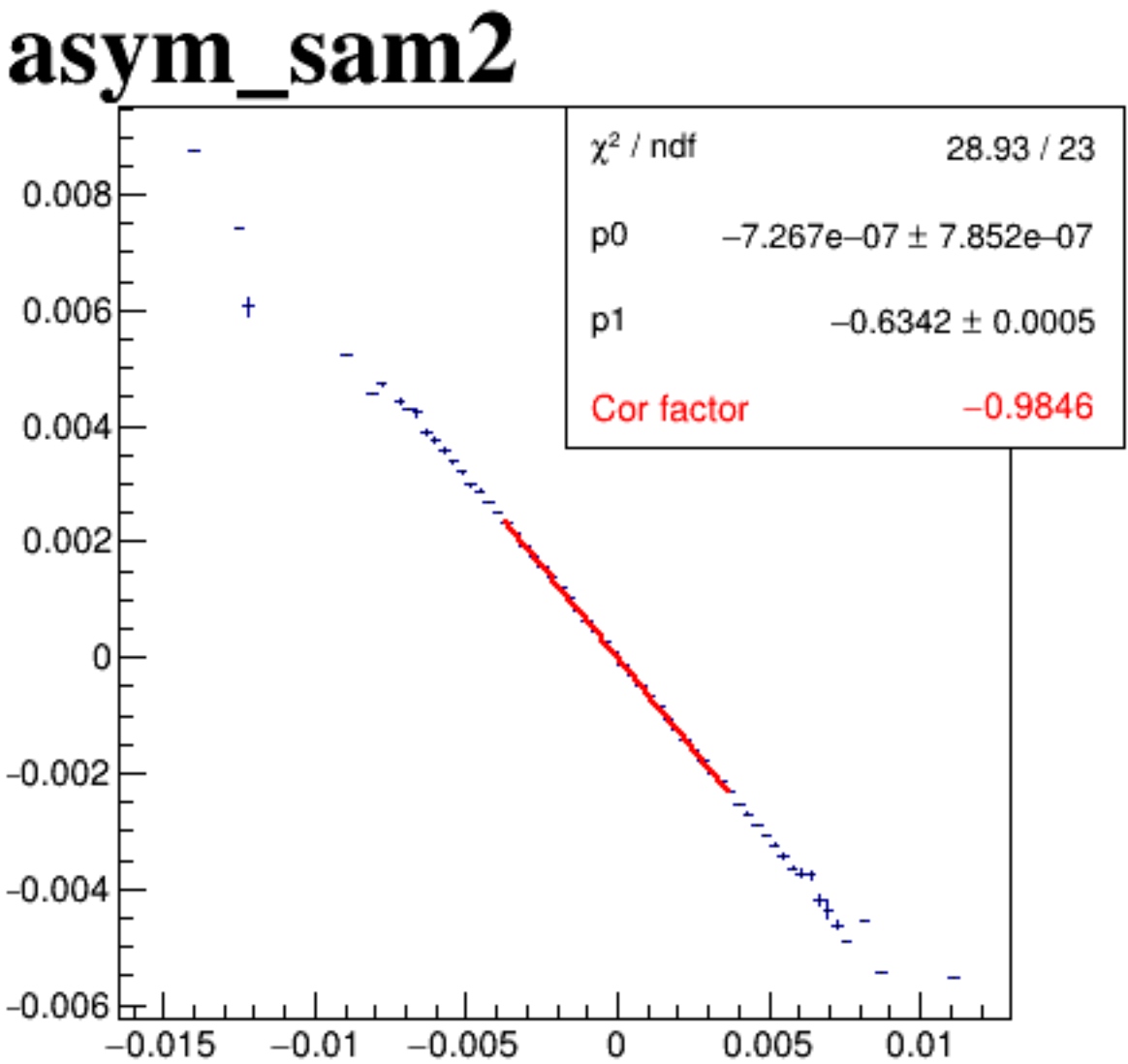
asym_sam2



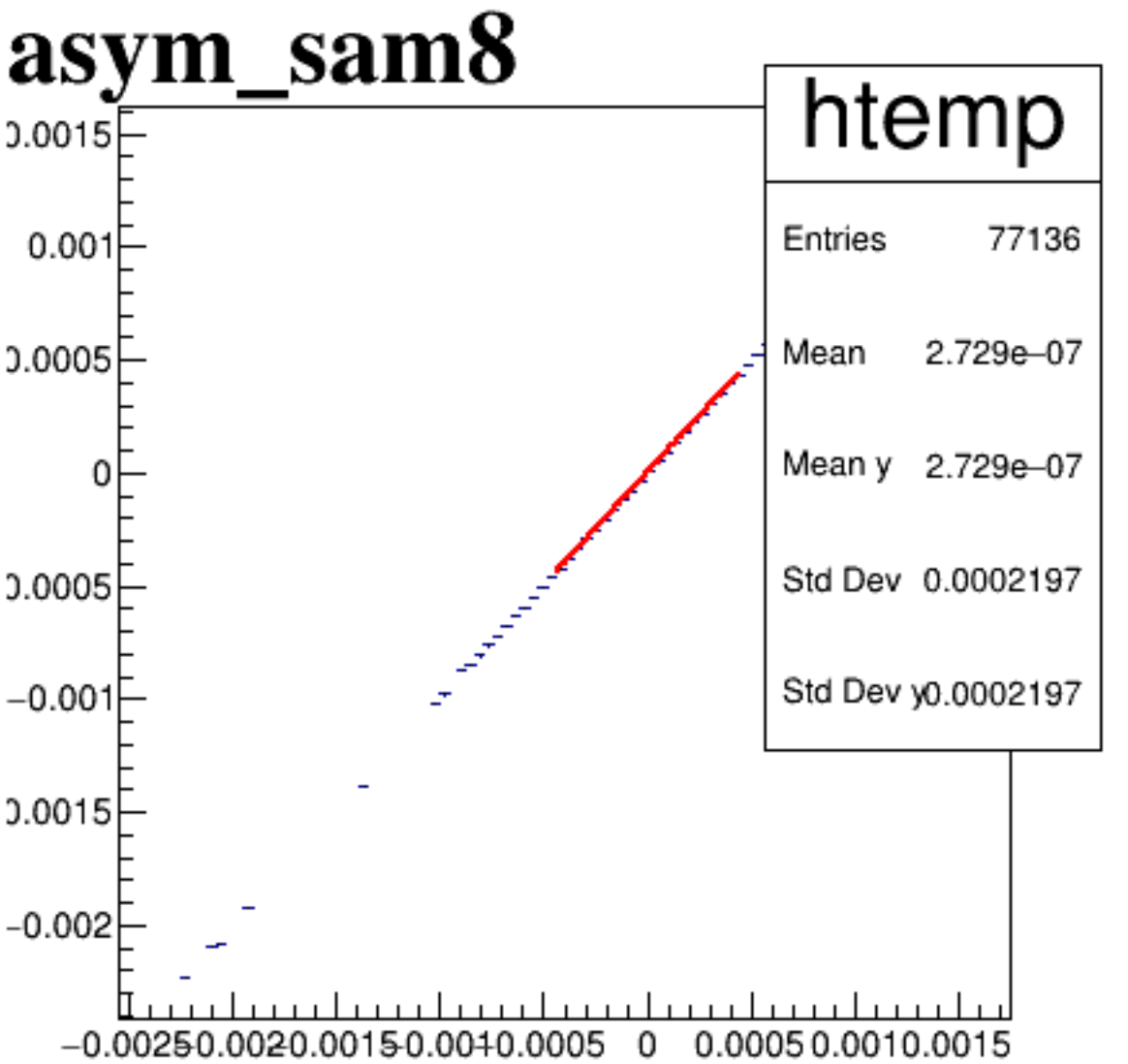
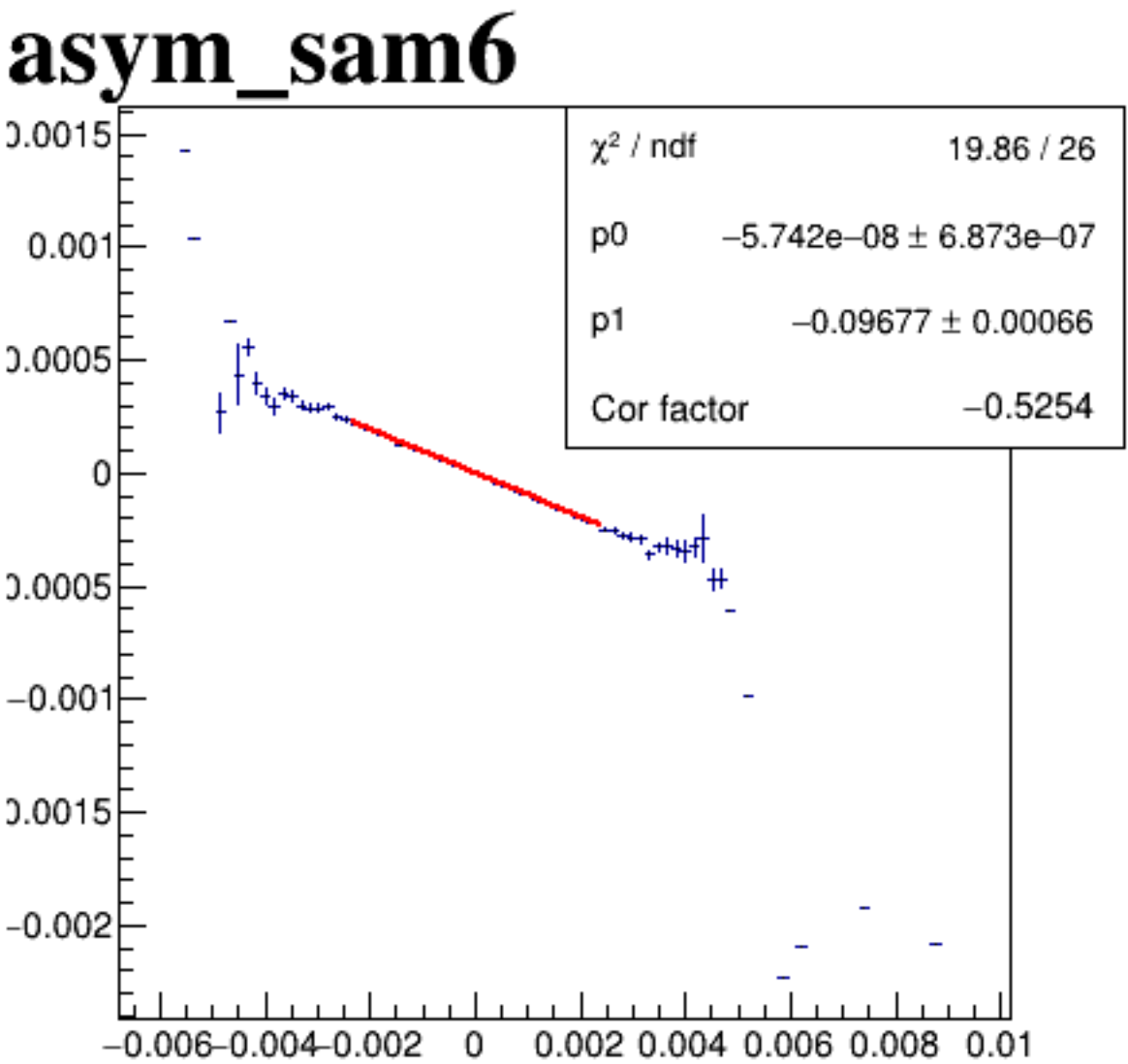
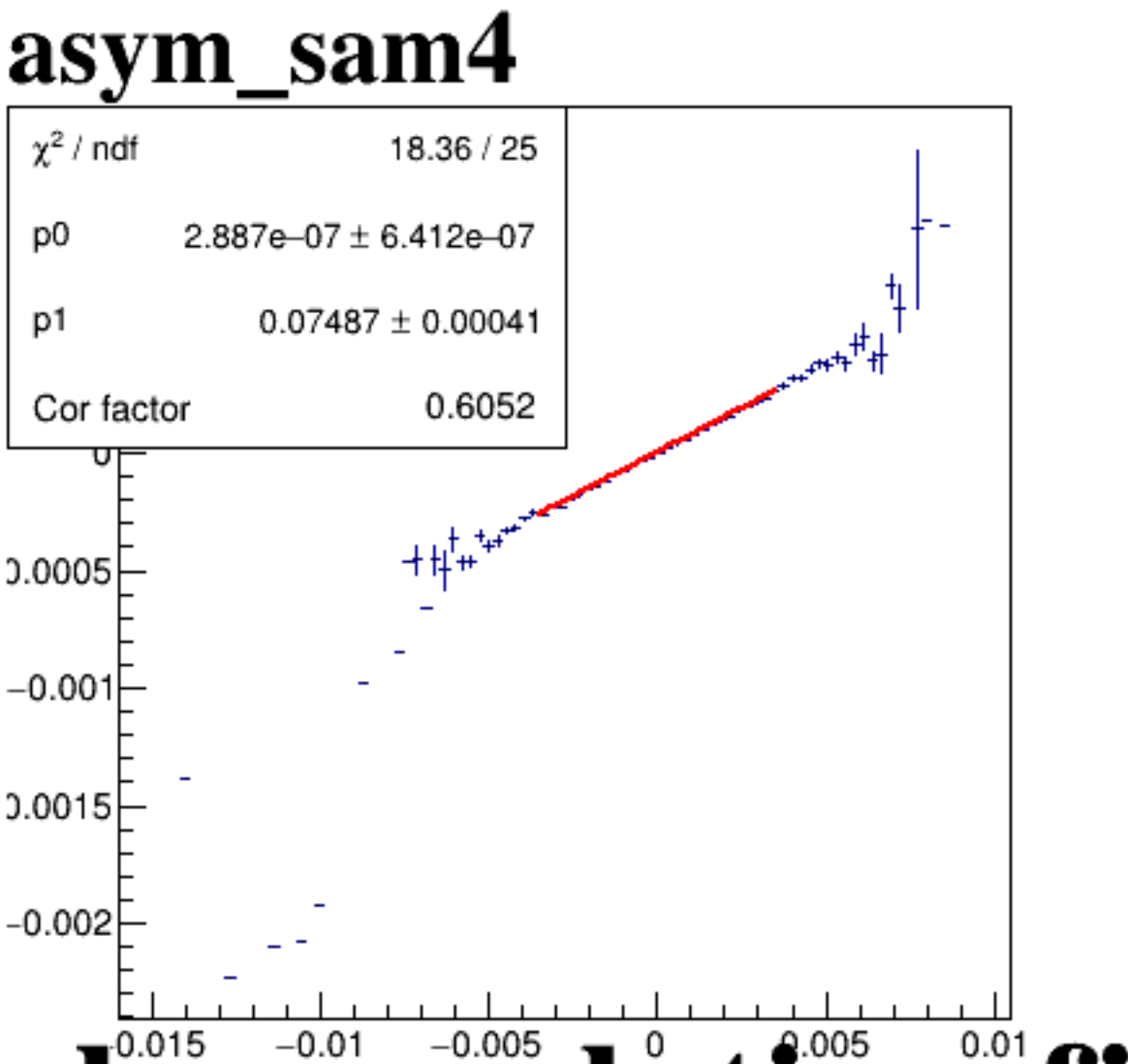
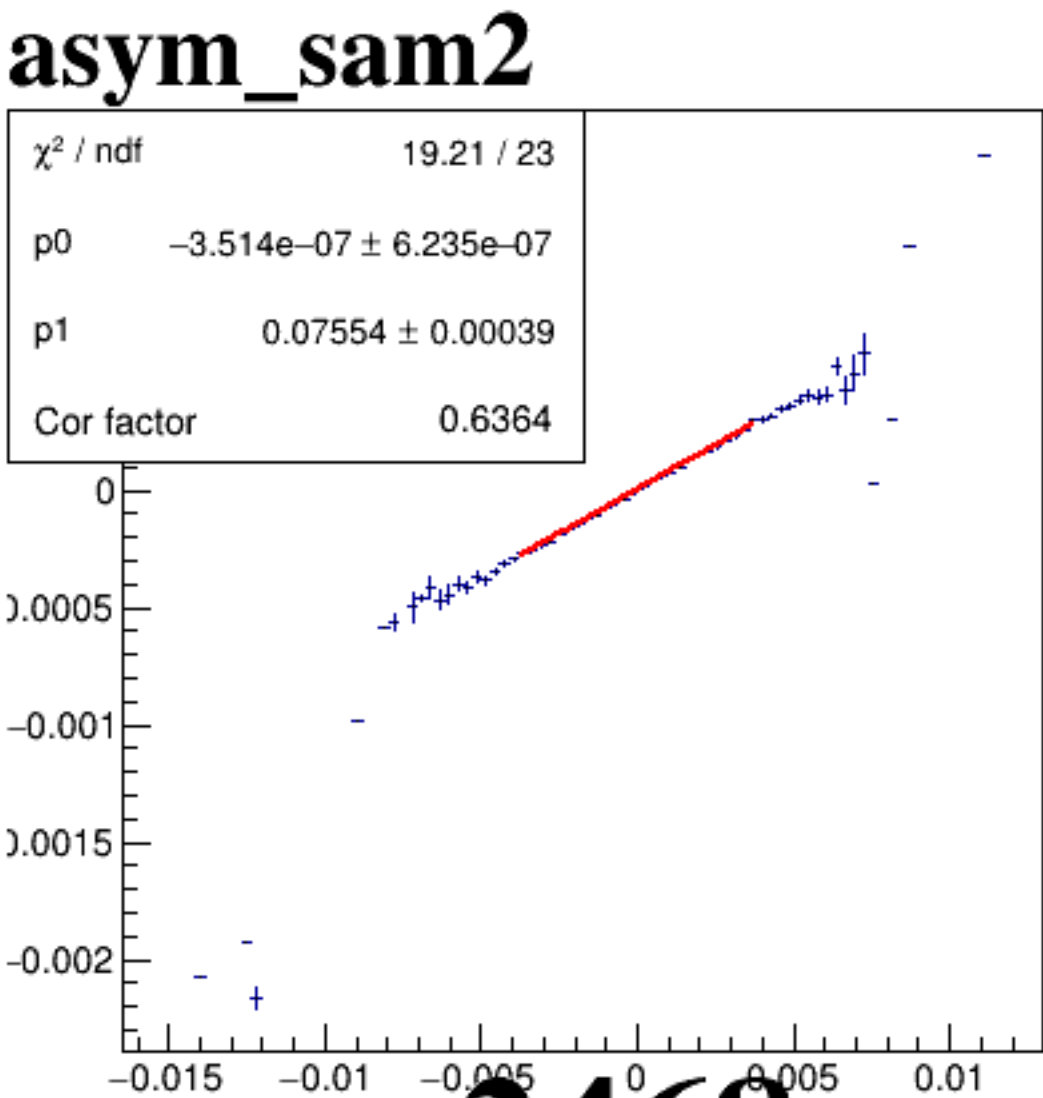
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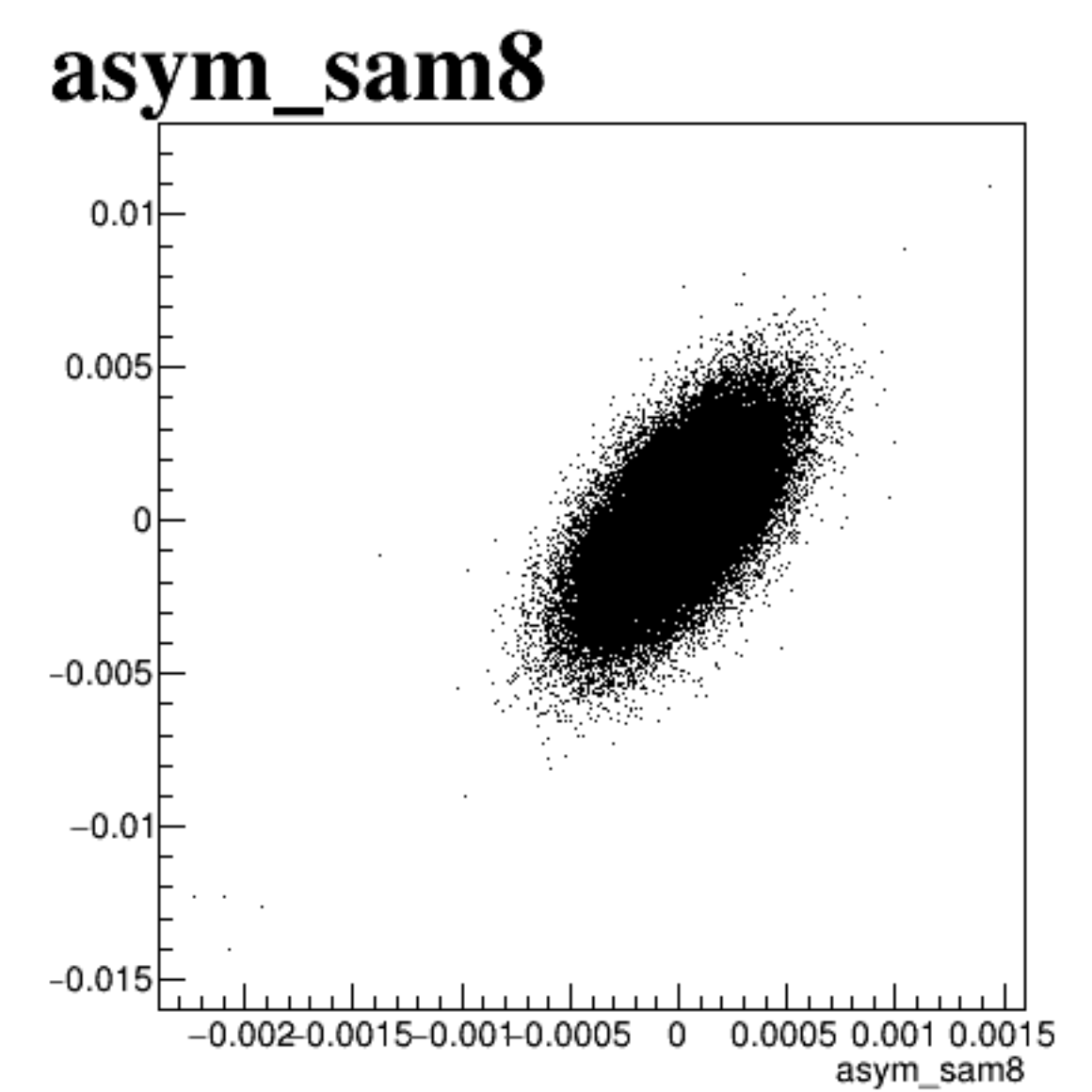
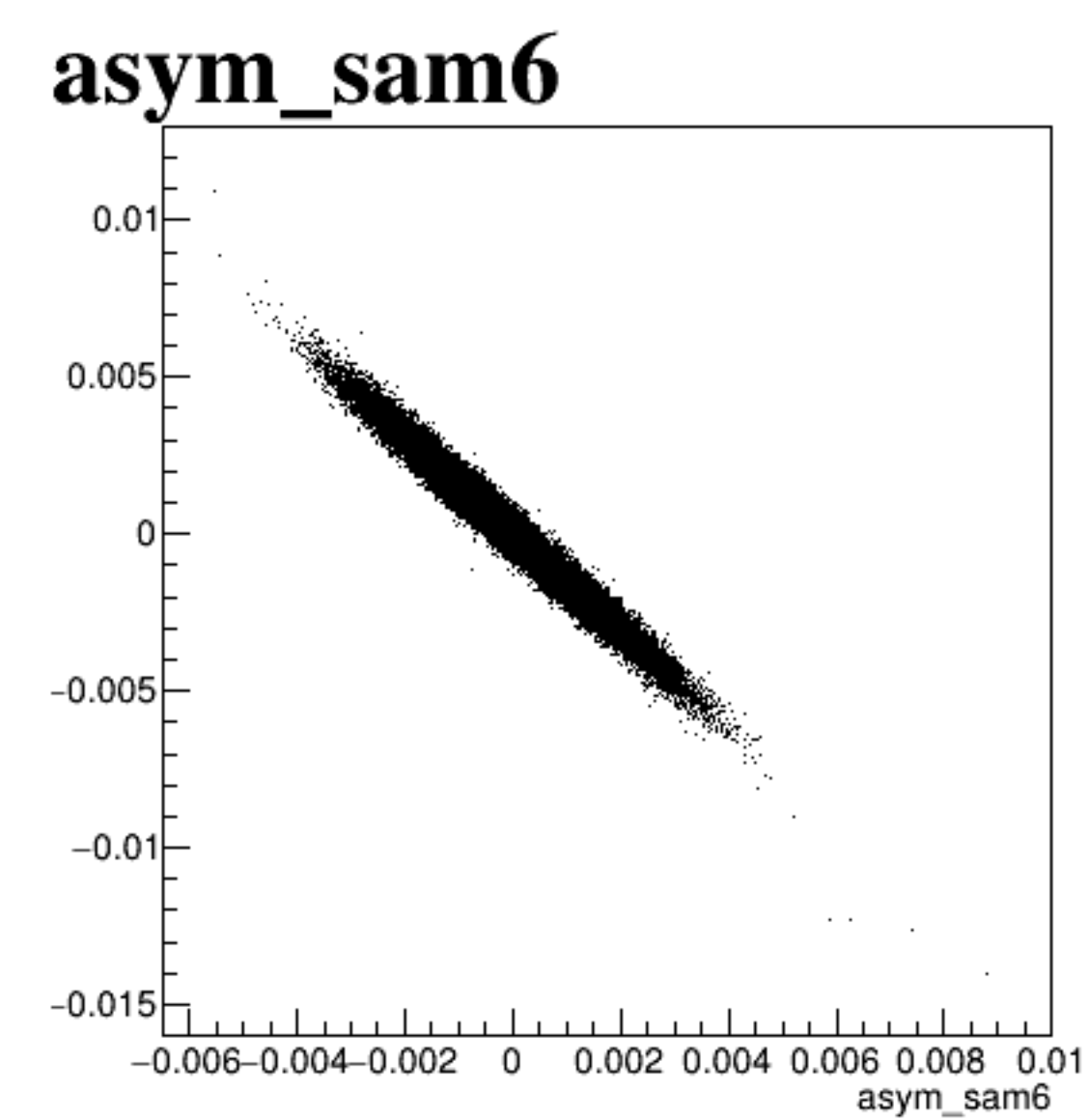
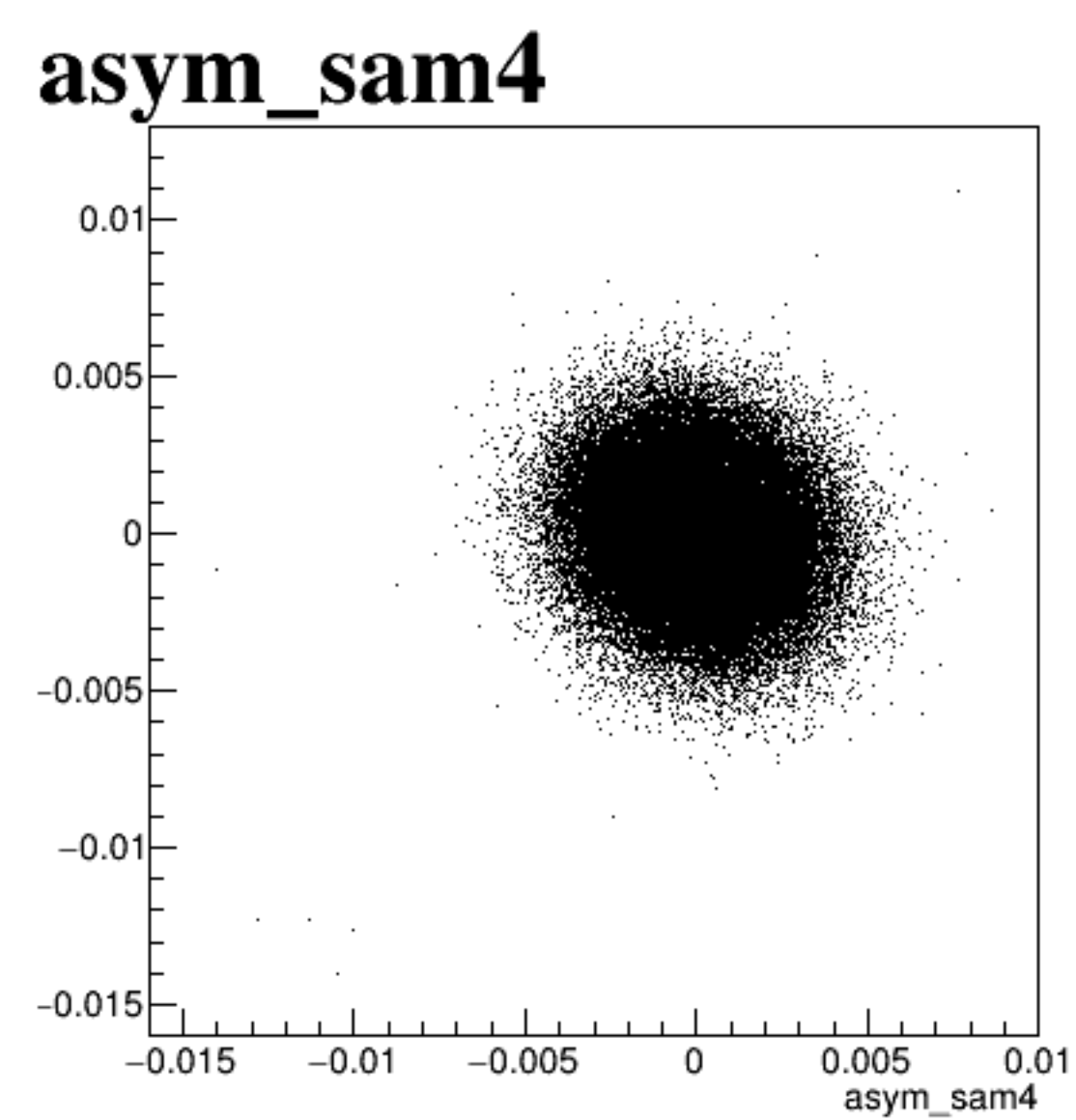
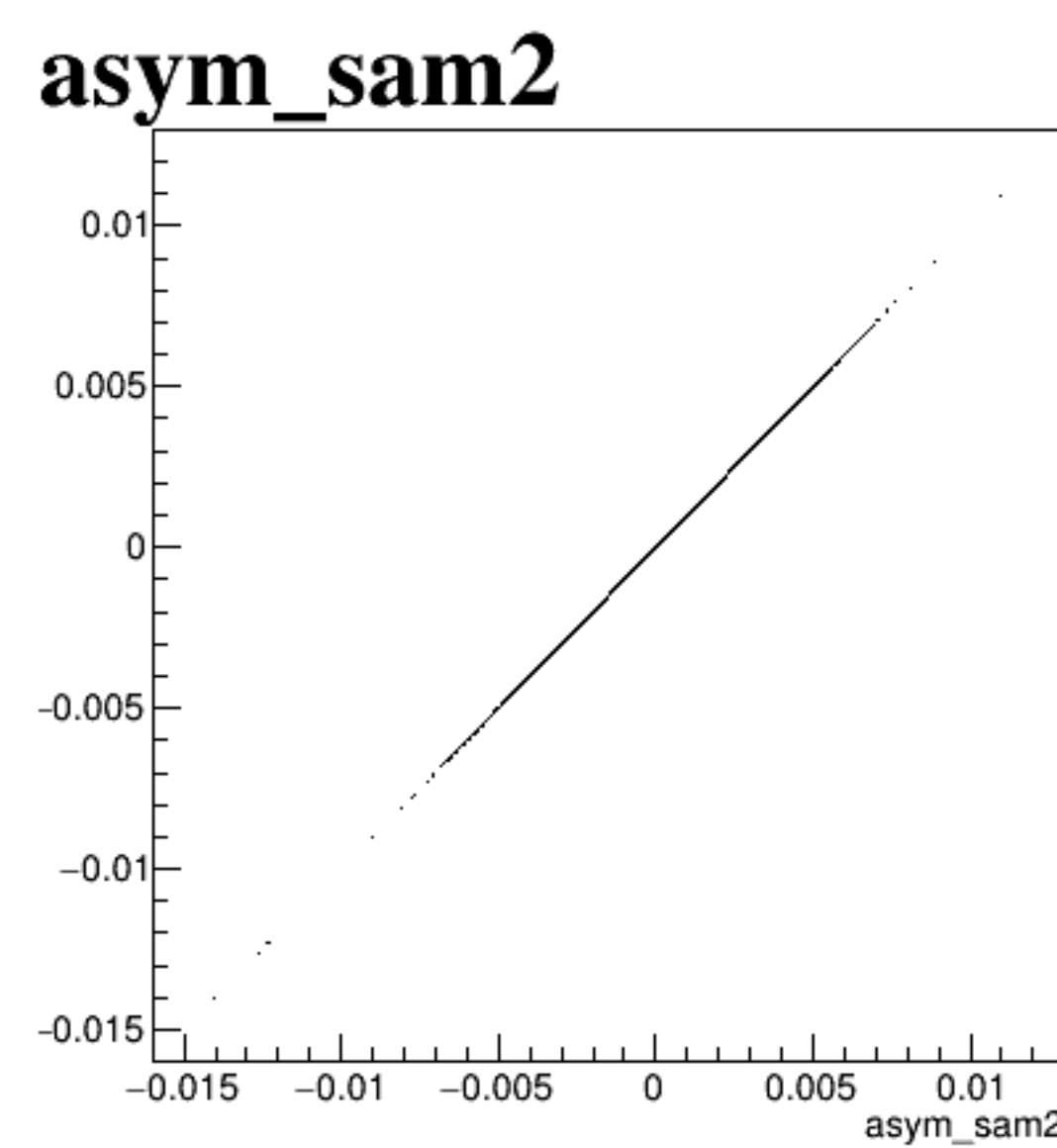
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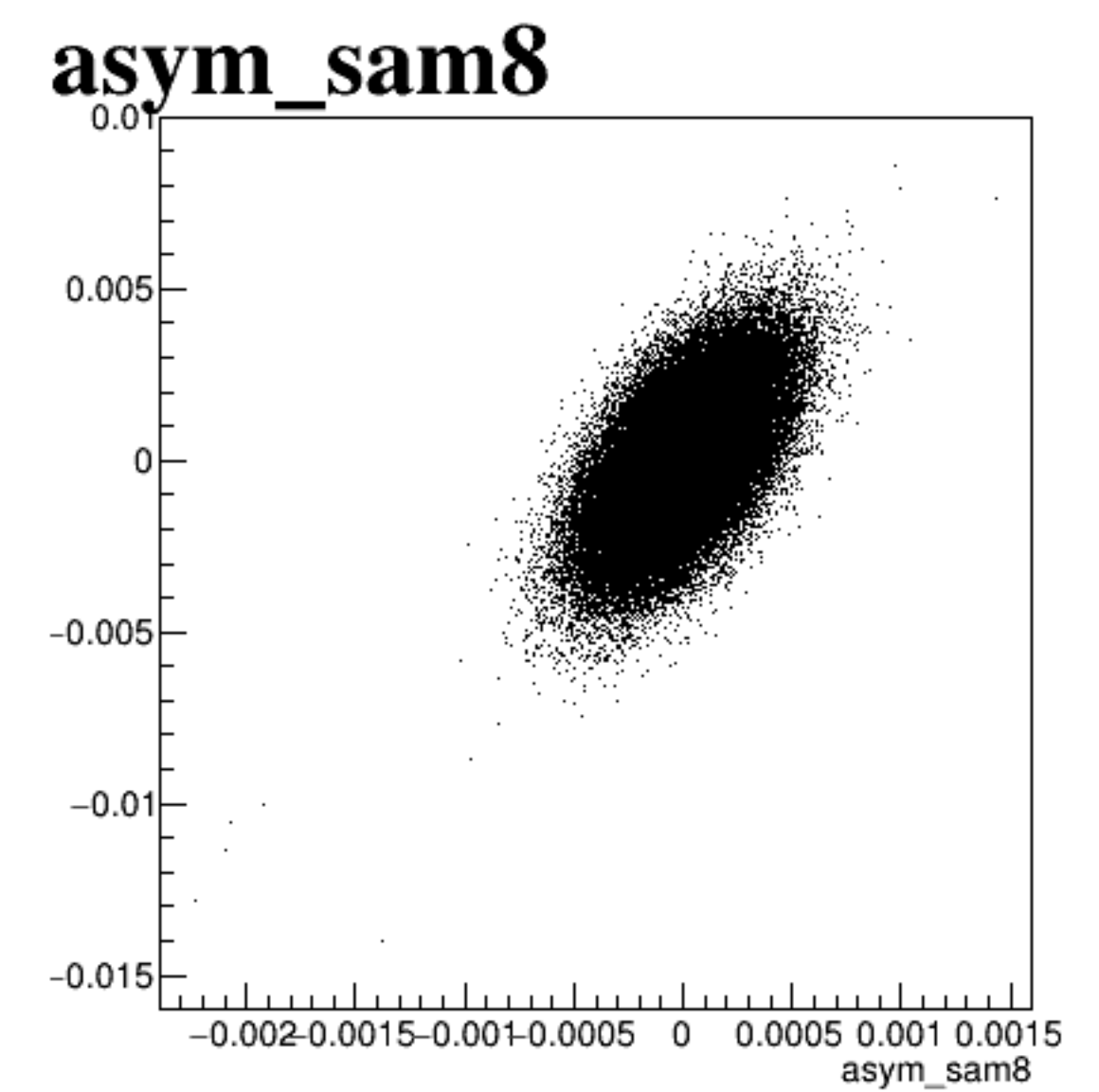
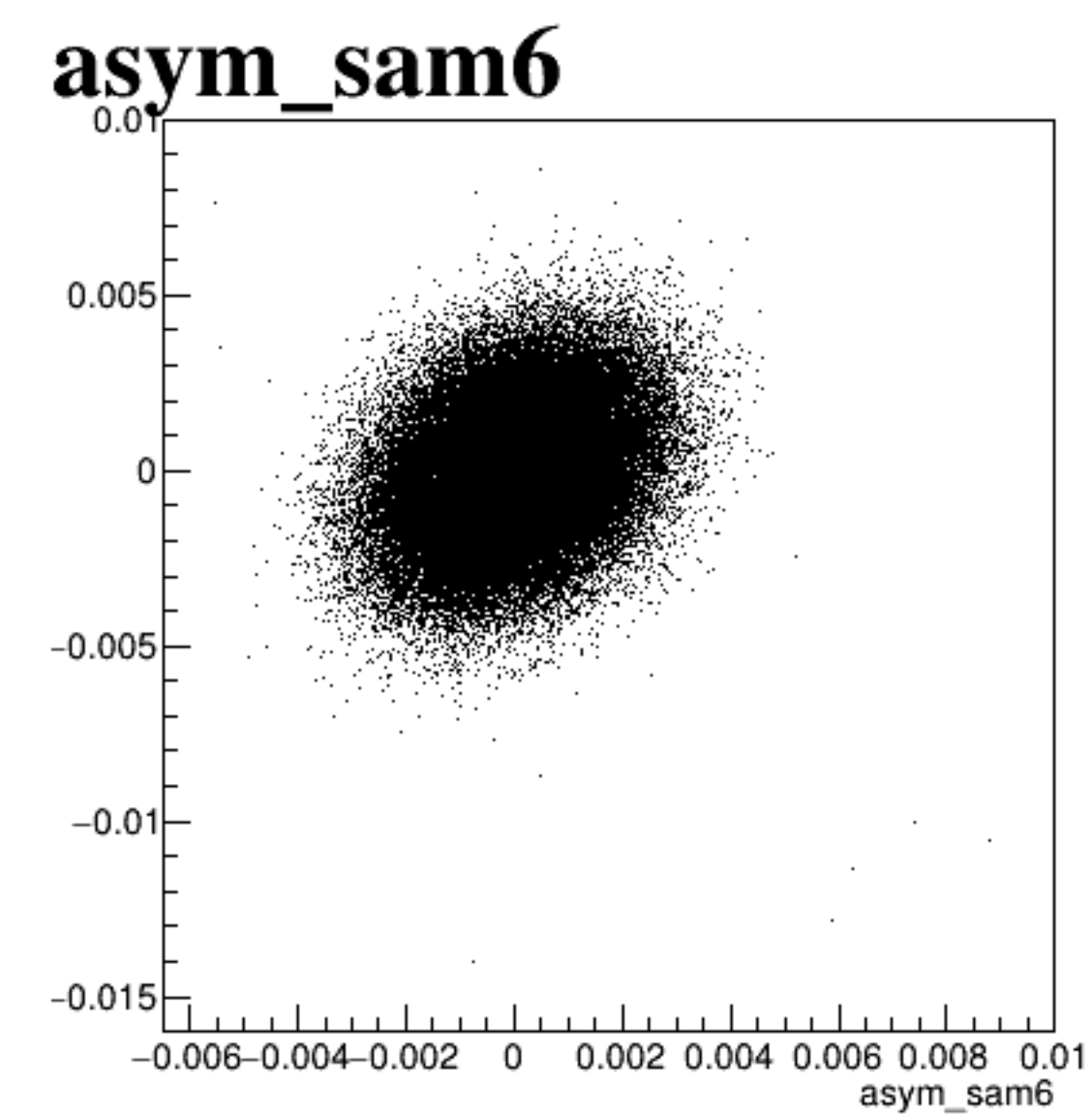
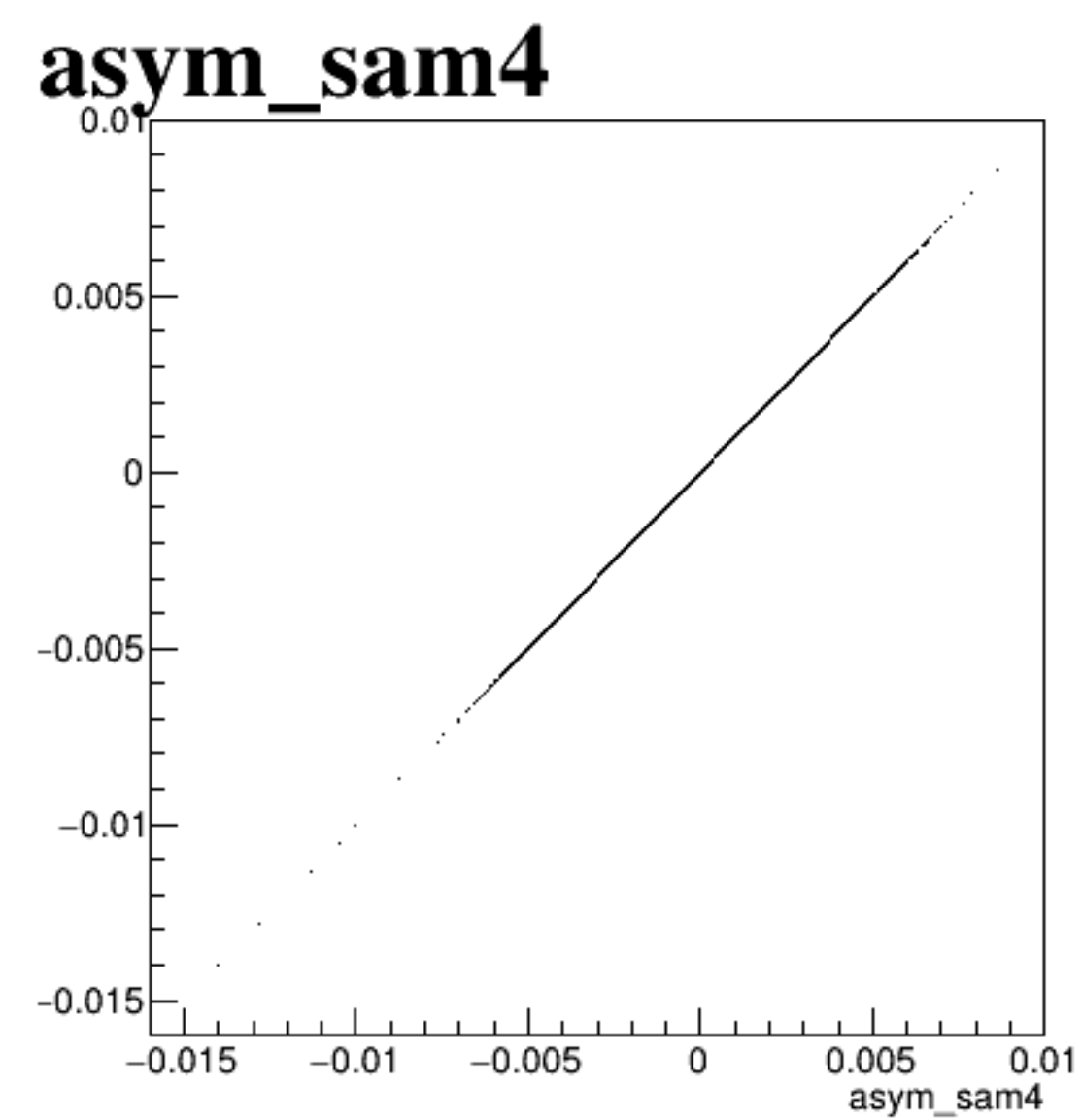
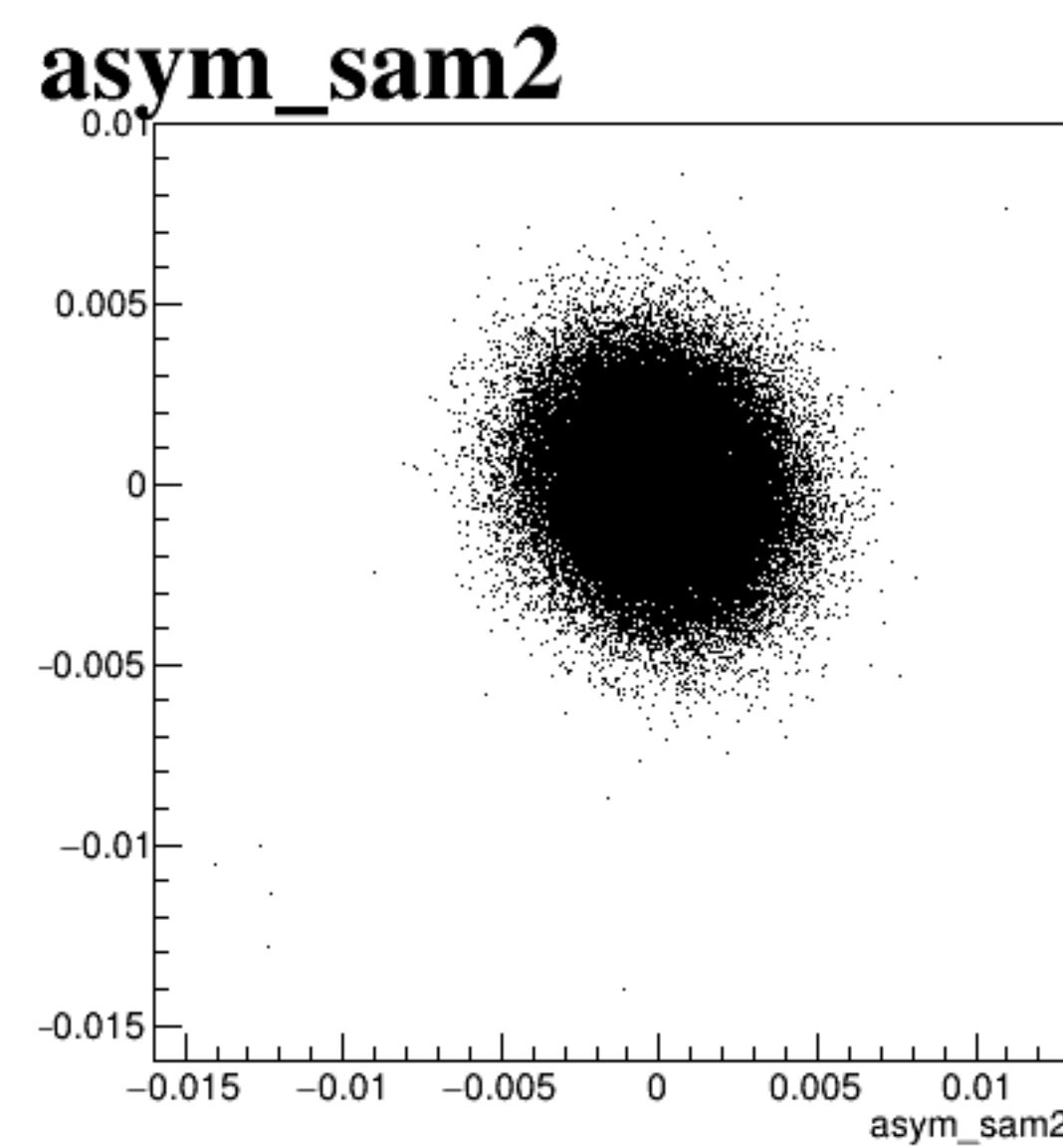
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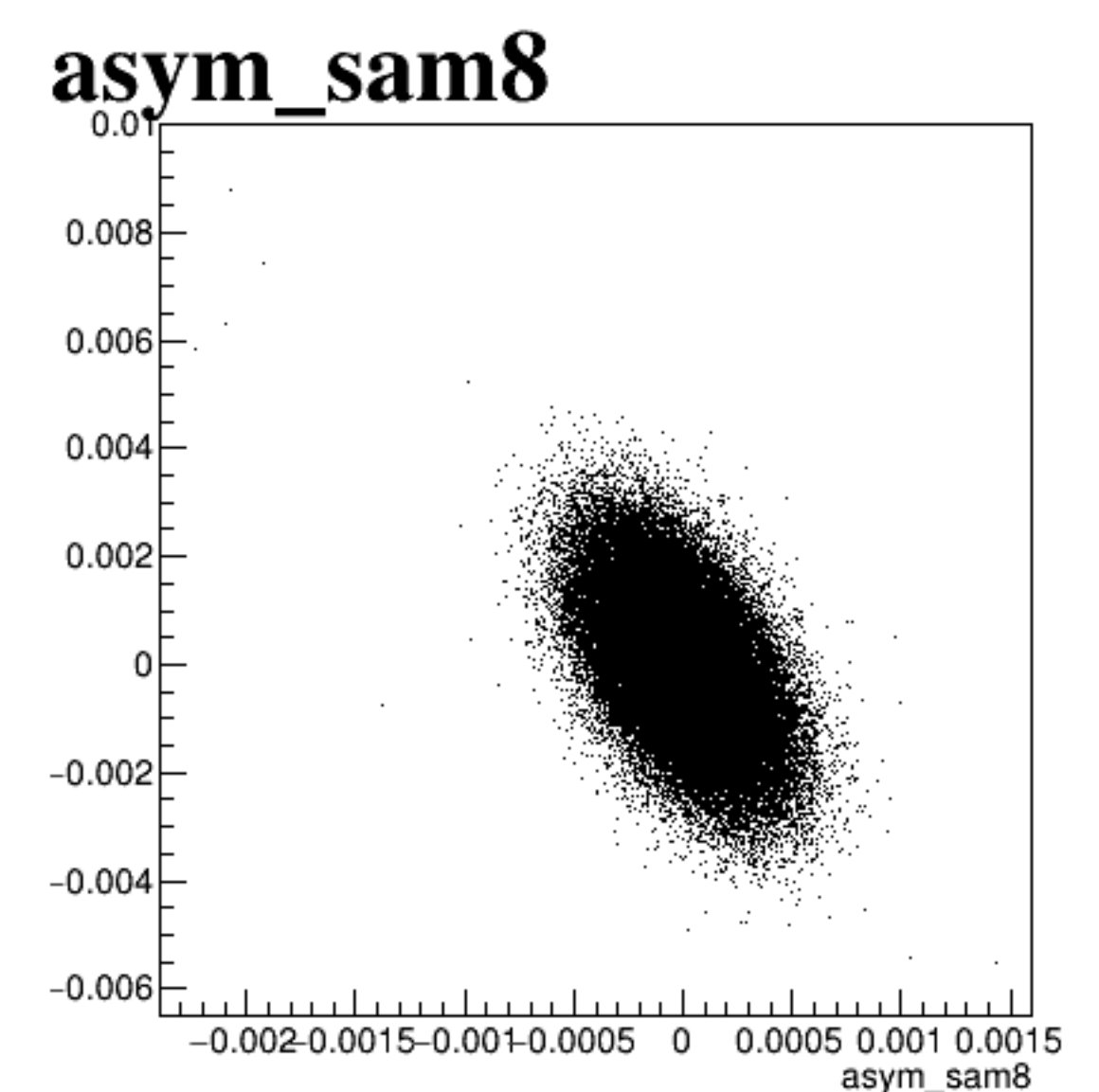
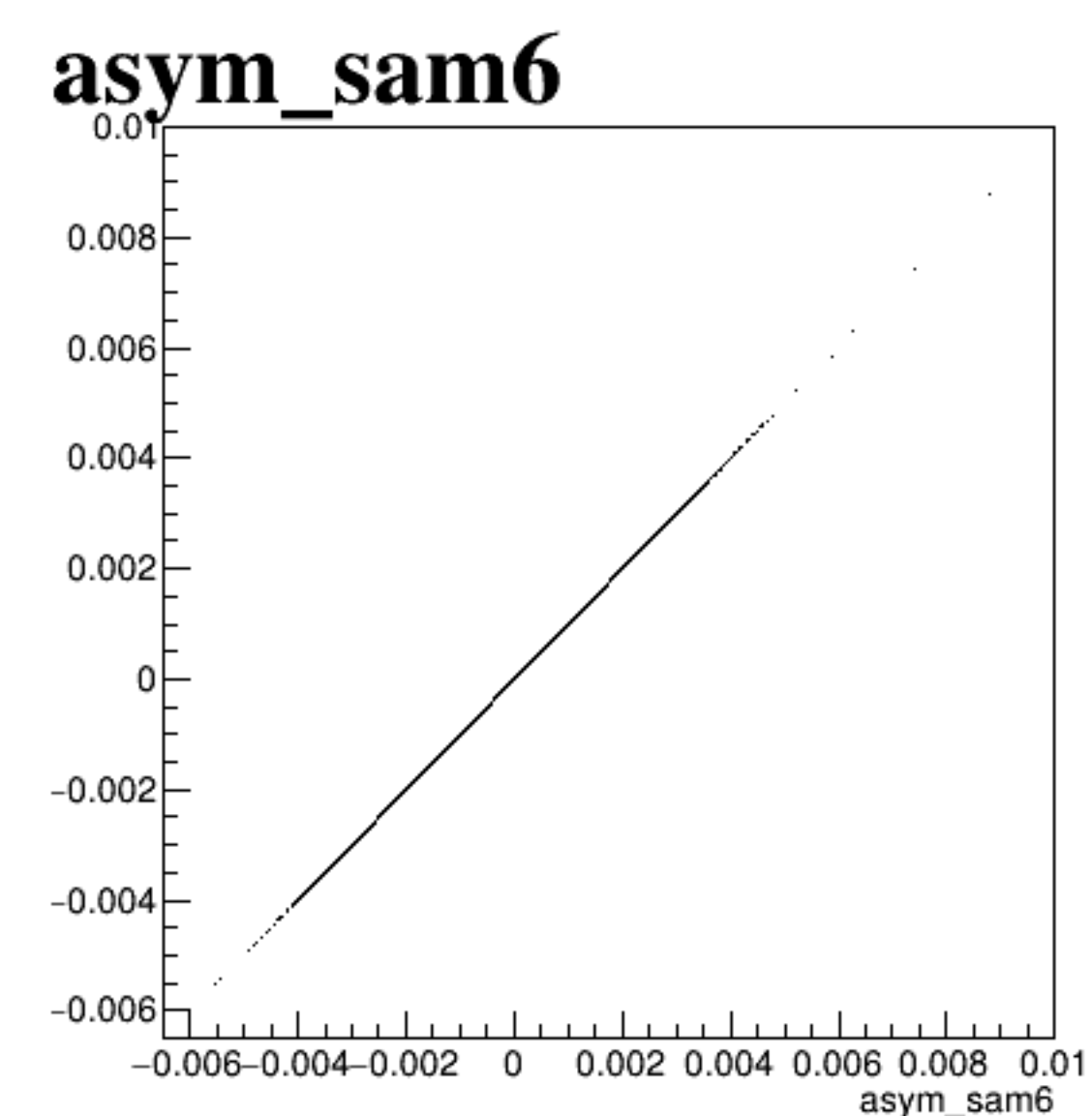
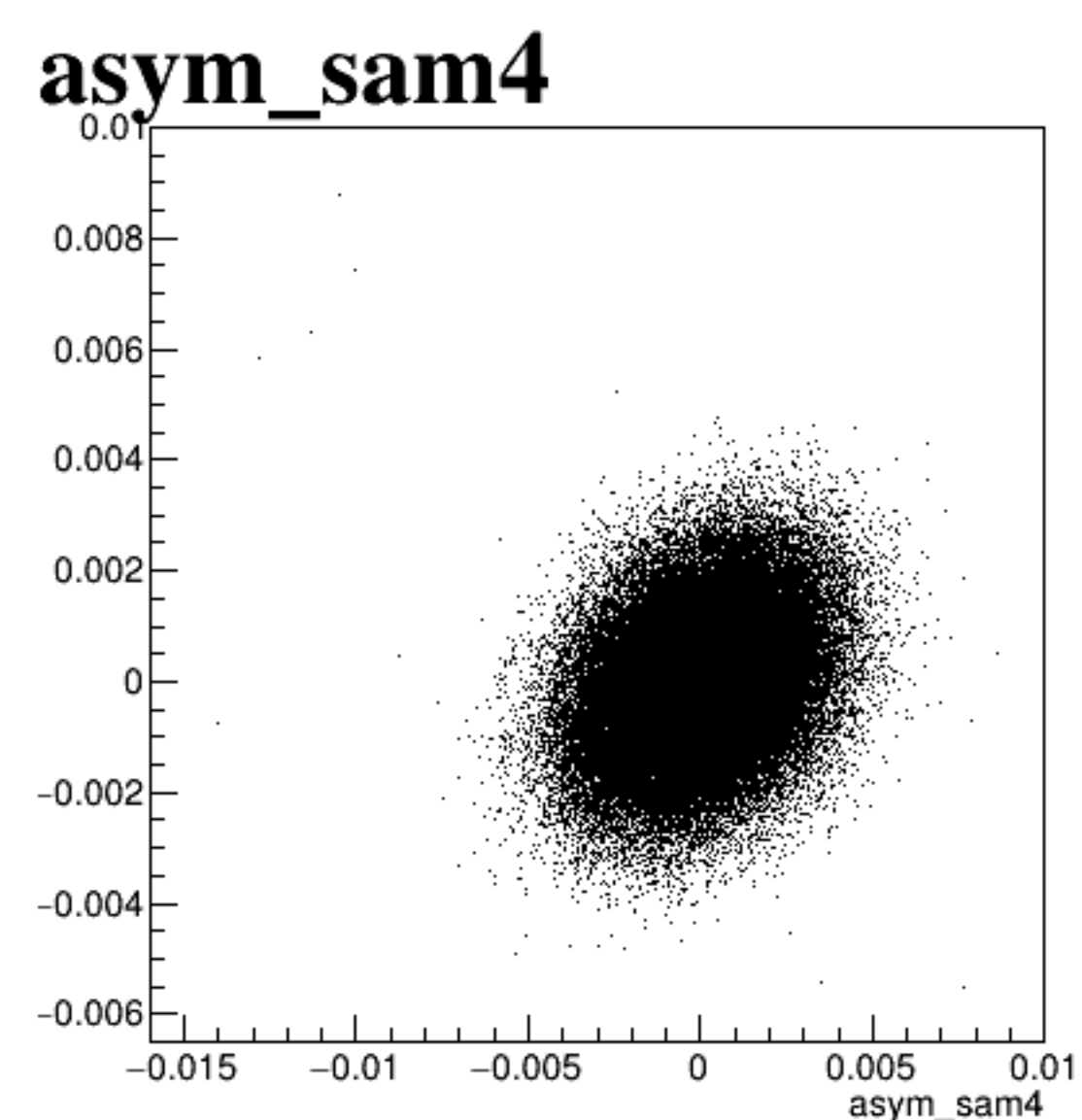
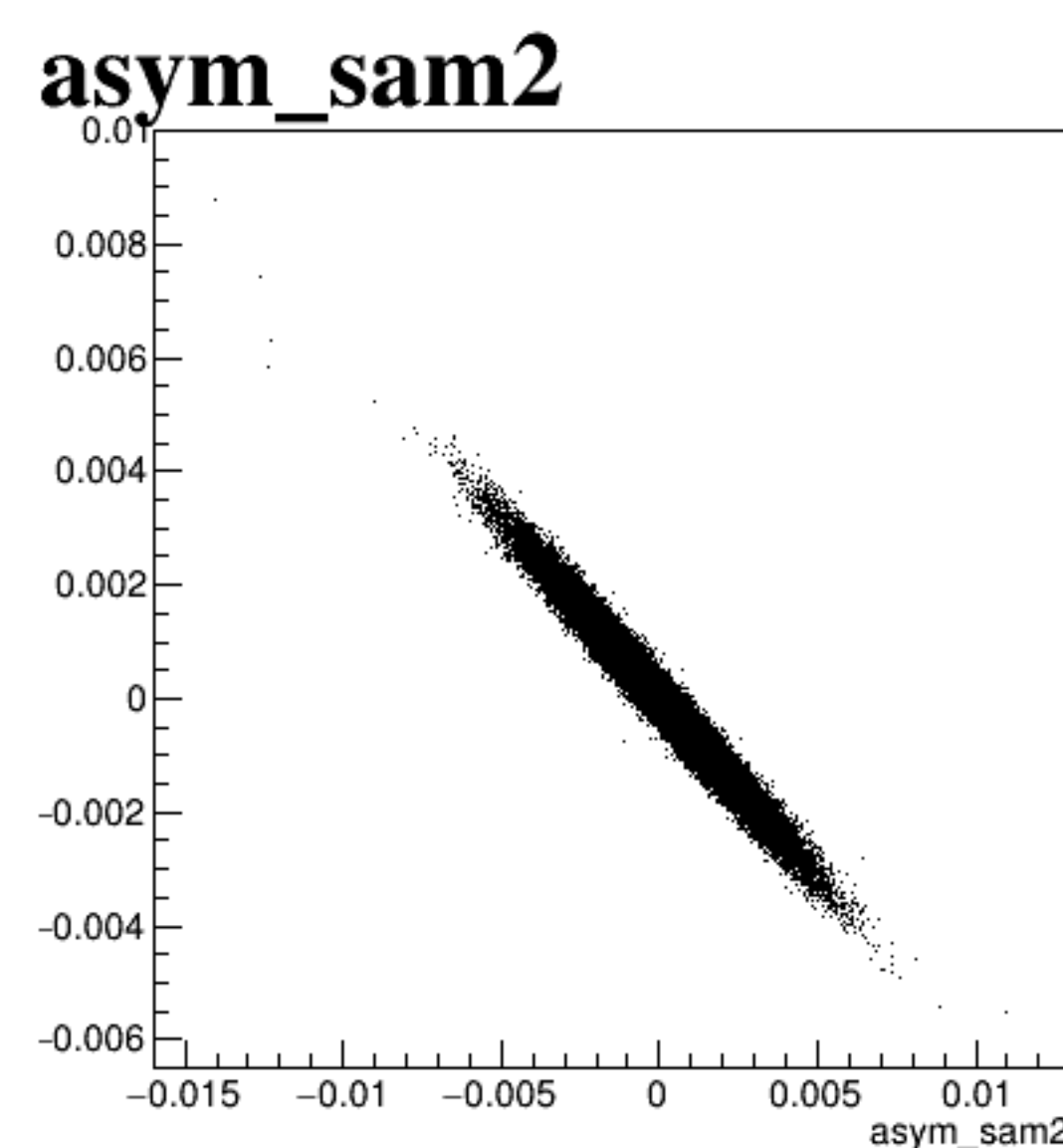
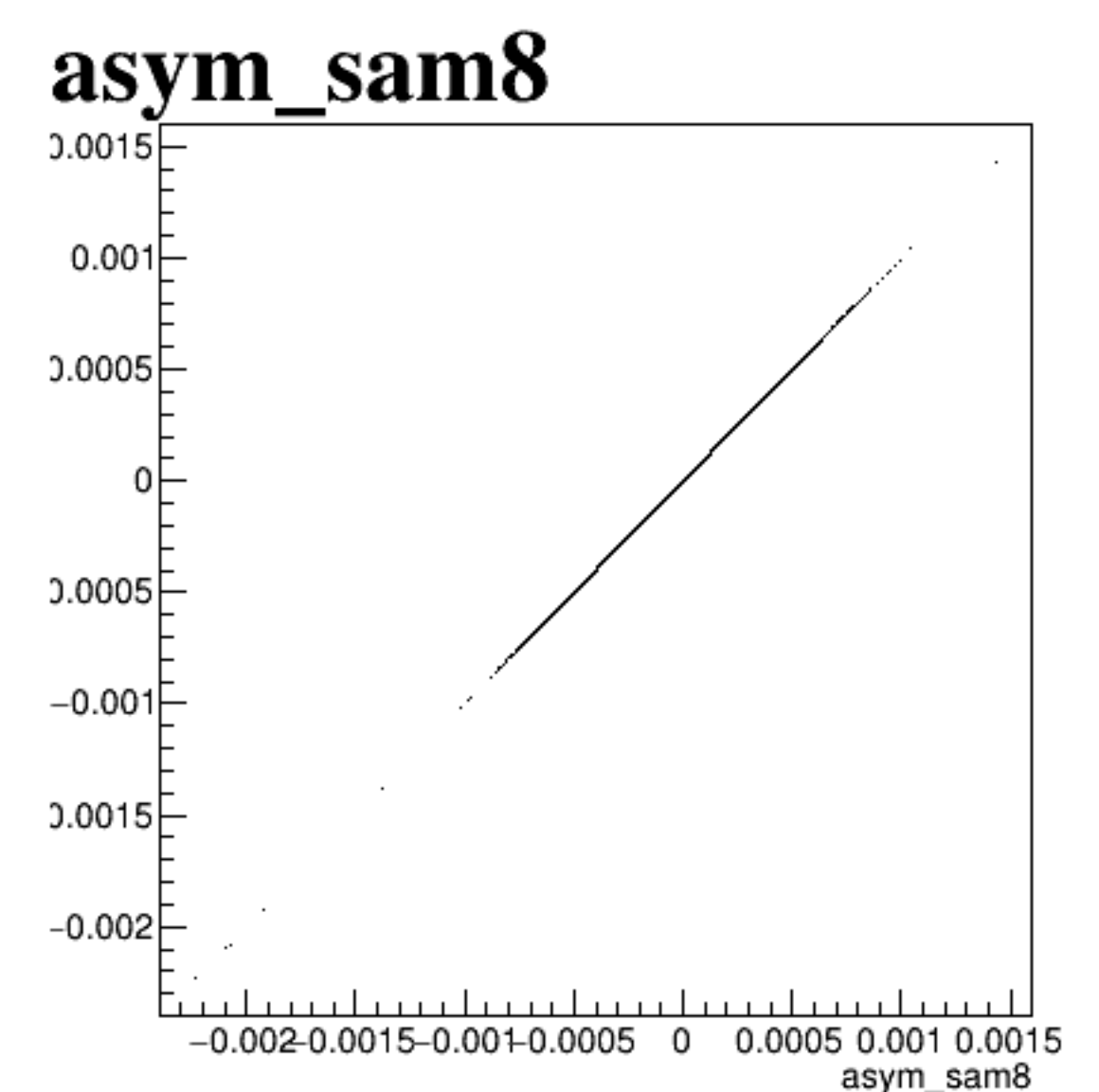
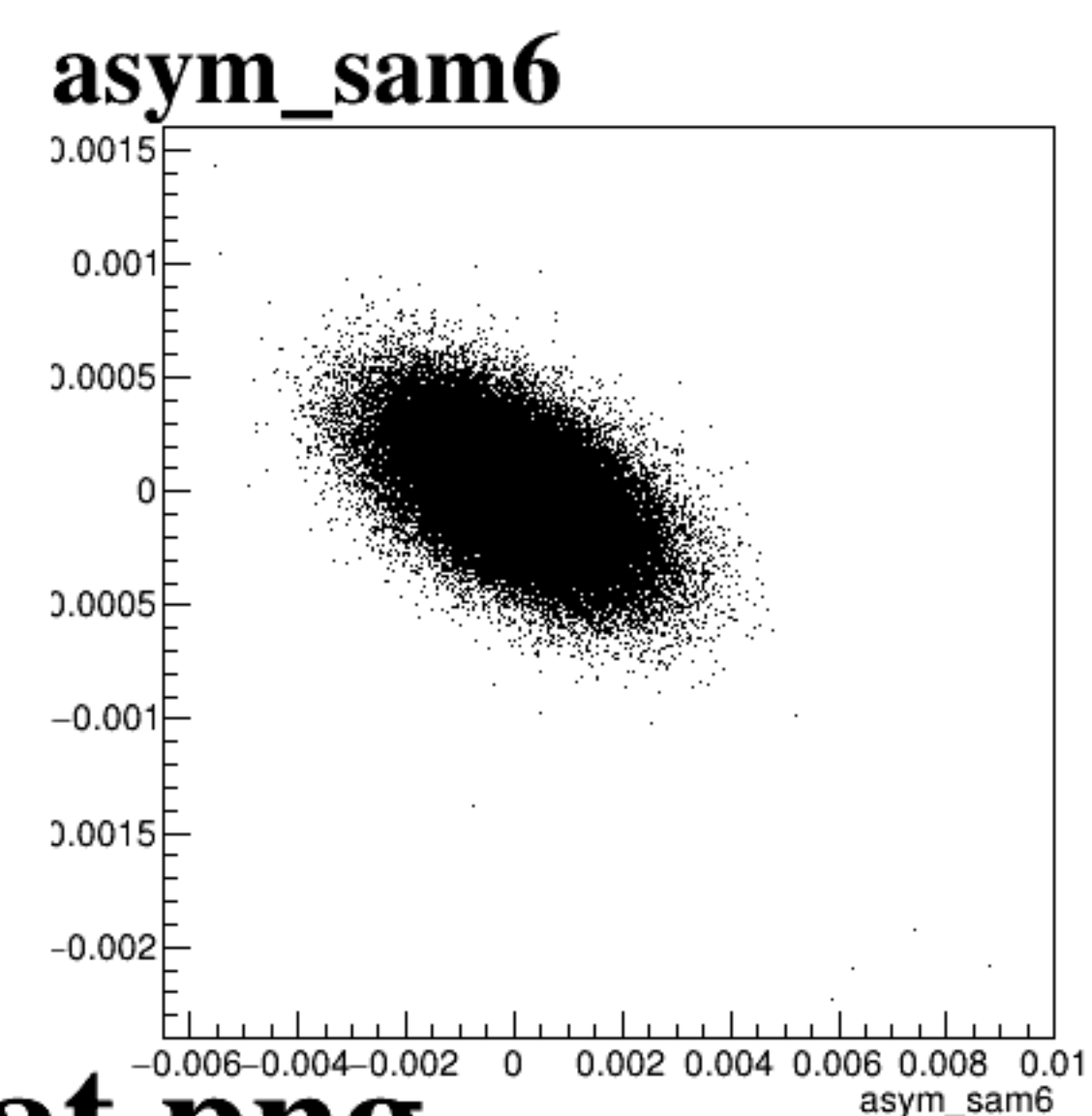
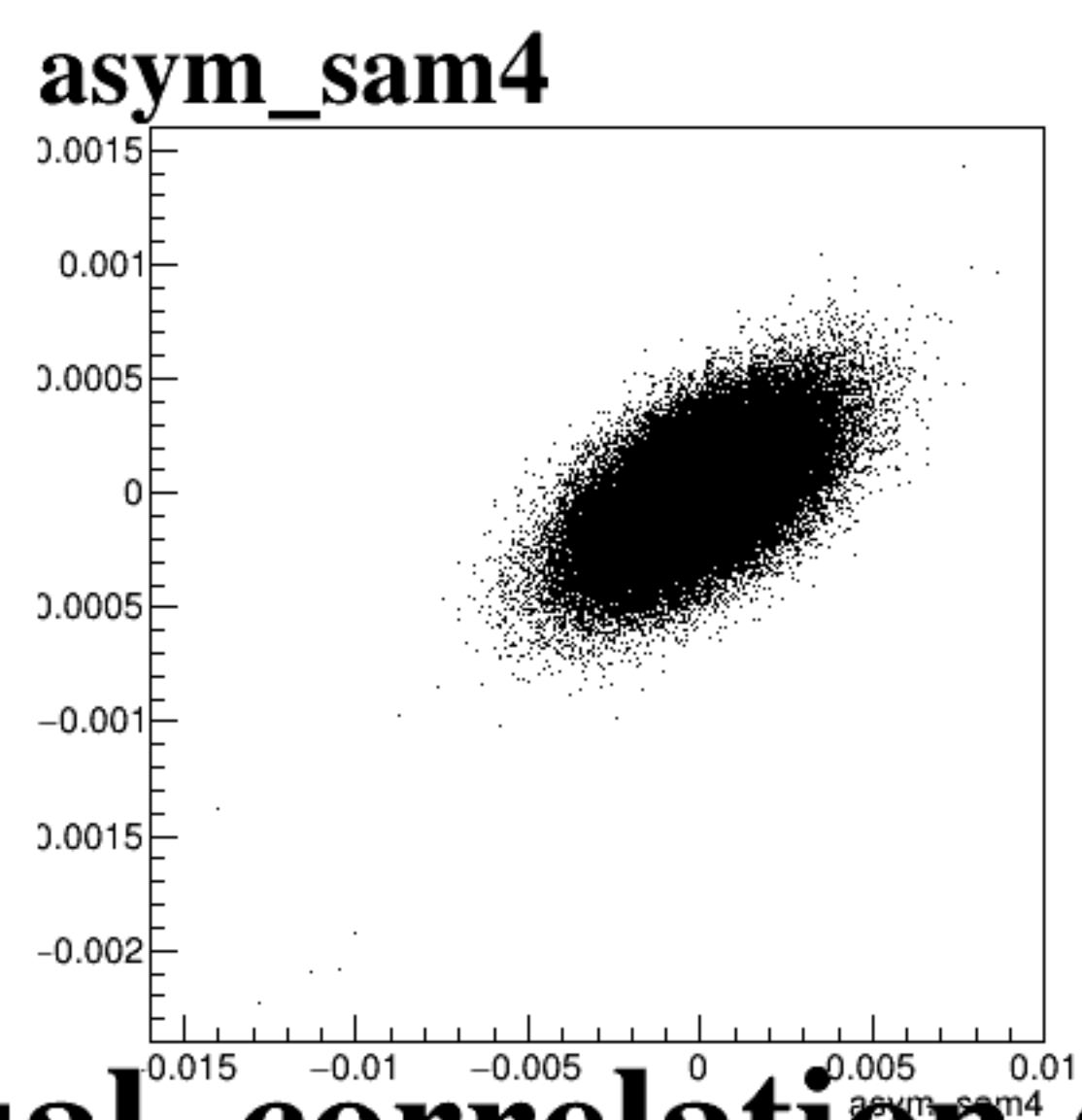
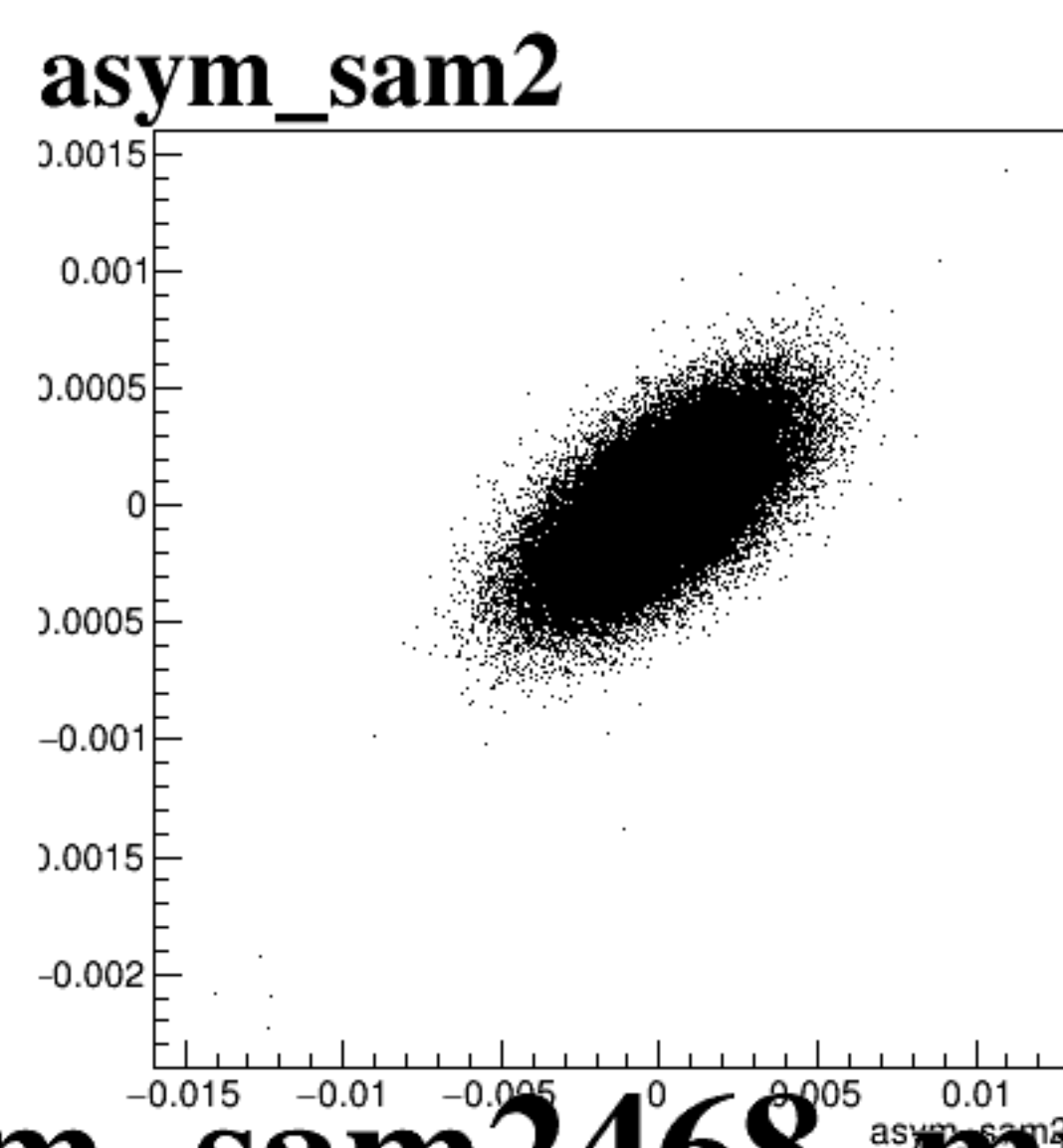
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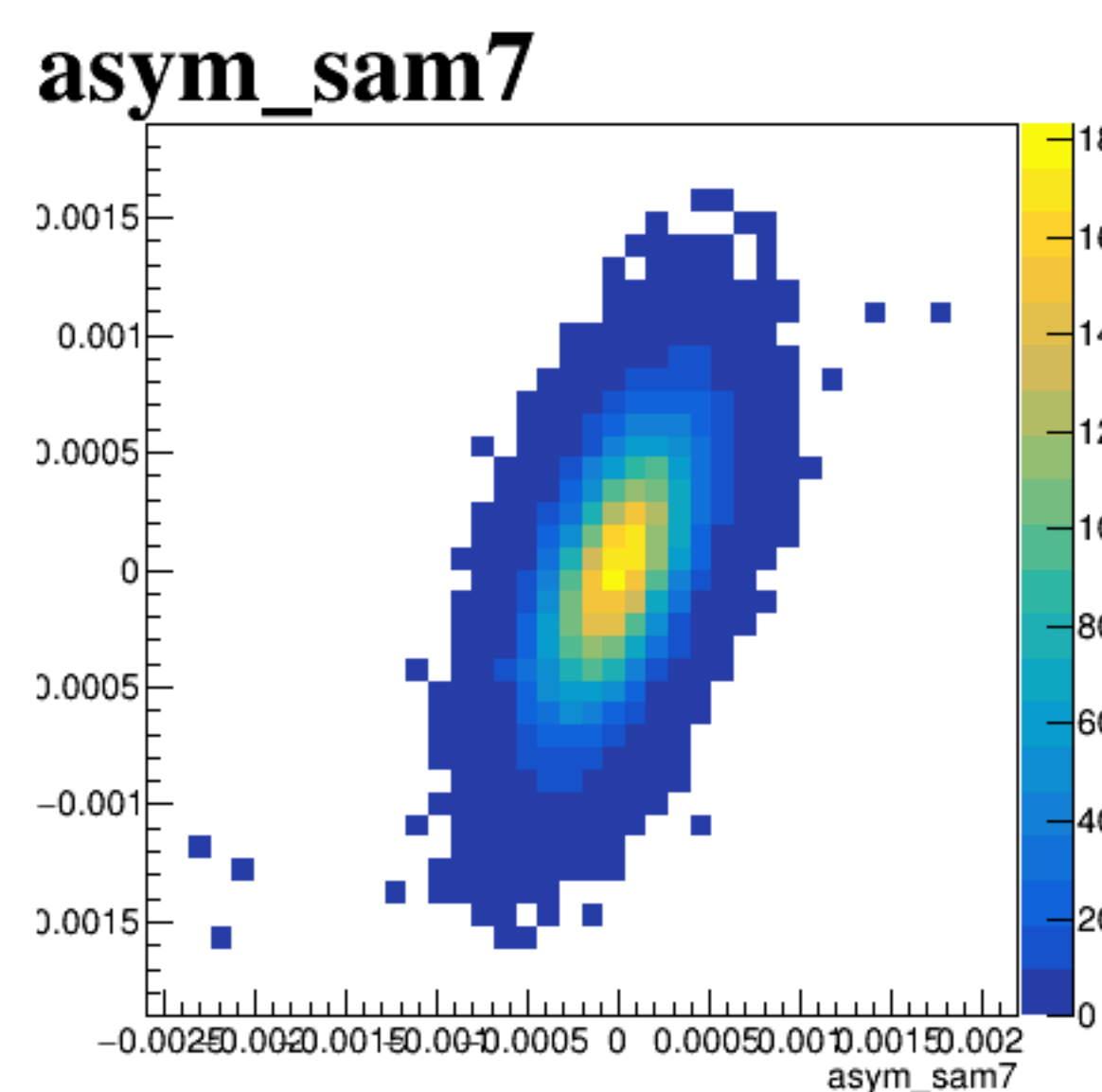
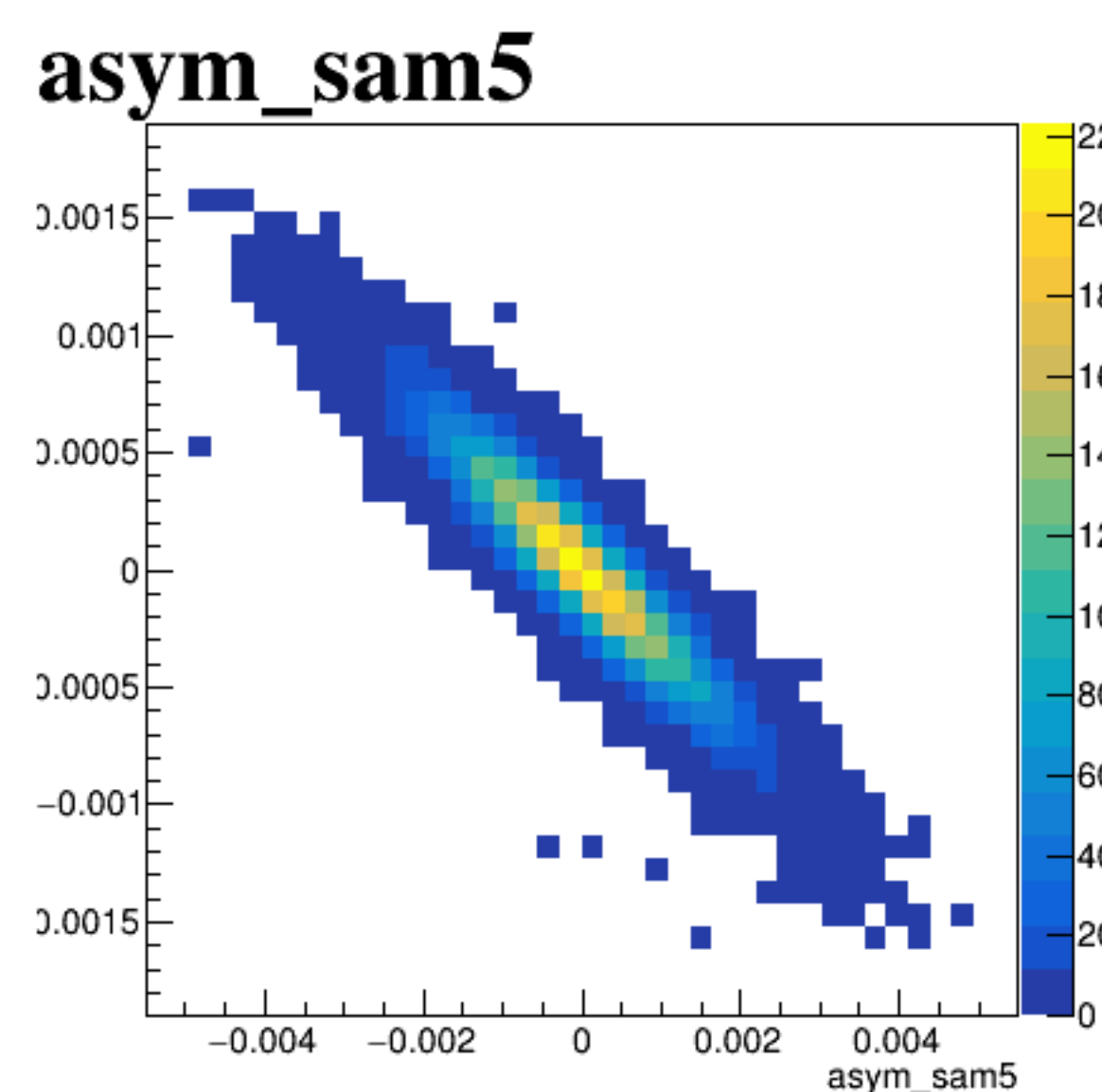
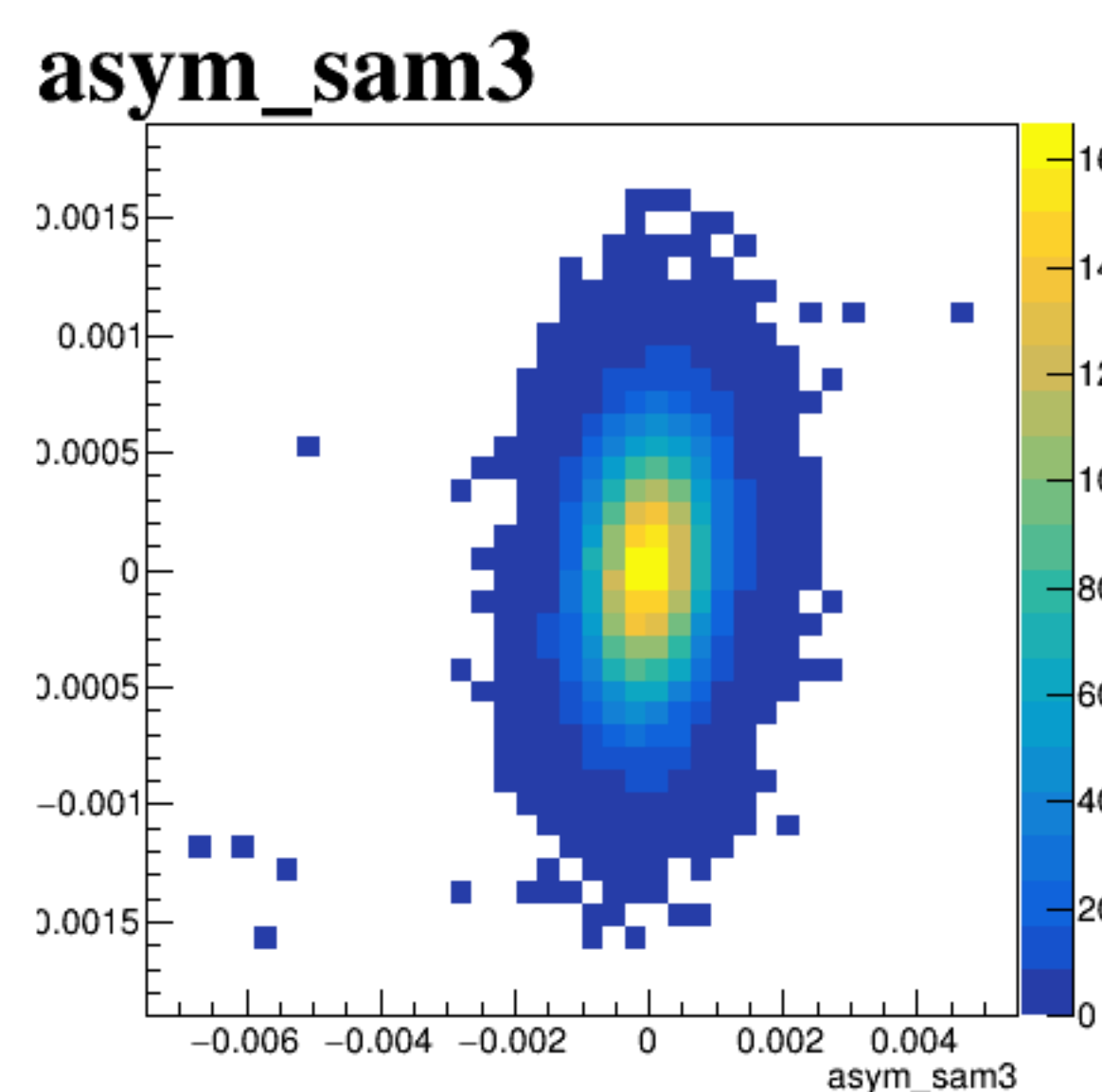
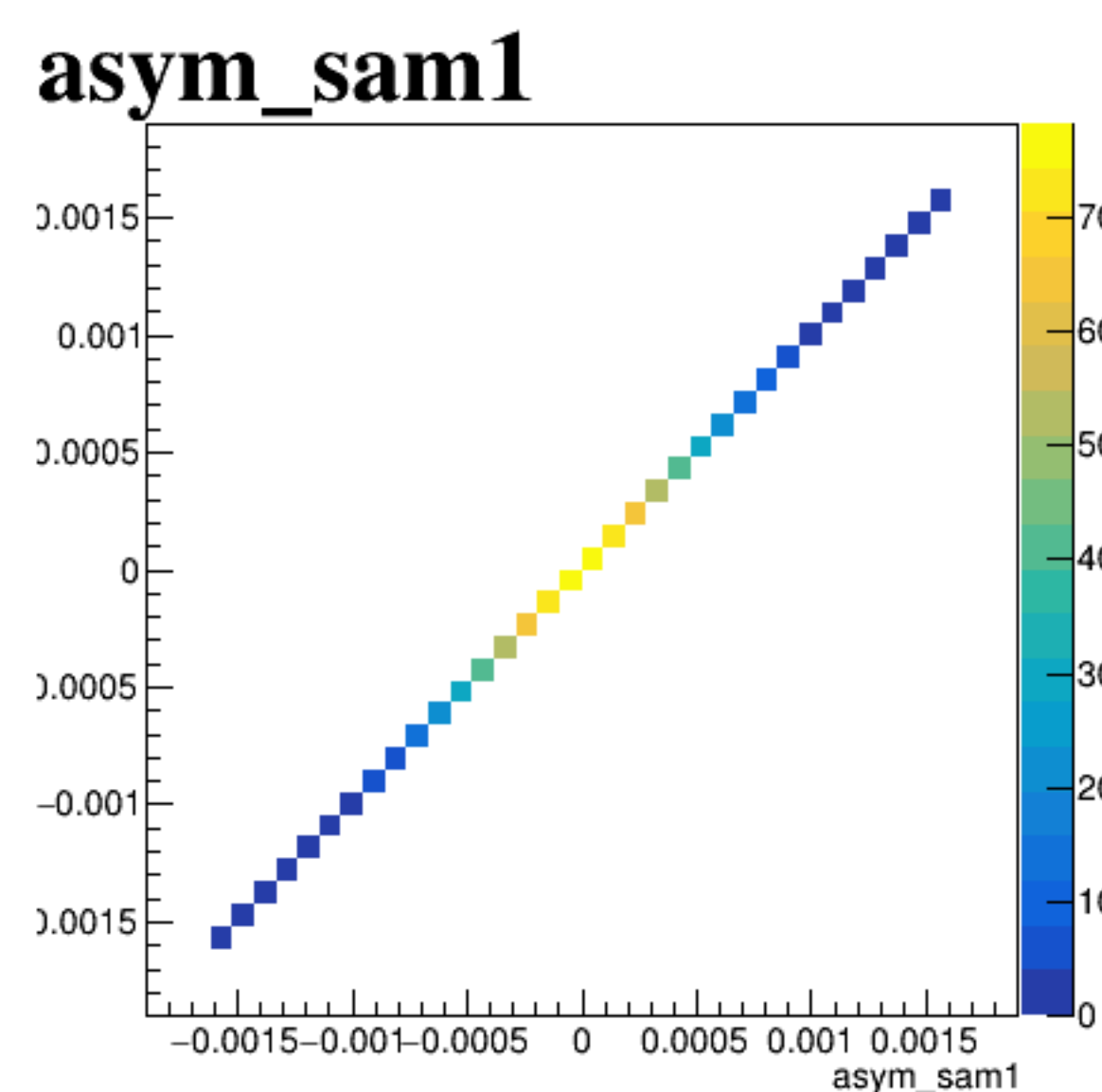


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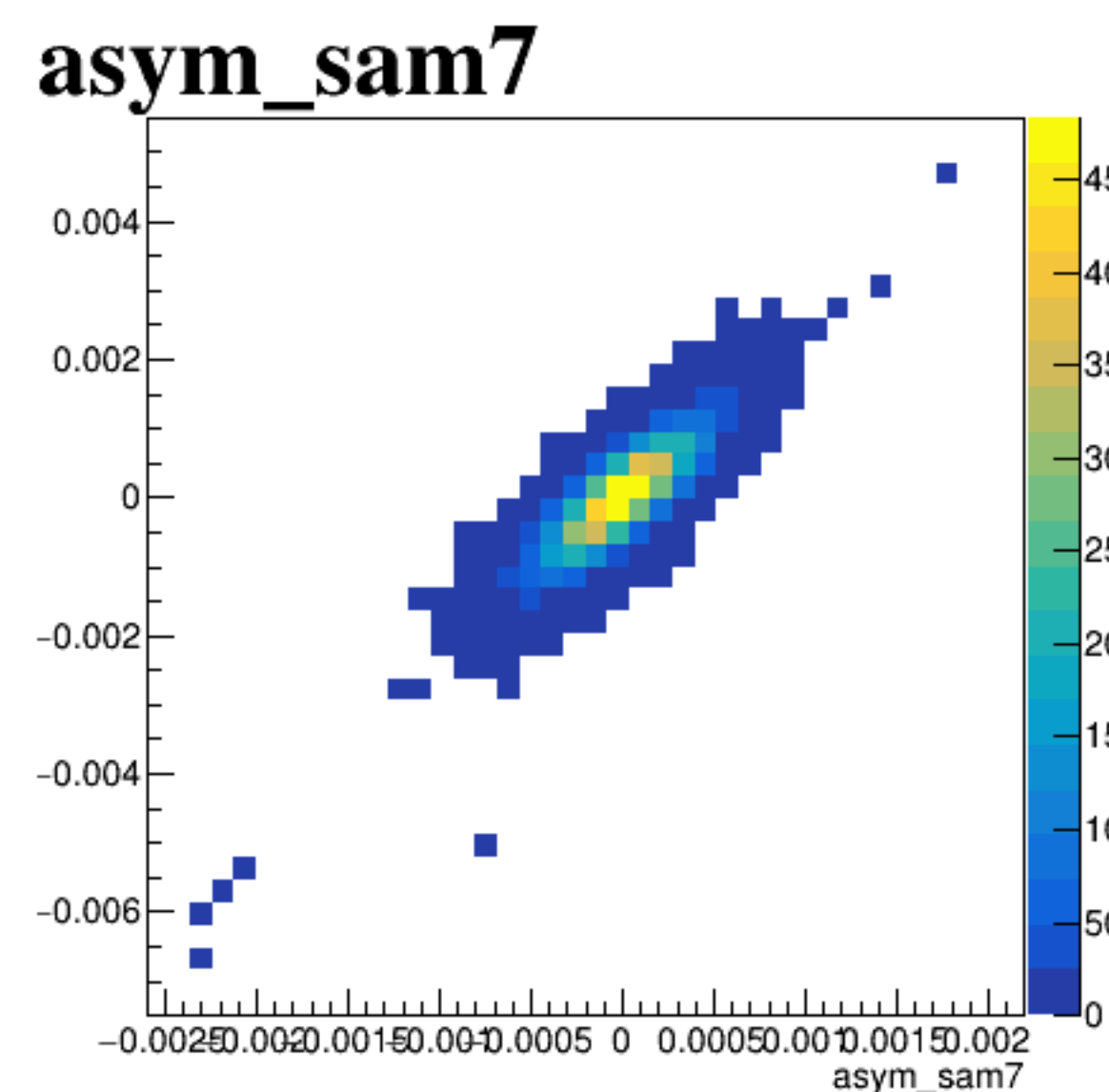
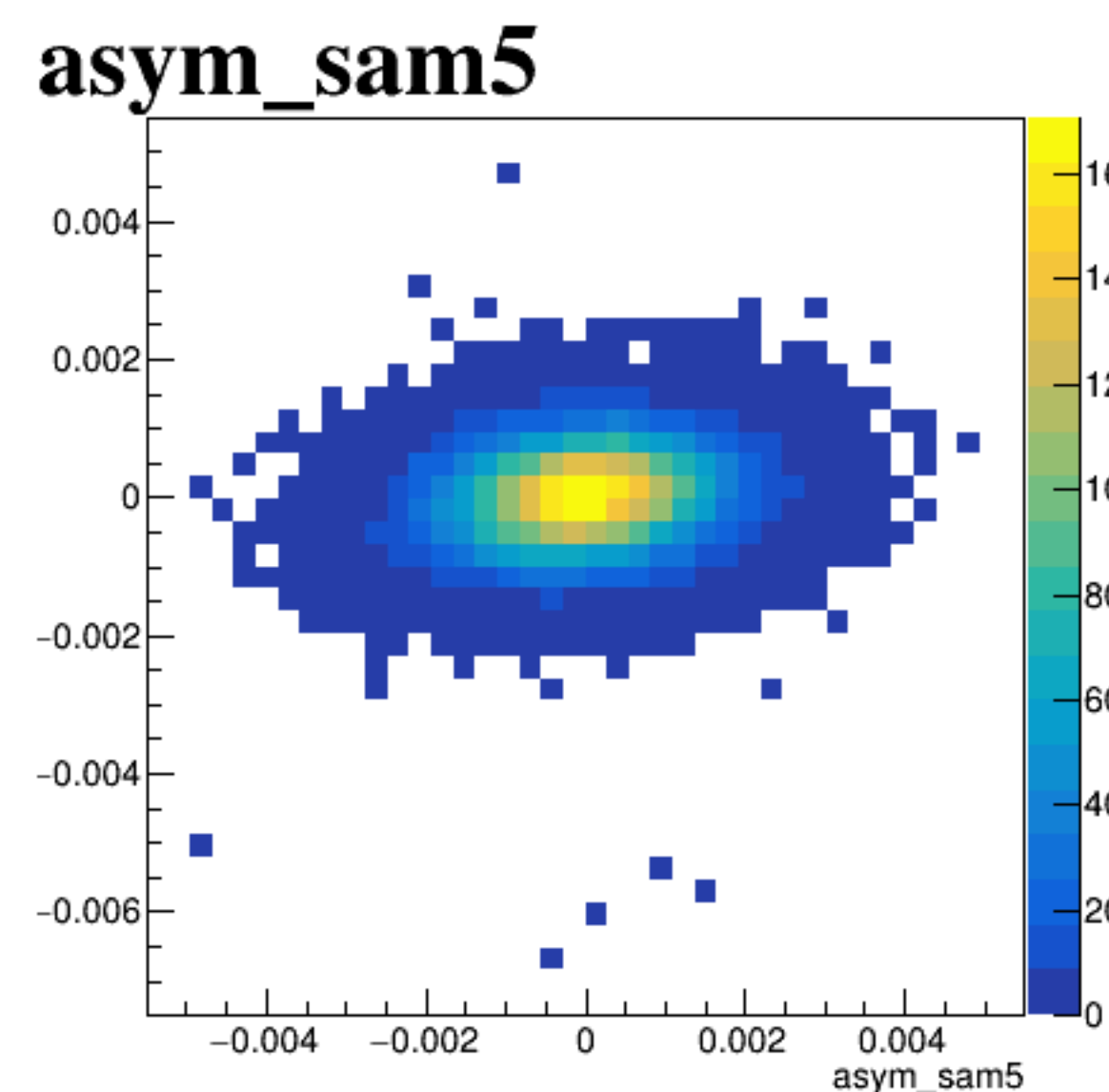
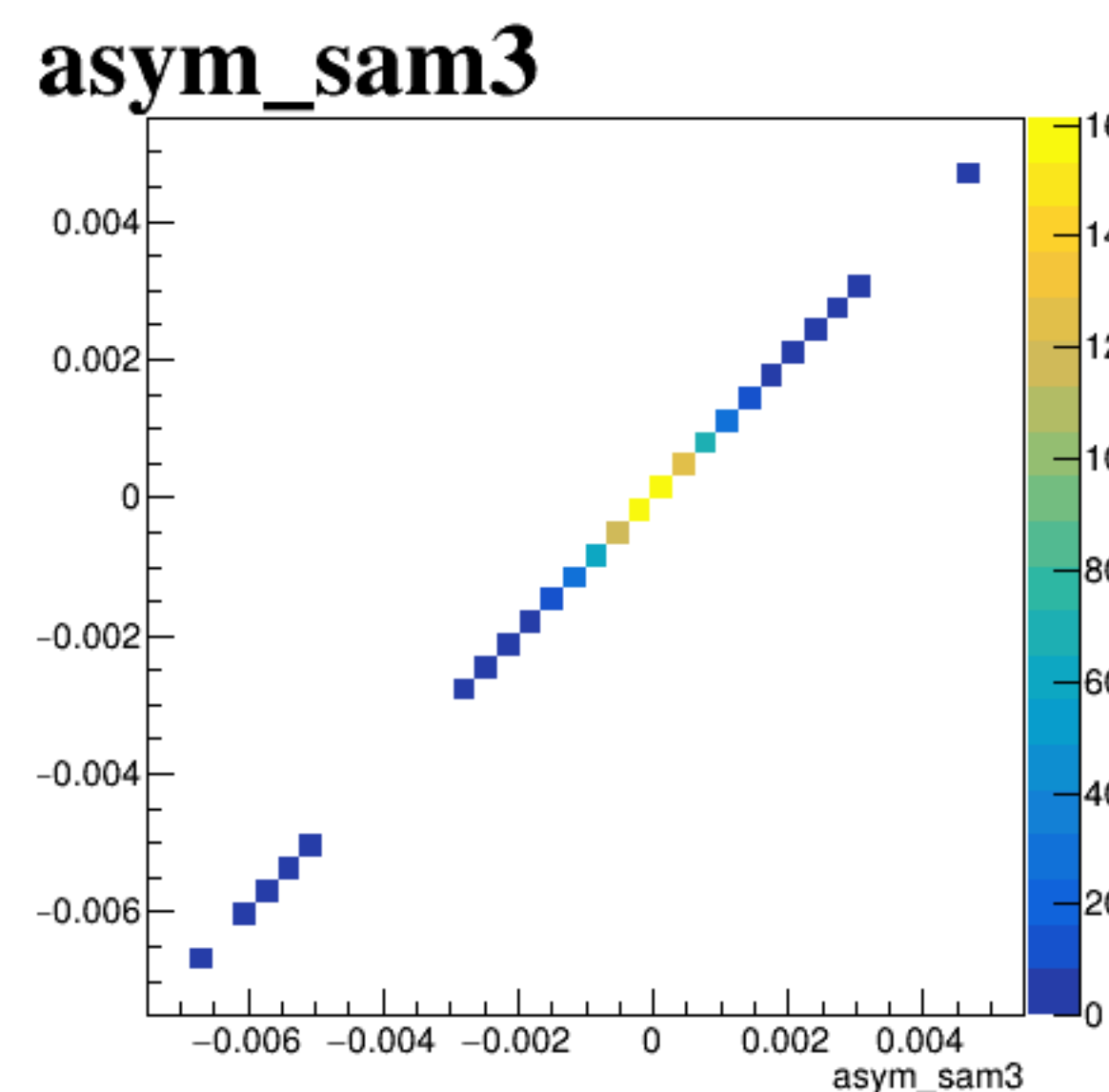
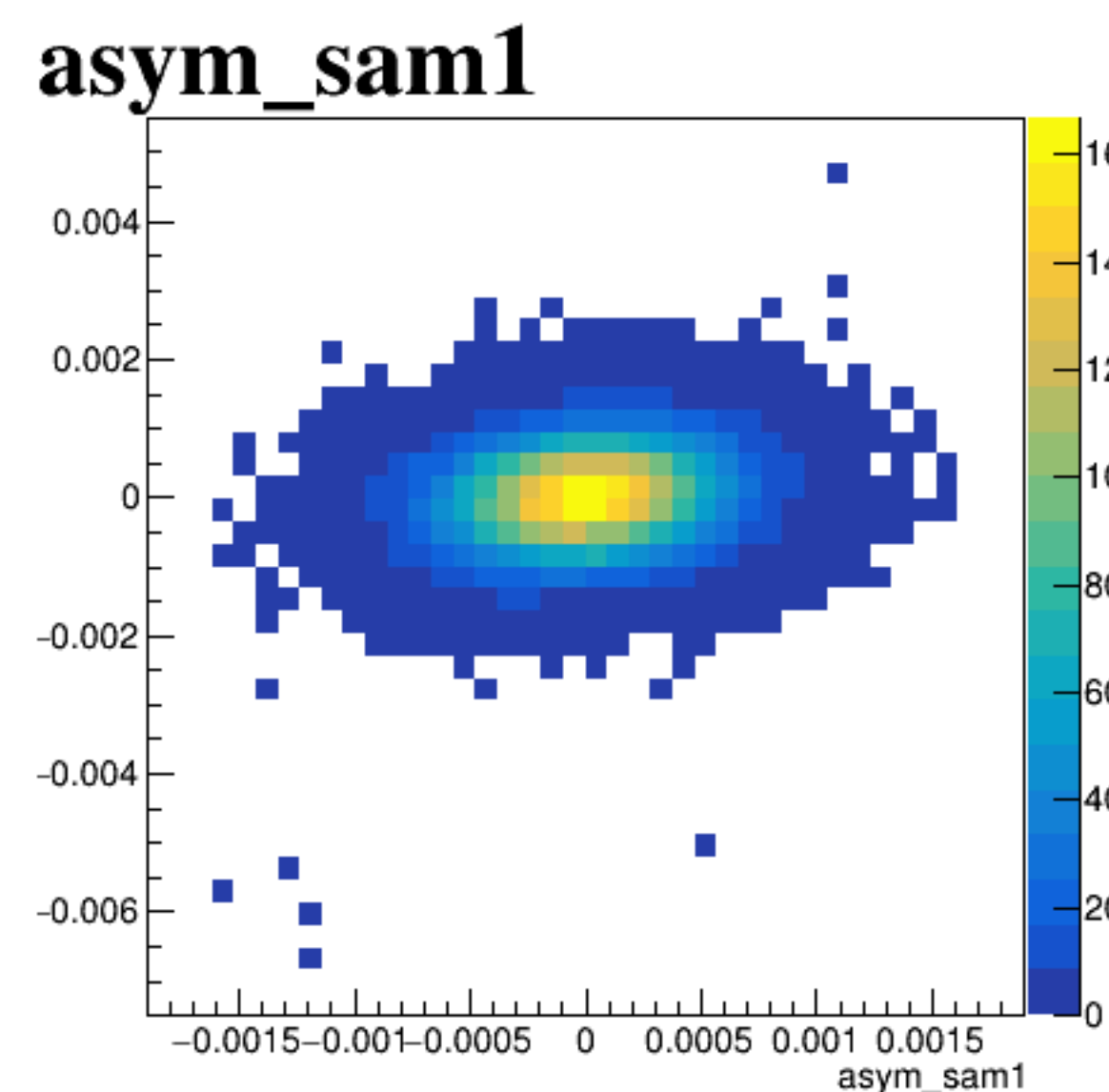
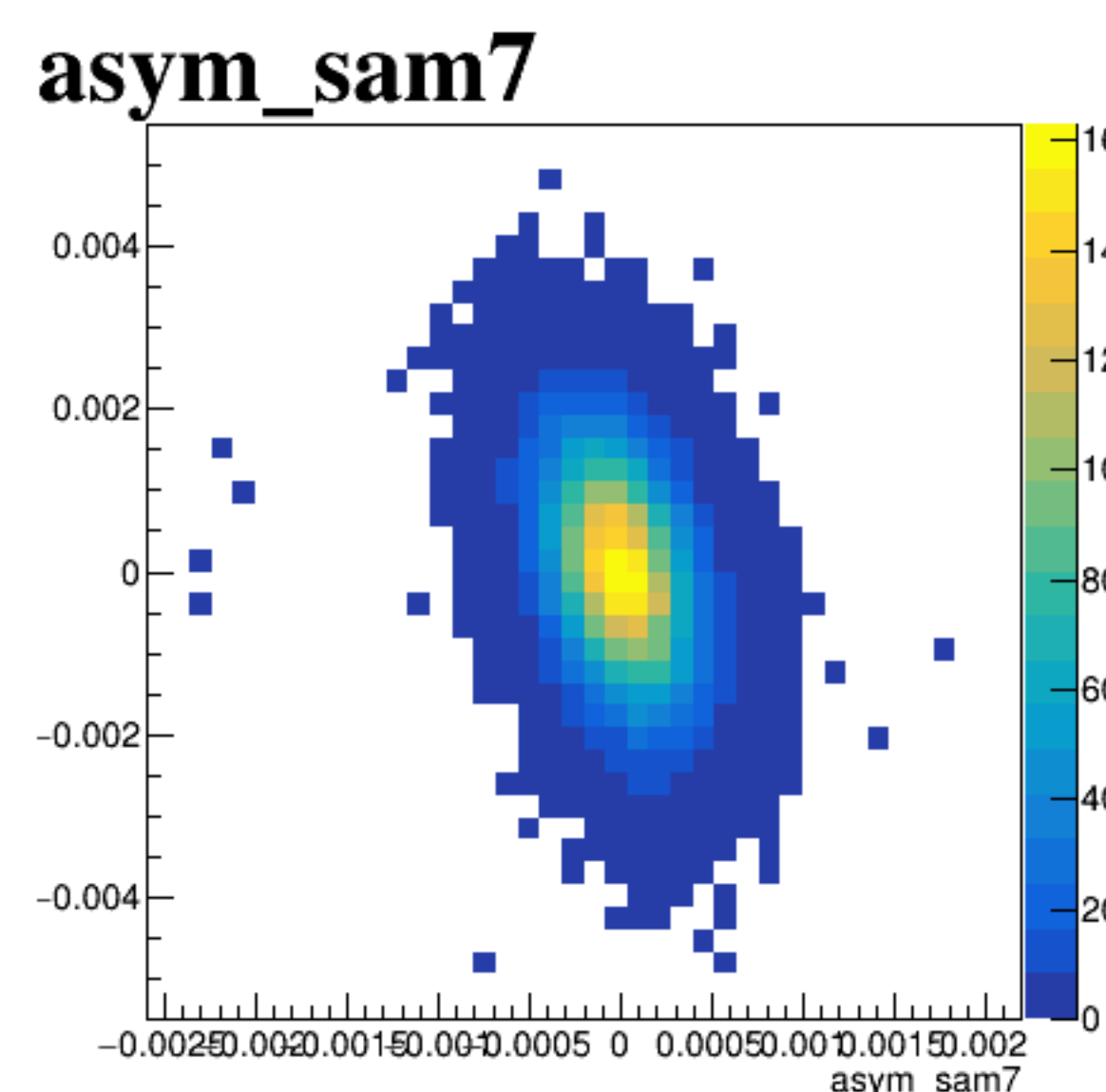
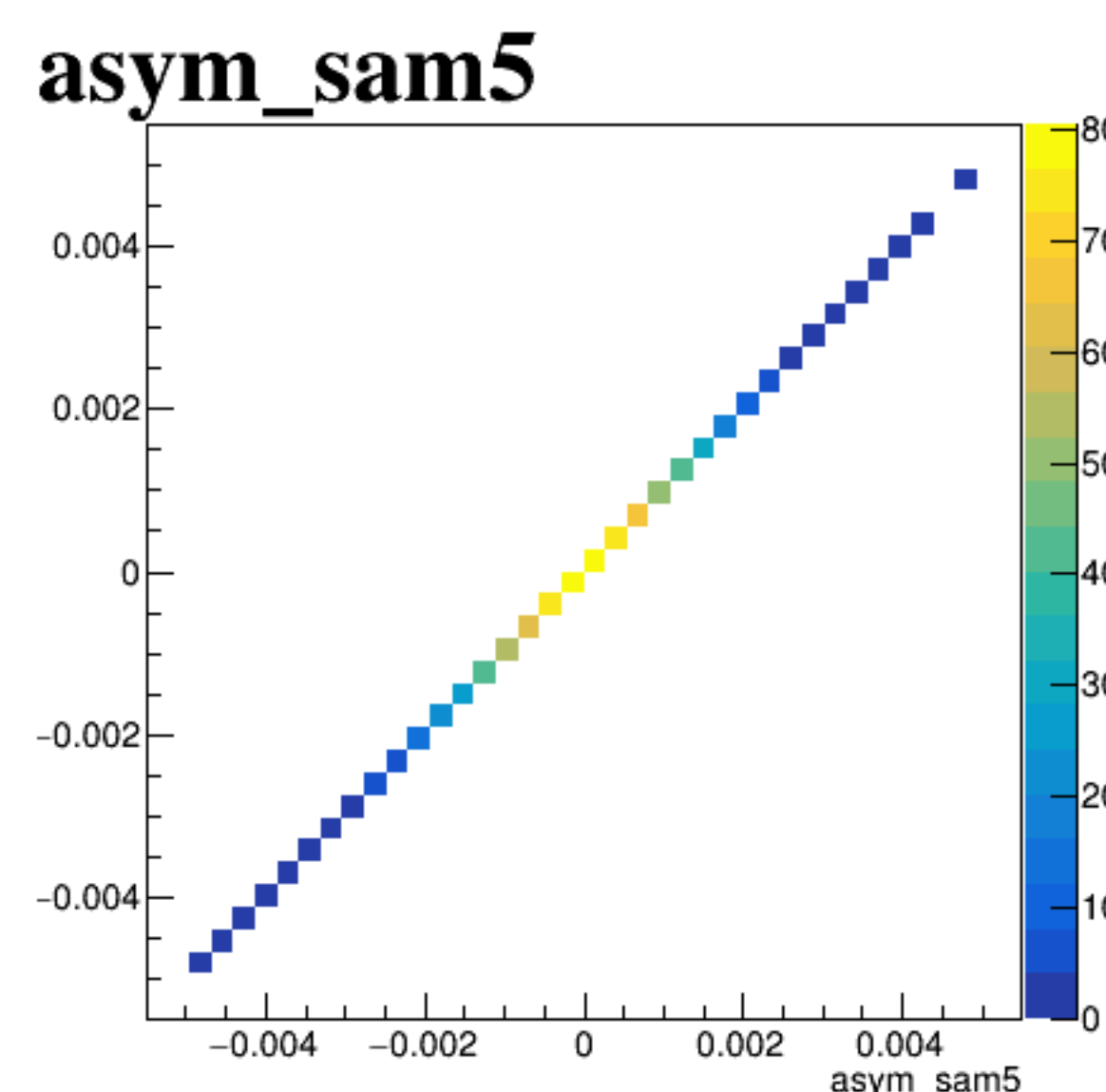
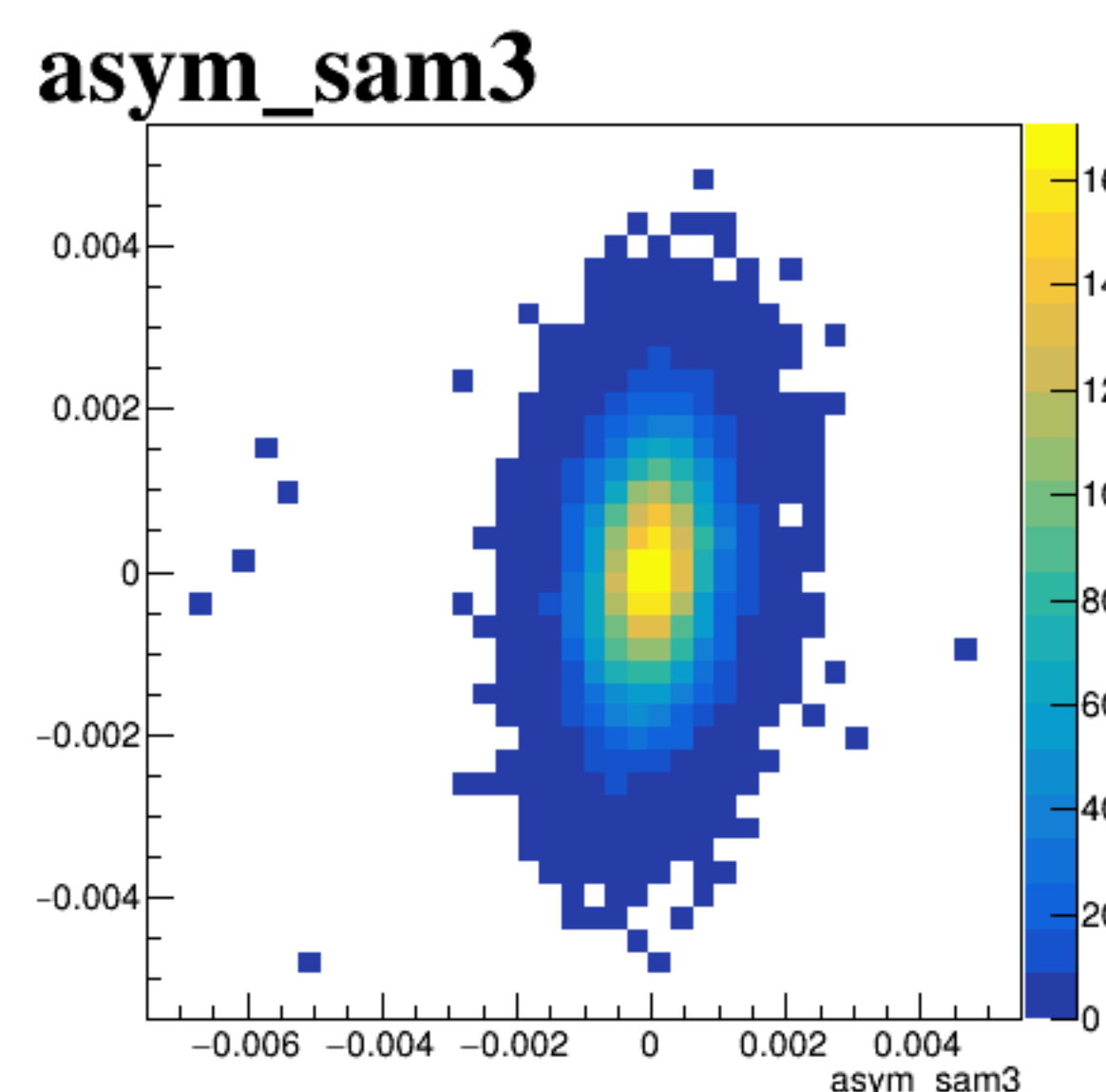
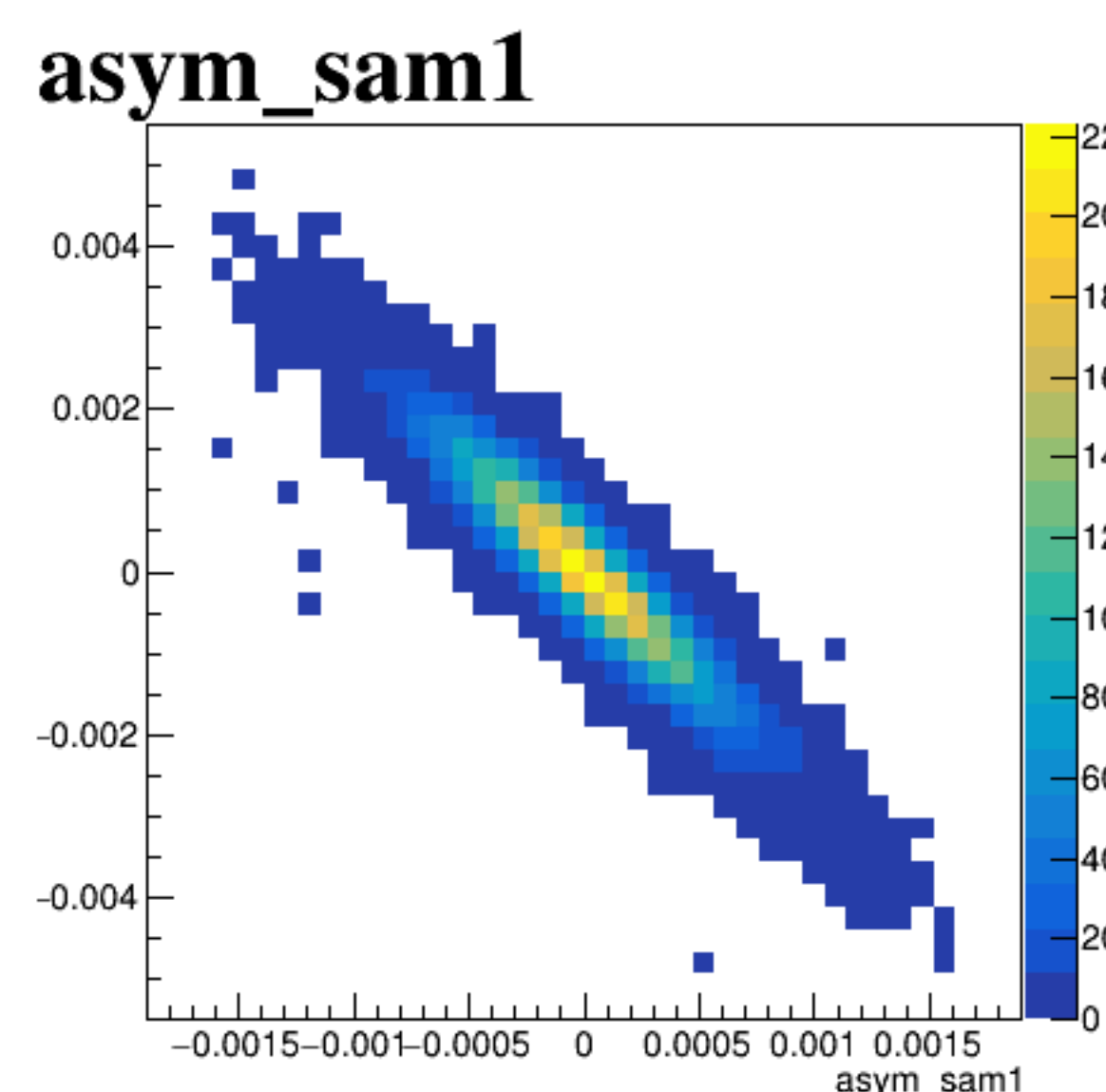


asym_sam6

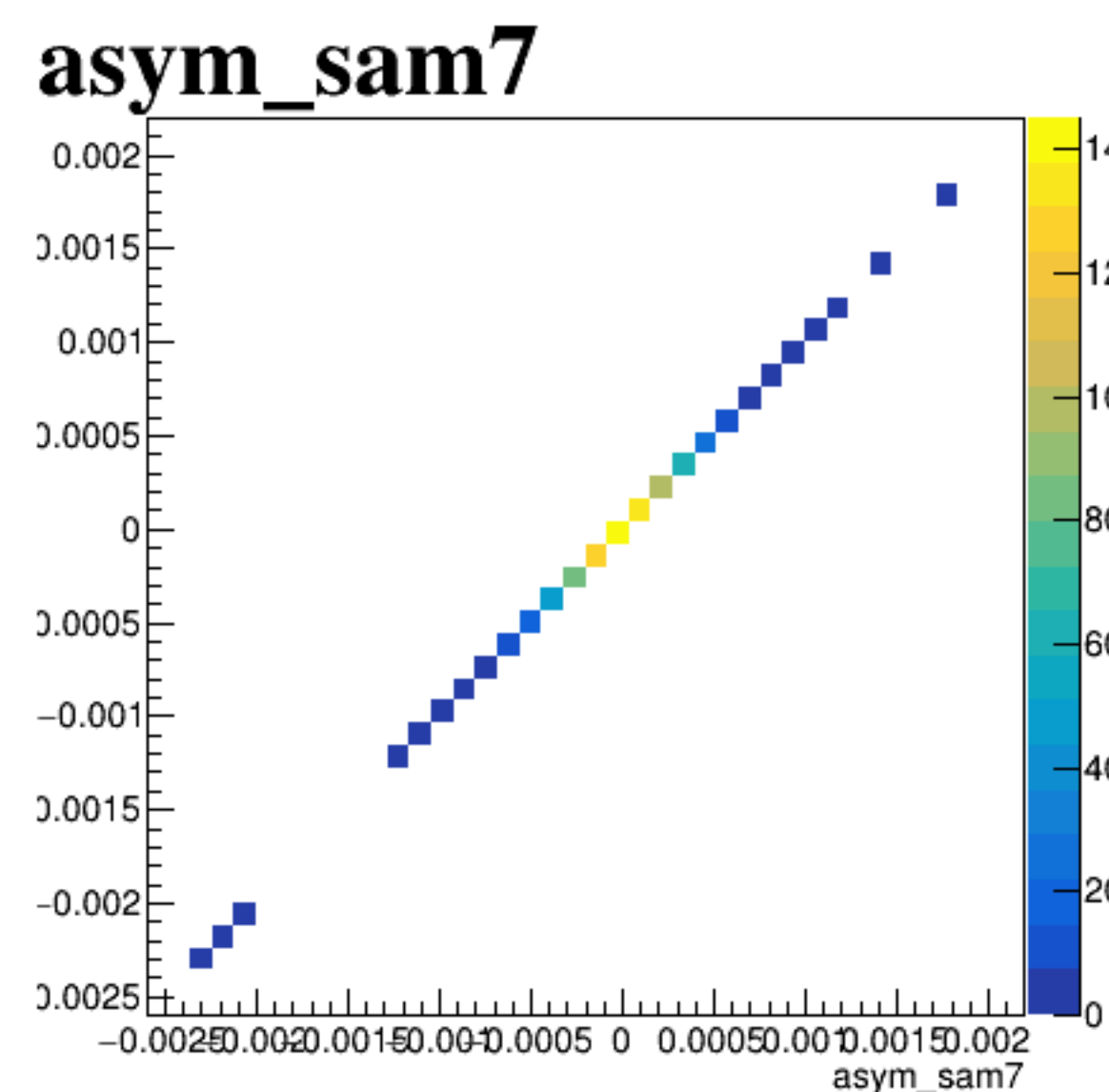
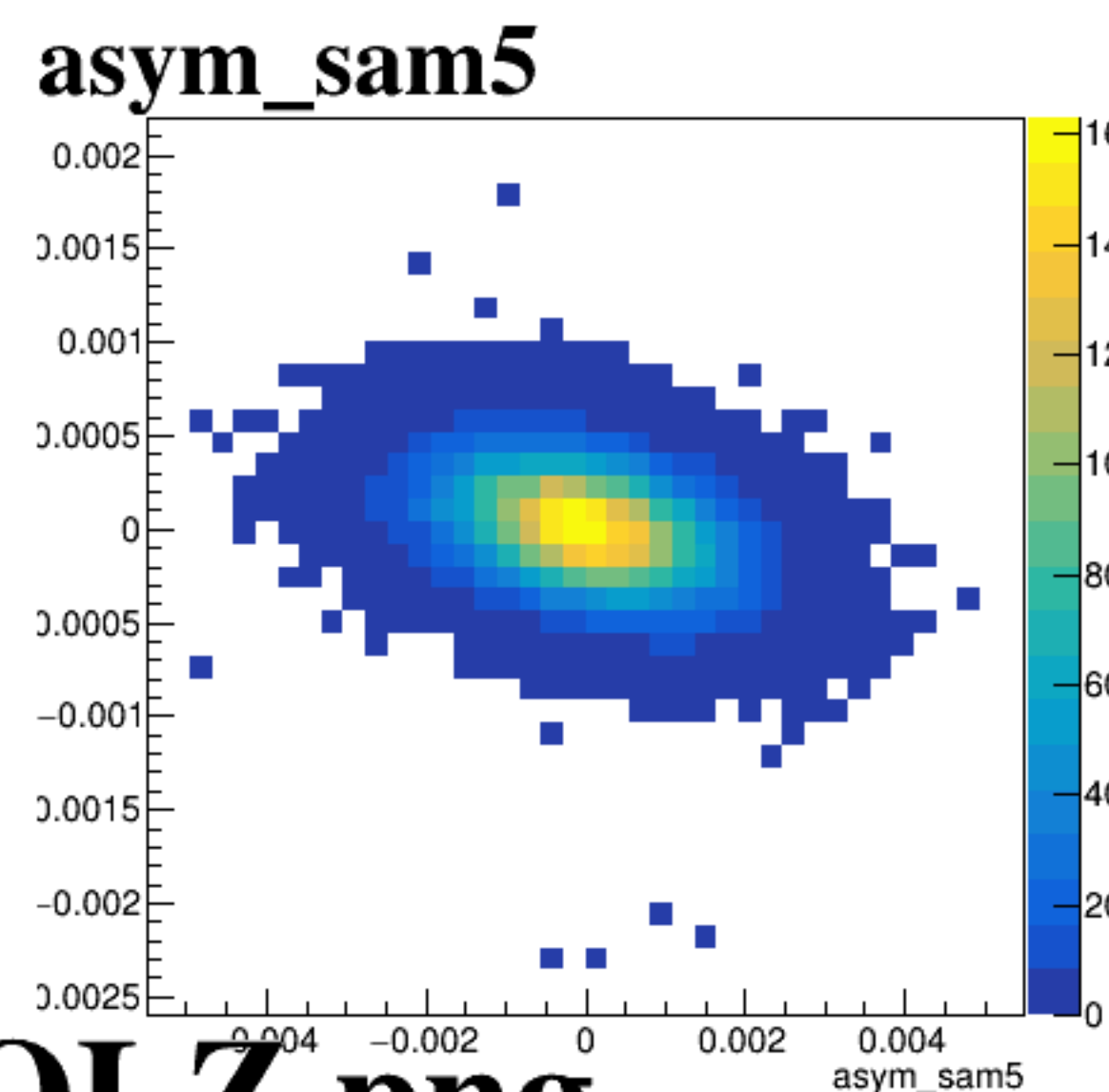
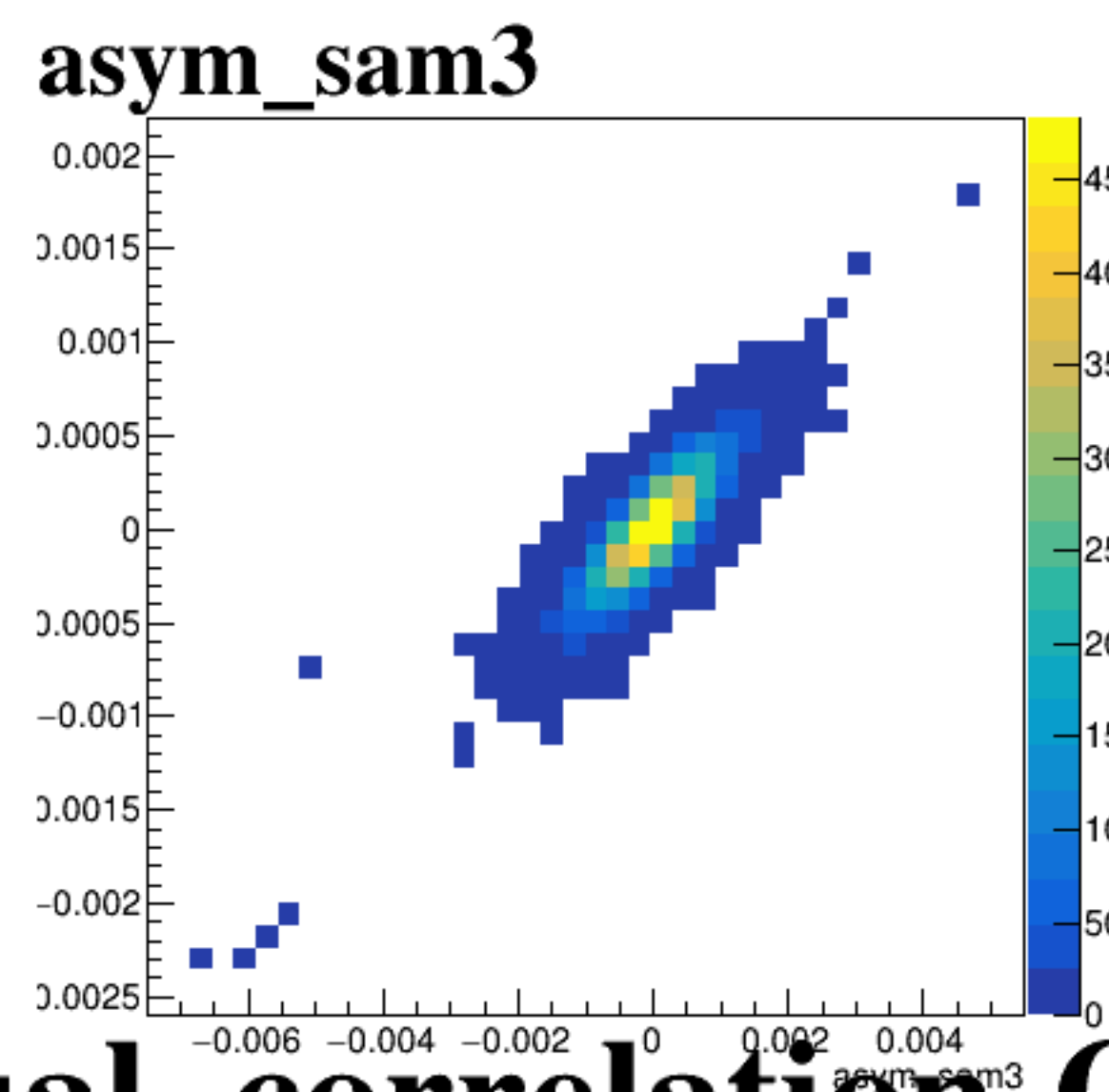
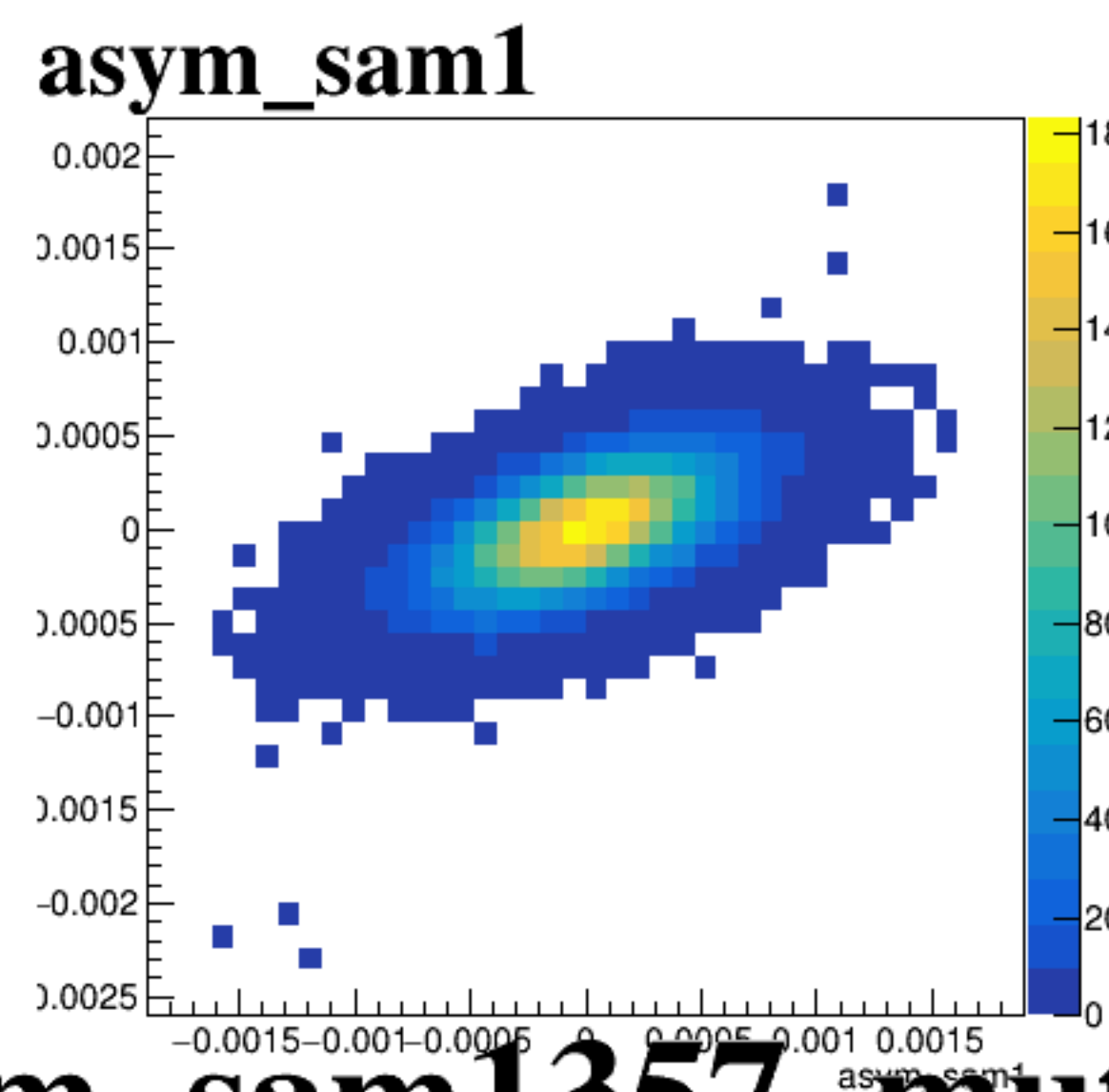
**asym_sam8**

asym_sam1

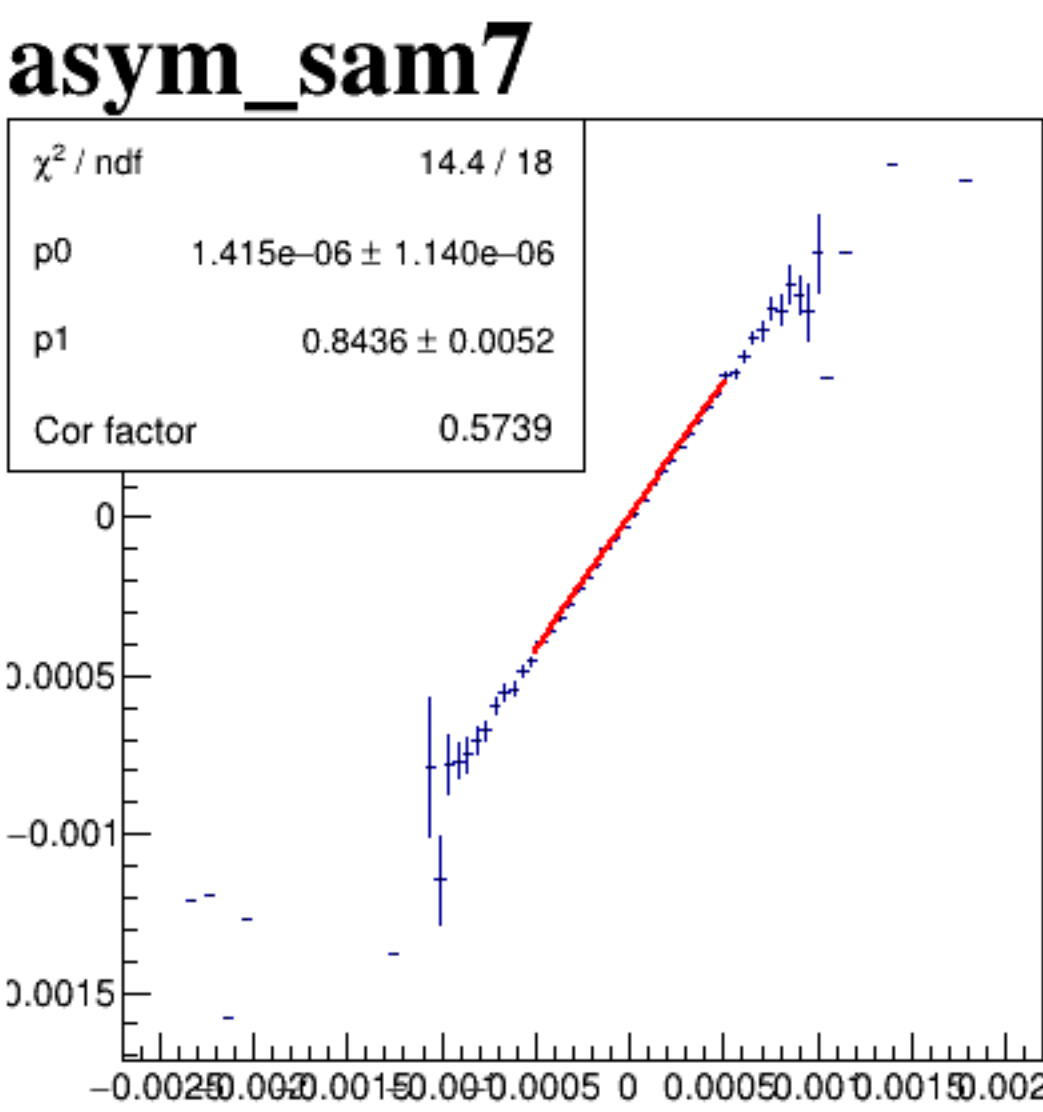
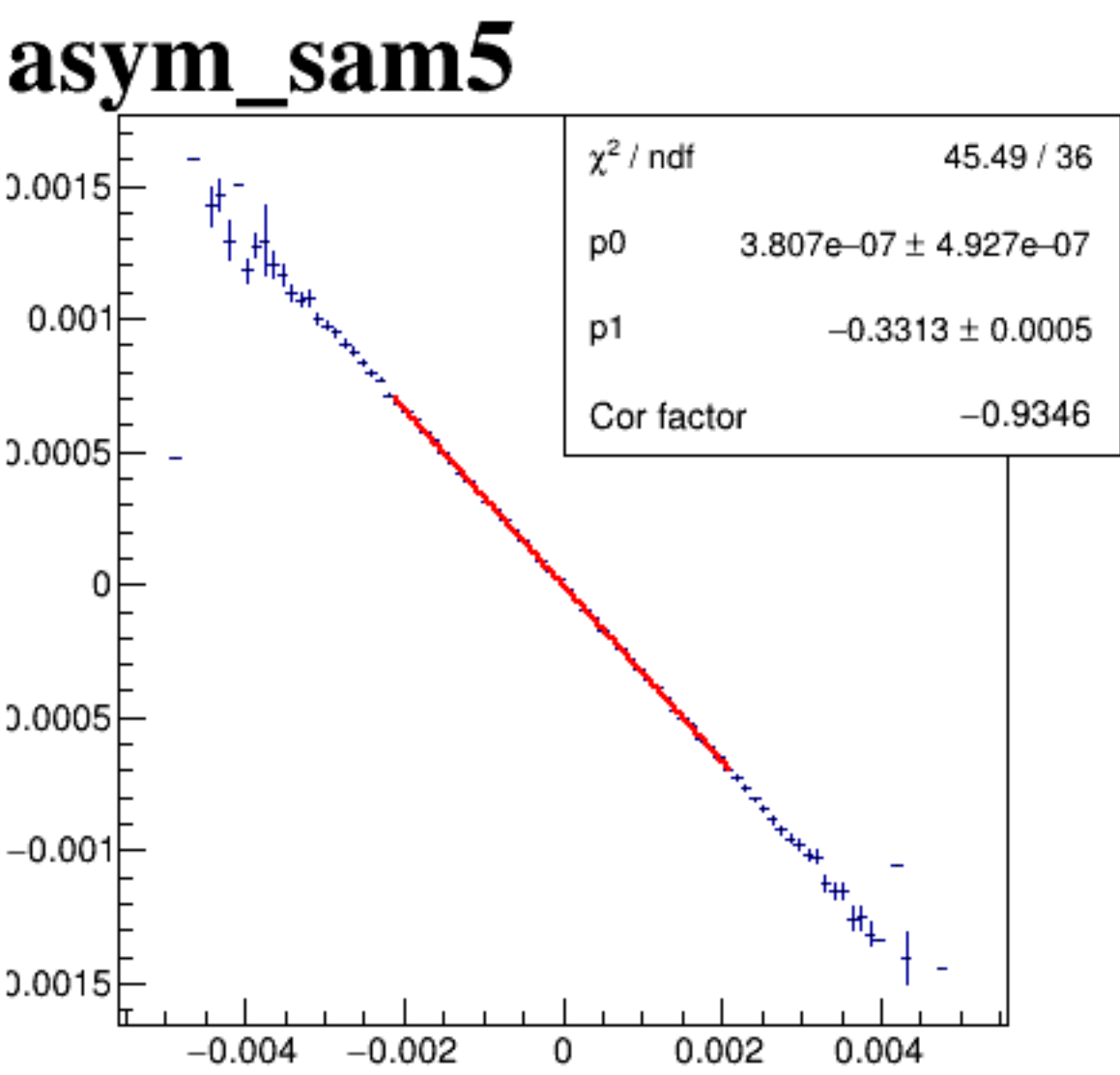
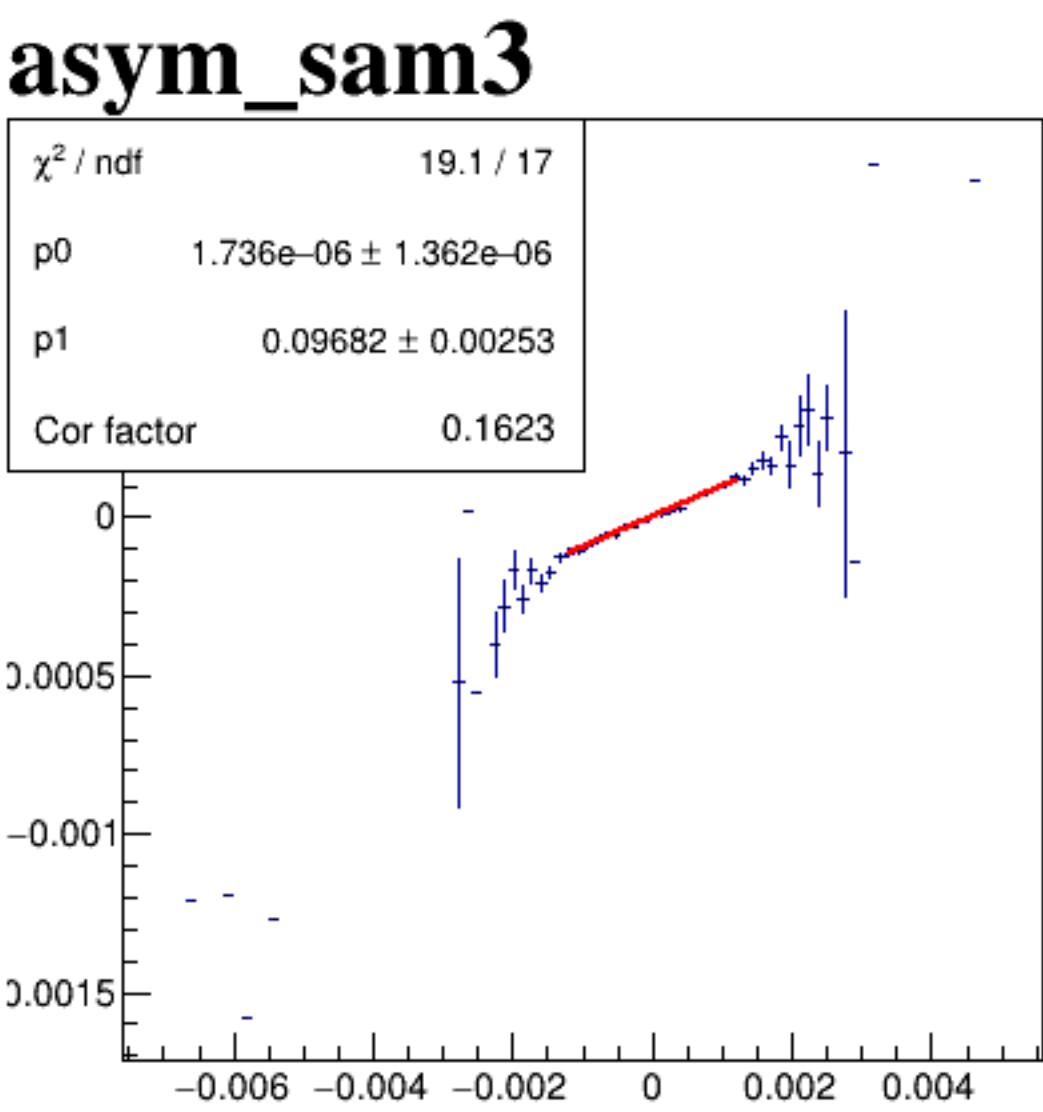
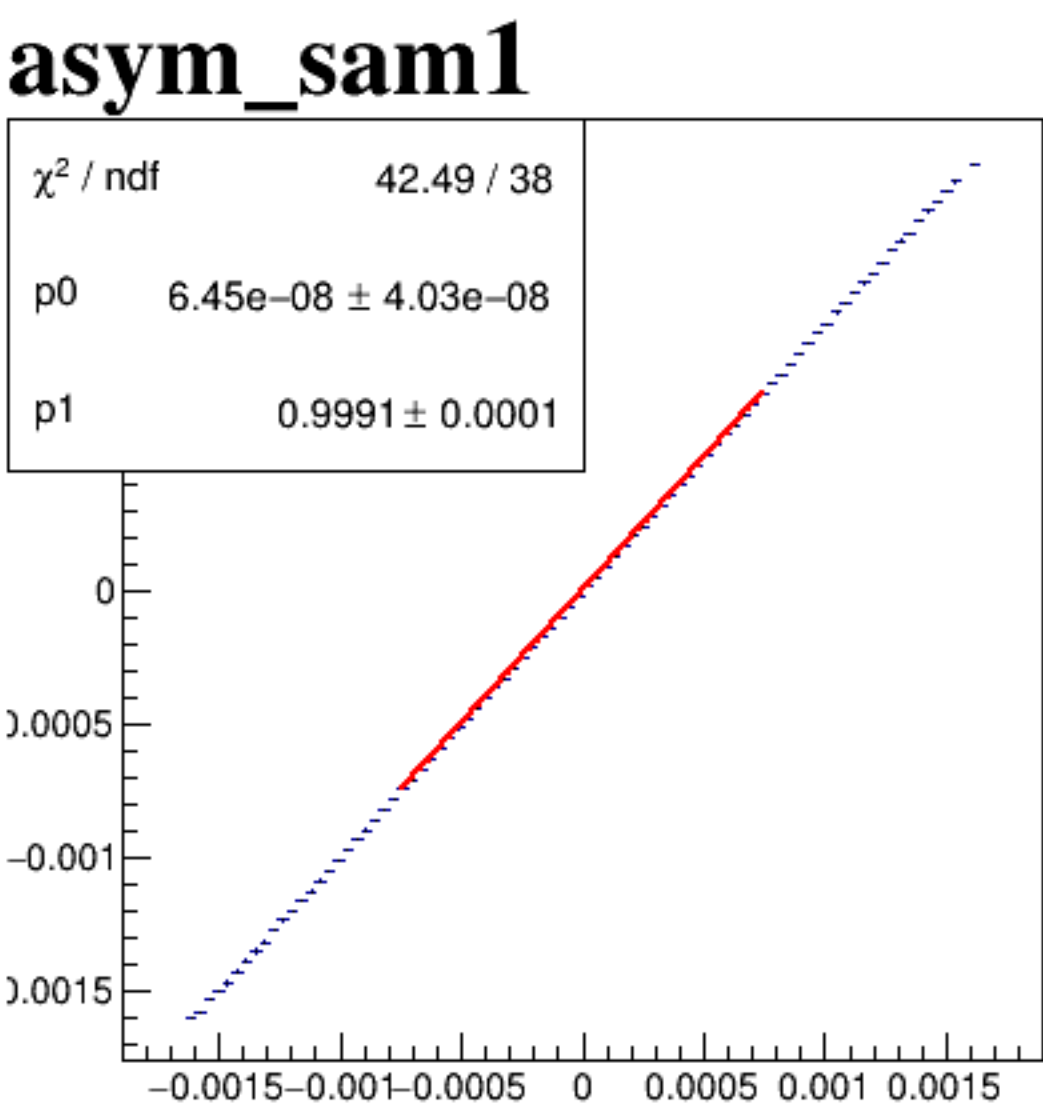
asym_sam3

**asym_sam5**

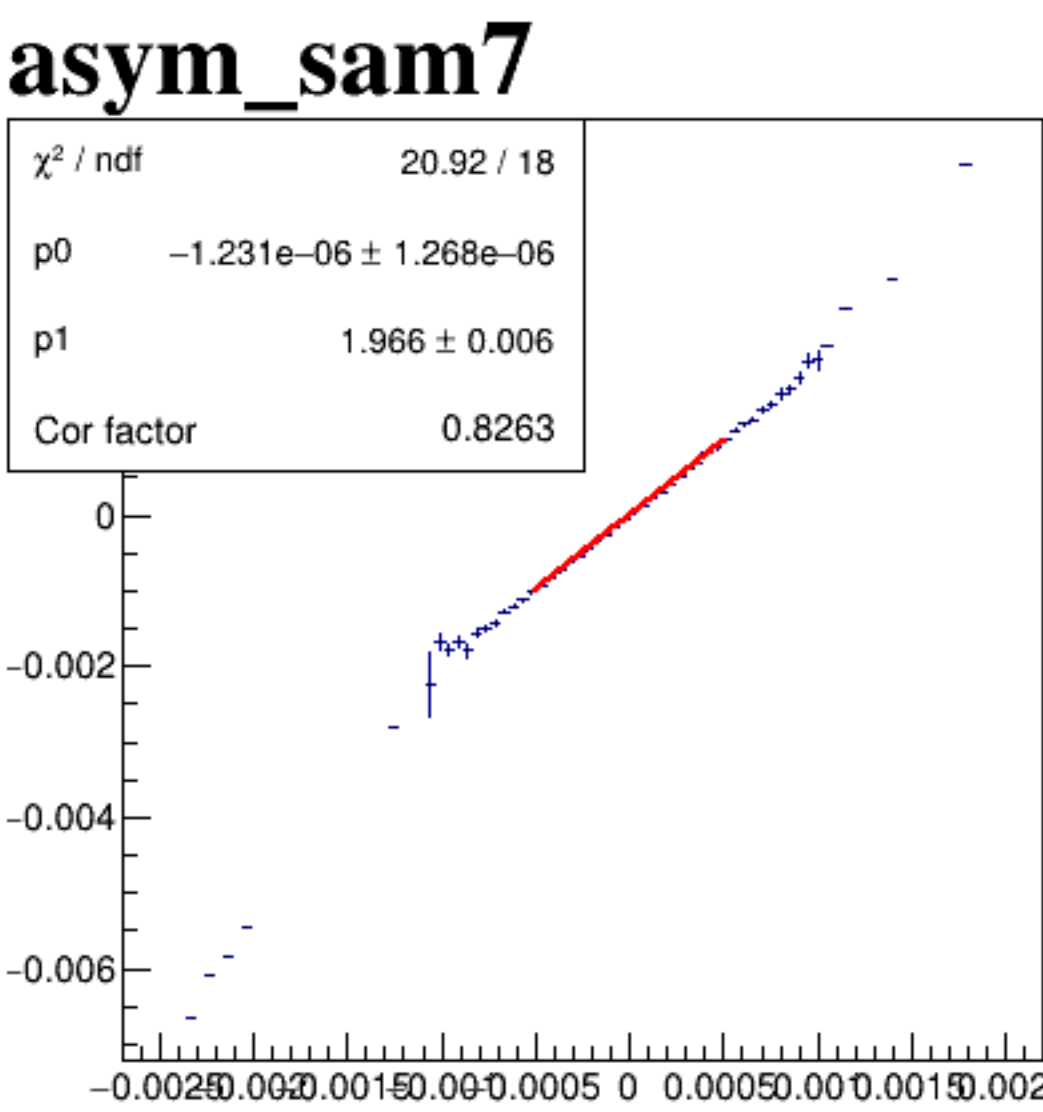
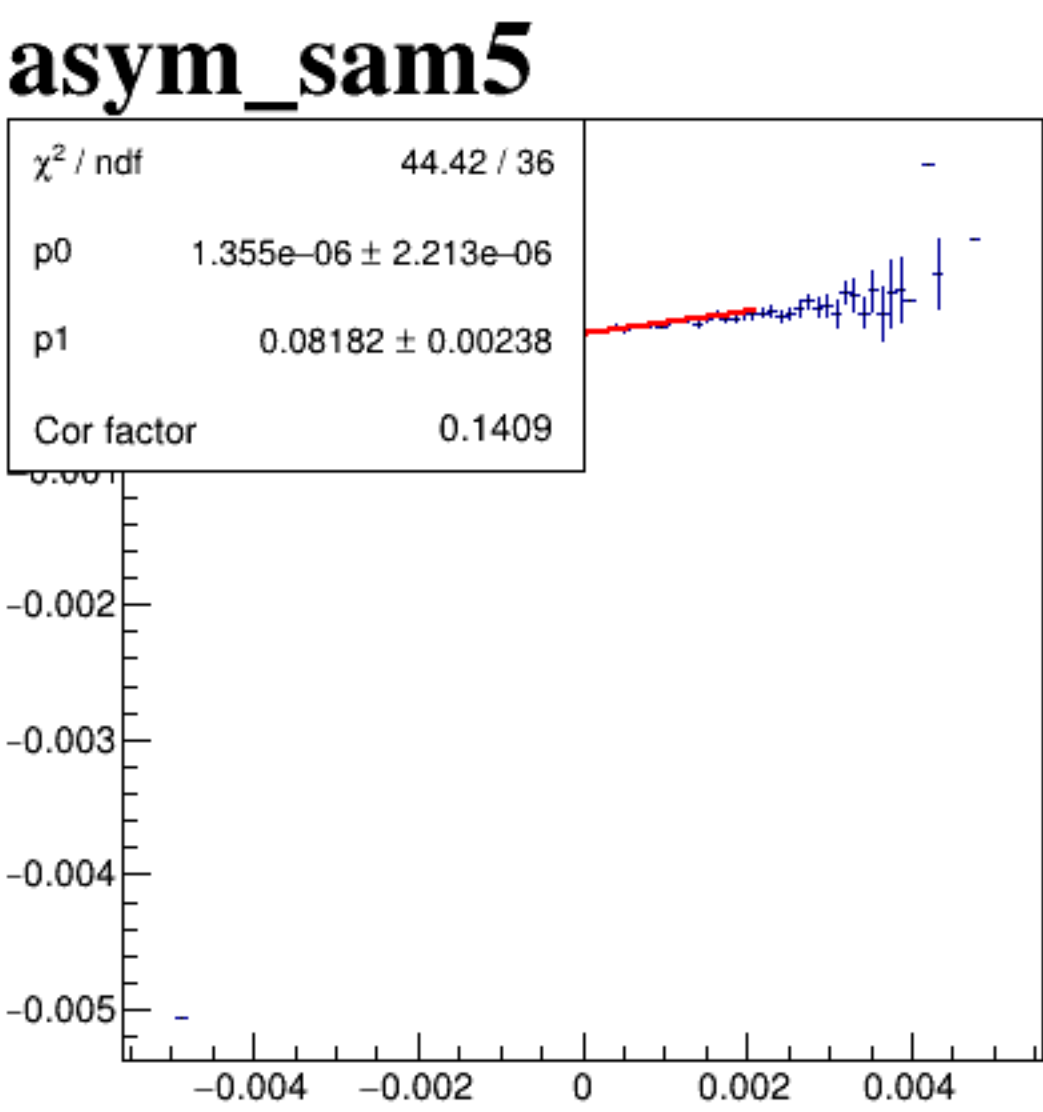
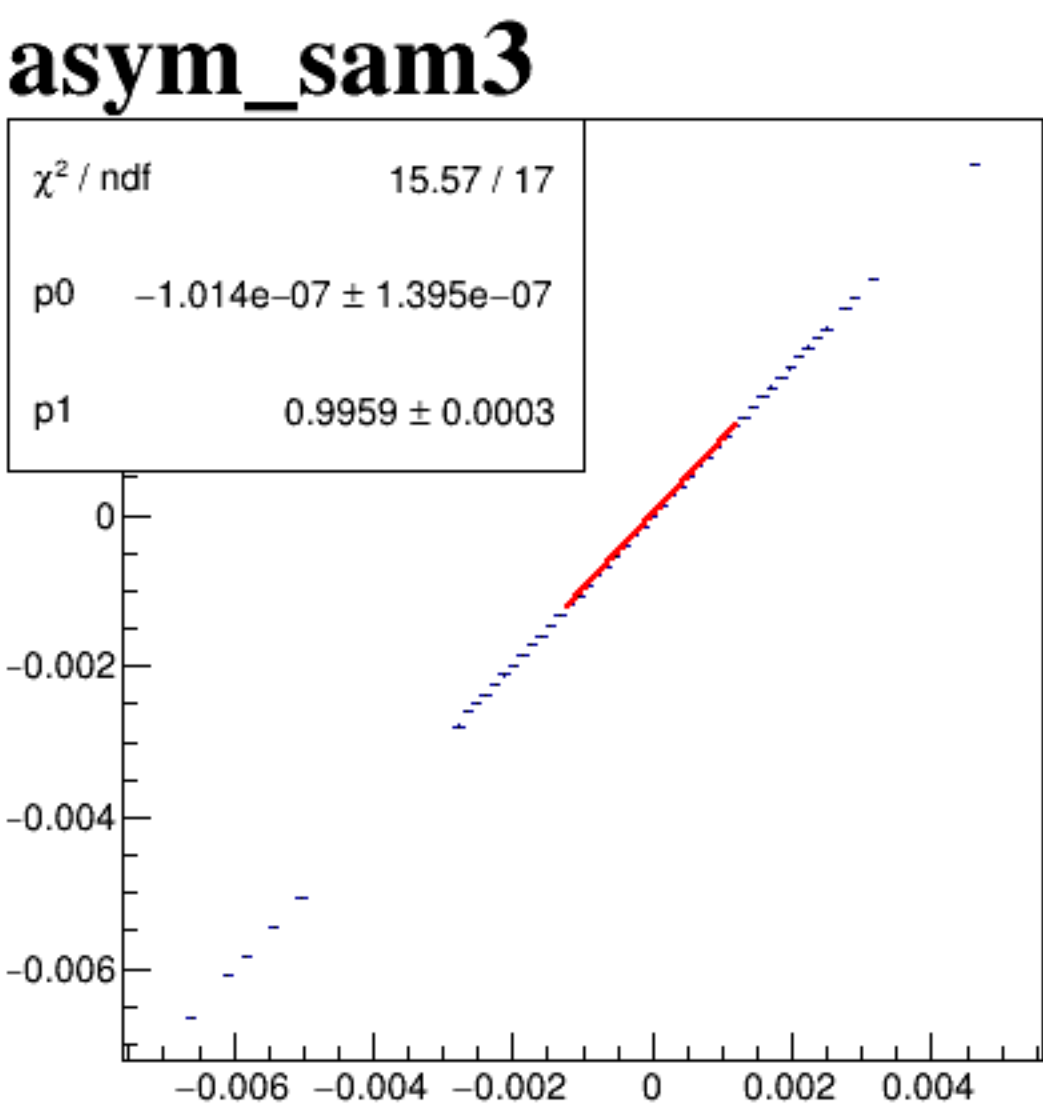
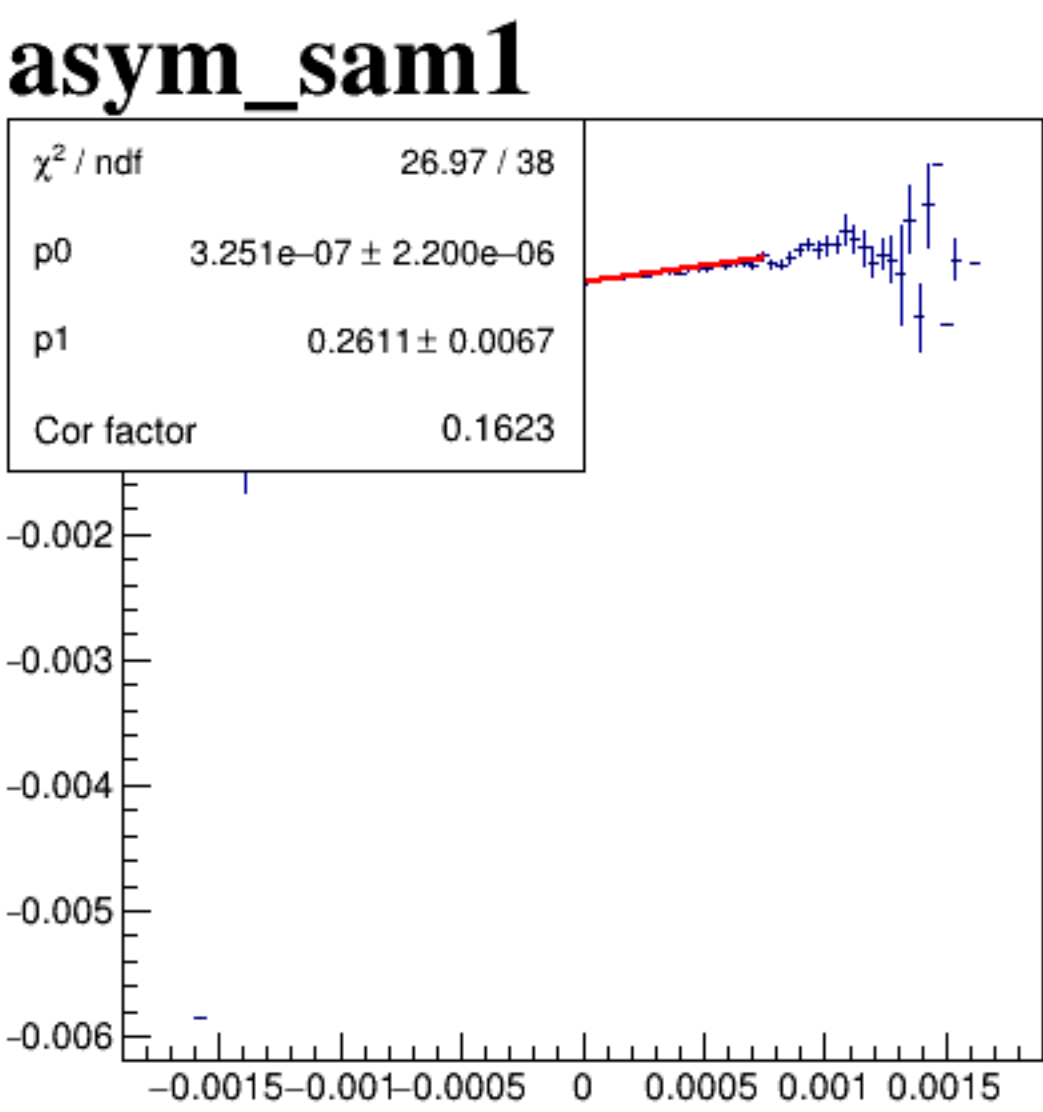
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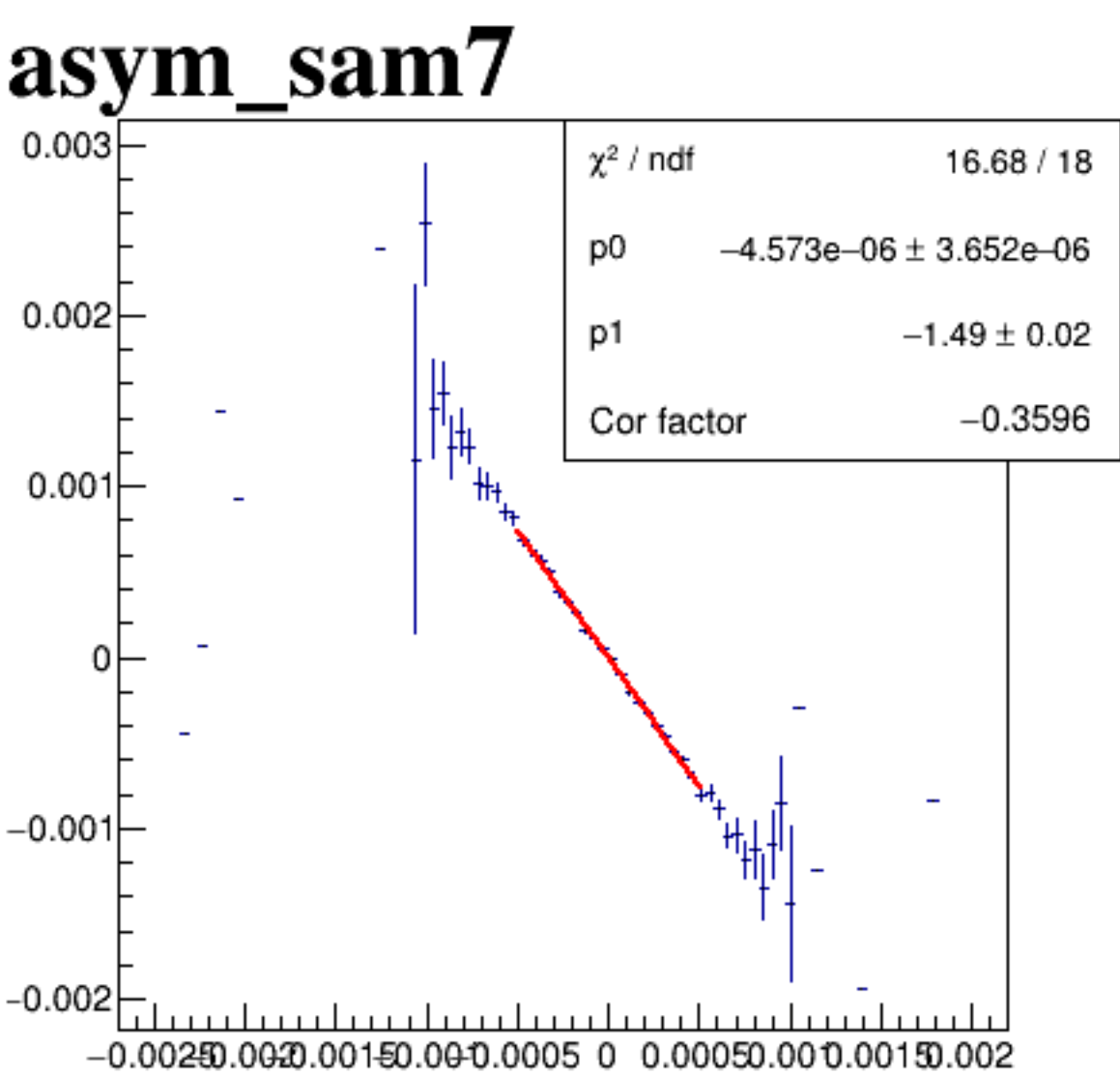
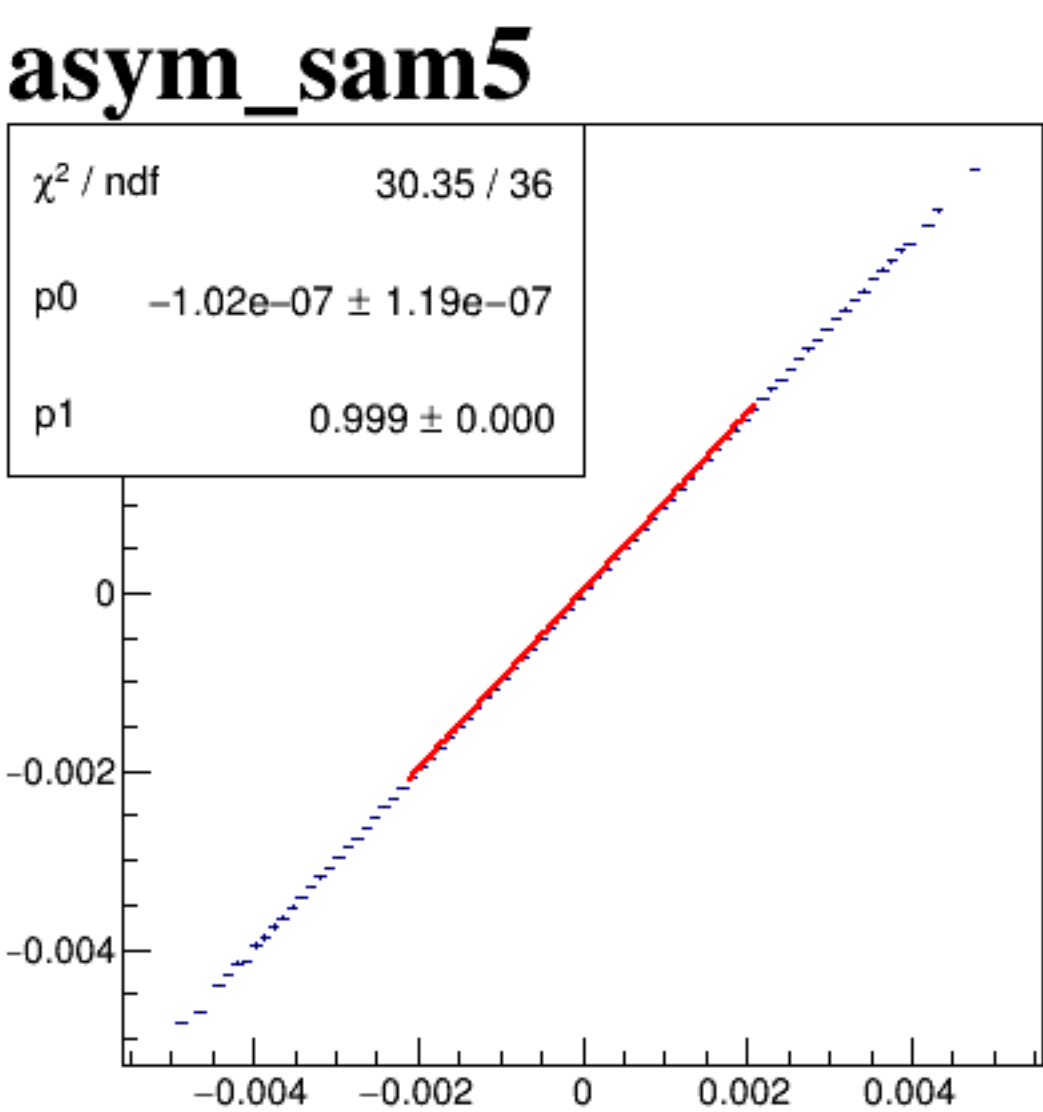
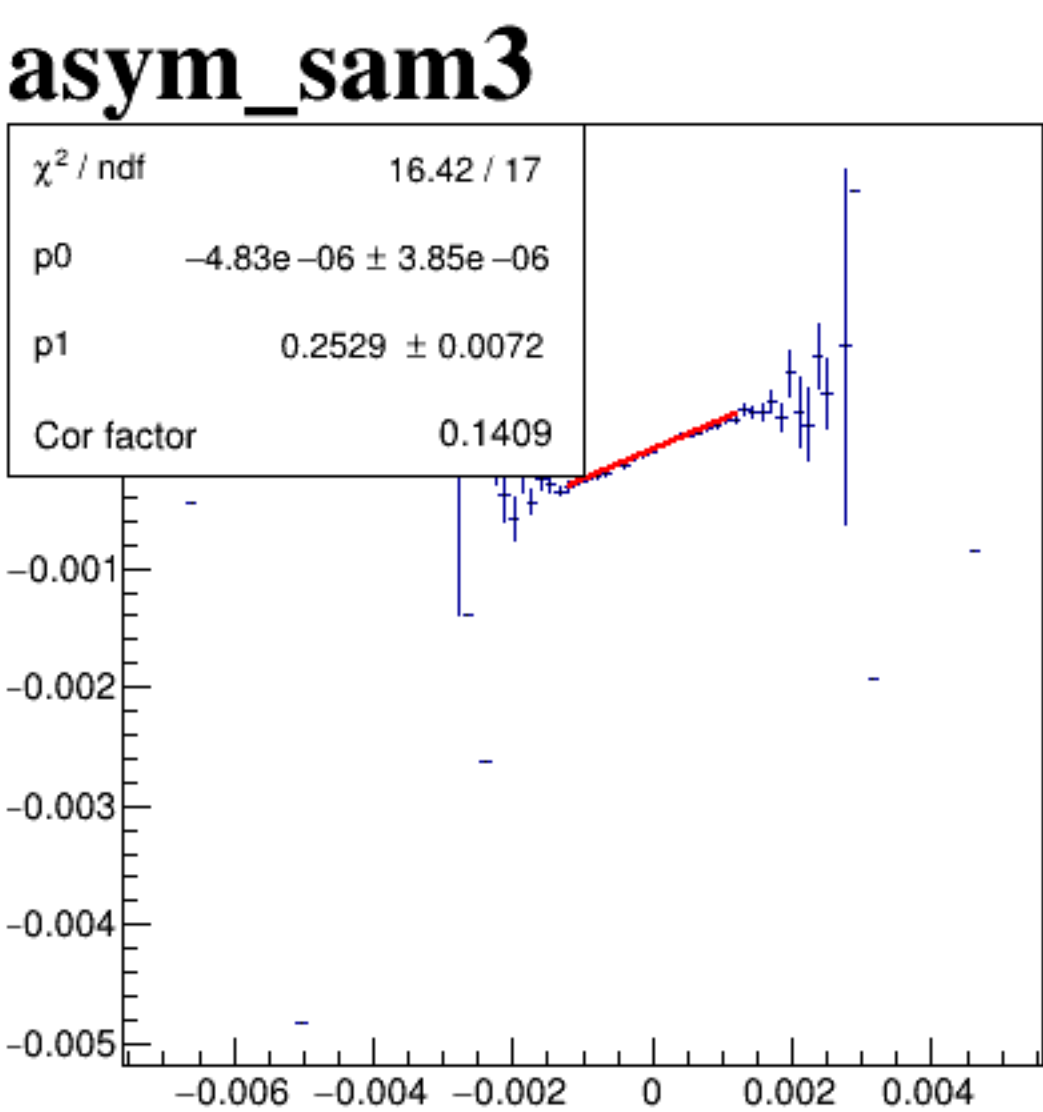
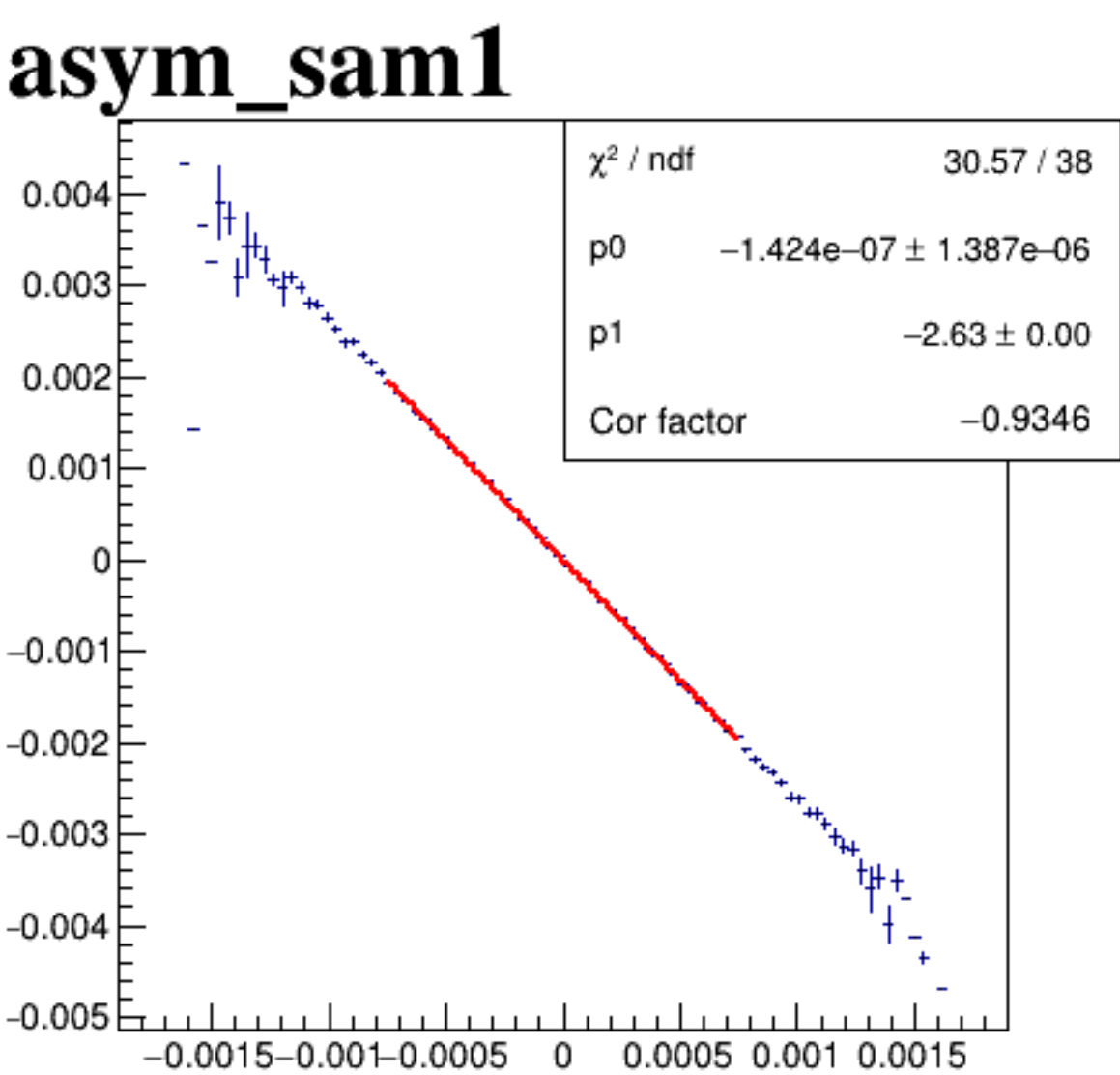
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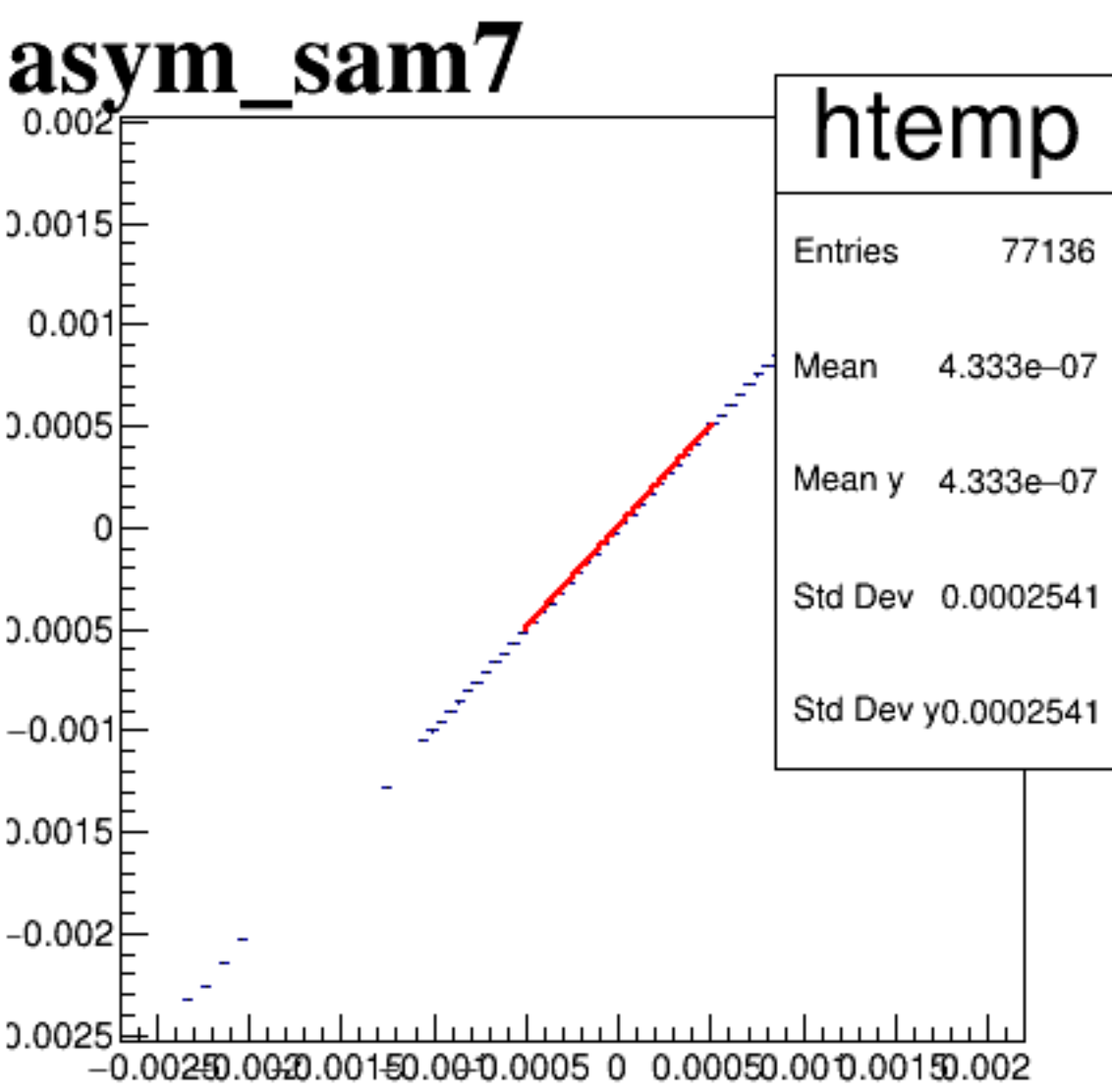
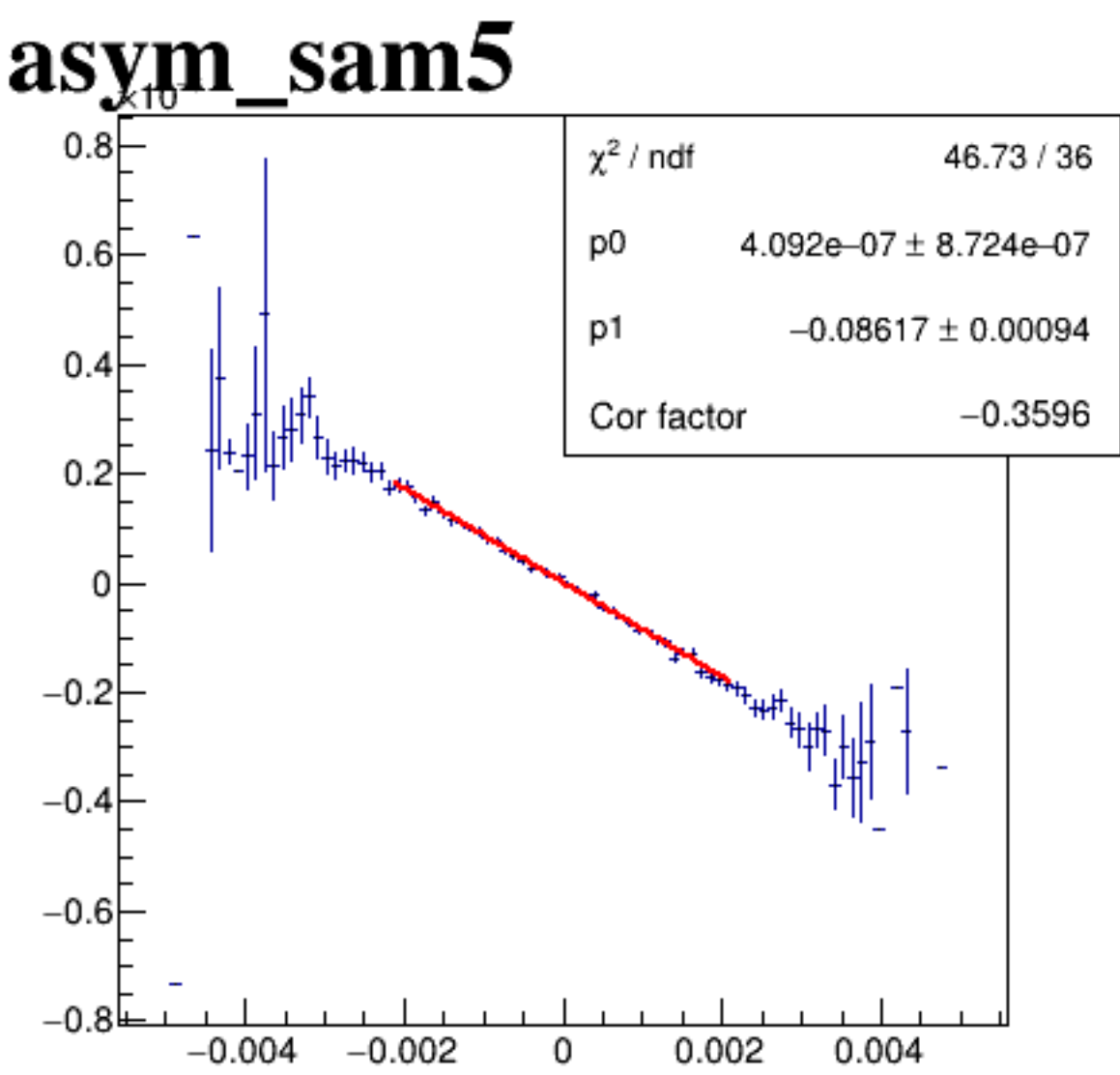
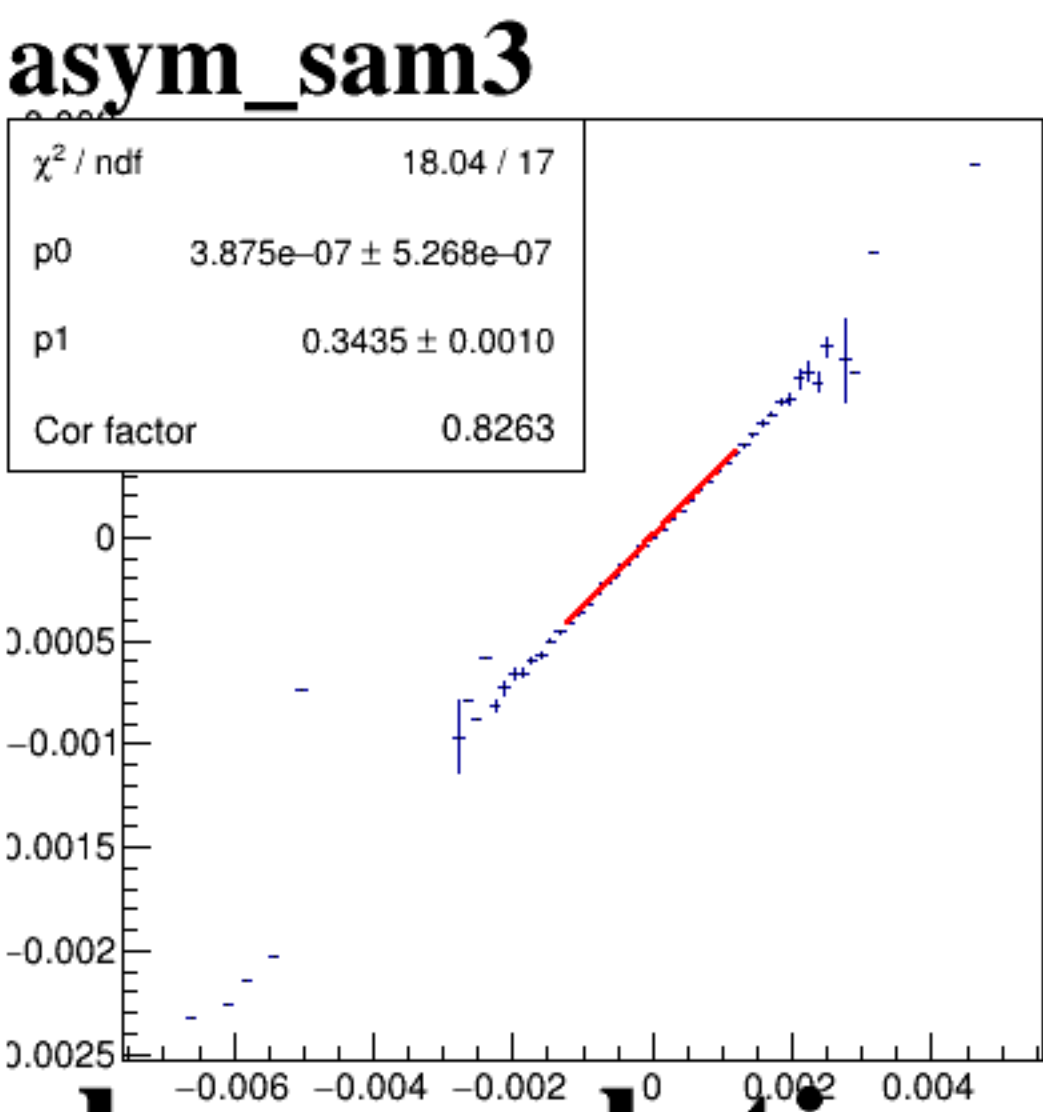
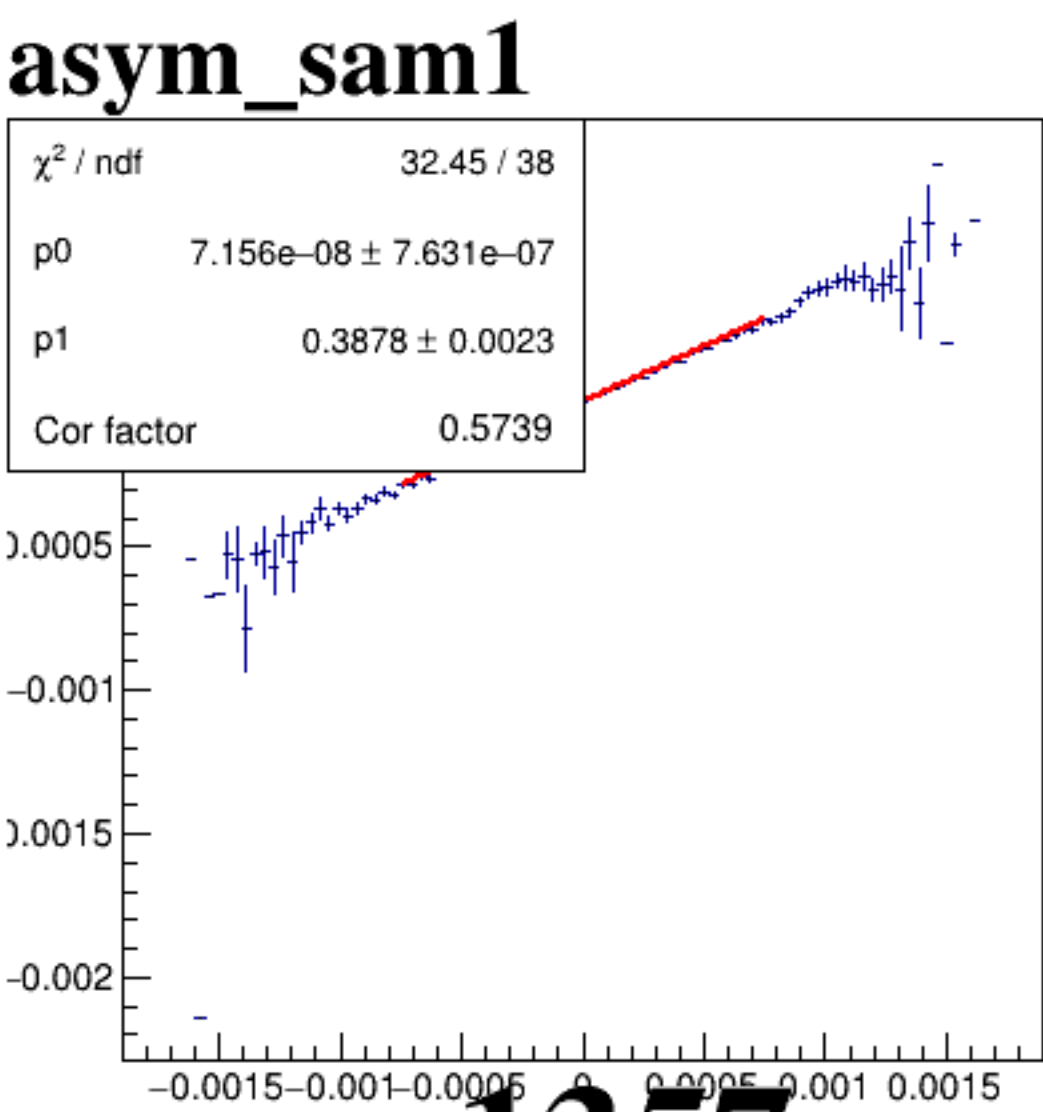
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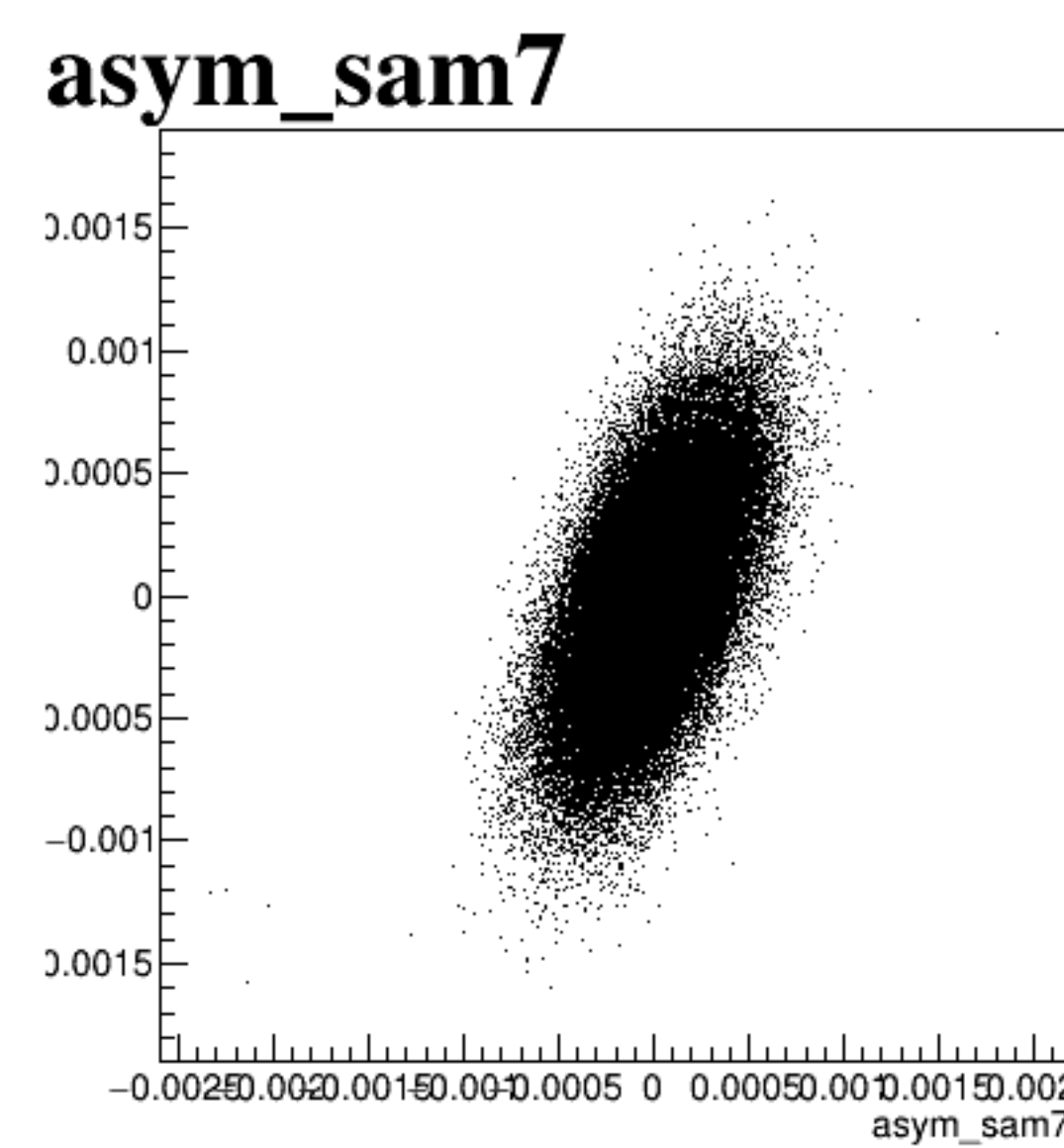
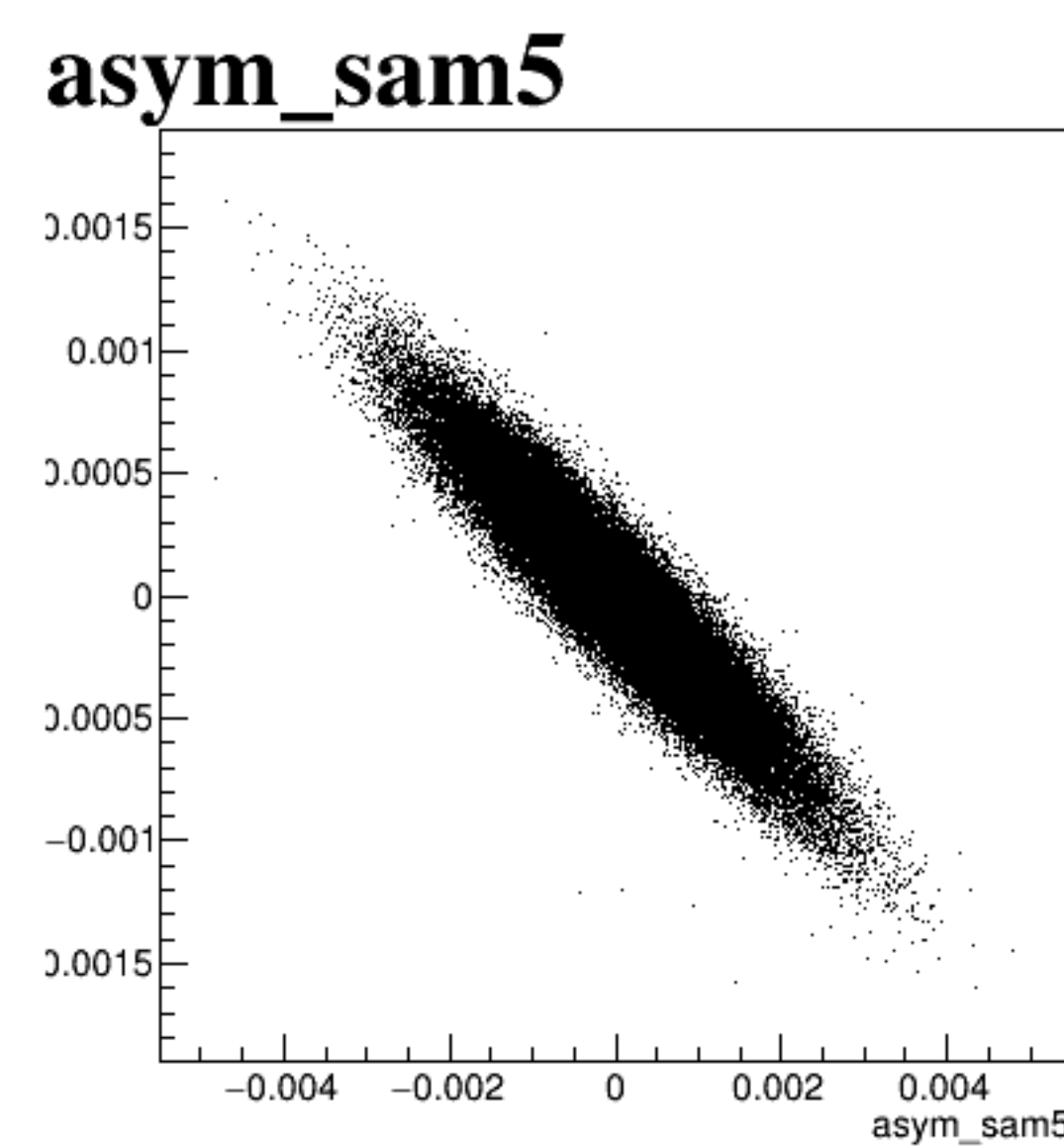
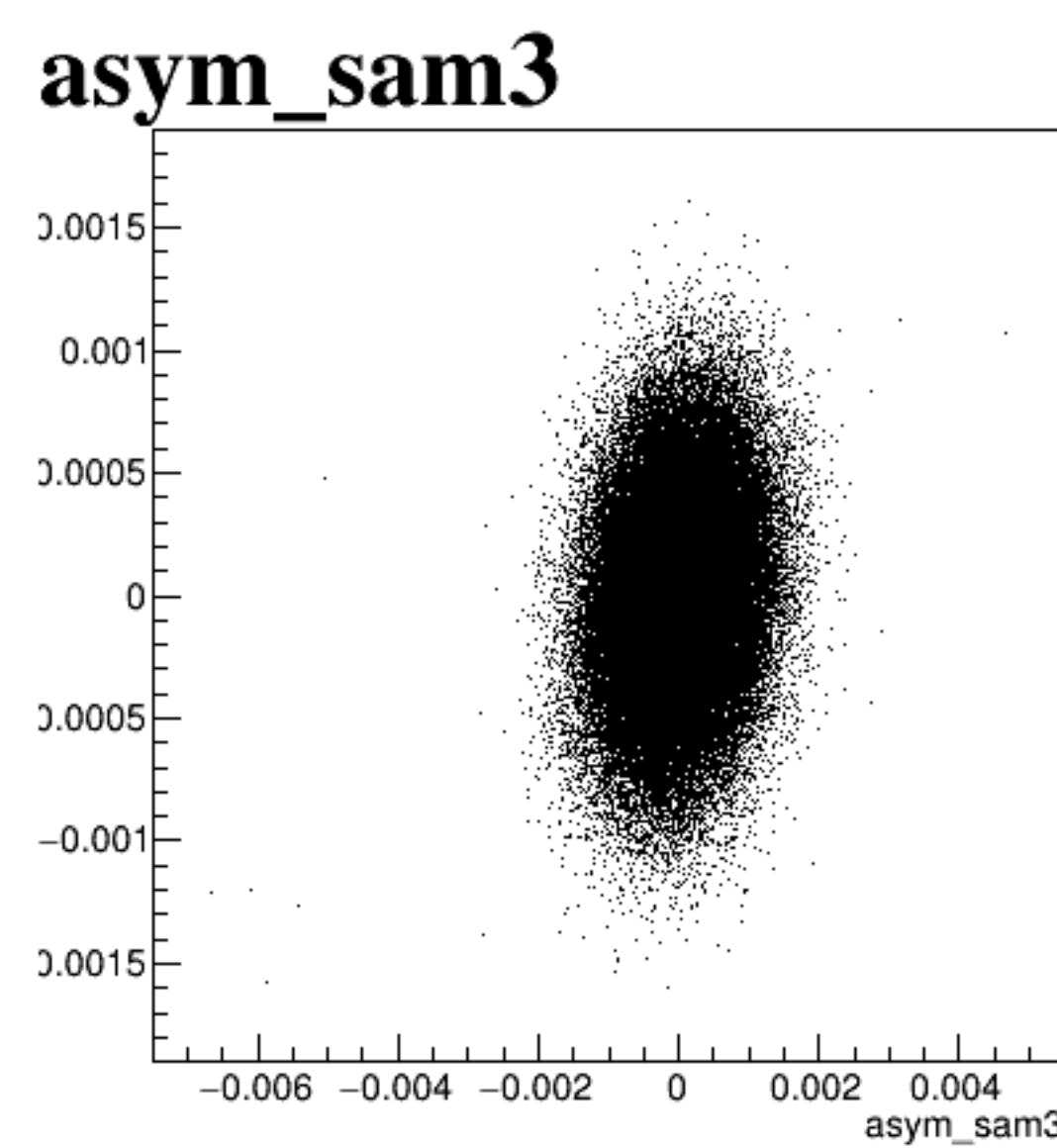
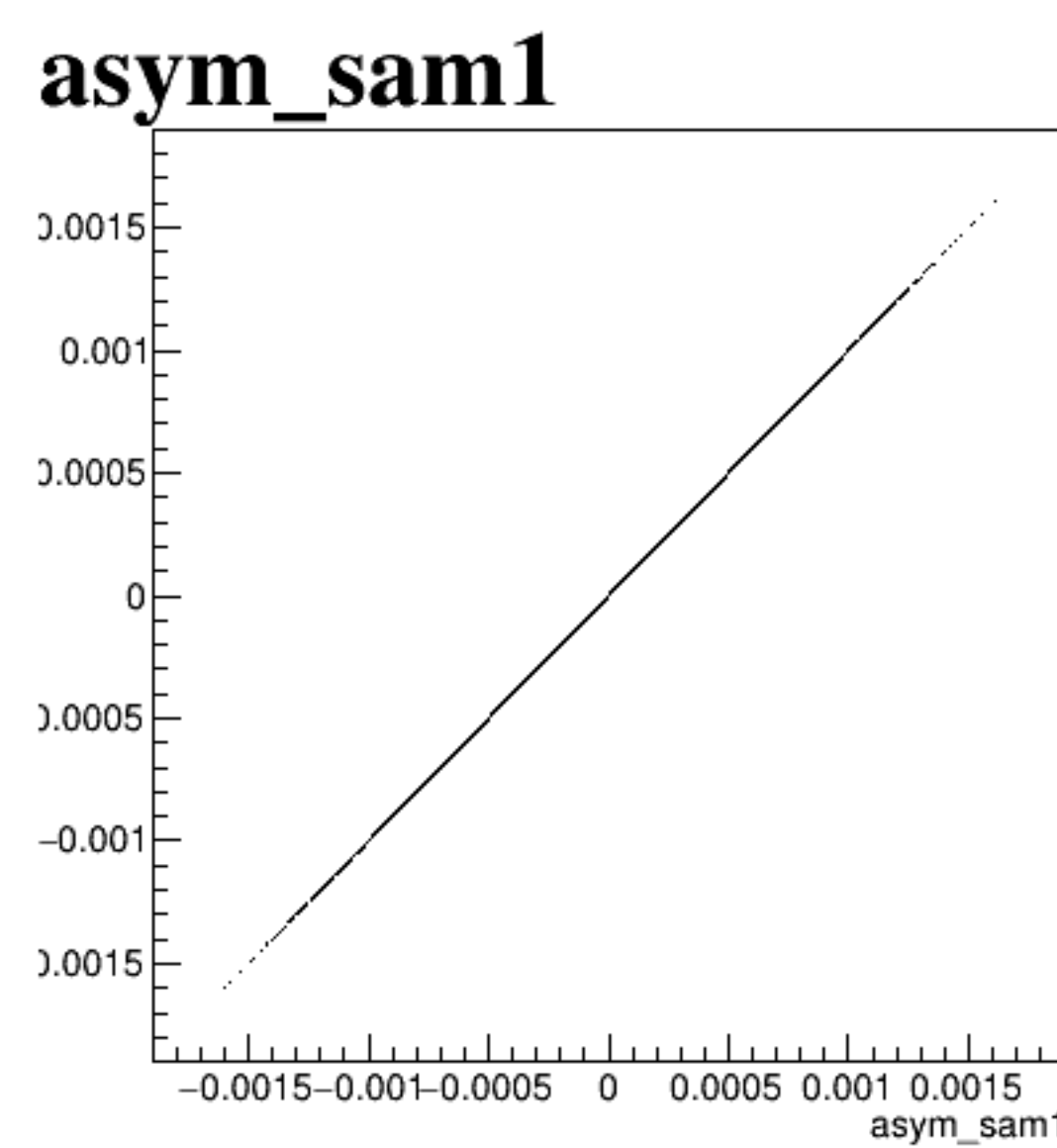


asym_sam5

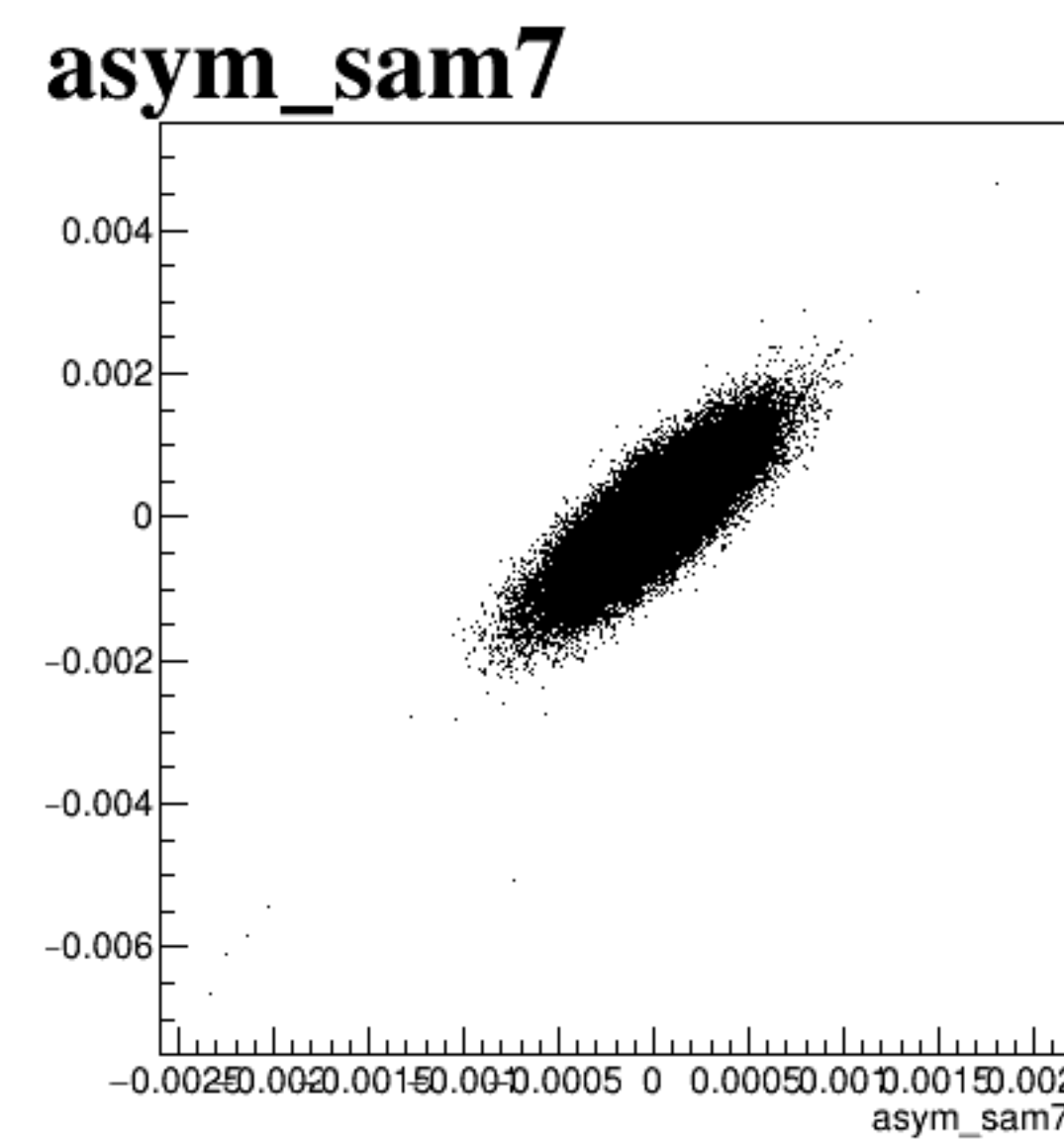
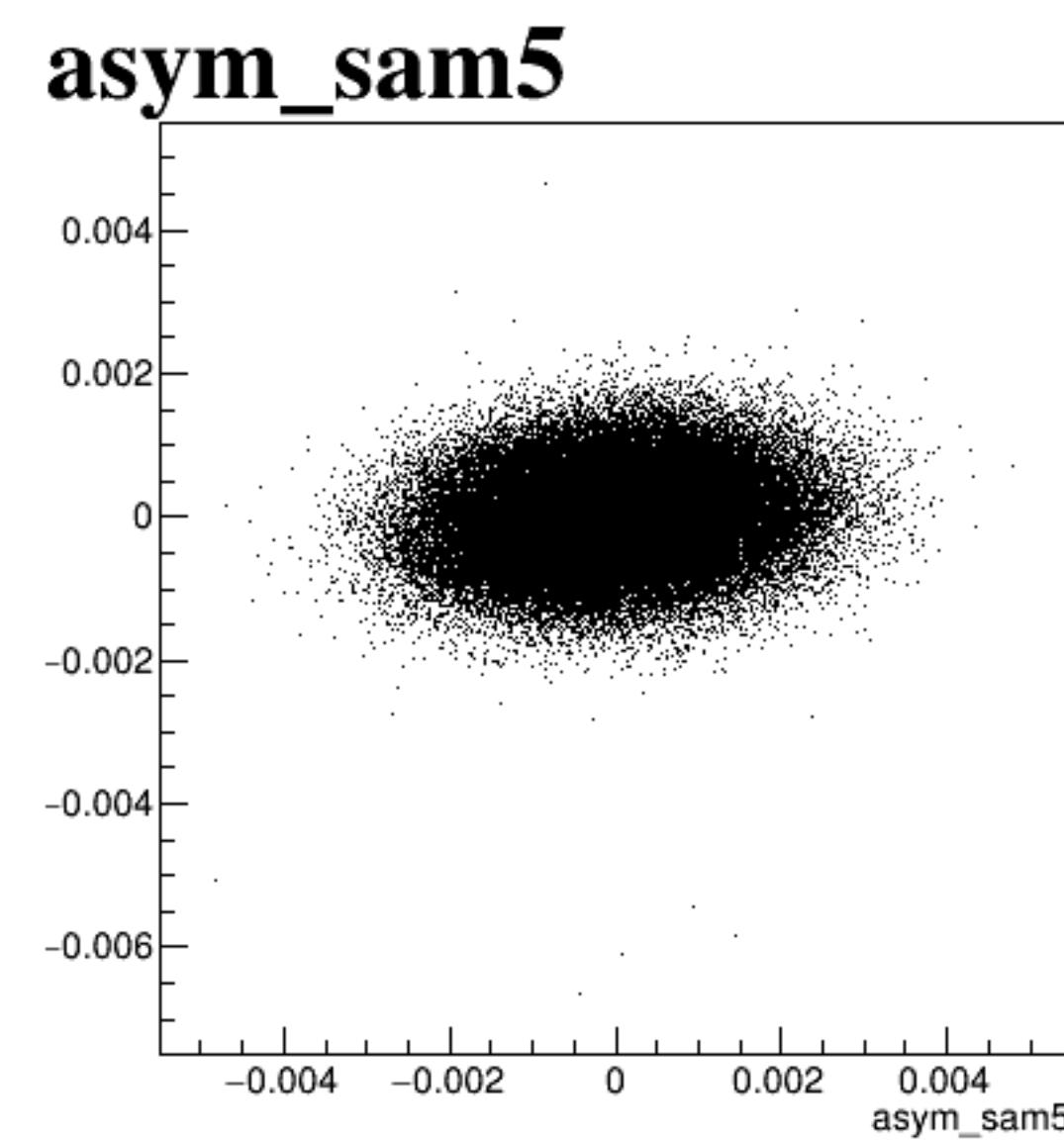
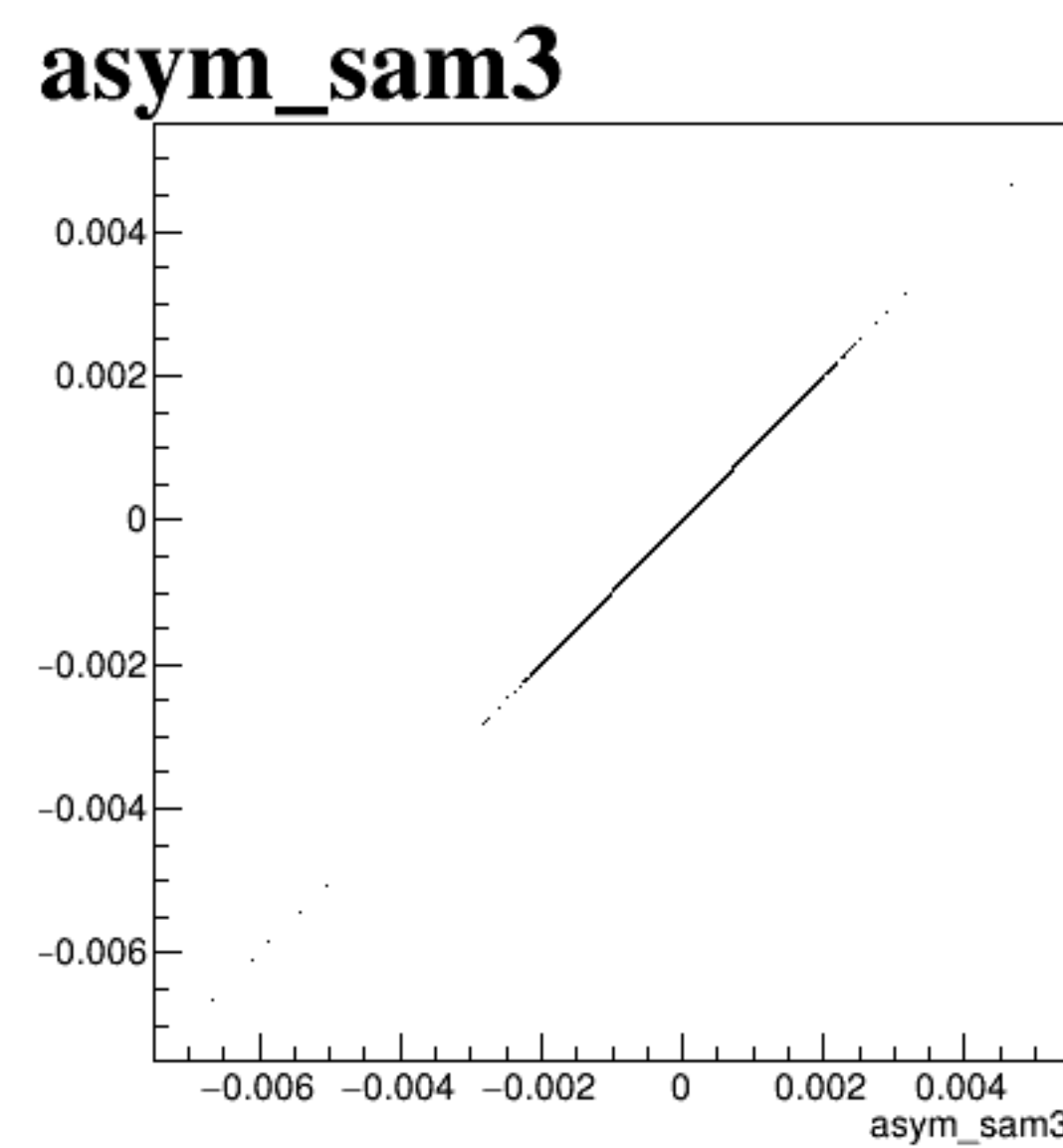
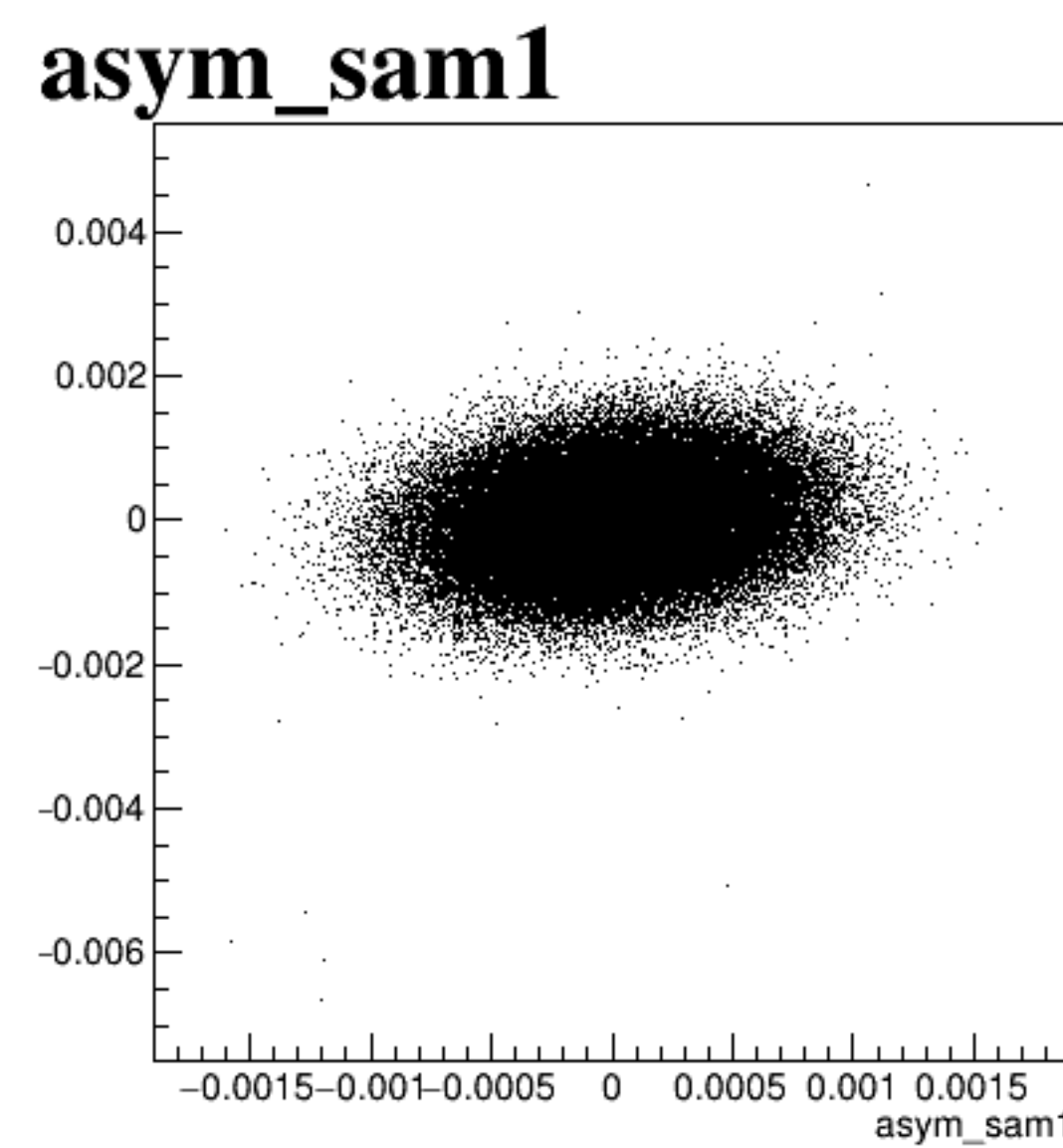
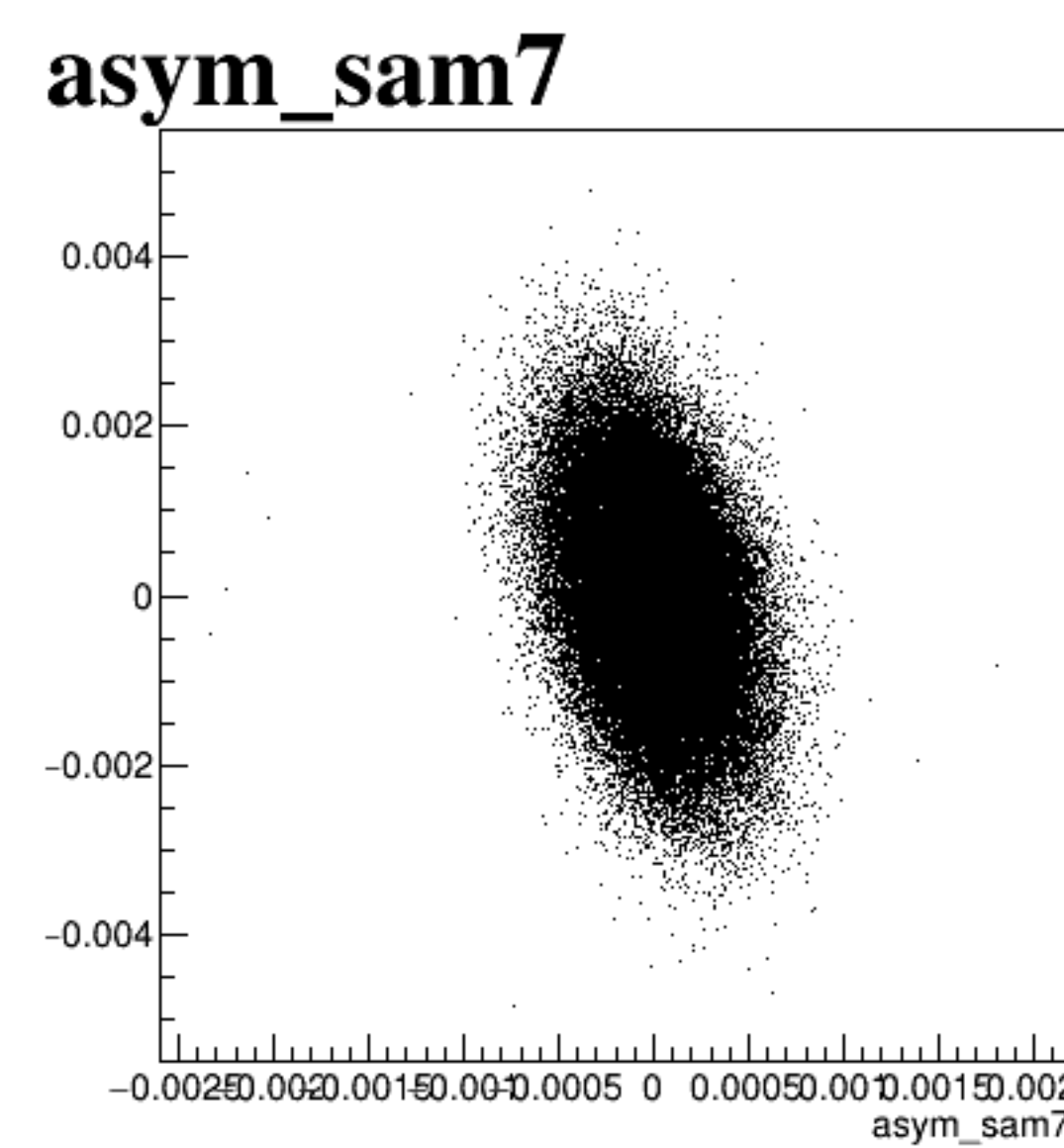
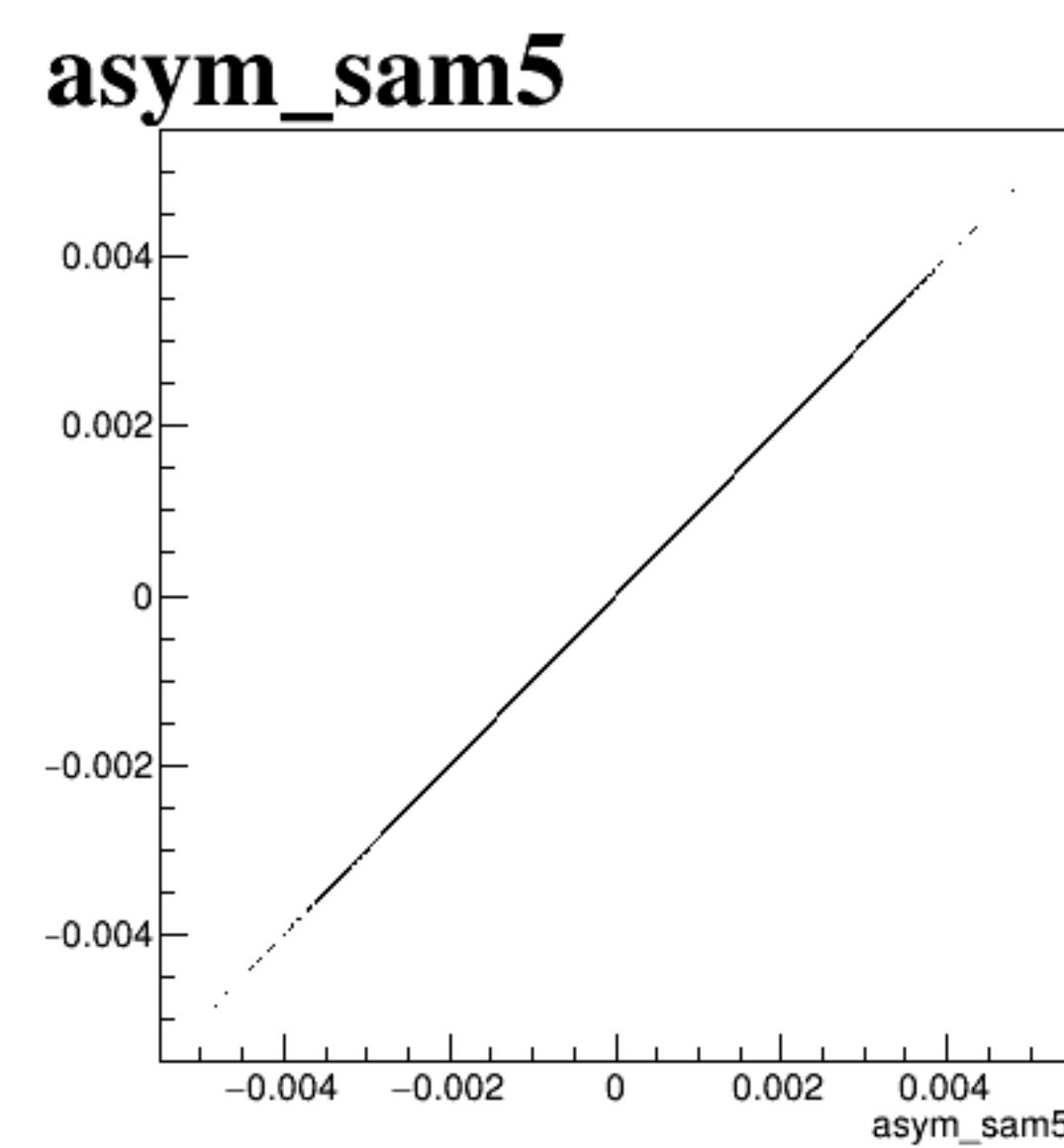
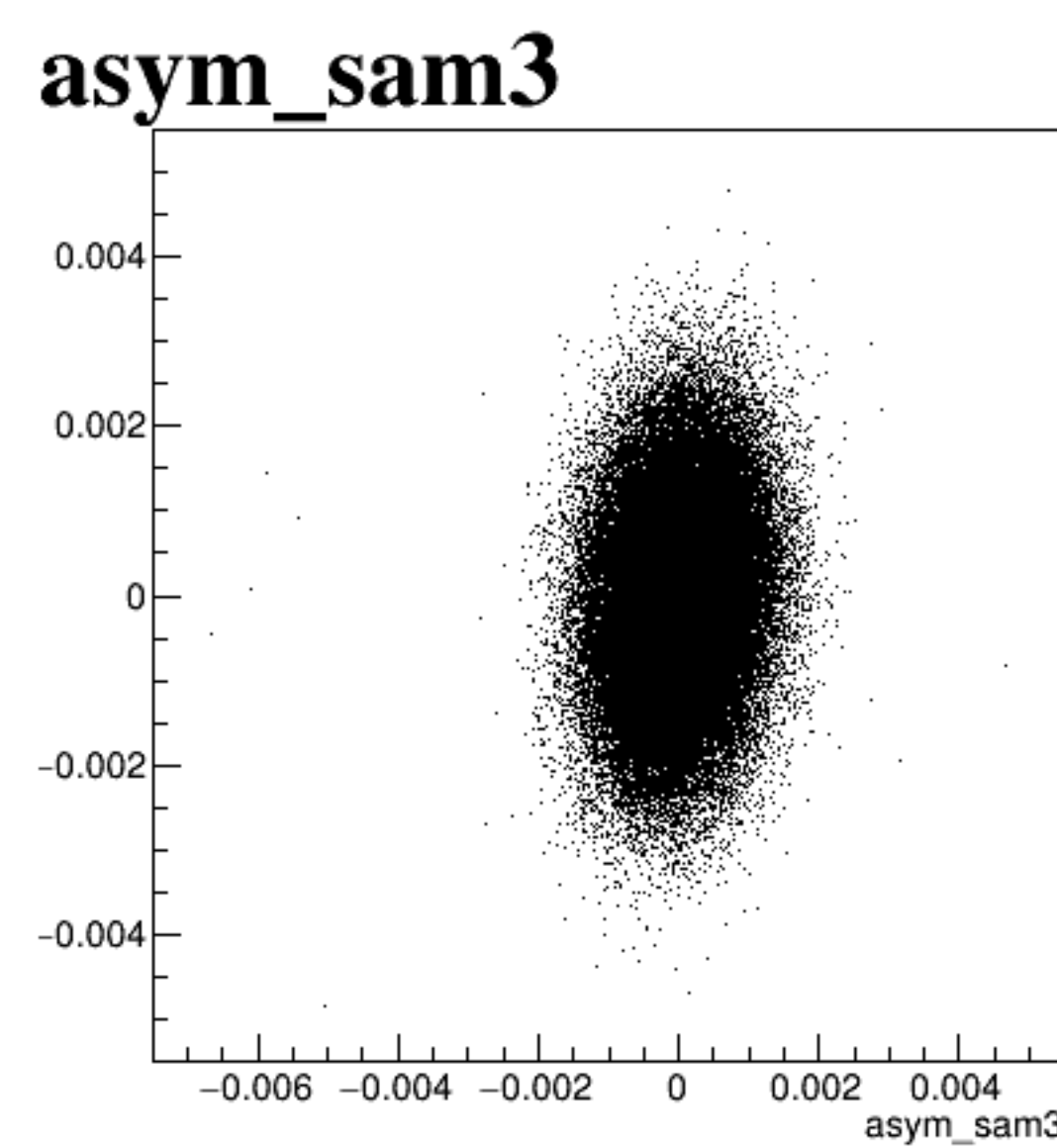
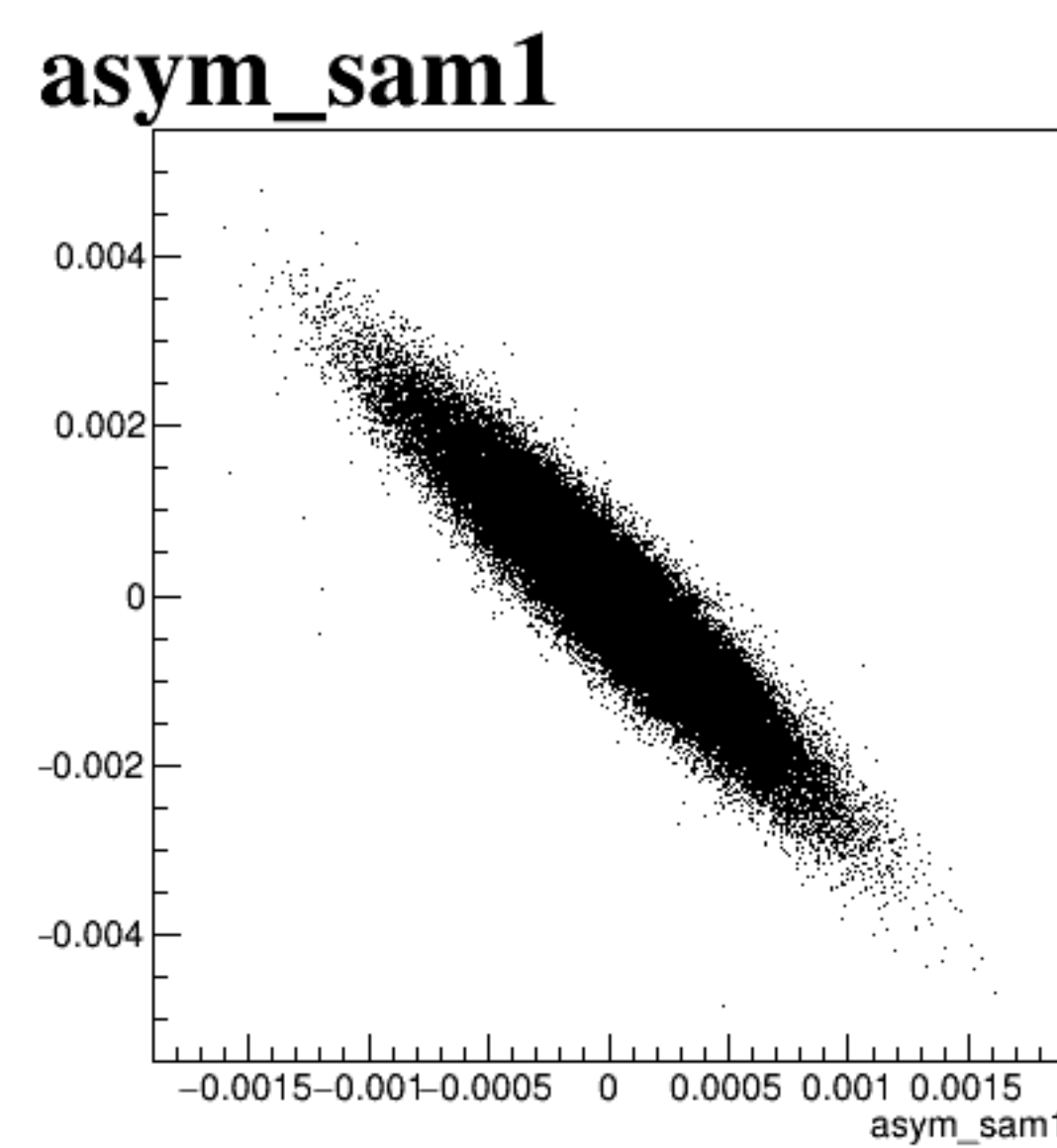


asym_sam7



asym_sam1

asym_sam3

**asym_sam5**

asym_sam7

