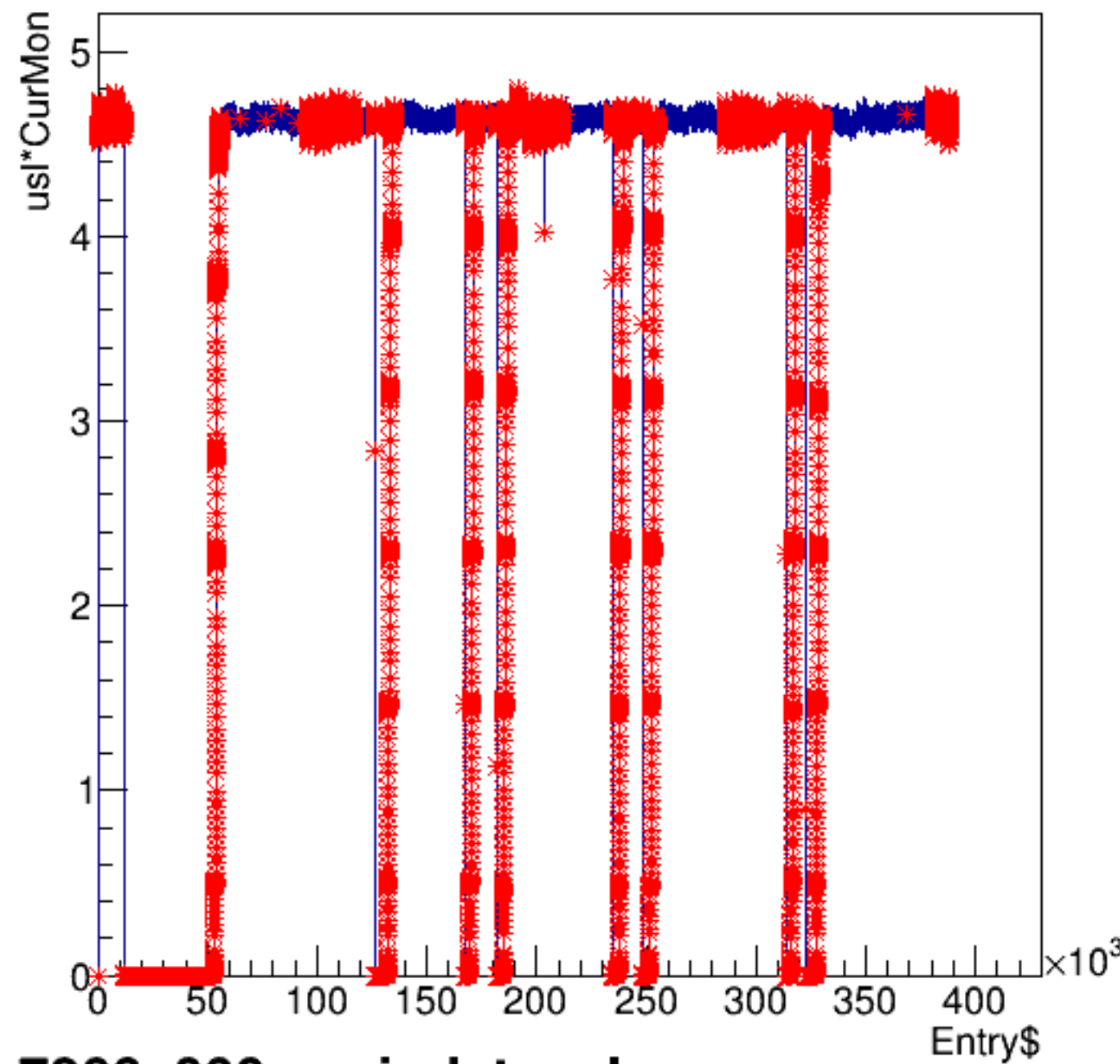
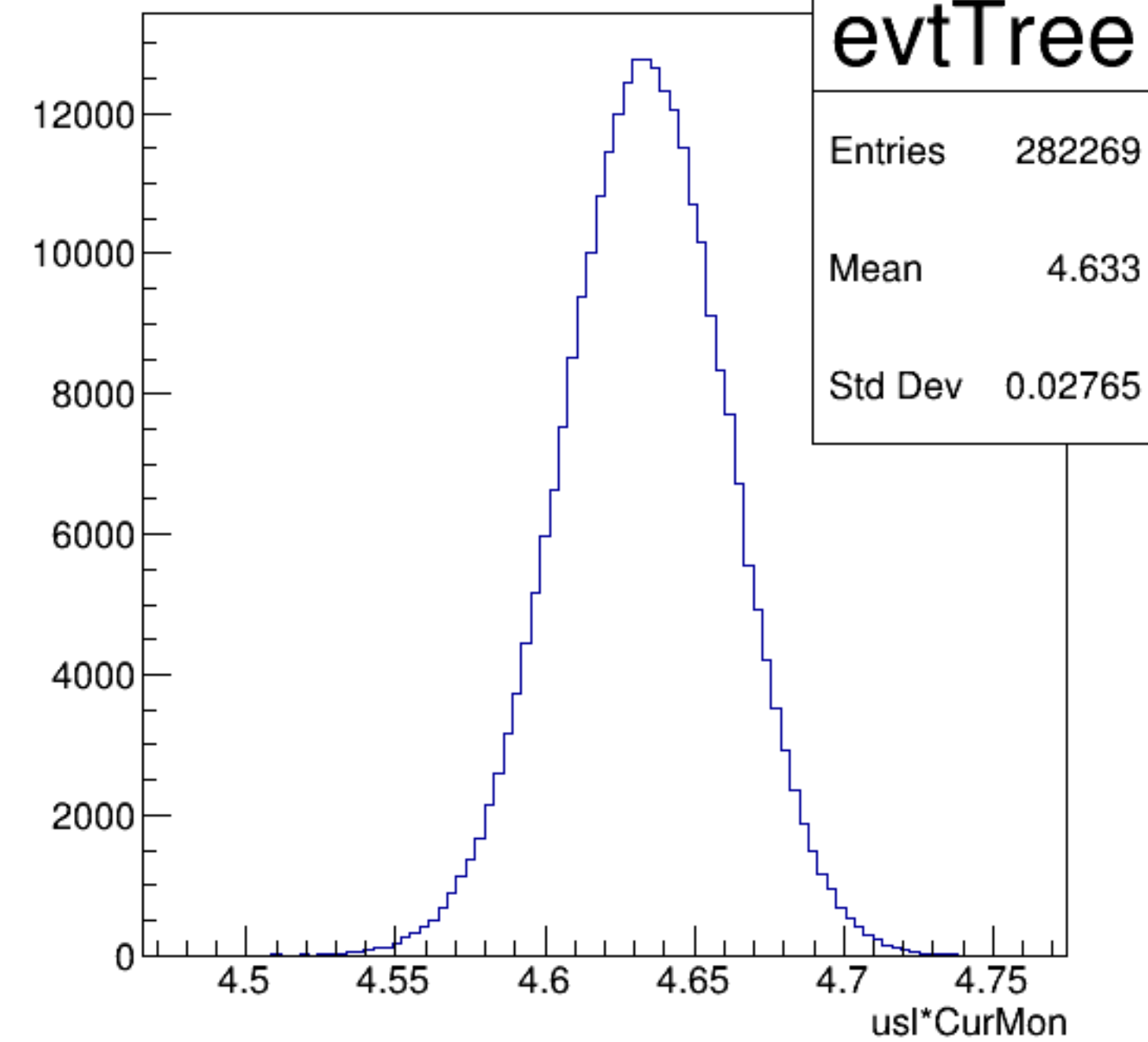


usl*CurMon:Entry\$



run7266_000_maindet_usl.png

usl*CurMon {ErrorFlag==0}



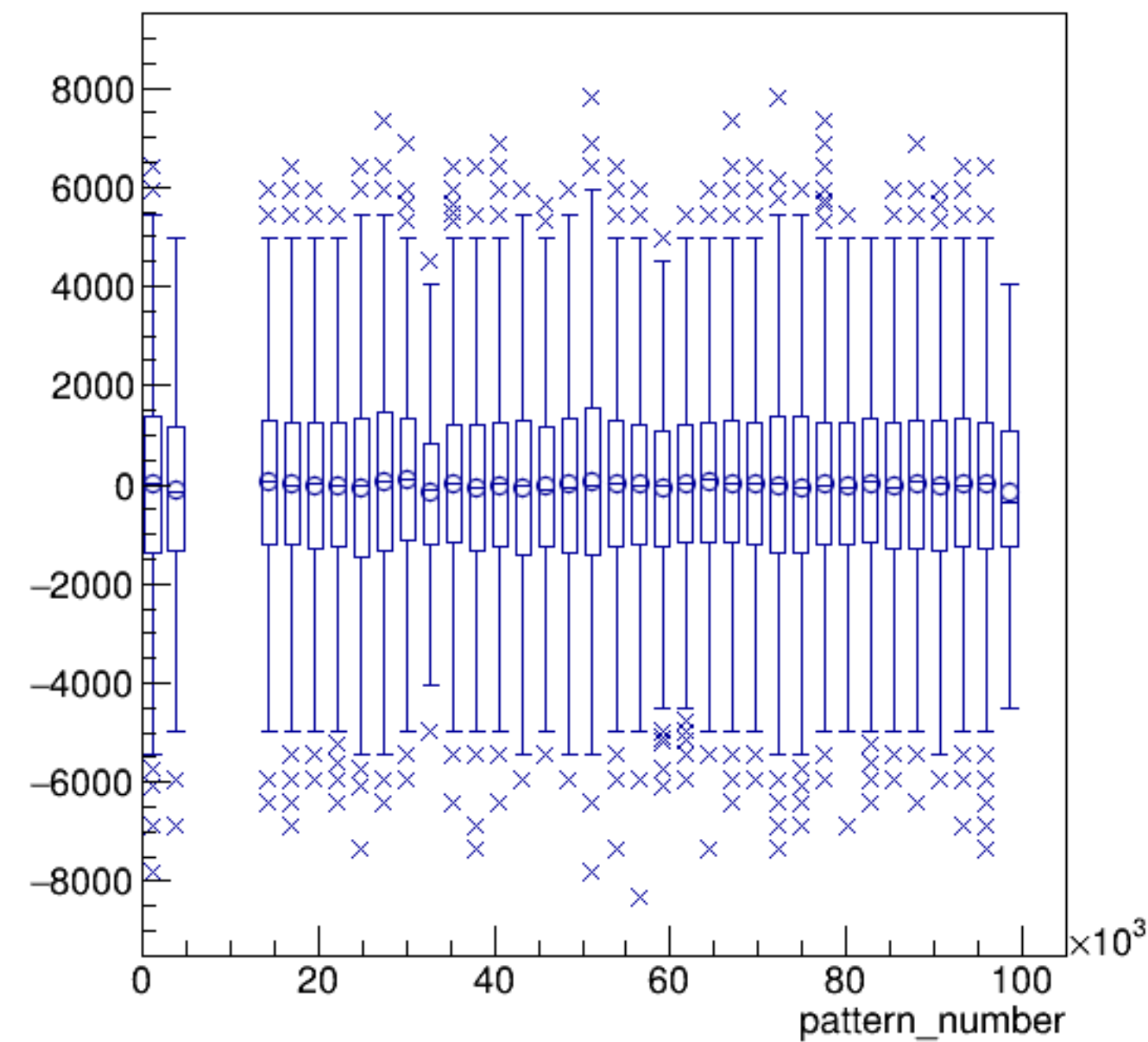
evtTree

Entries 282269

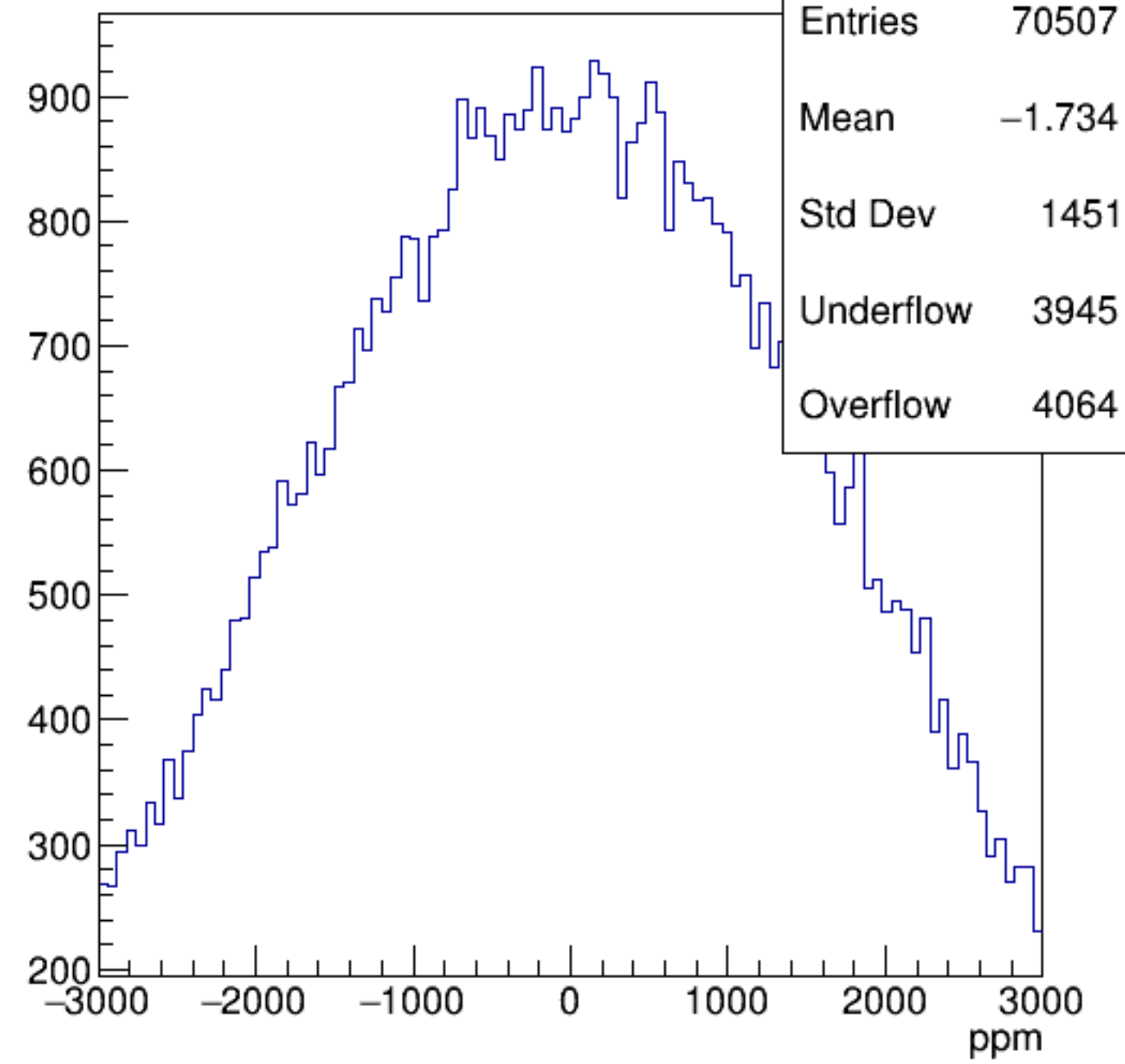
Mean 4.633

Std Dev 0.02765

asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl



Entries 70507

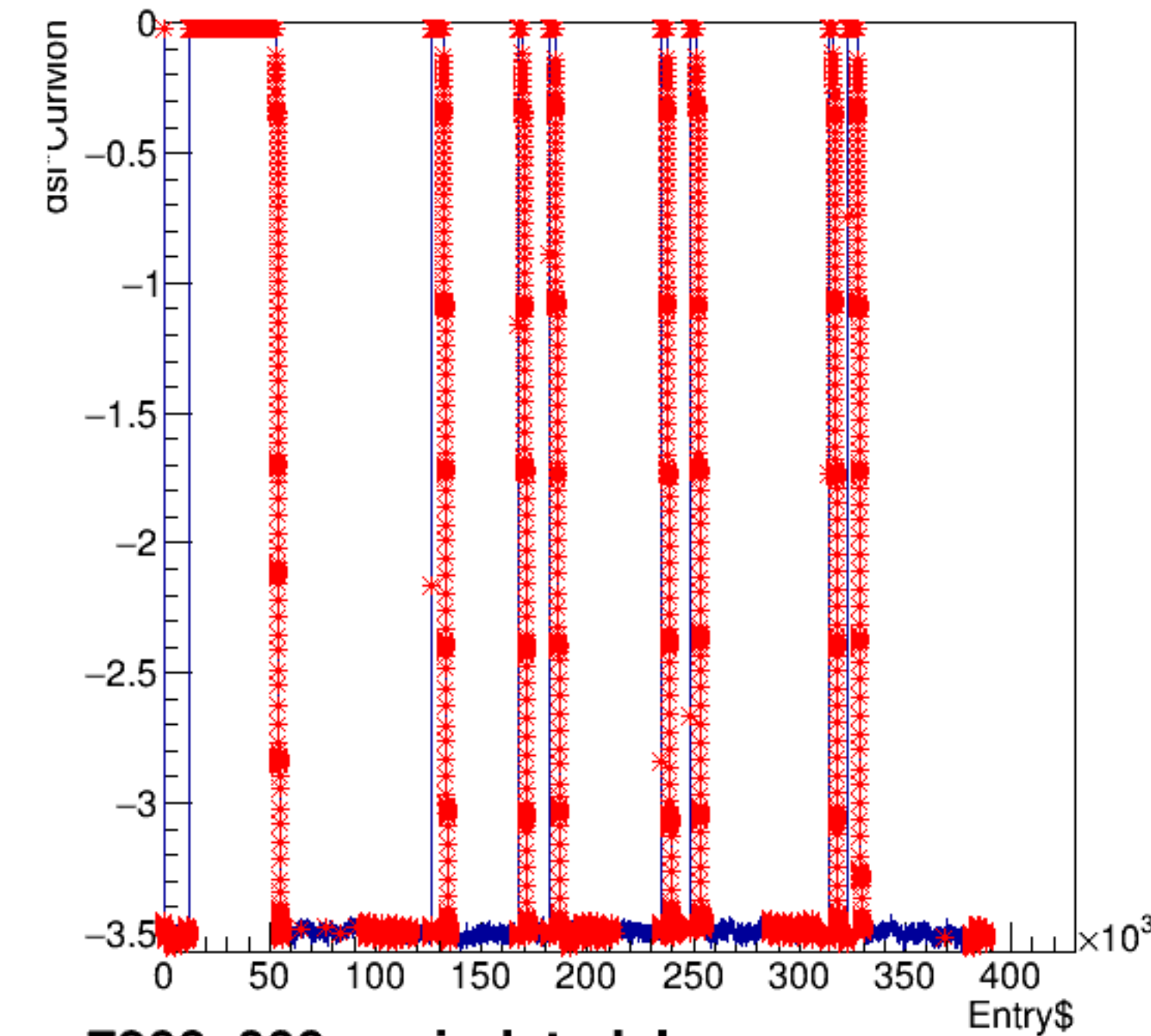
Mean -1.734

Std Dev 1451

Underflow 3945

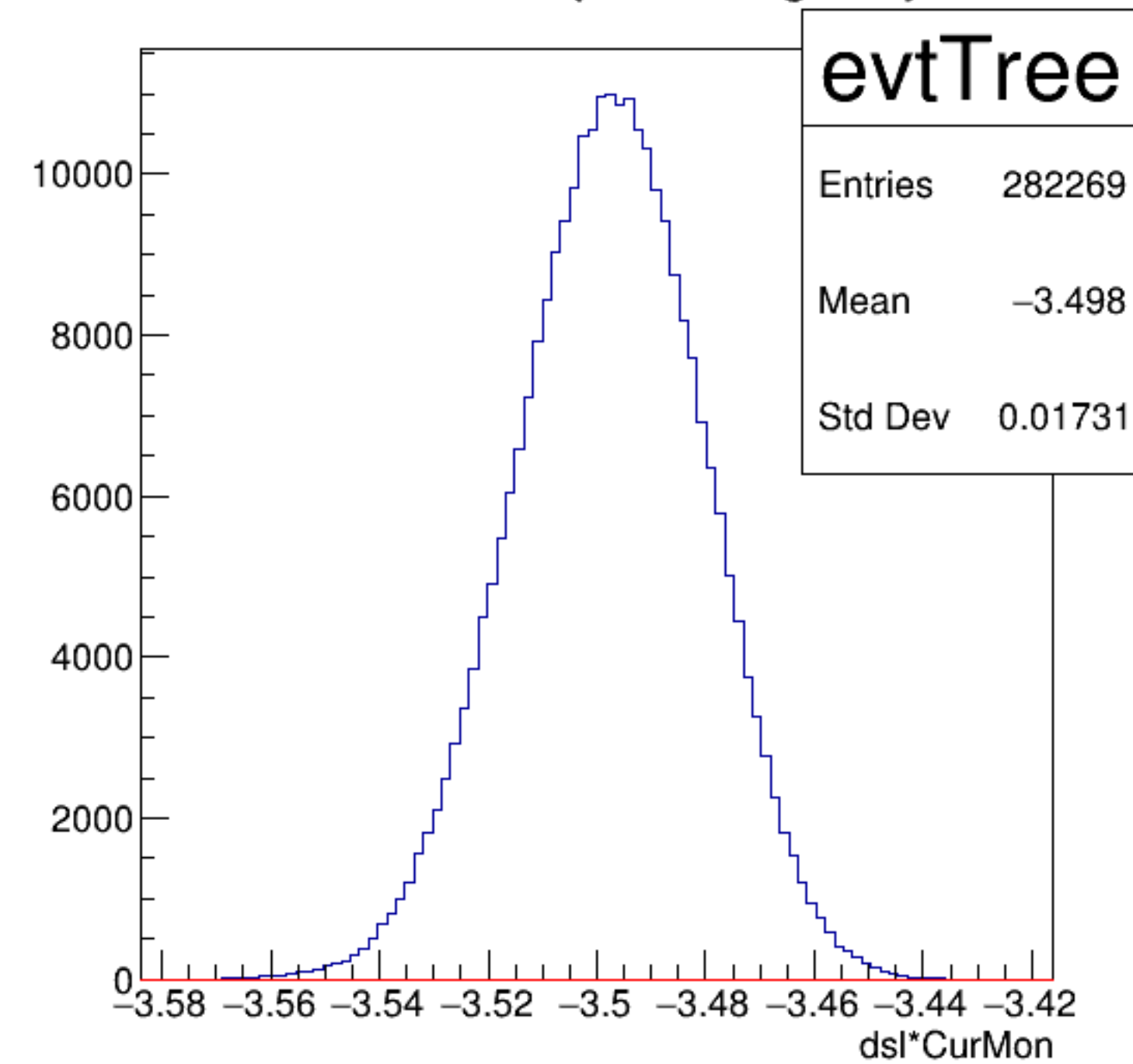
Overflow 4064

dsl*CurMon:Entry\$

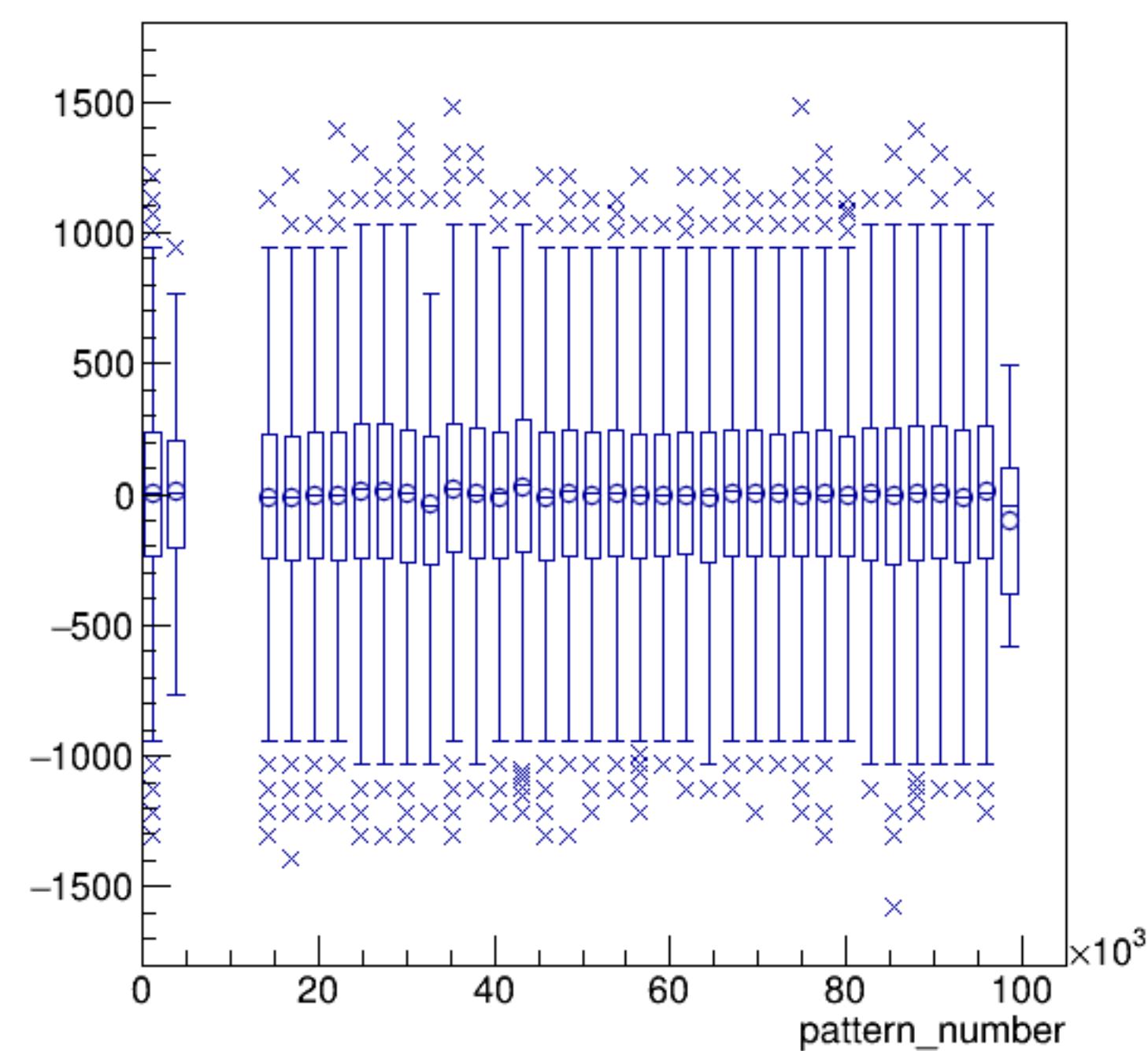


run7266_000_maindet_dsl.png

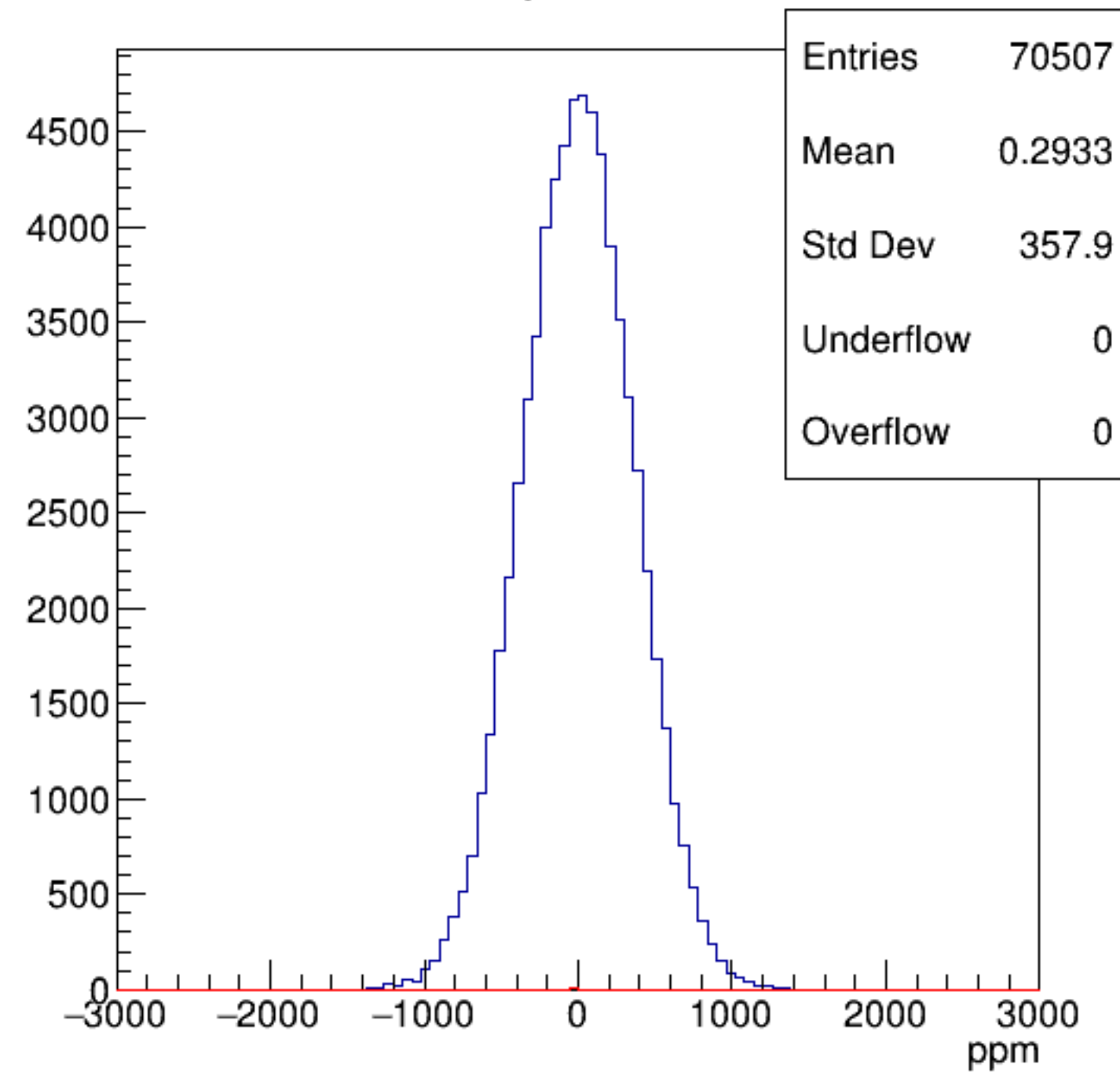
dsl*CurMon {ErrorFlag==0}



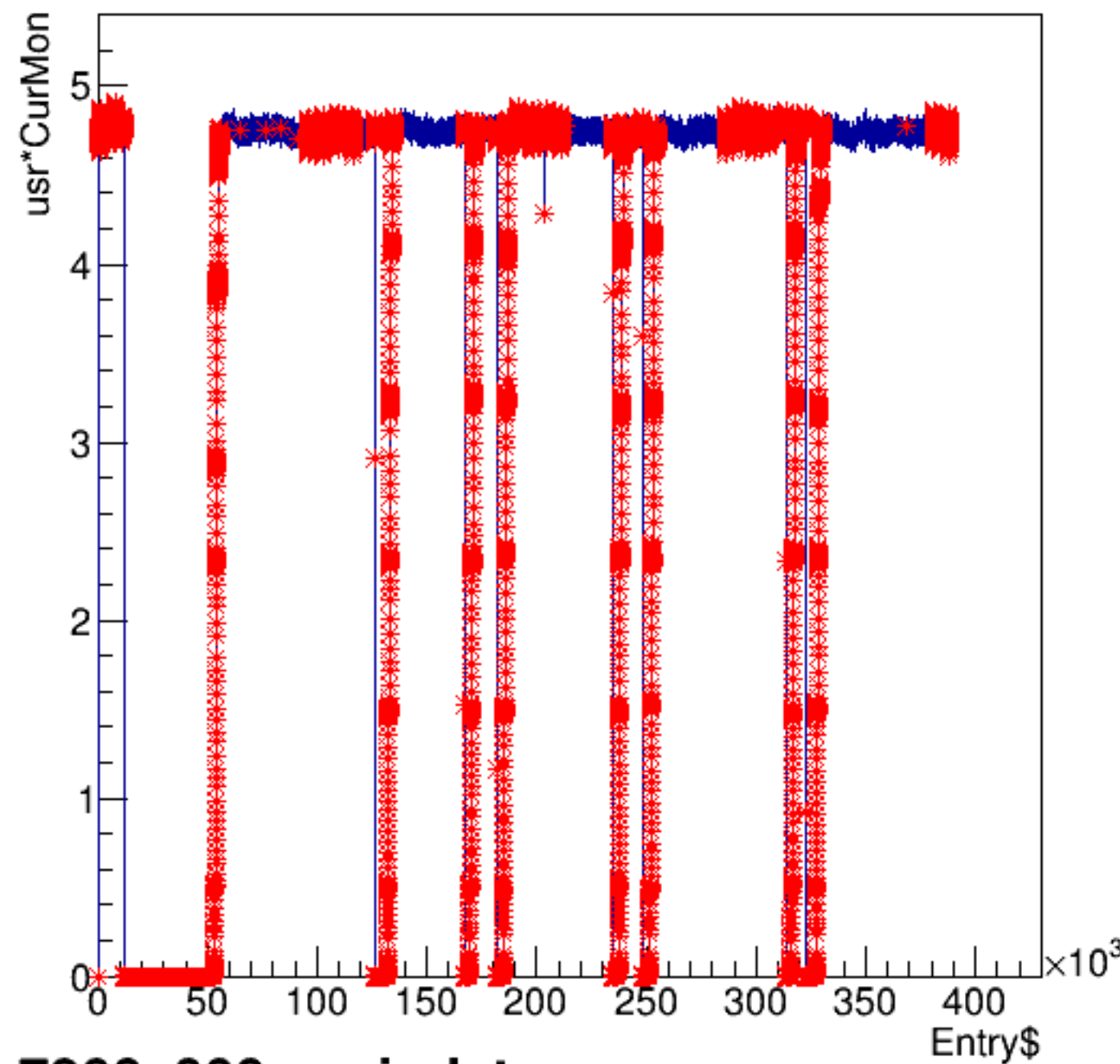
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl

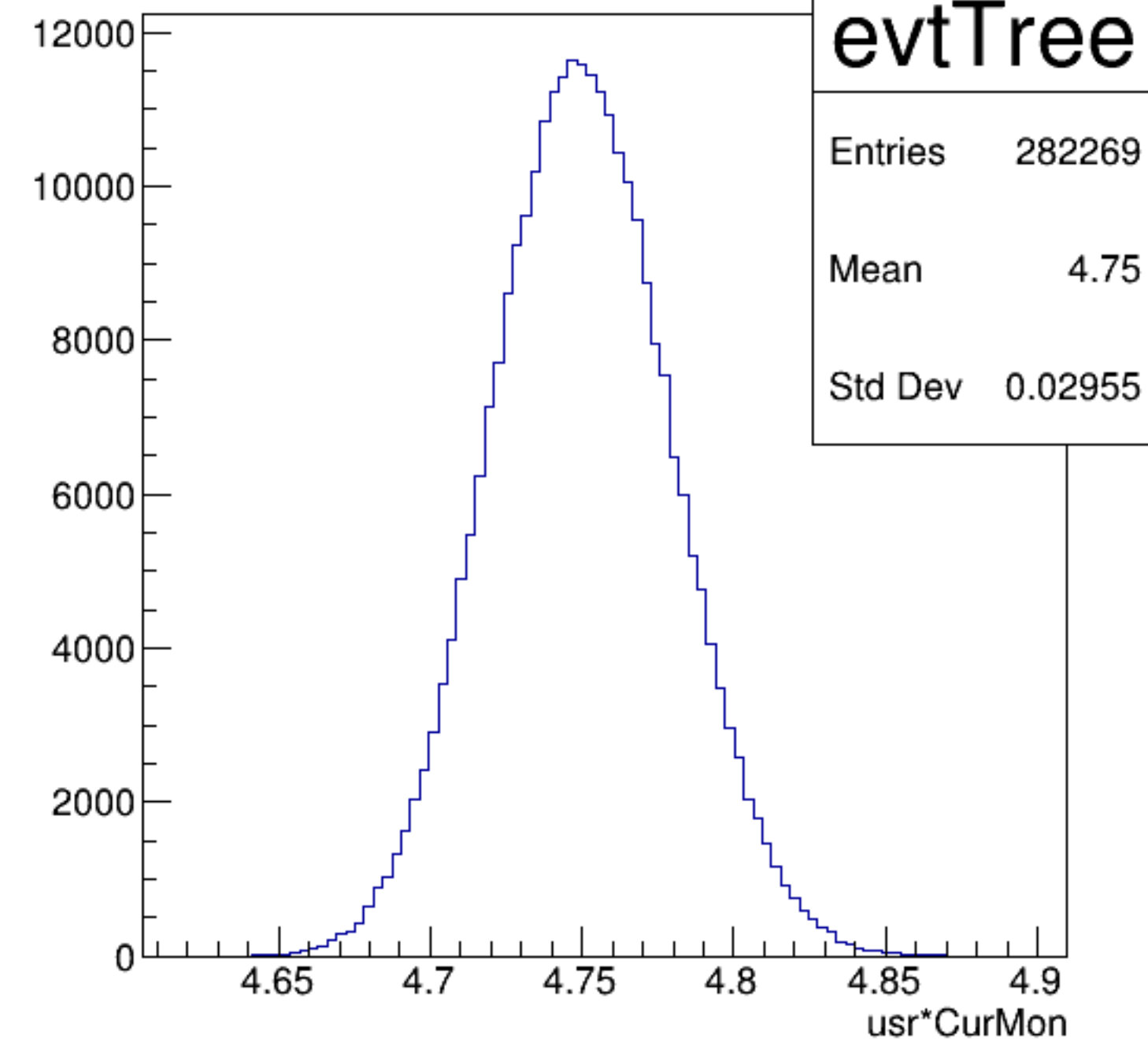


usr*CurMon:Entry\$



run7266_000_maindet_usr.png

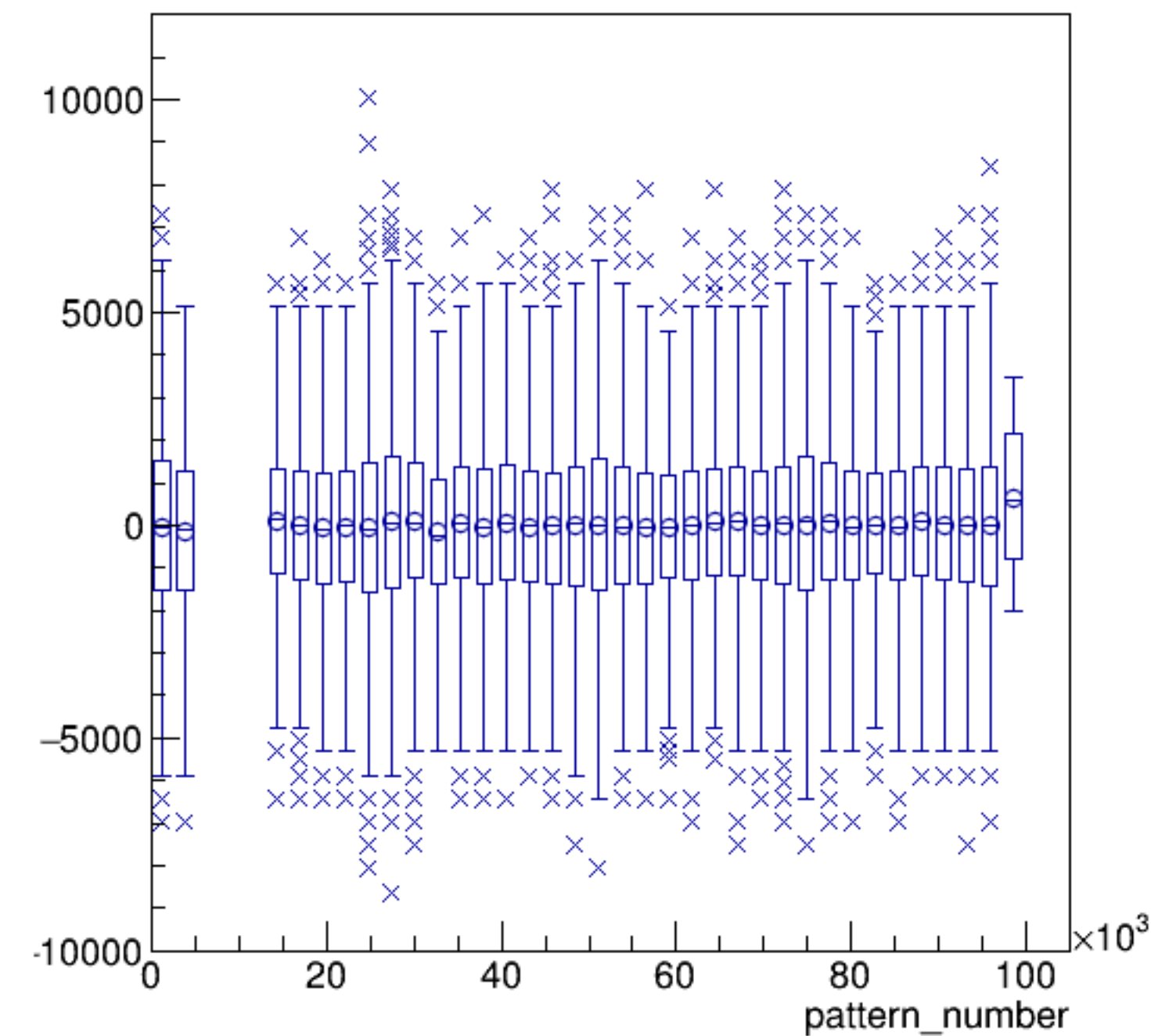
usr*CurMon {ErrorFlag==0}



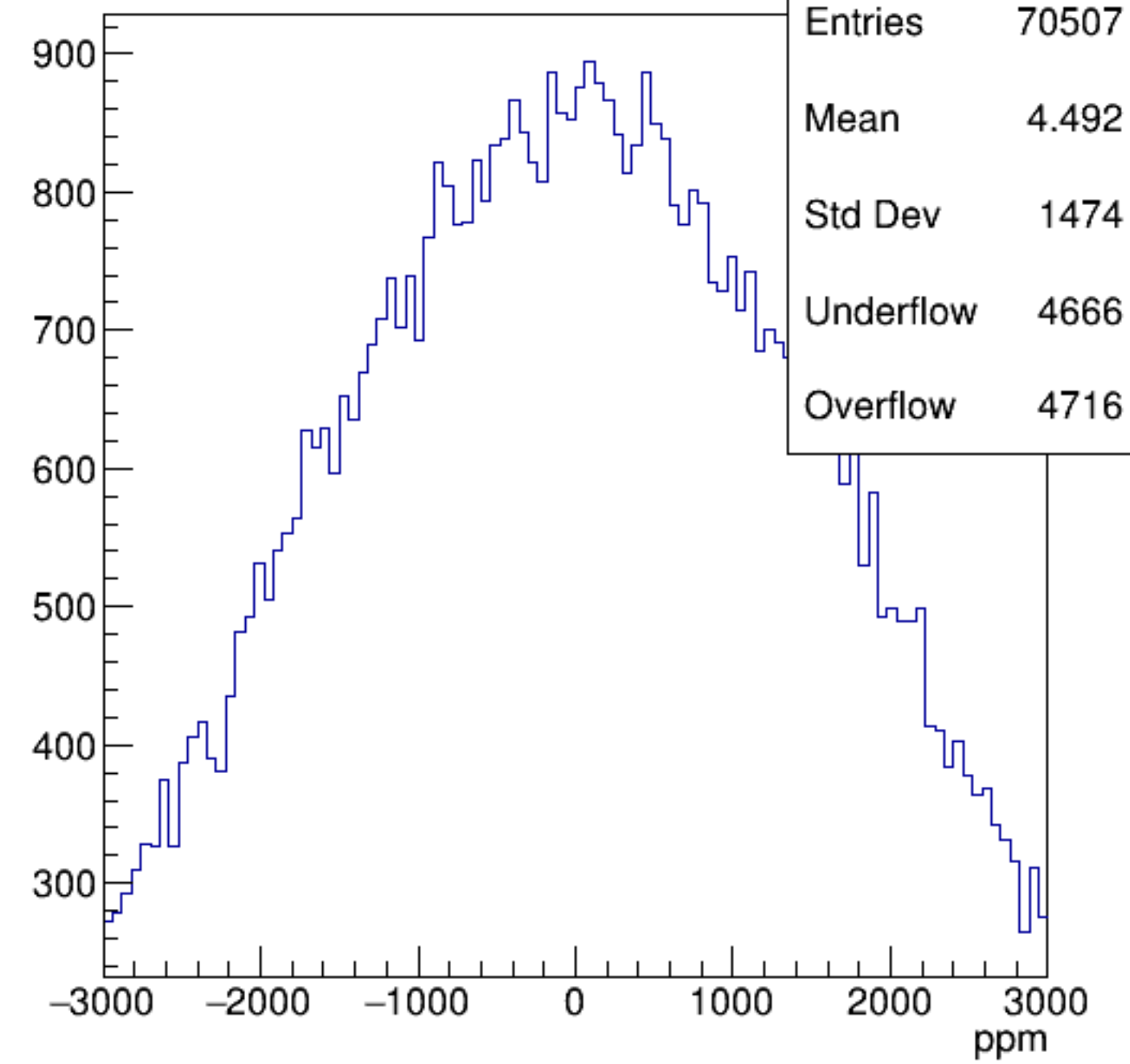
evtTree

Entries	282269
Mean	4.75
Std Dev	0.02955

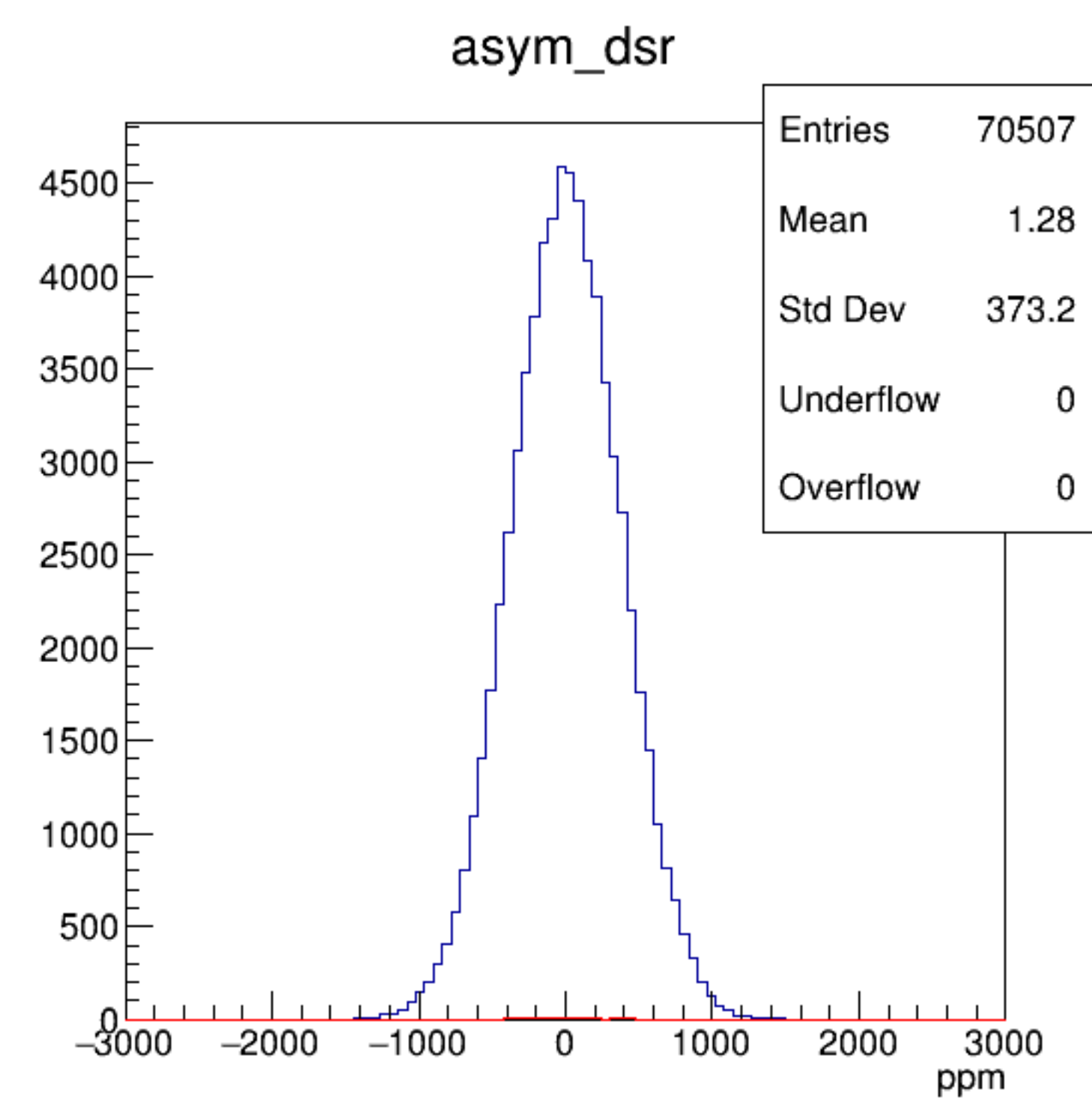
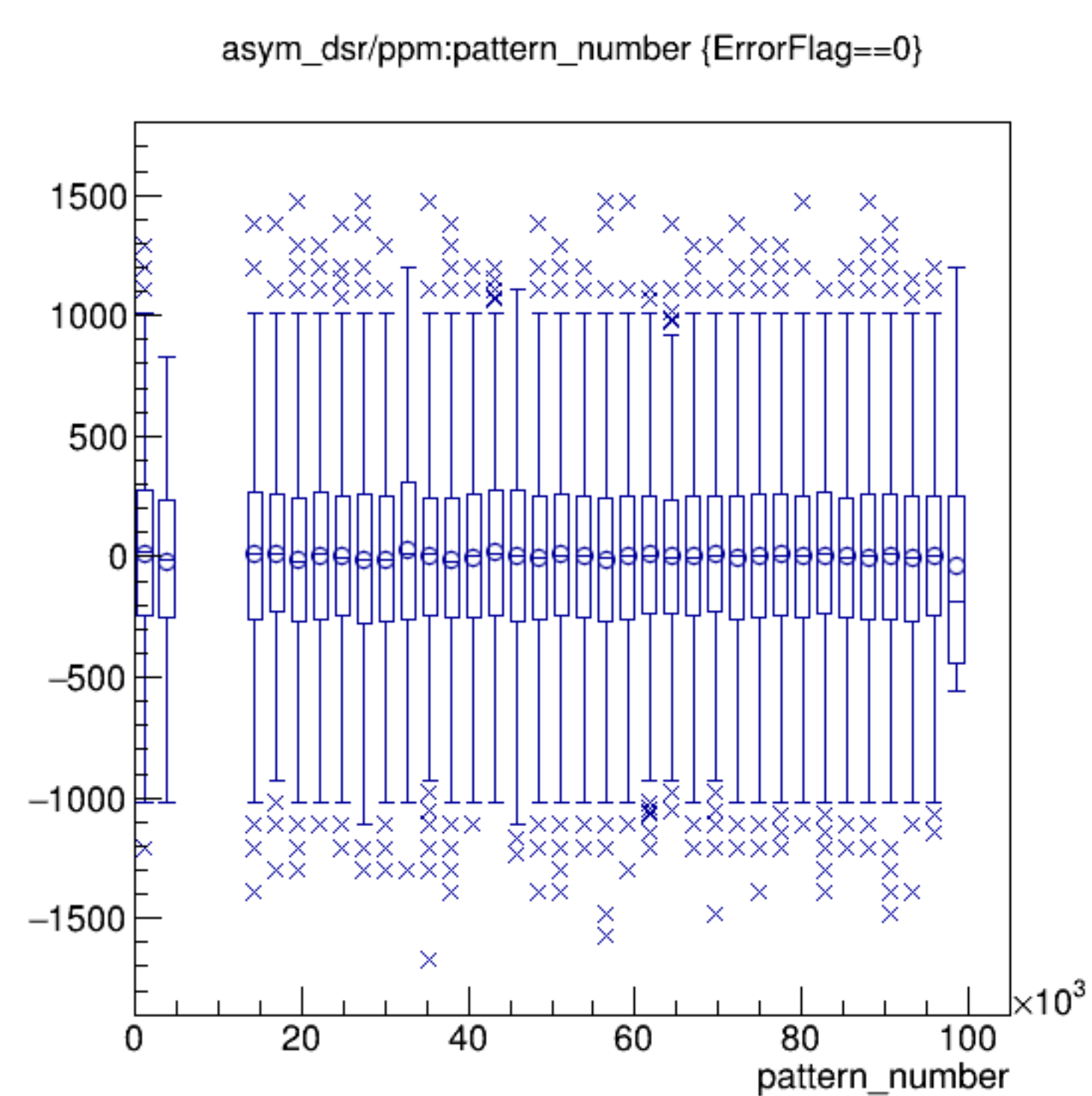
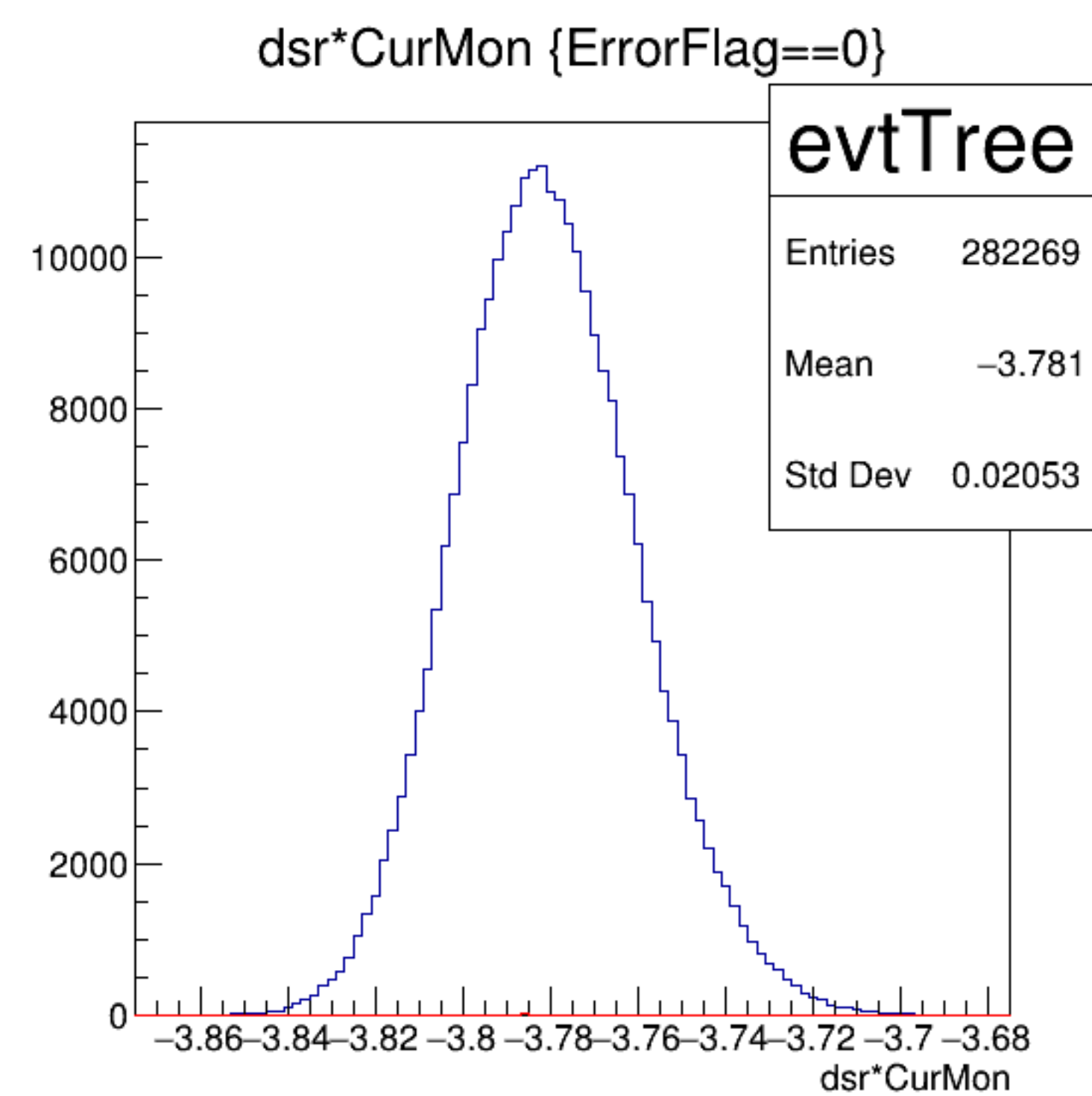
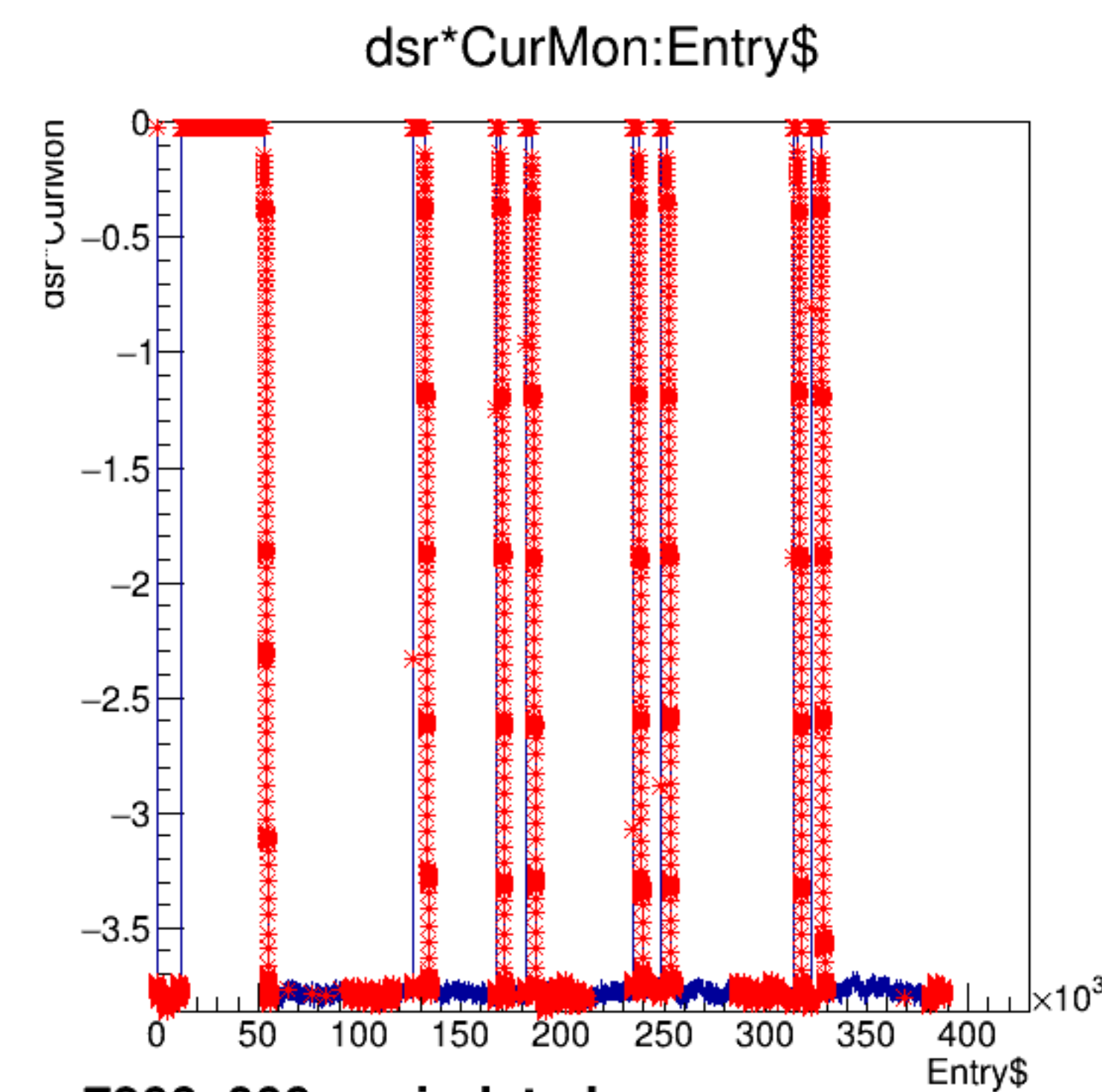
asym_usr/ppm:pattern_number {ErrorFlag==0}



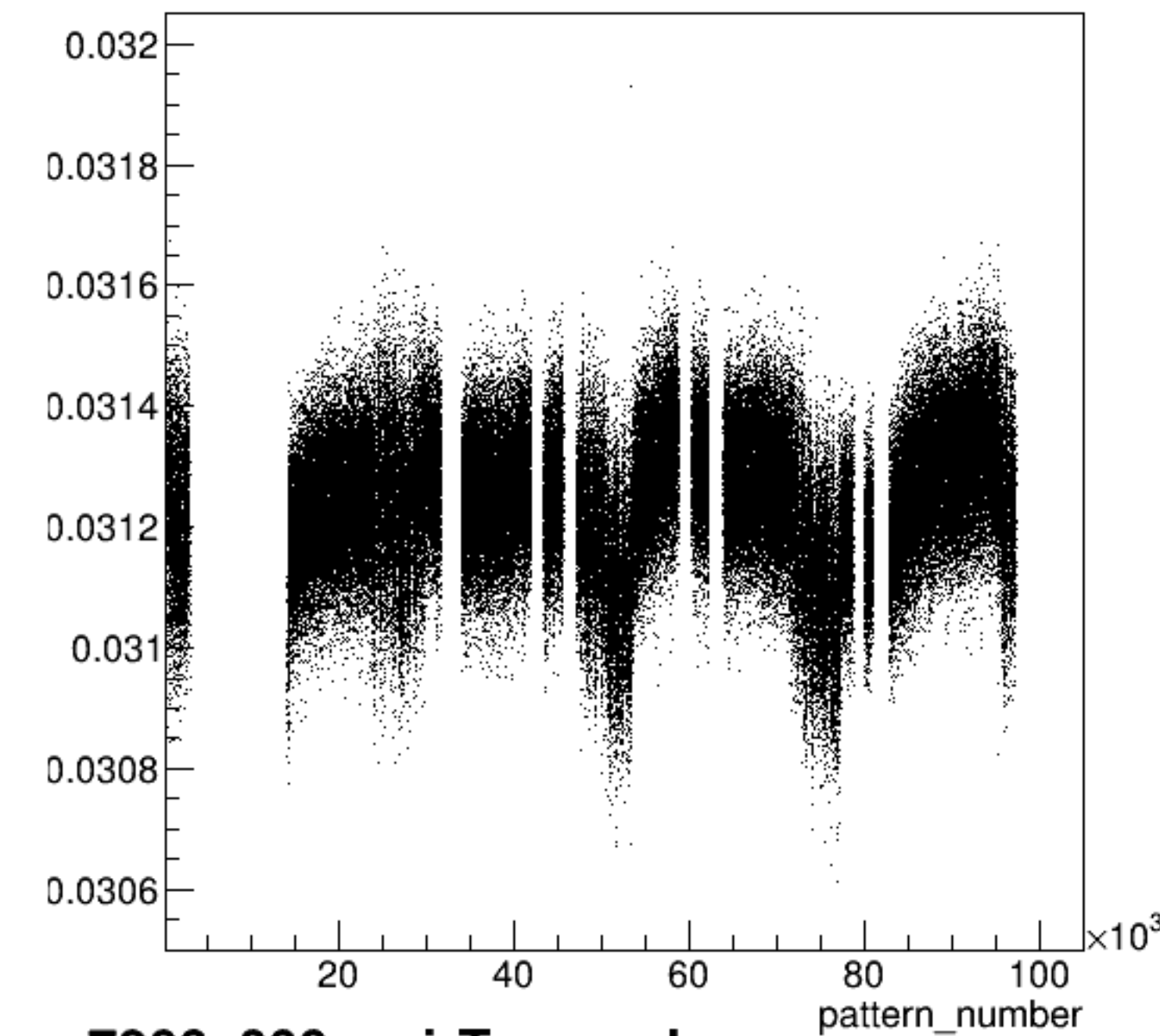
asym_usr



Entries	70507
Mean	4.492
Std Dev	1474
Underflow	4666
Overflow	4716

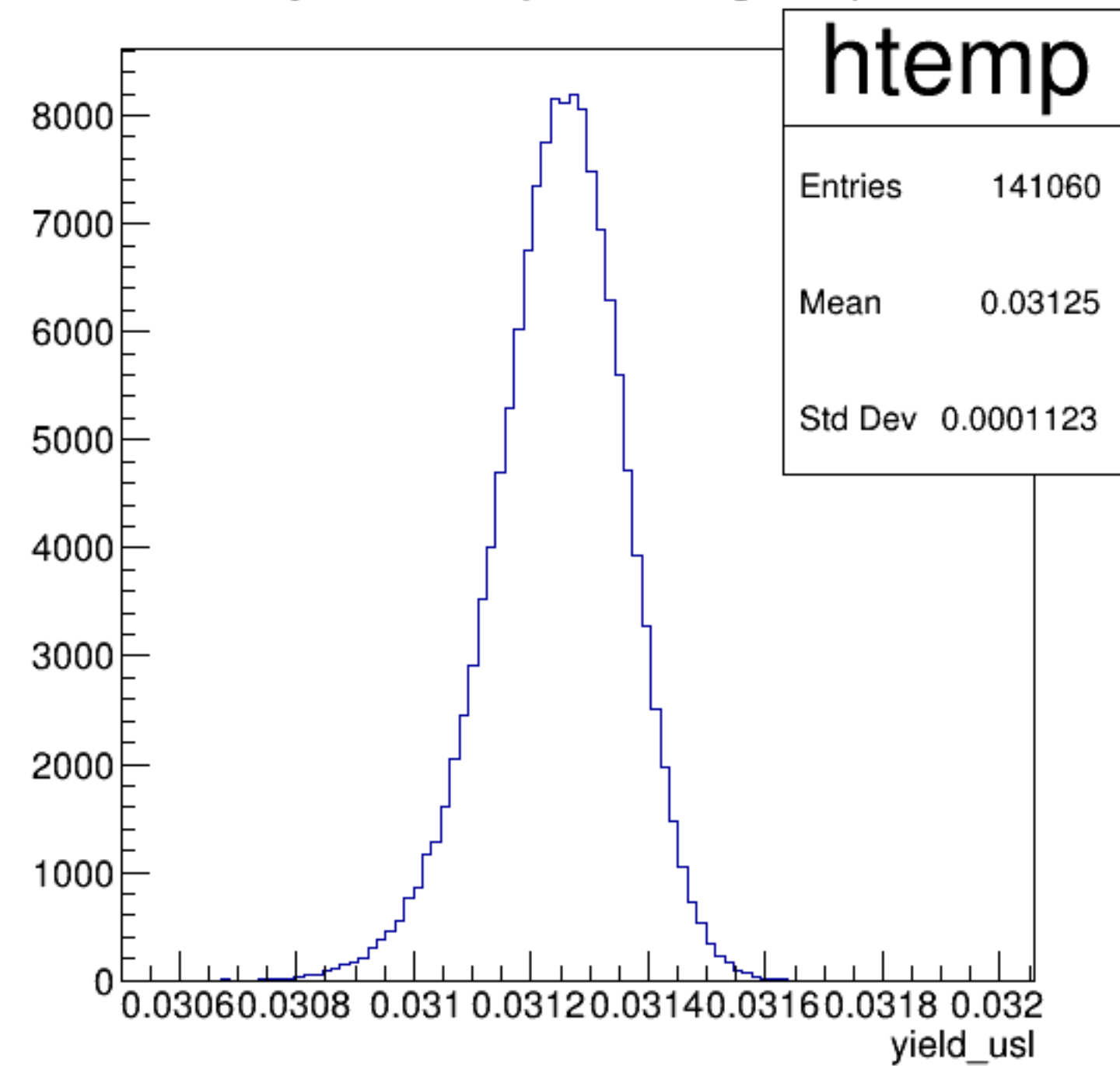


yield_usl:pattern_number {ErrorFlag==0}

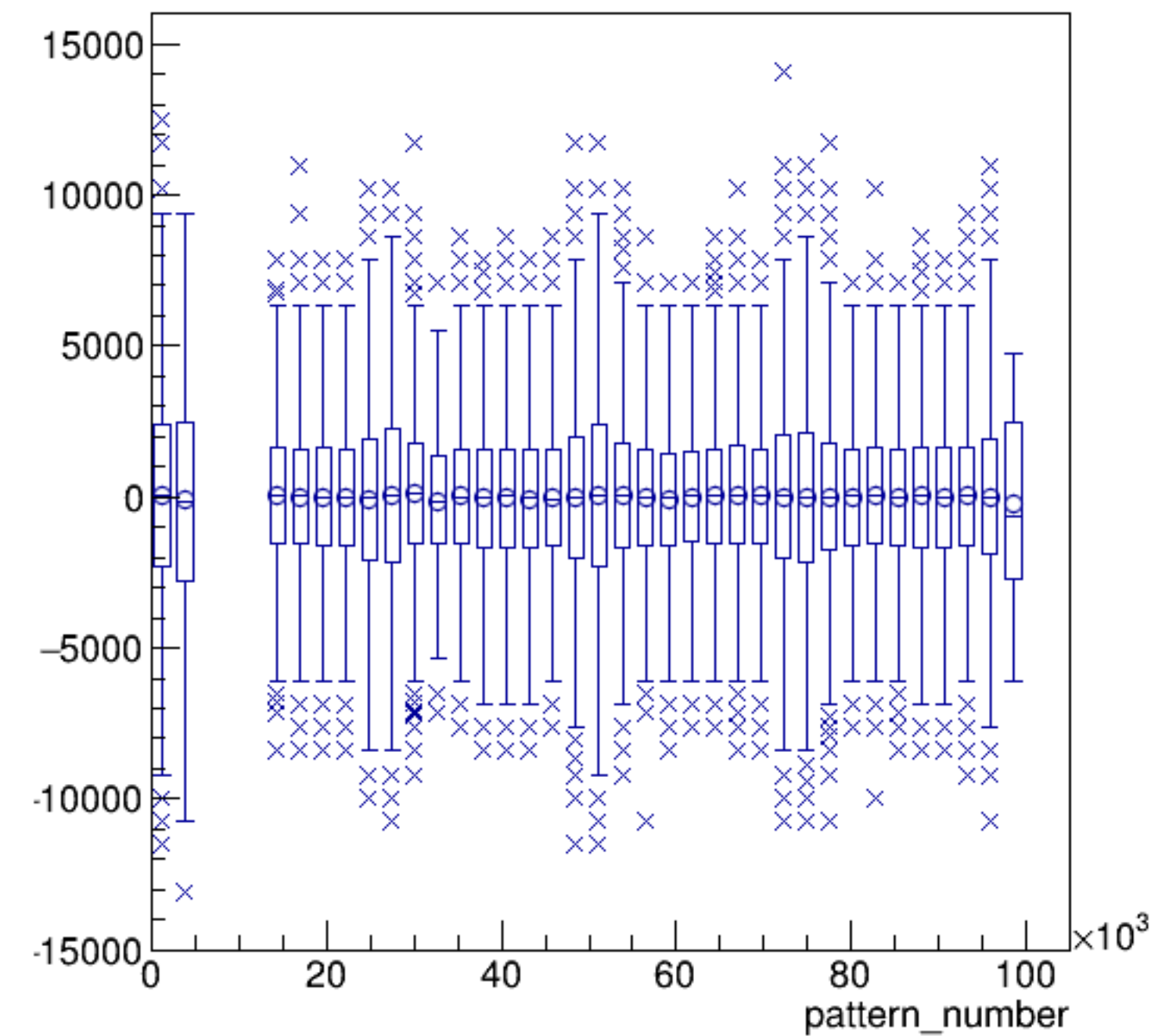


run7266_000_pairTree_usl.png

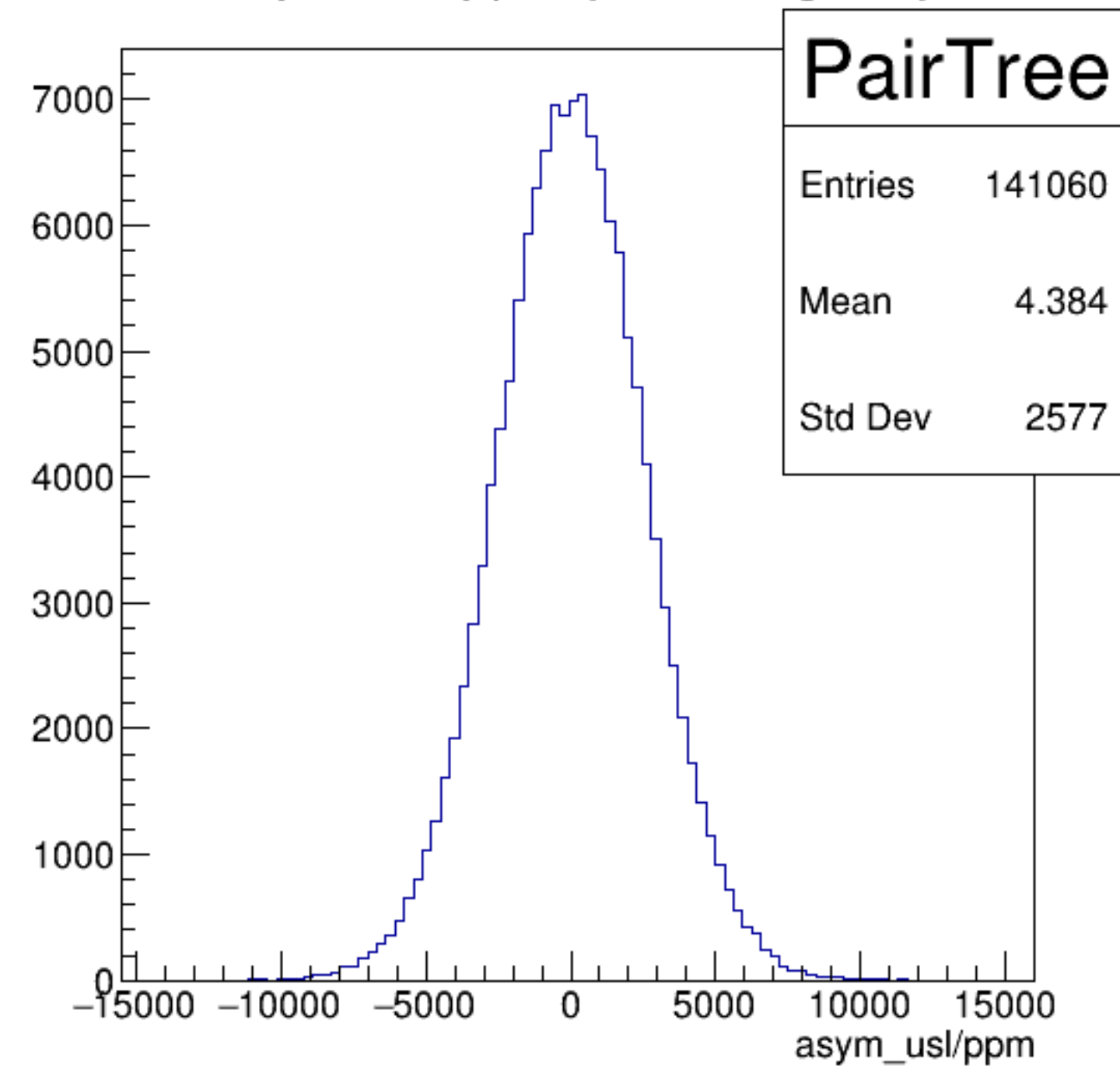
yield_usl {ErrorFlag==0}



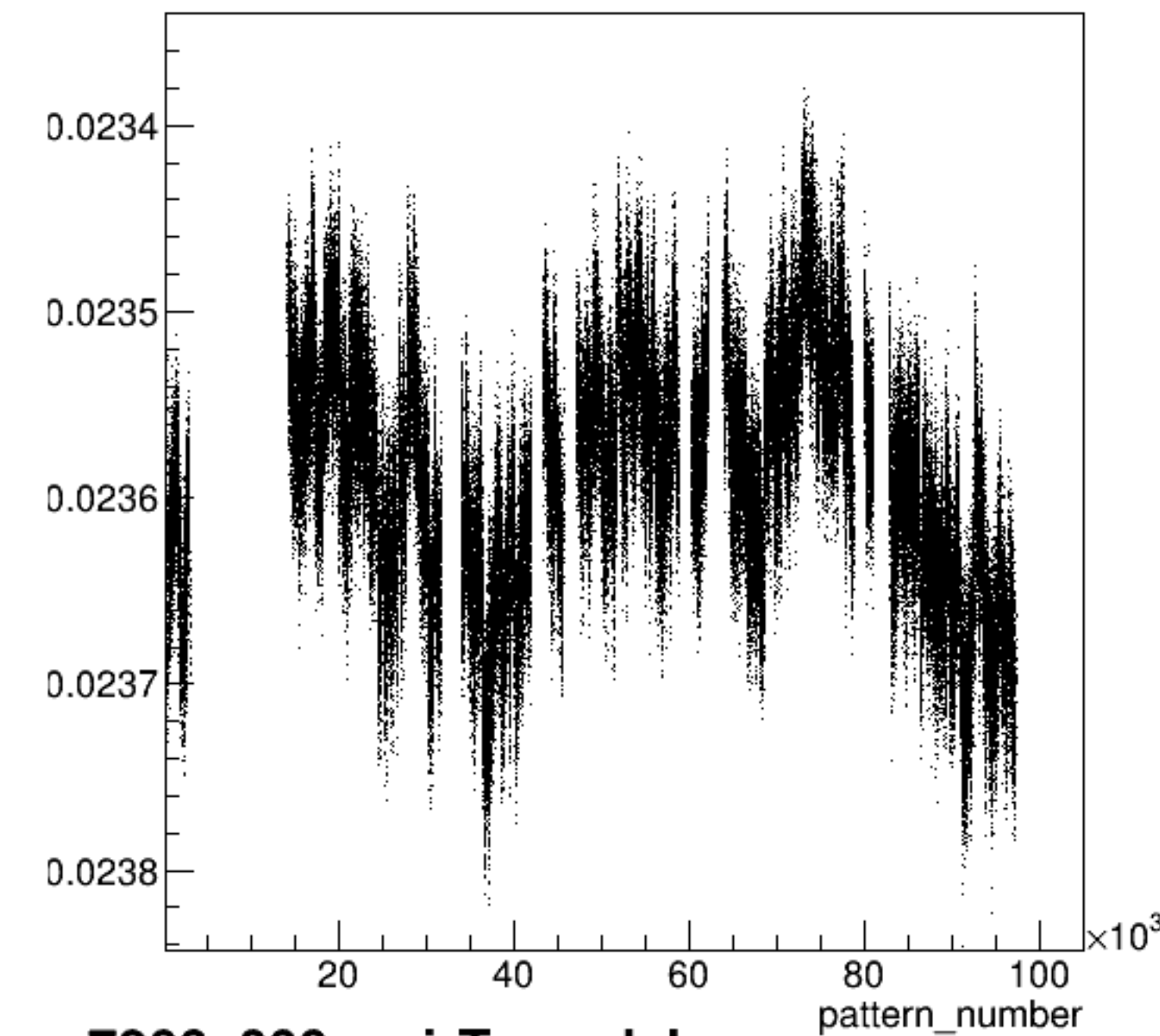
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl/ppm {ErrorFlag==0}

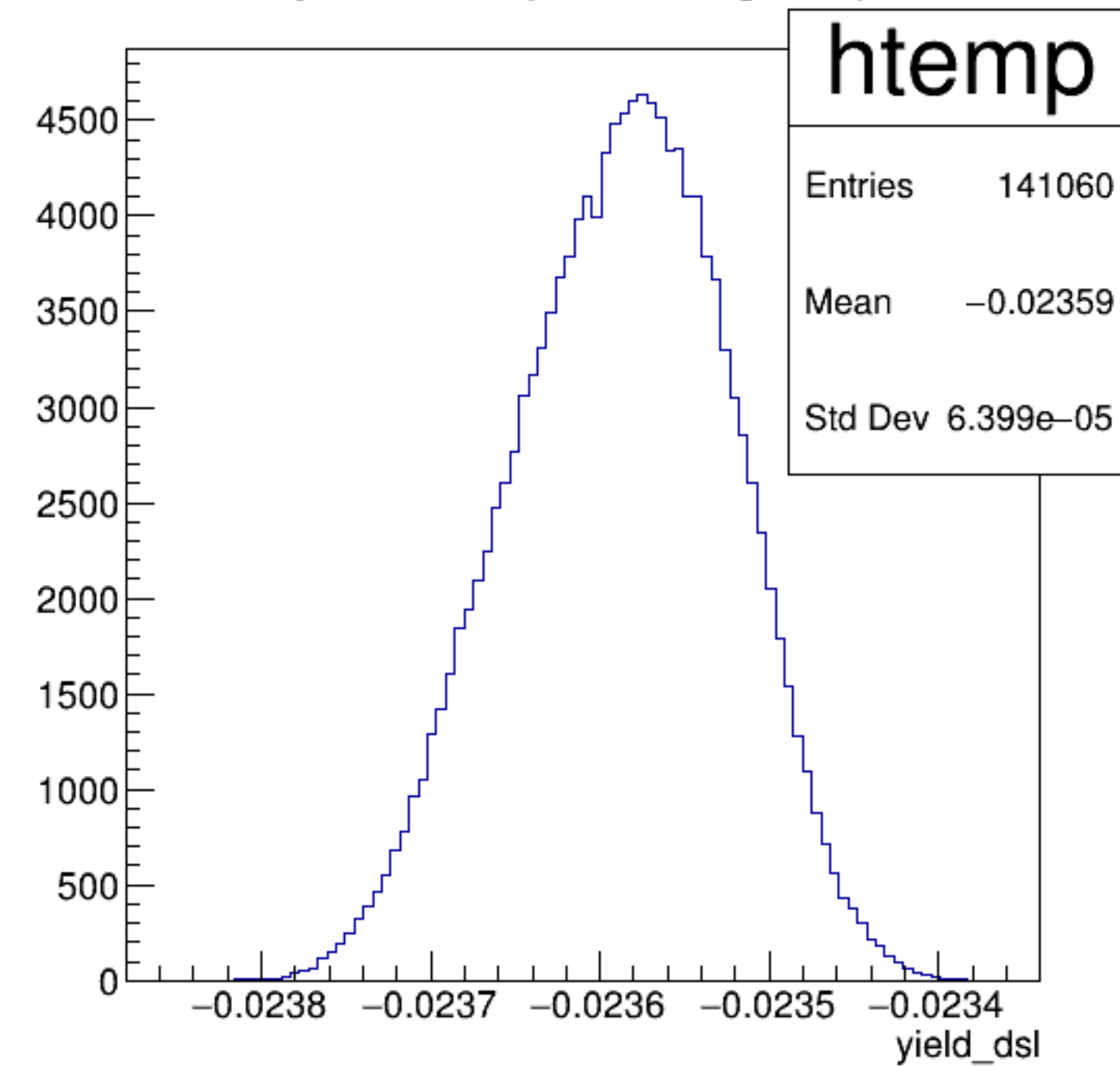


yield_dsl:pattern_number {ErrorFlag==0}

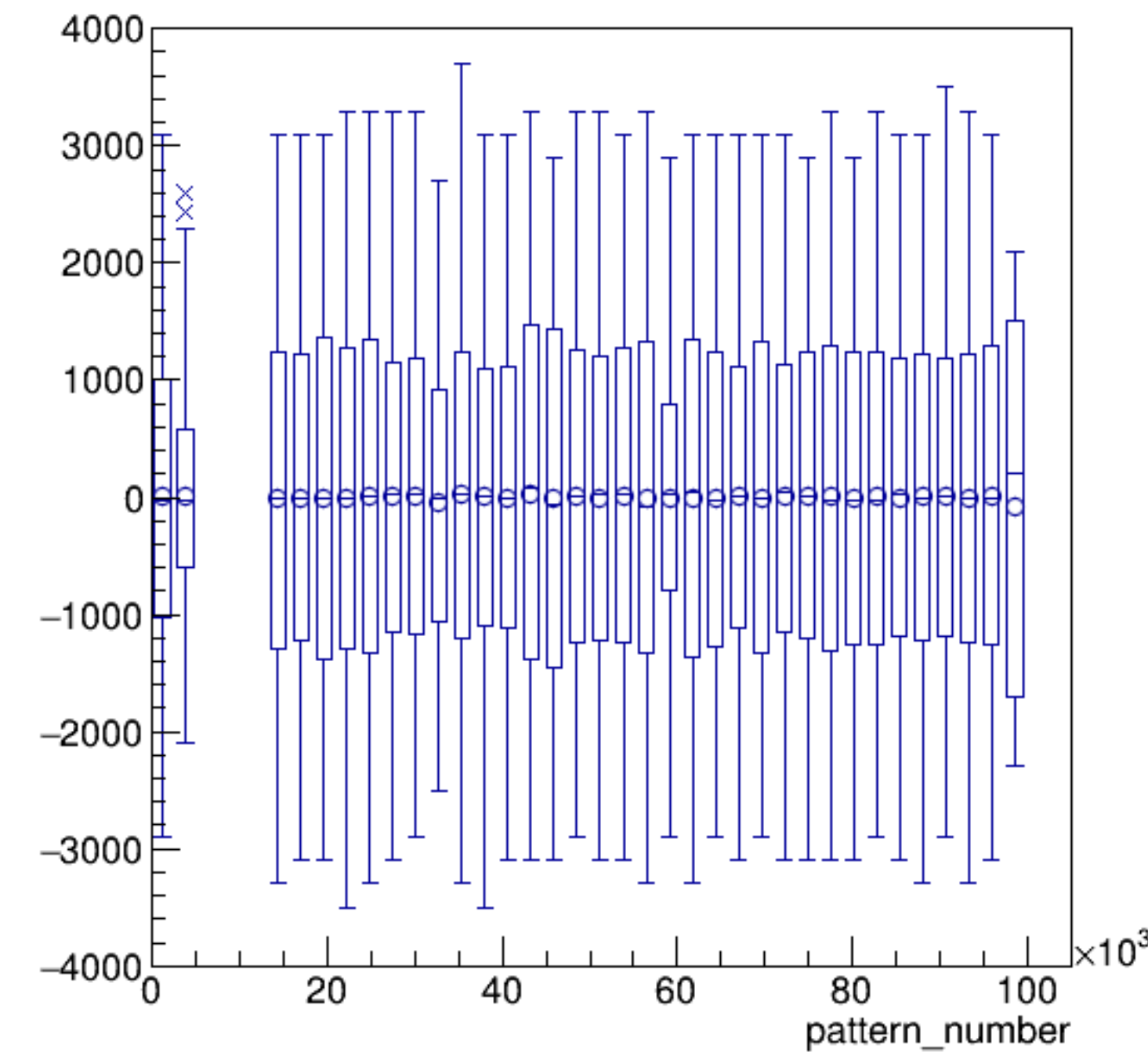


run7266_000_pairTree_dsl.png

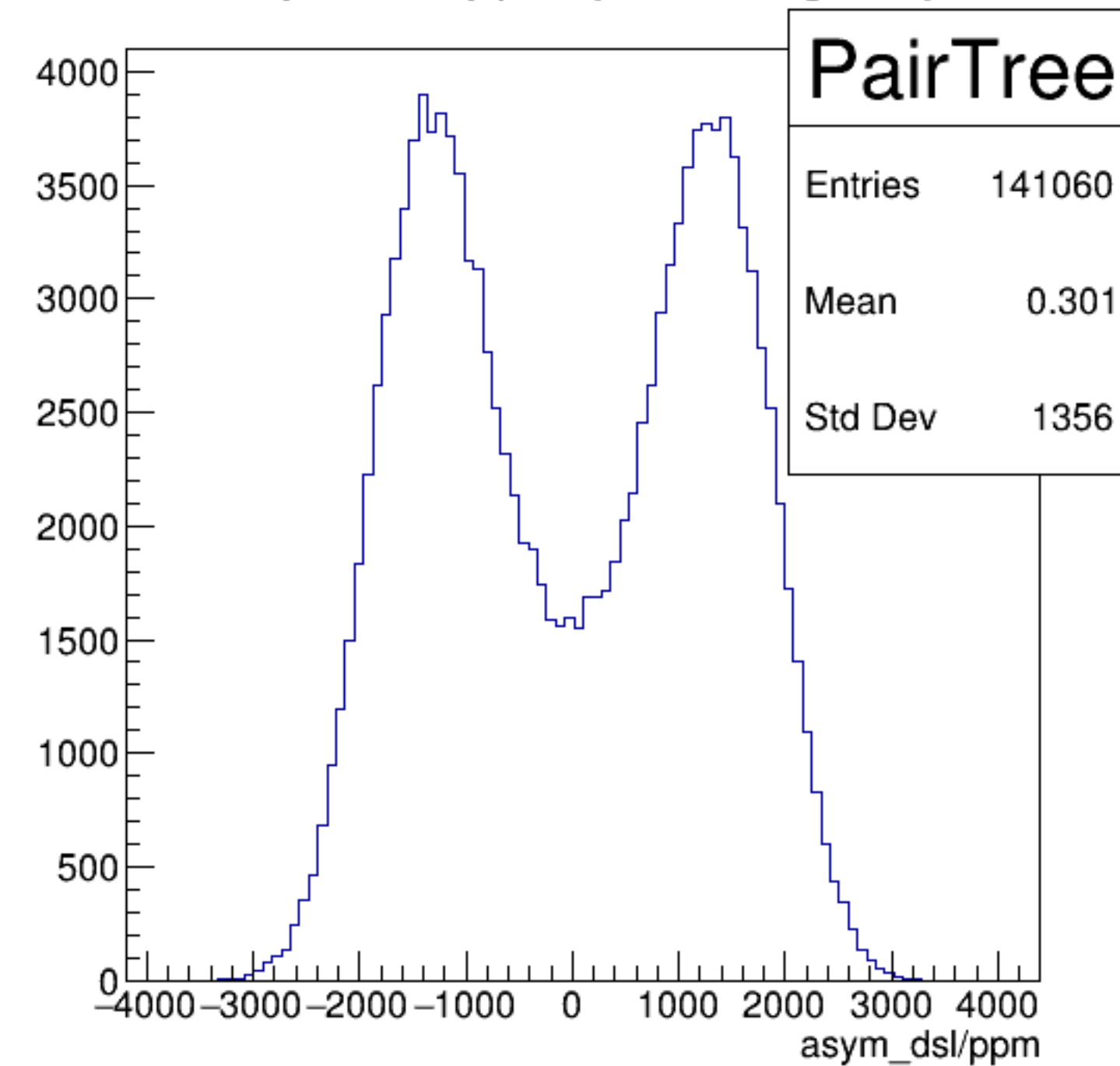
yield_dsl {ErrorFlag==0}



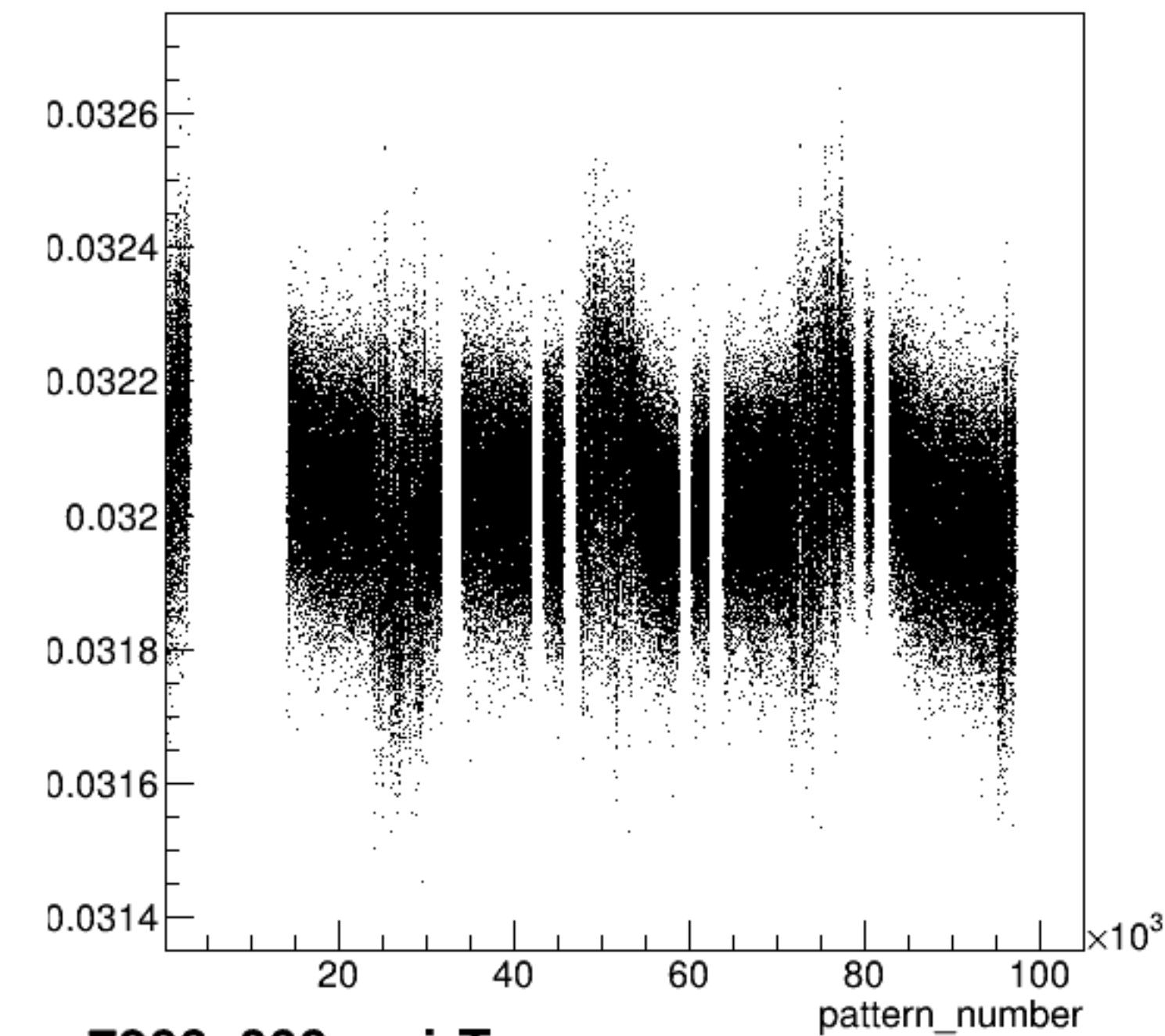
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl/ppm {ErrorFlag==0}

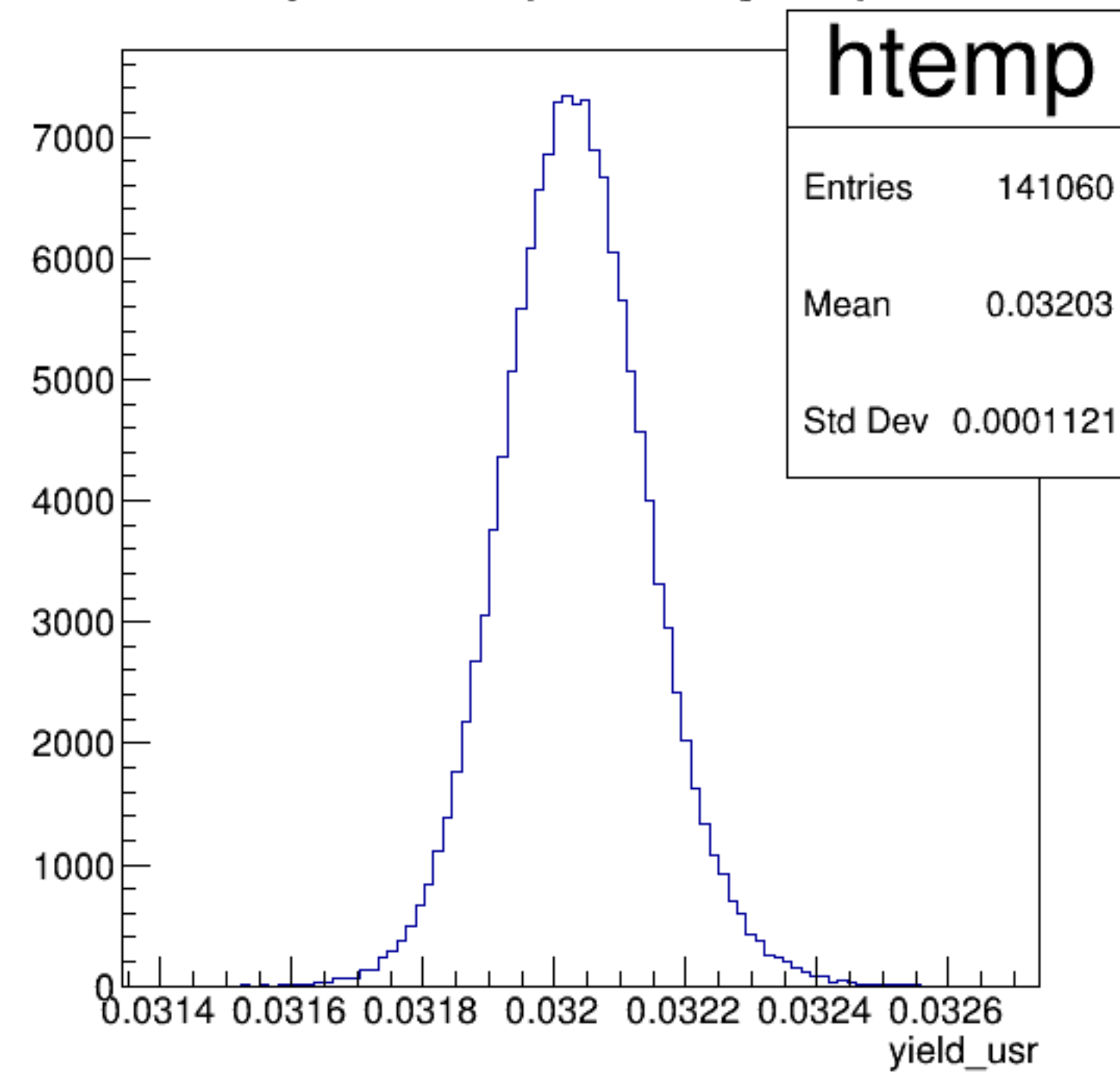


yield_usr:pattern_number {ErrorFlag==0}

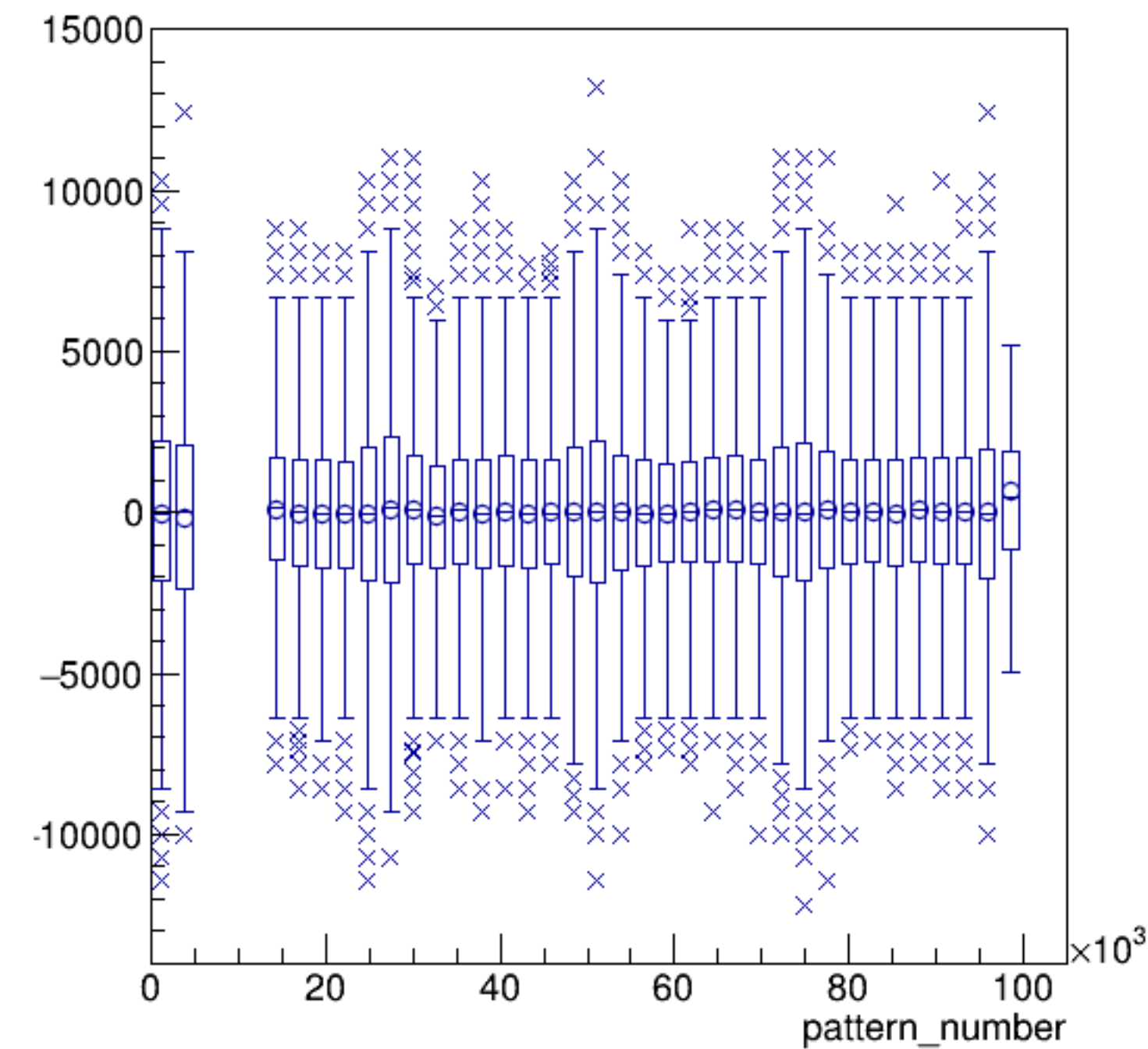


run7266_000_pairTree_usr.png

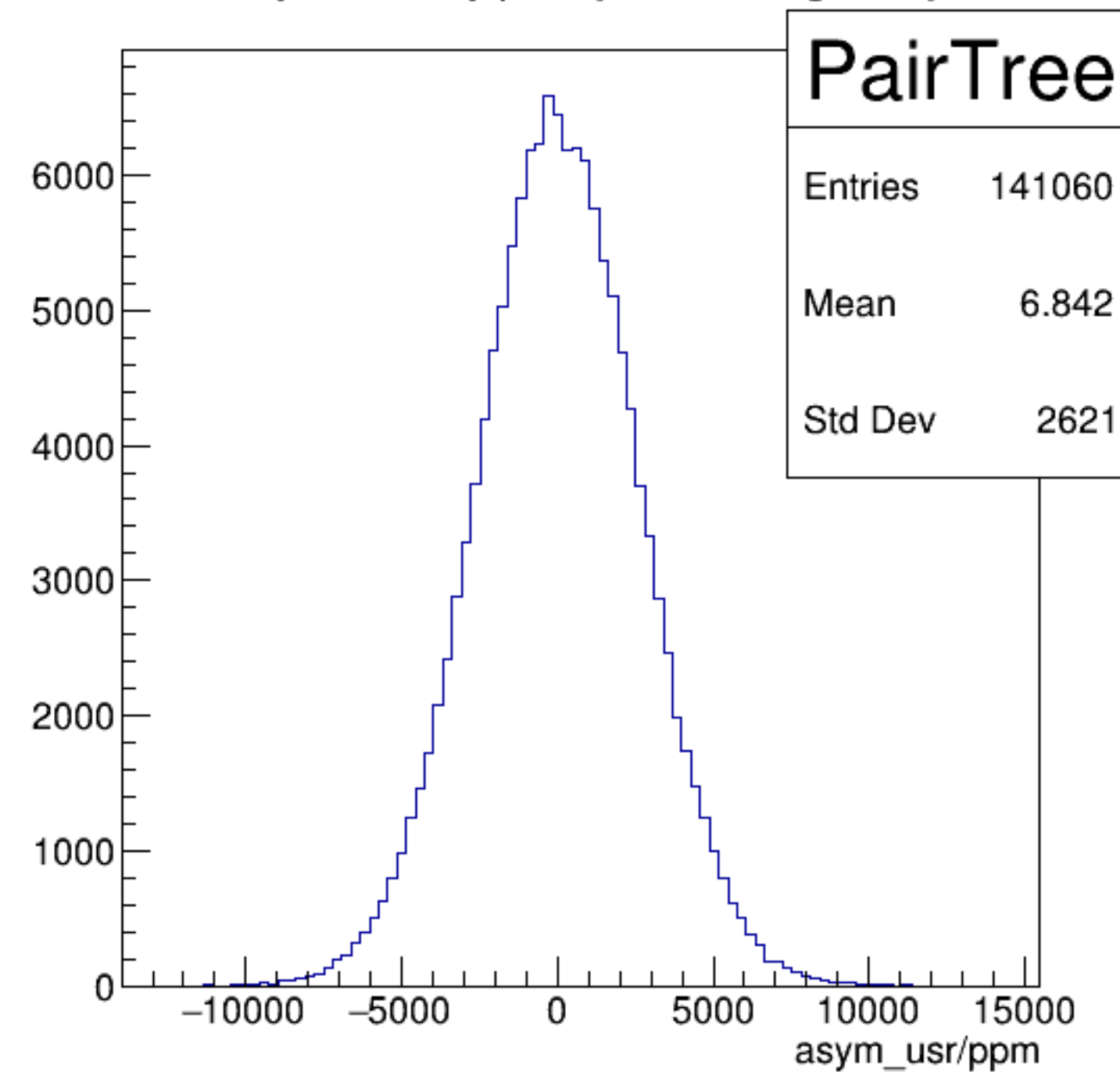
yield_usr {ErrorFlag==0}



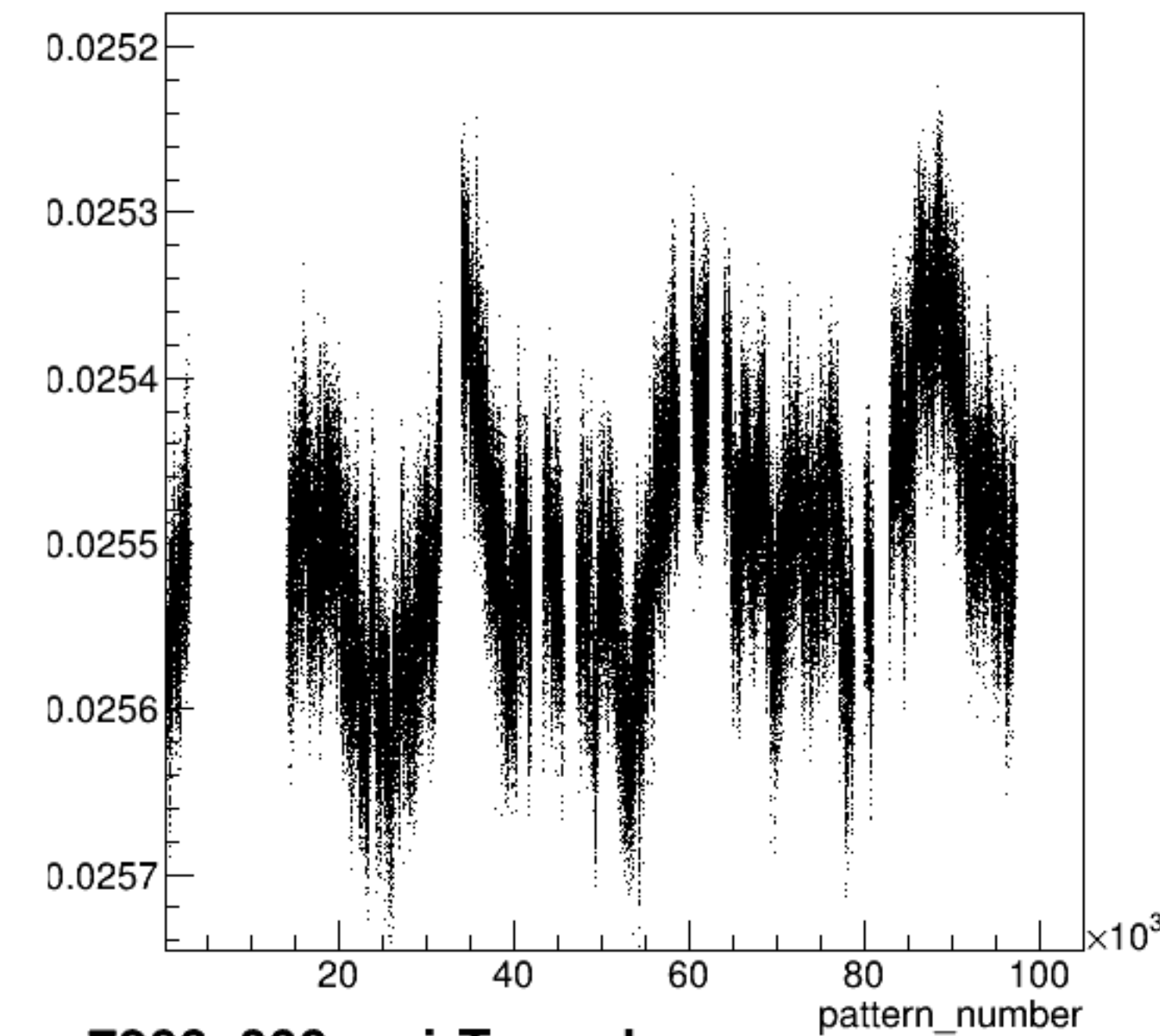
asym_usr/ppm:pattern_number {ErrorFlag==0}



asym_usr/ppm {ErrorFlag==0}

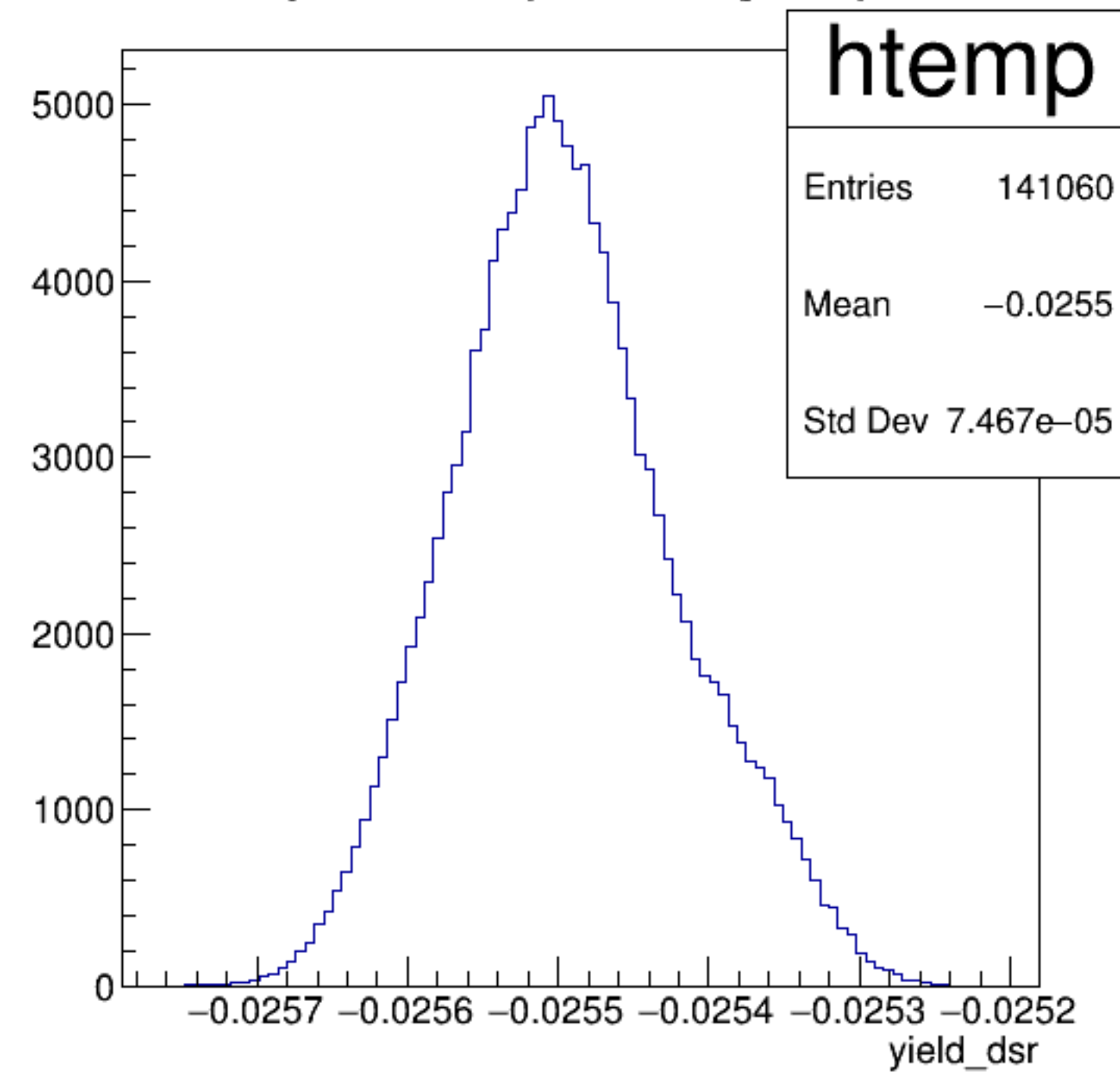


yield_dsr:pattern_number {ErrorFlag==0}

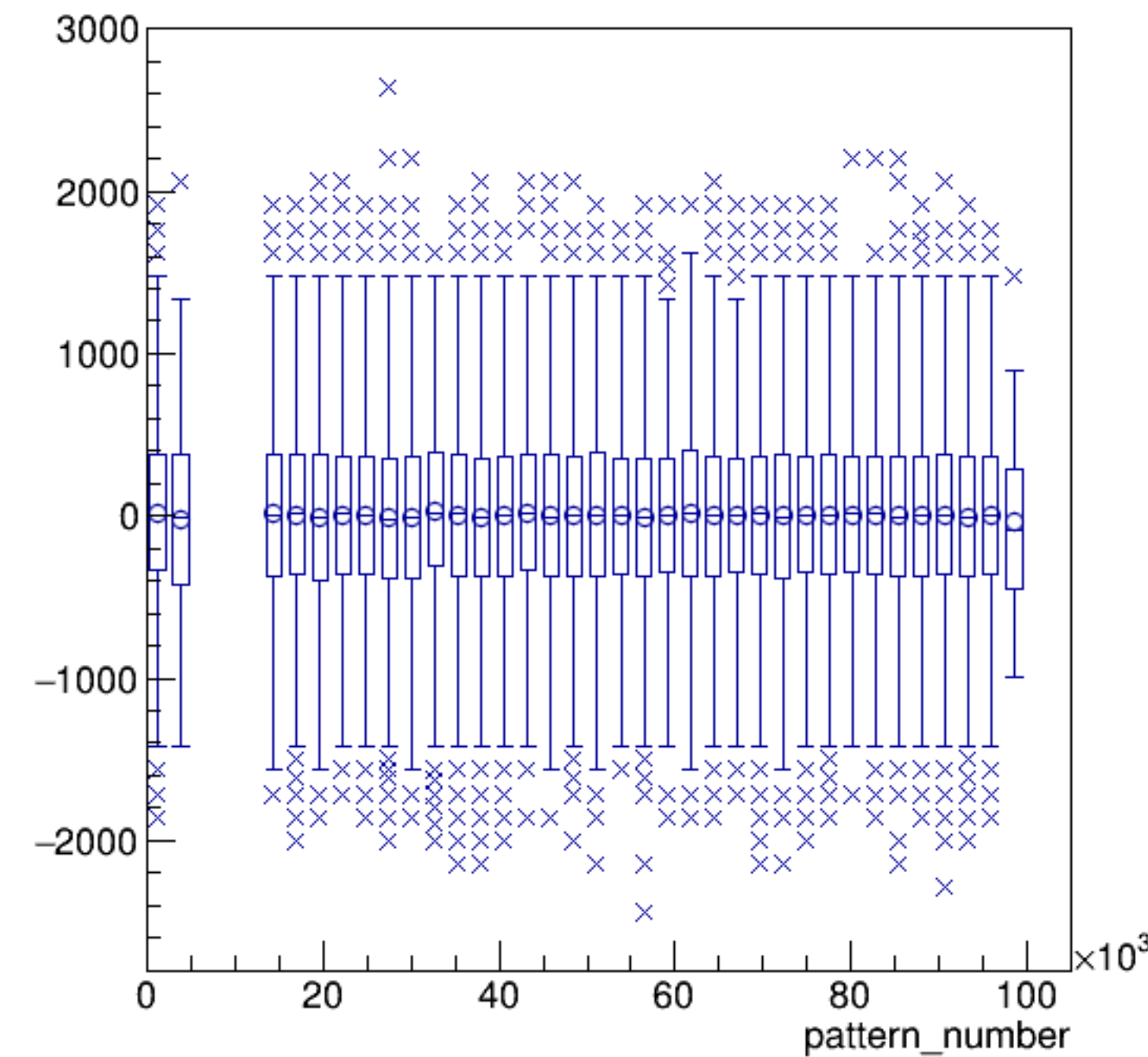


run7266_000_pairTree_dsr.png

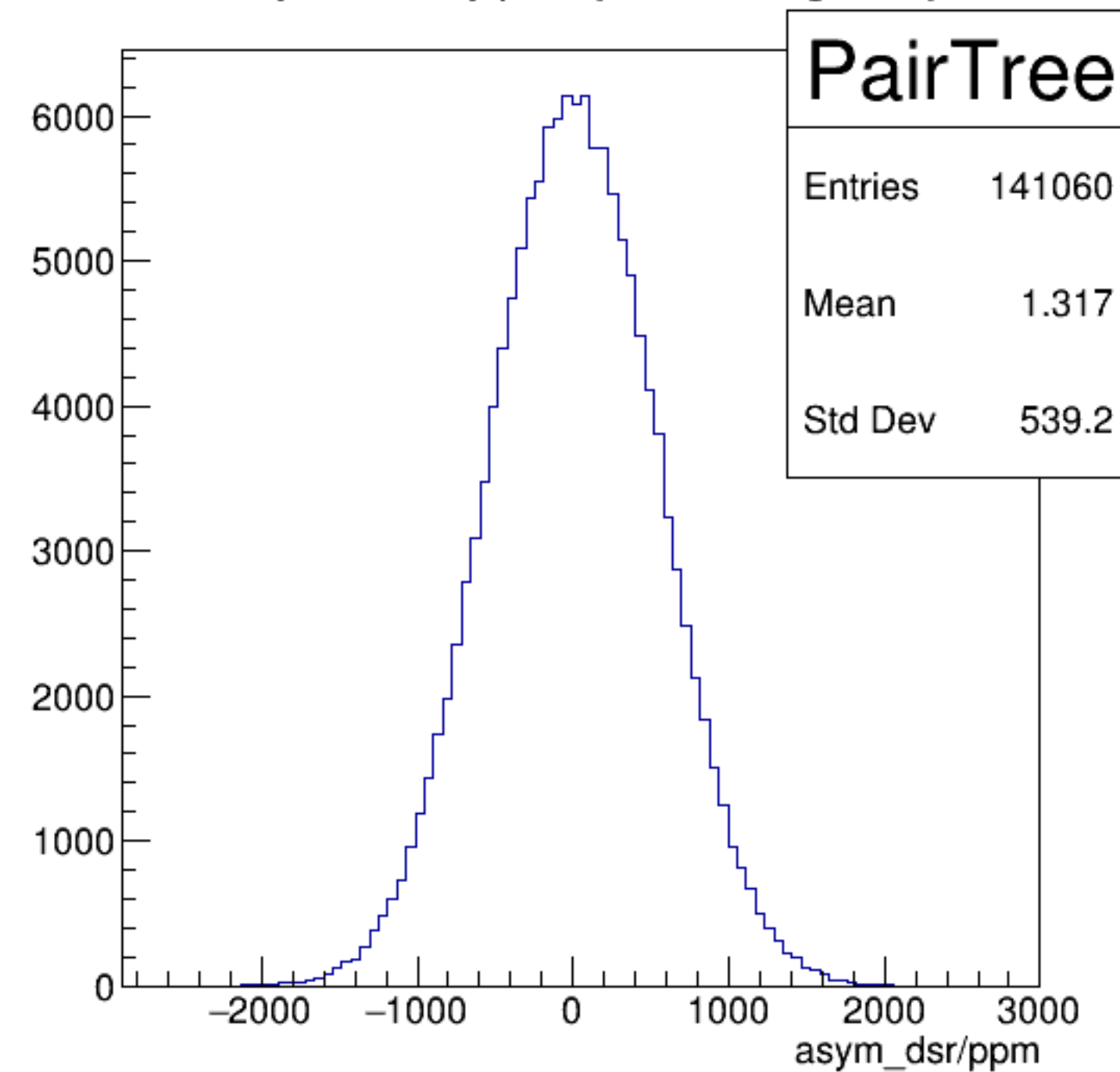
yield_dsr {ErrorFlag==0}



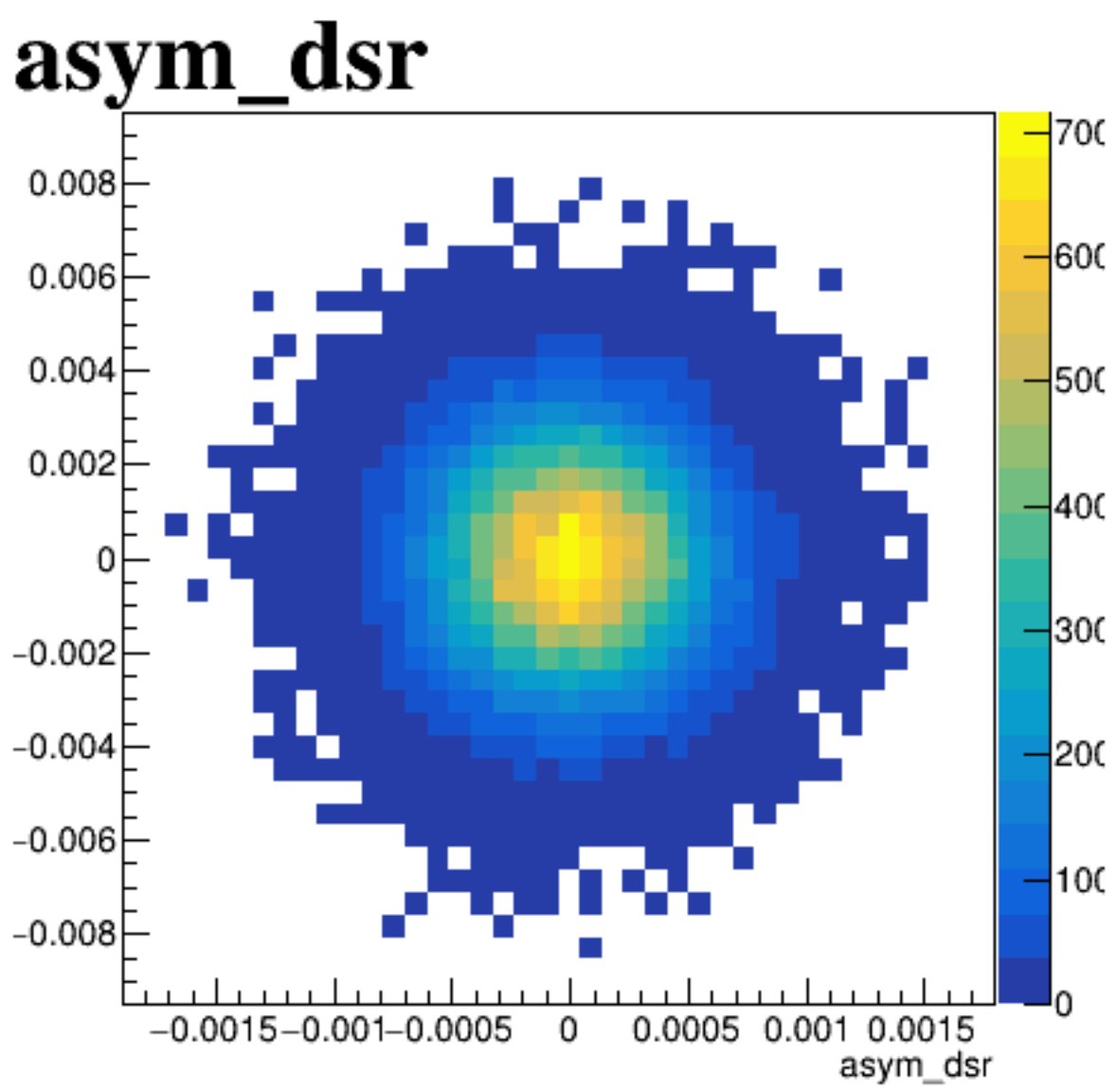
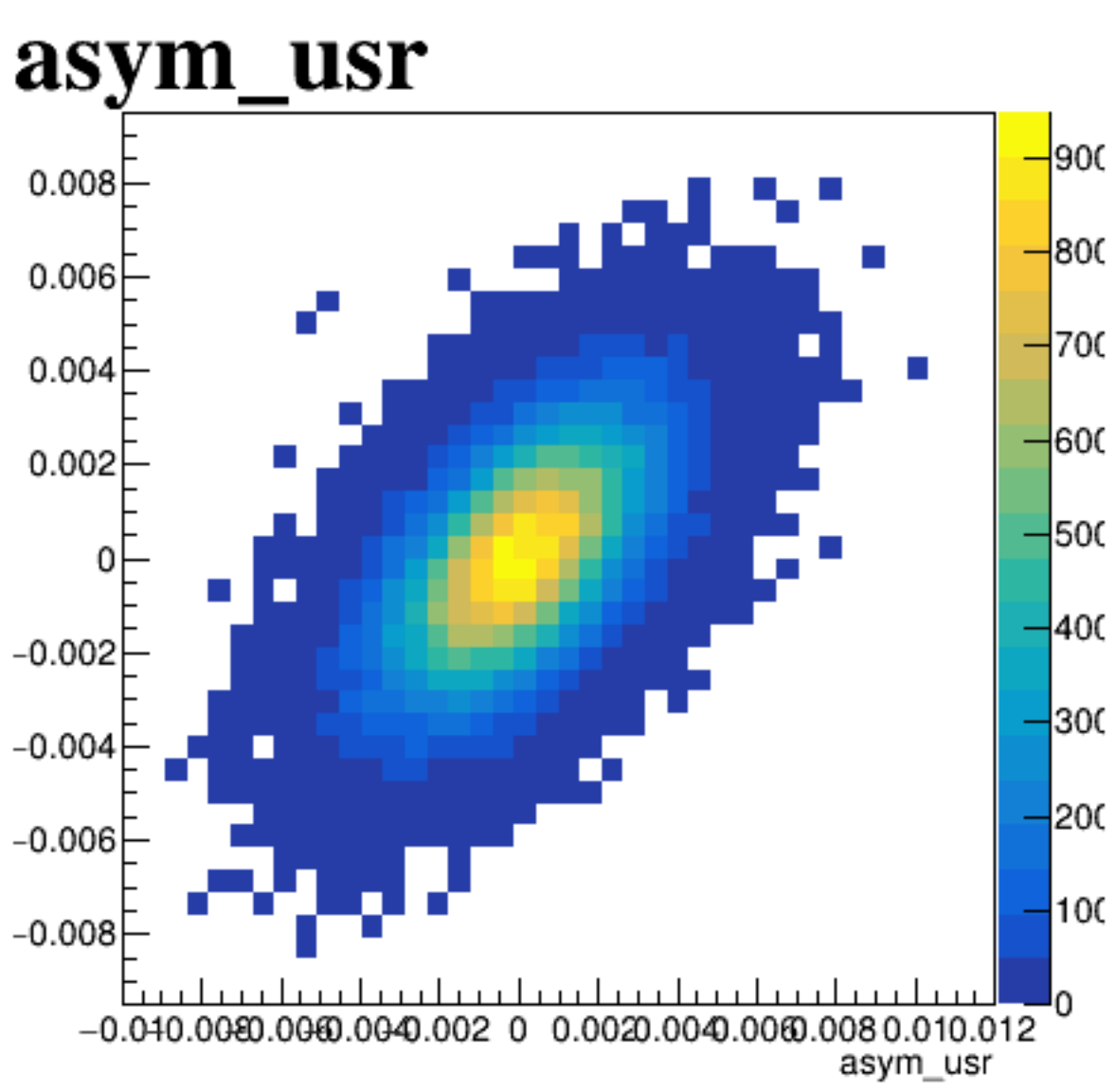
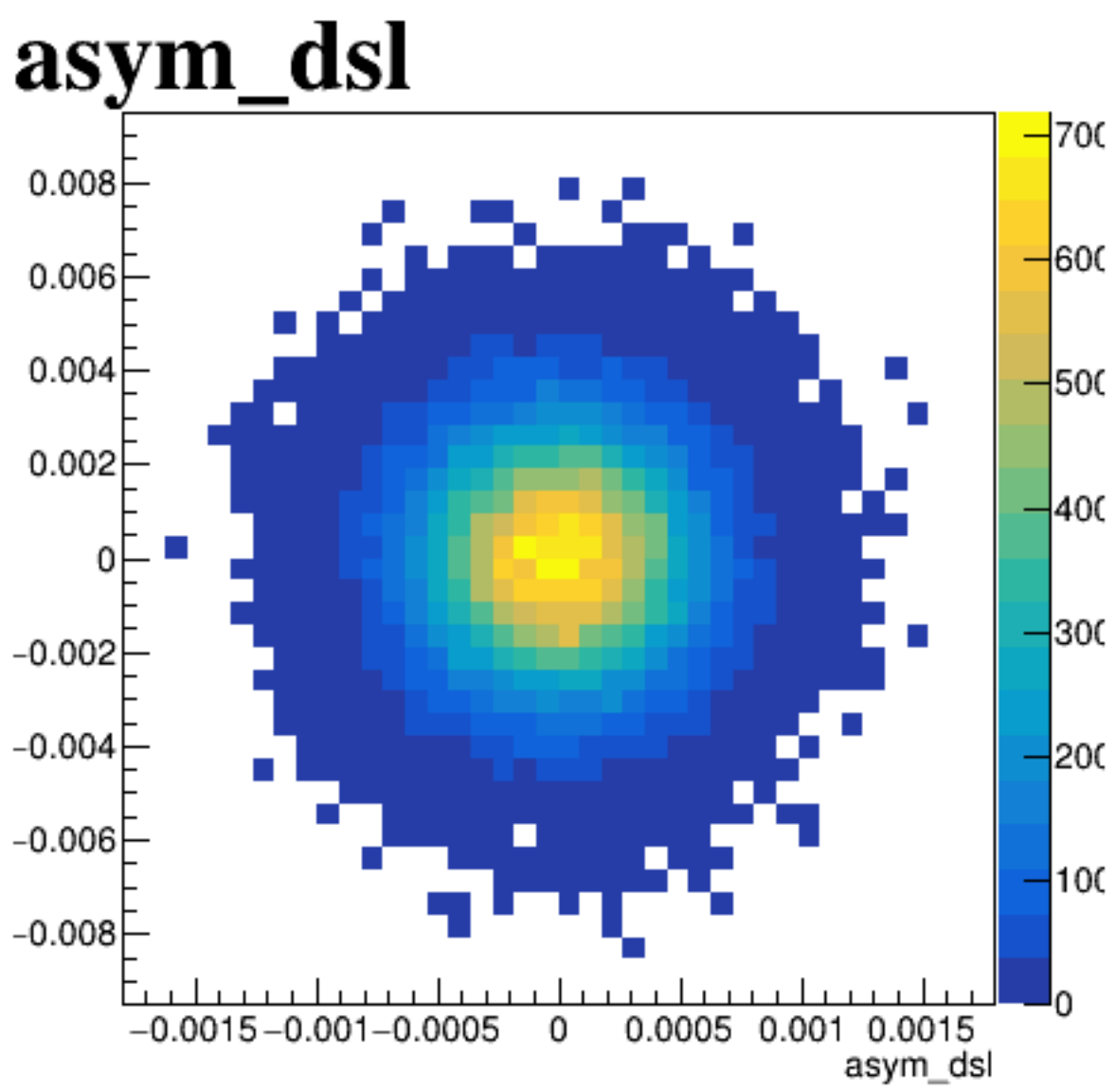
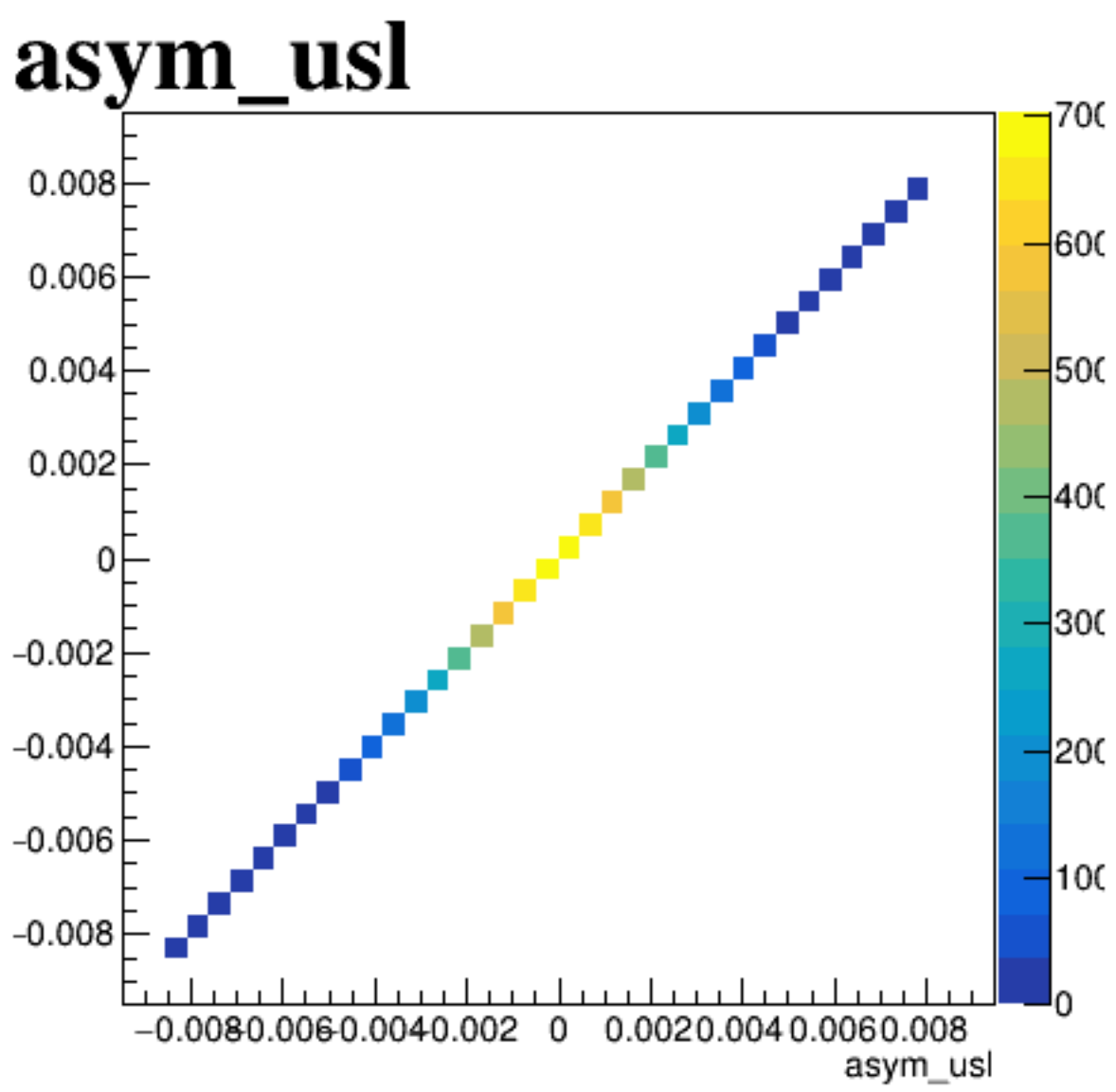
asym_dsr/ppm:pattern_number {ErrorFlag==0}



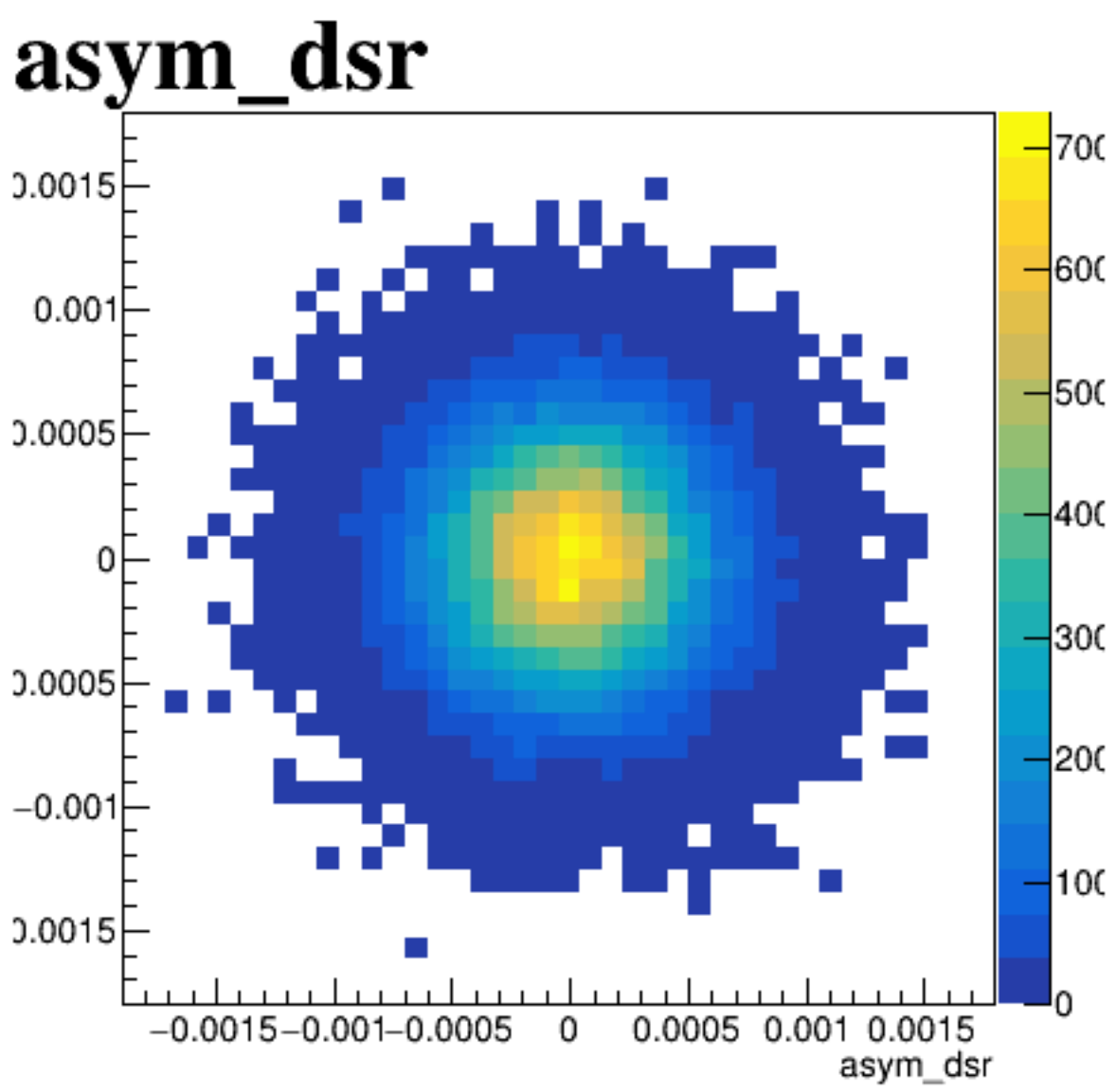
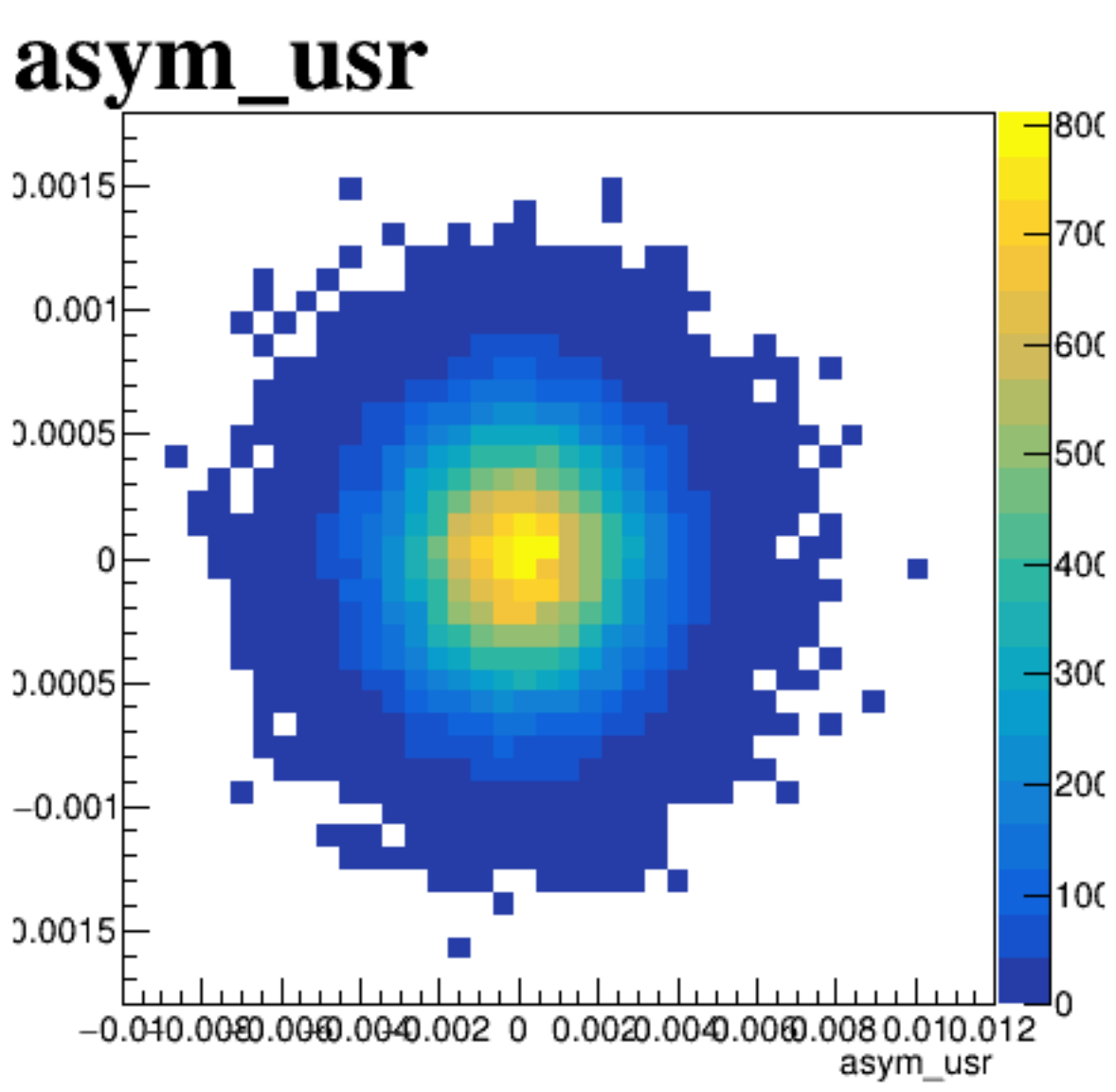
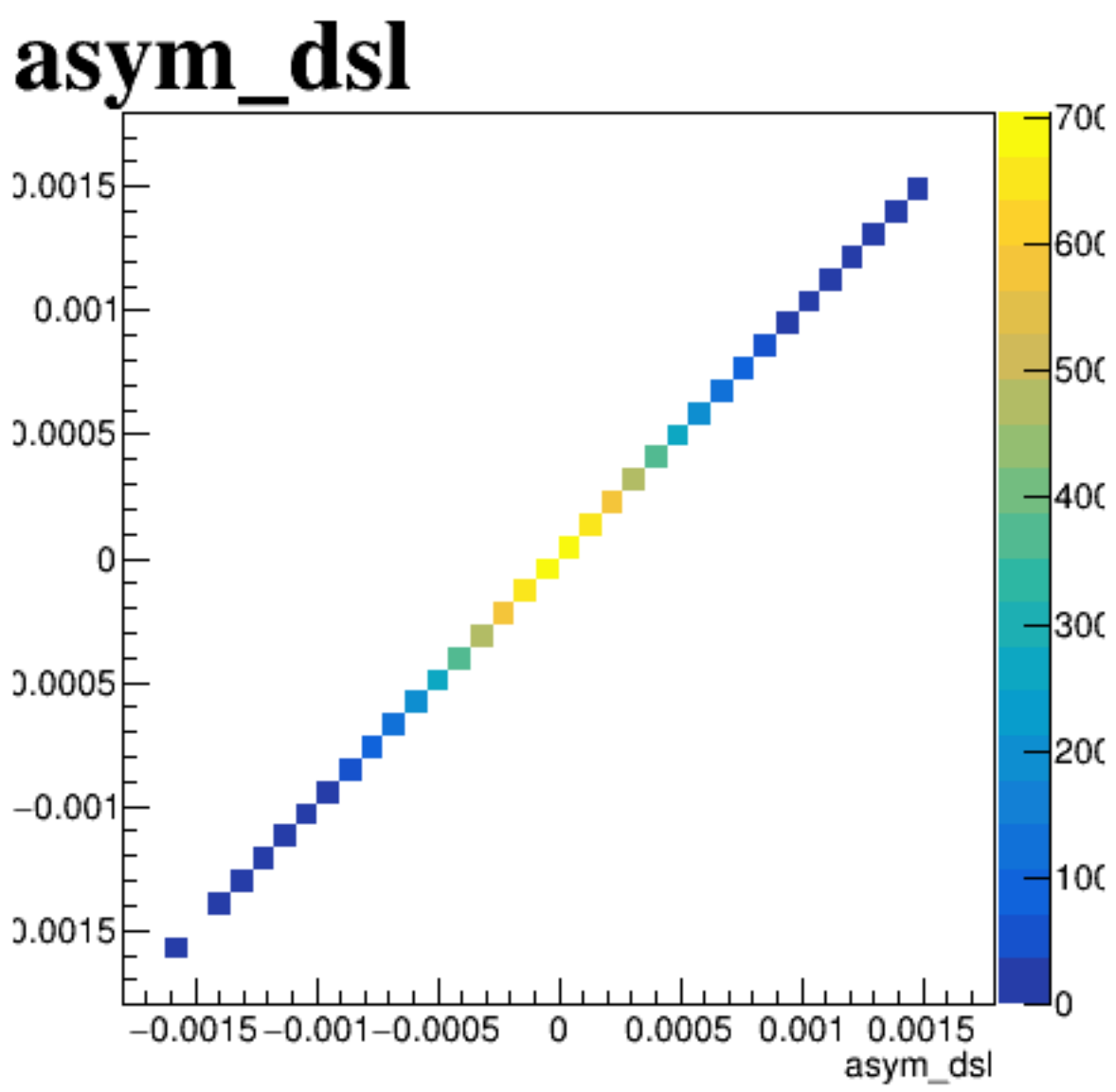
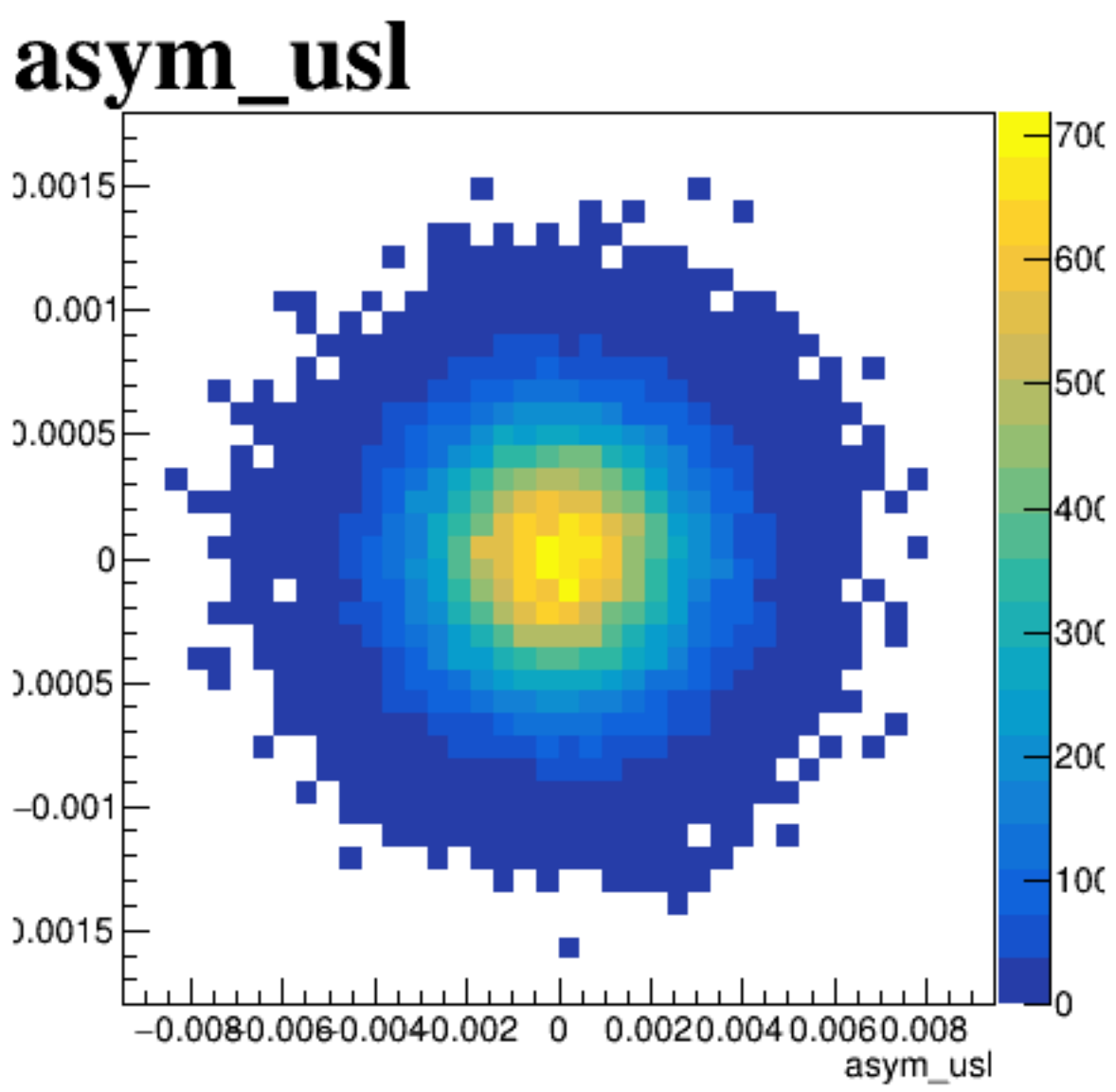
asym_dsr/ppm {ErrorFlag==0}



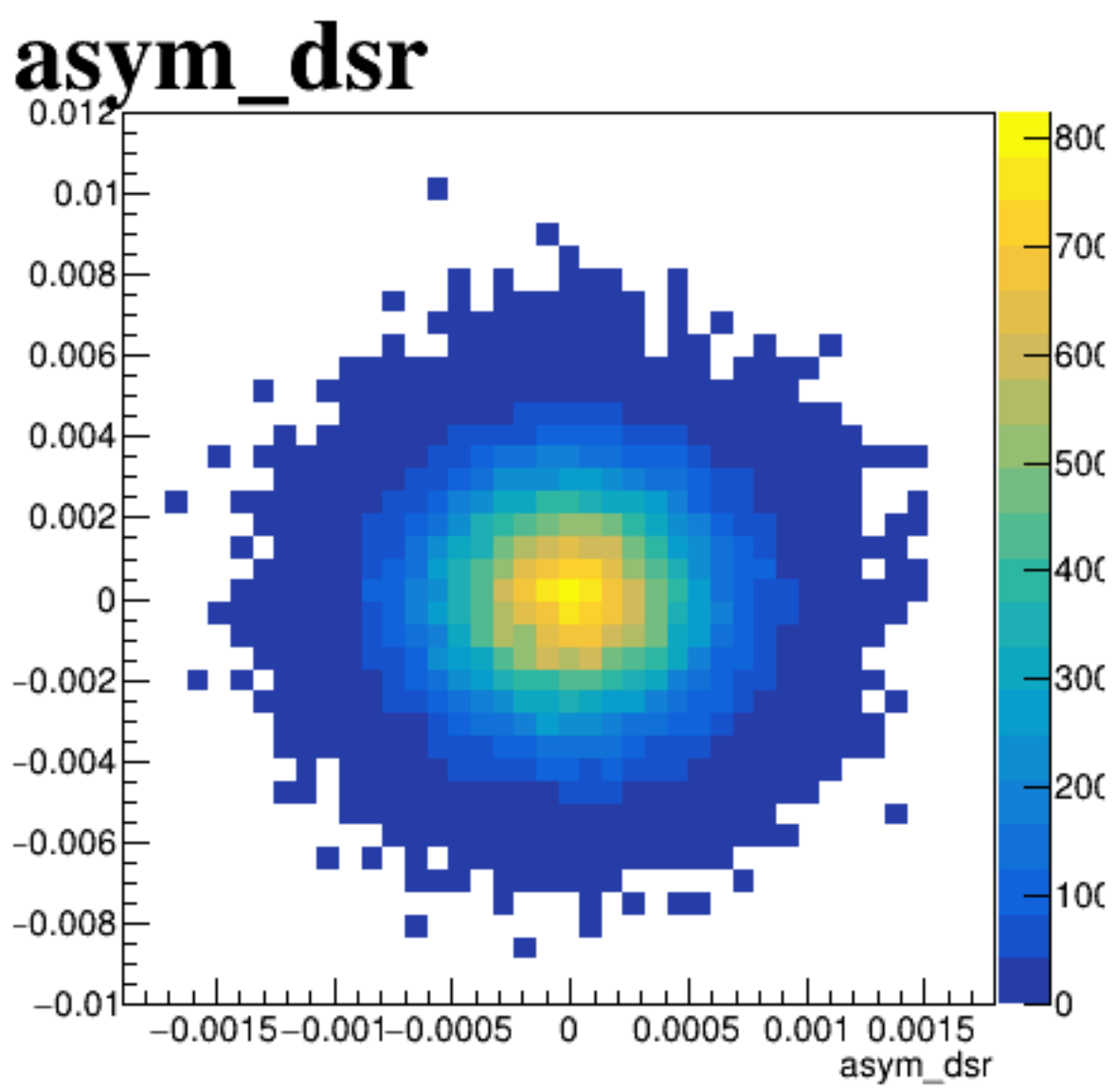
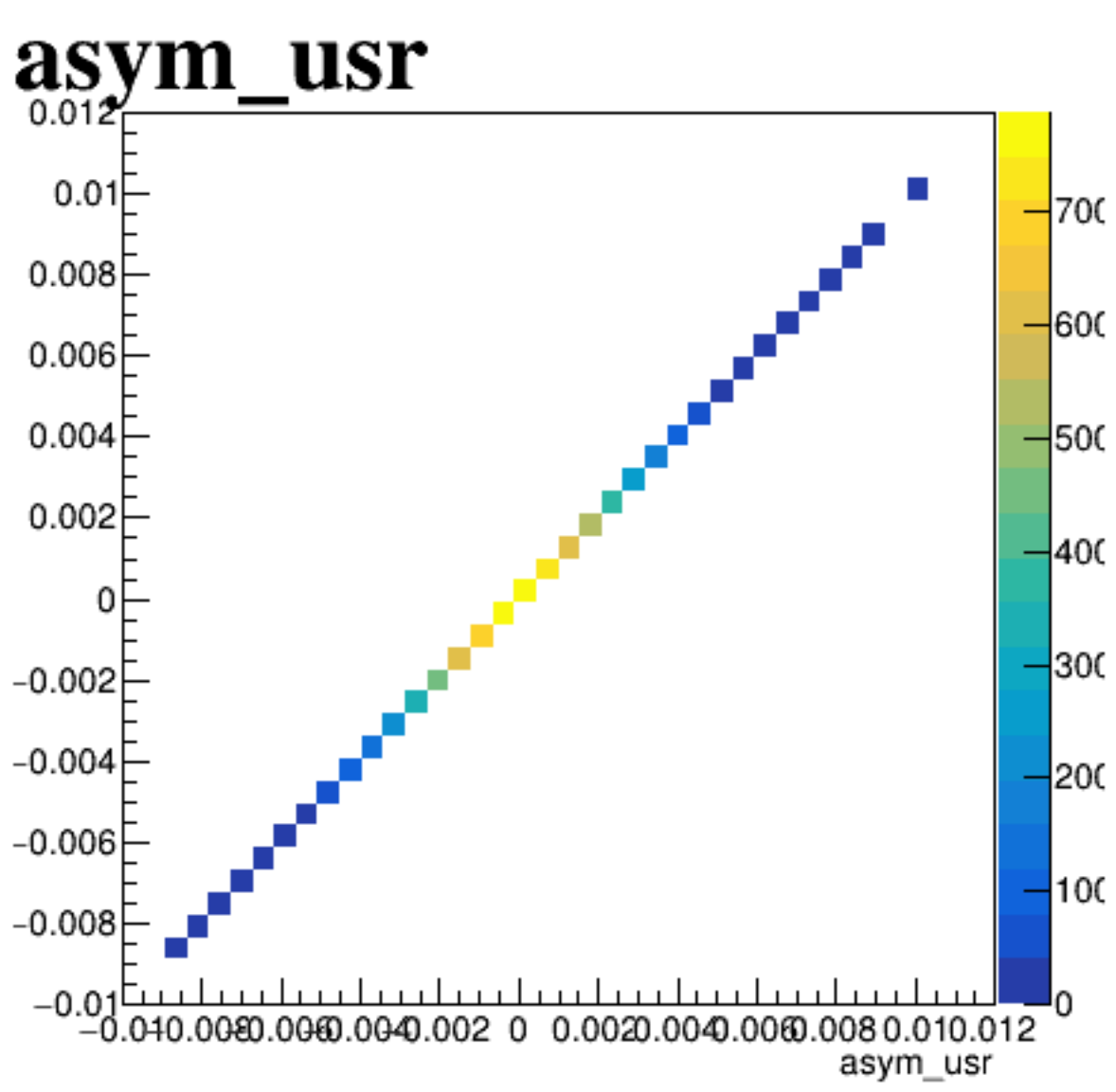
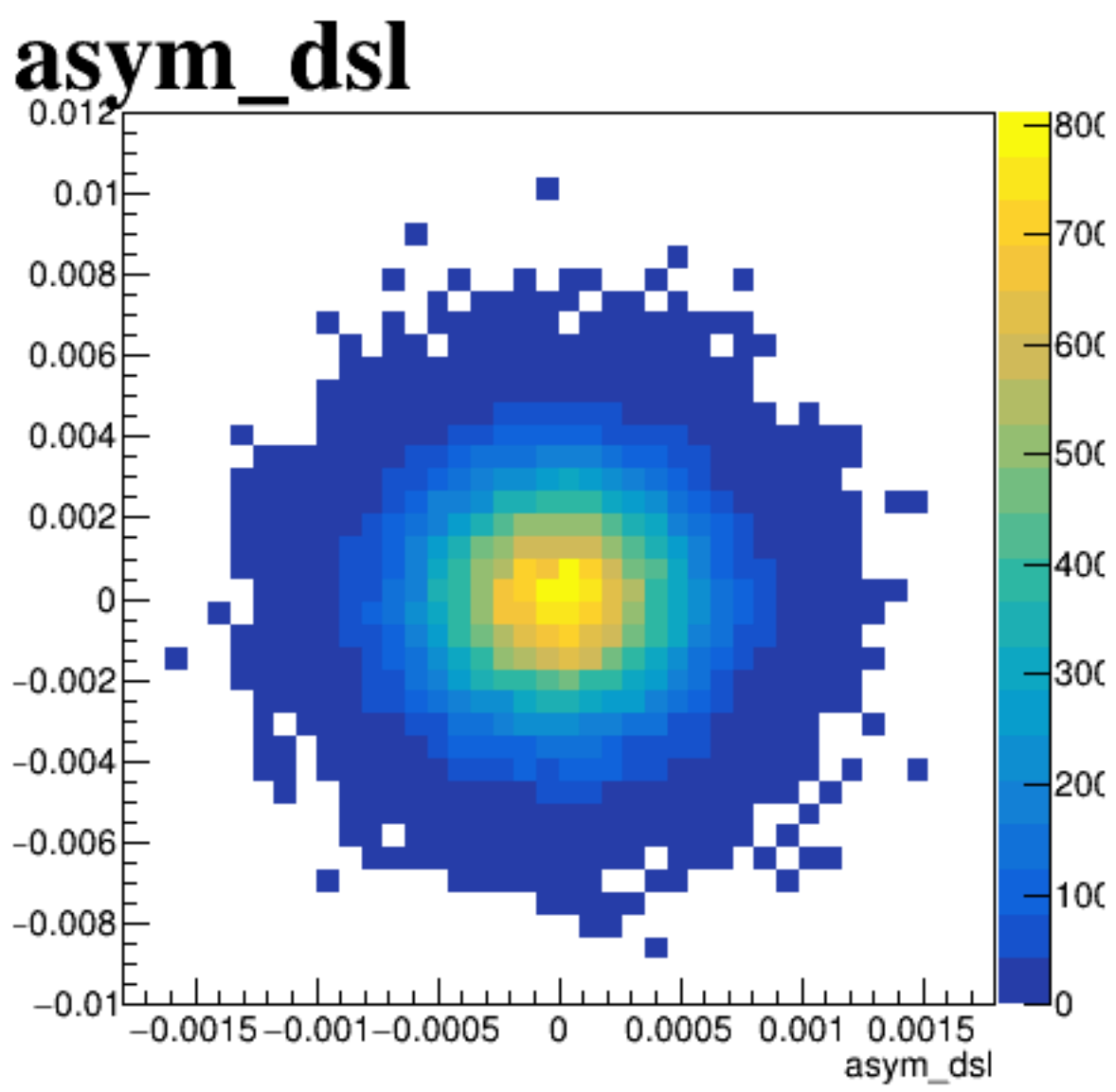
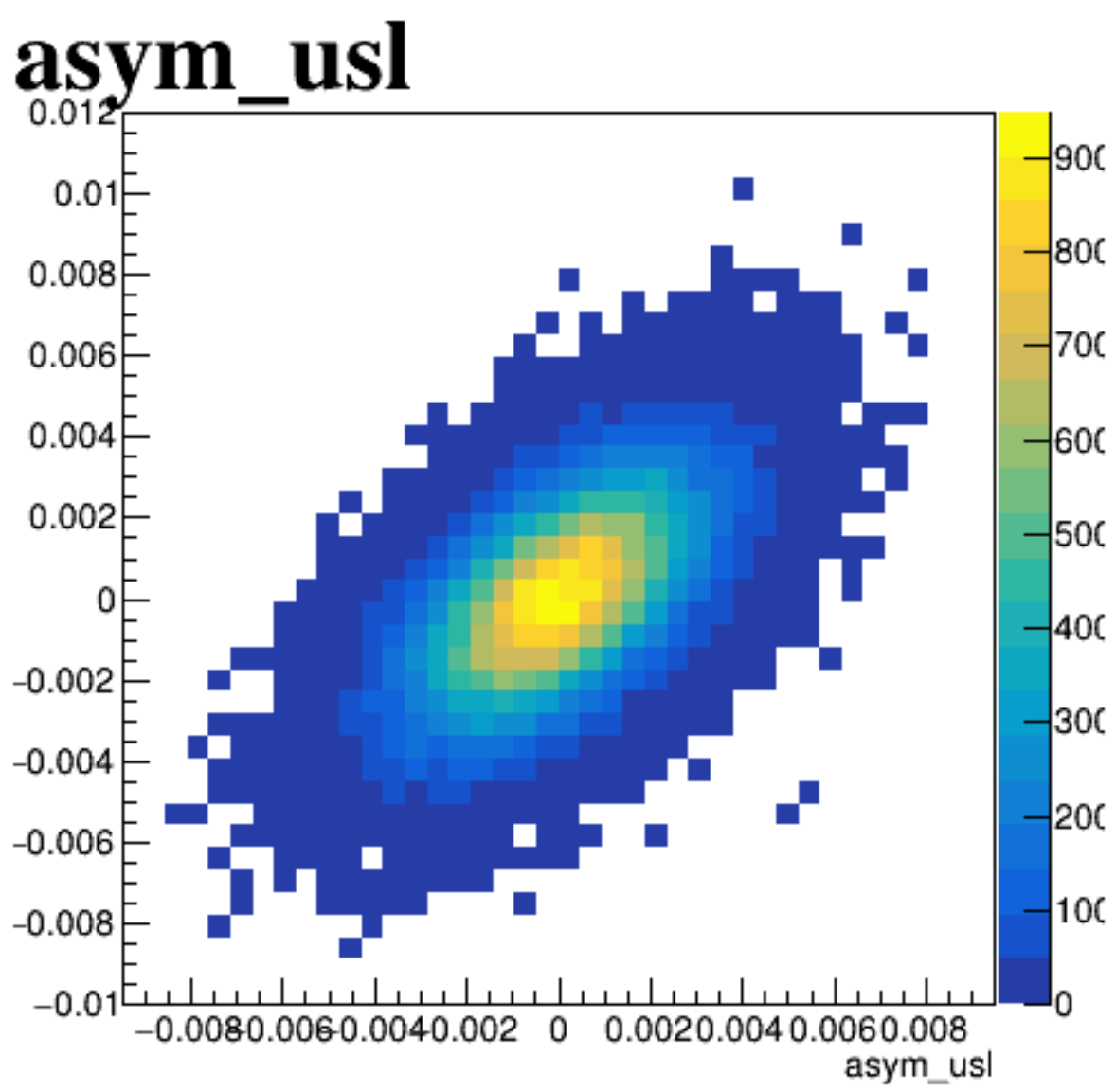
asym_usl



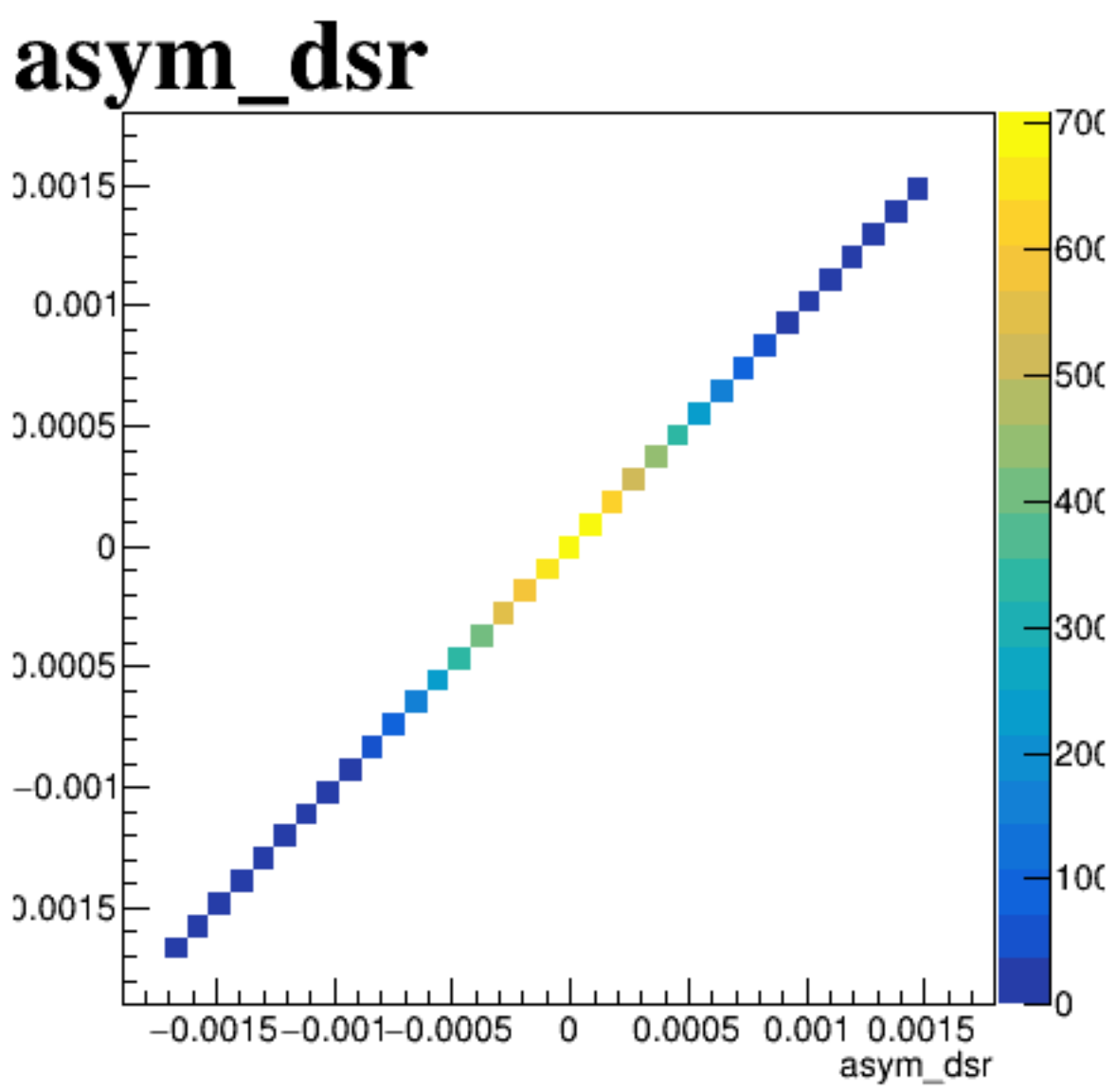
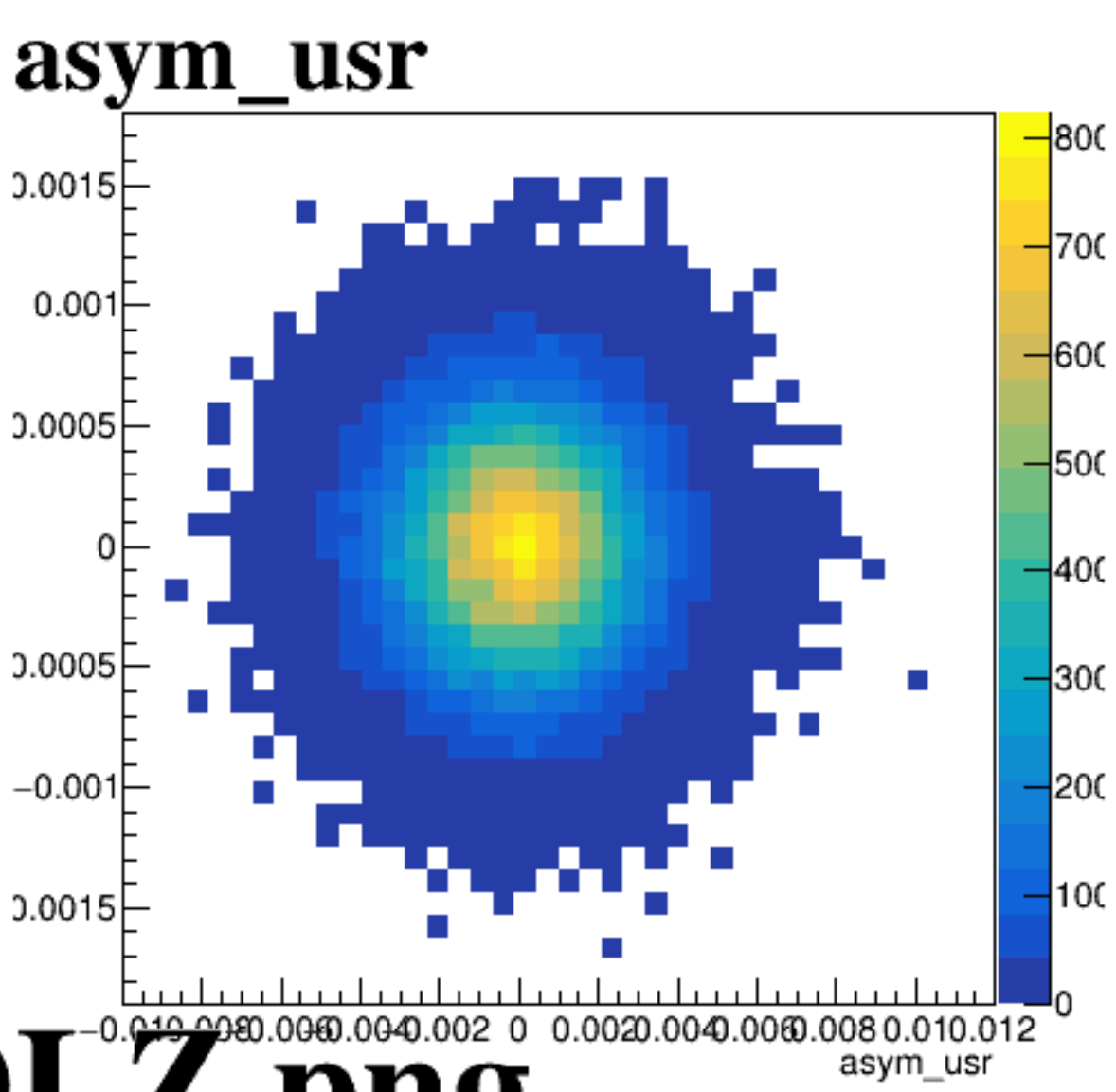
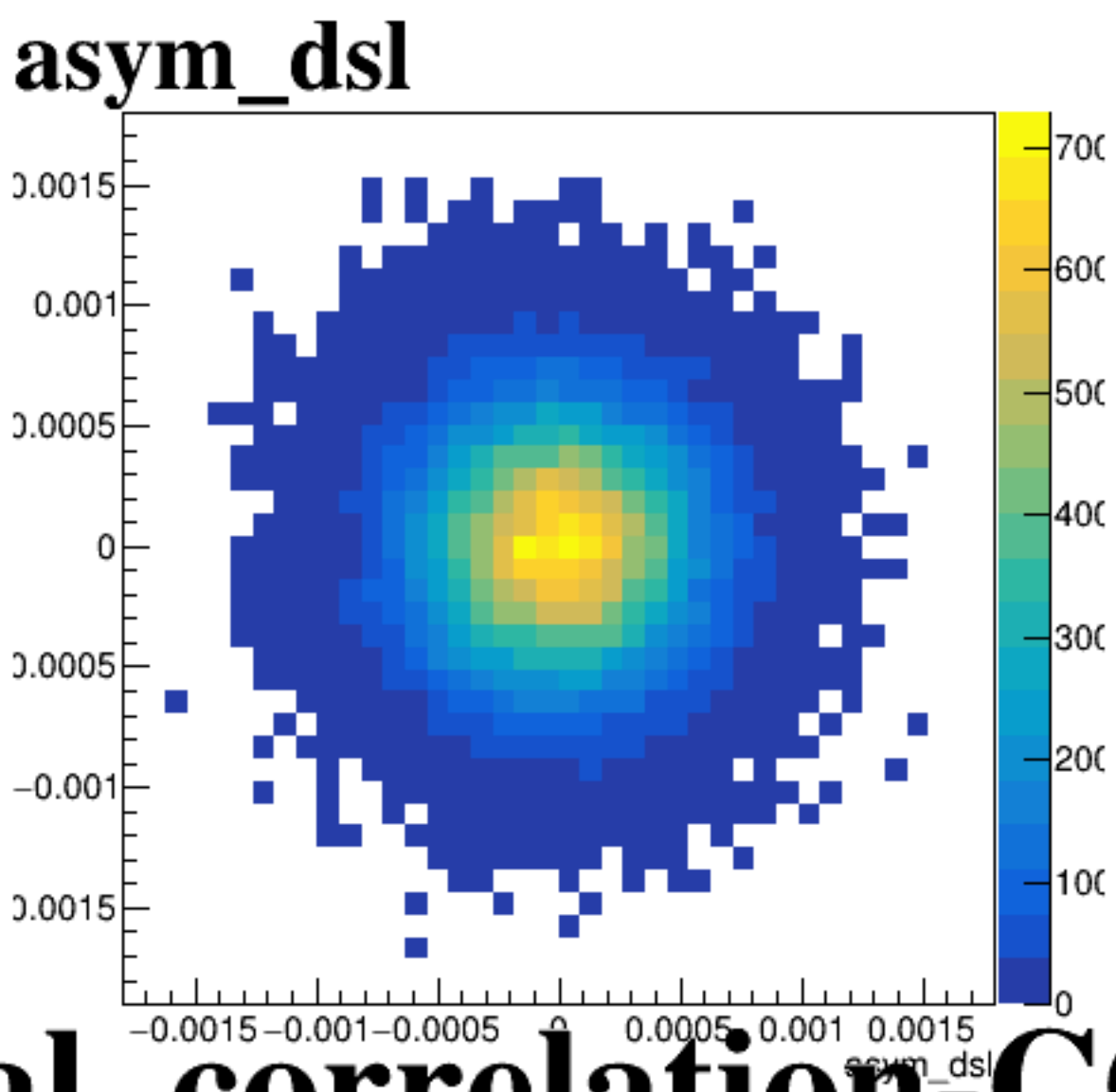
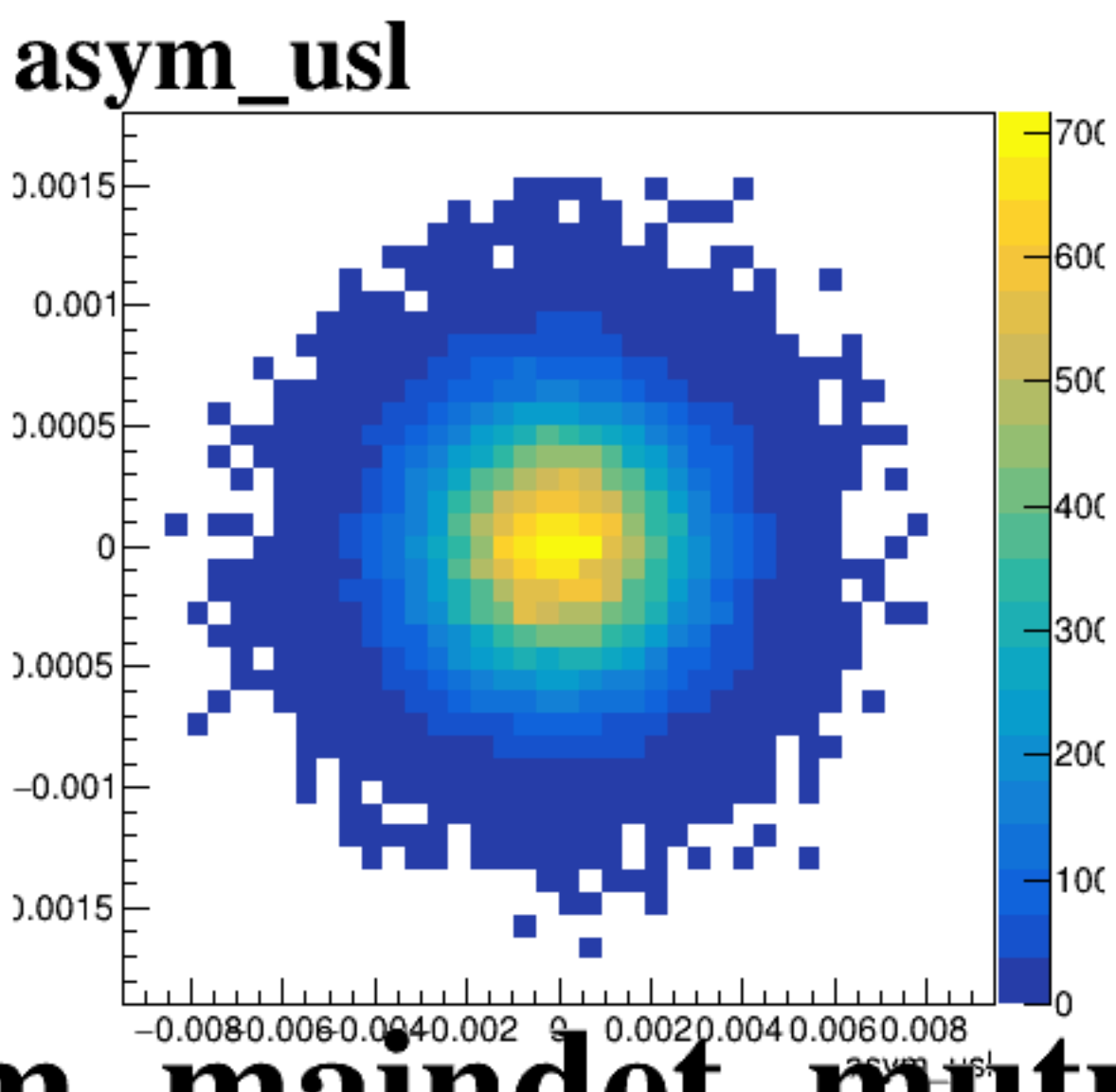
asym_dsl



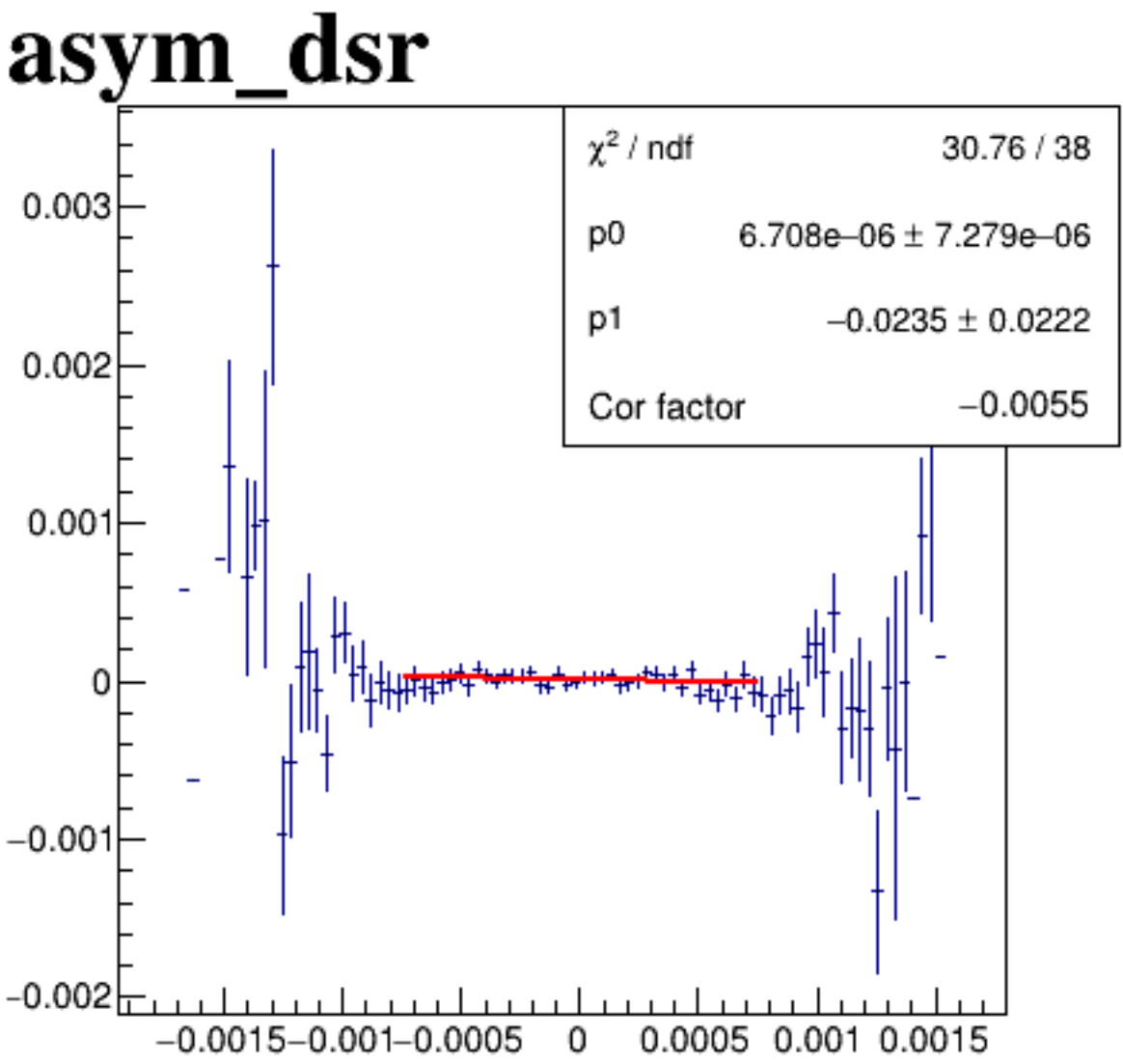
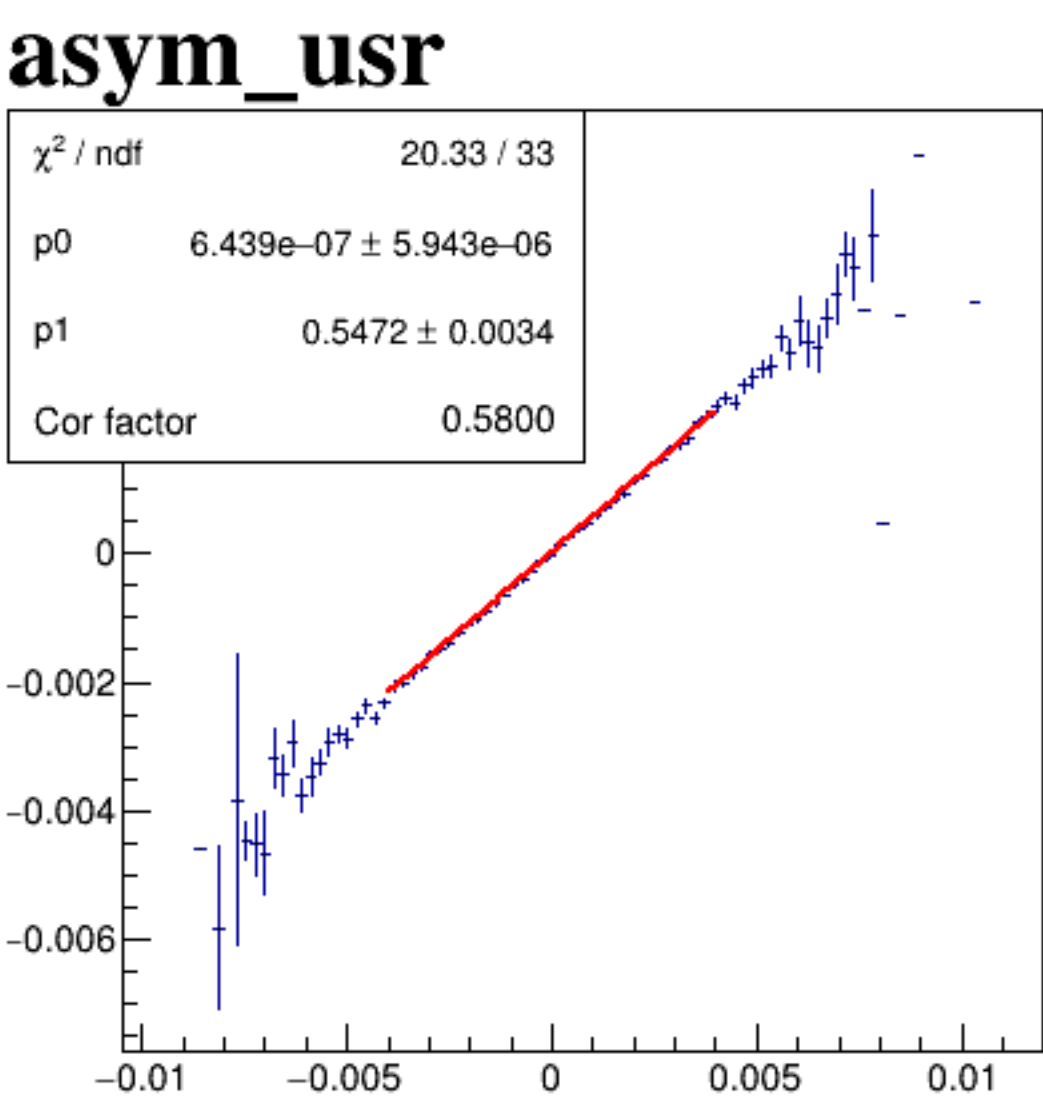
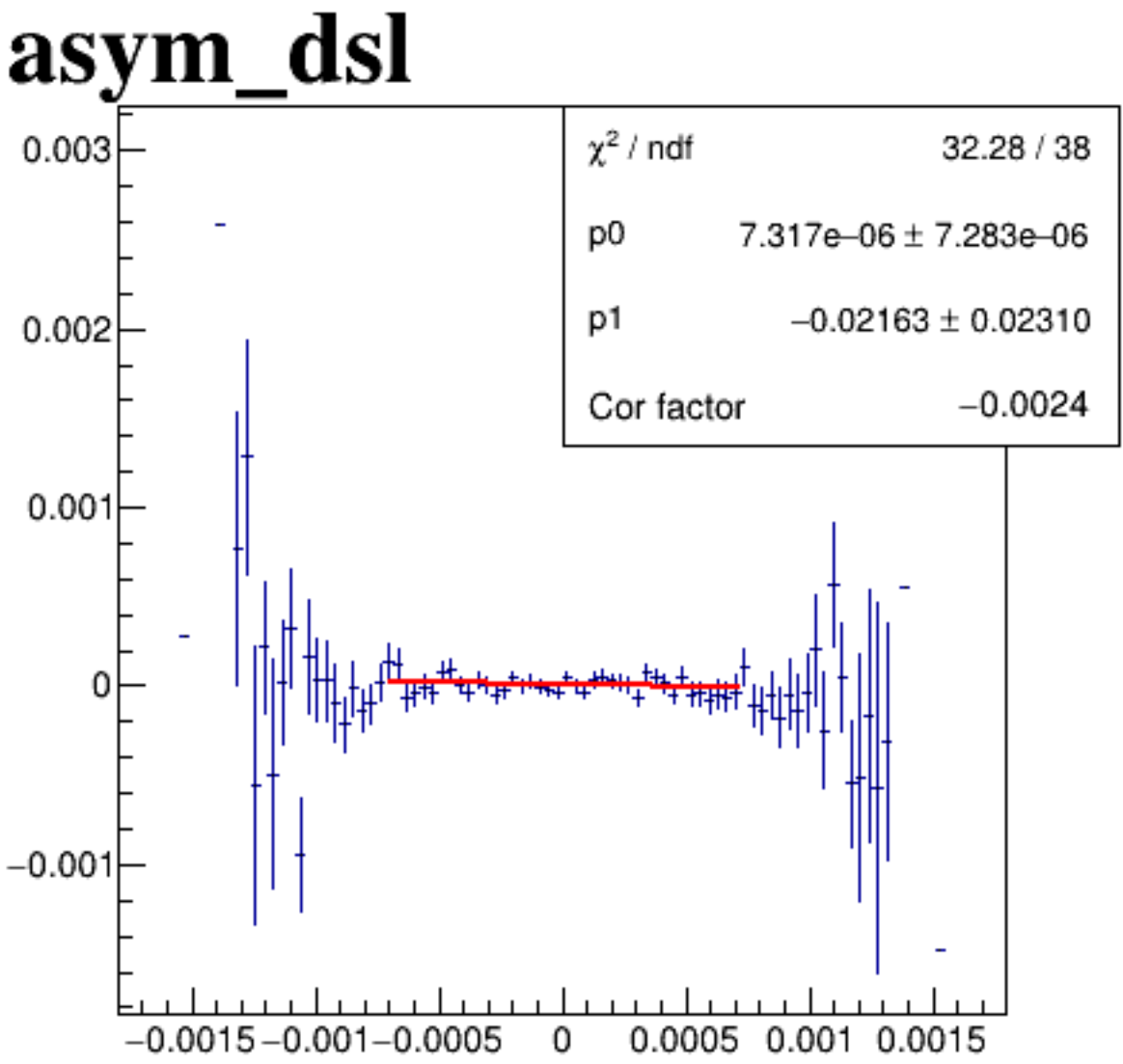
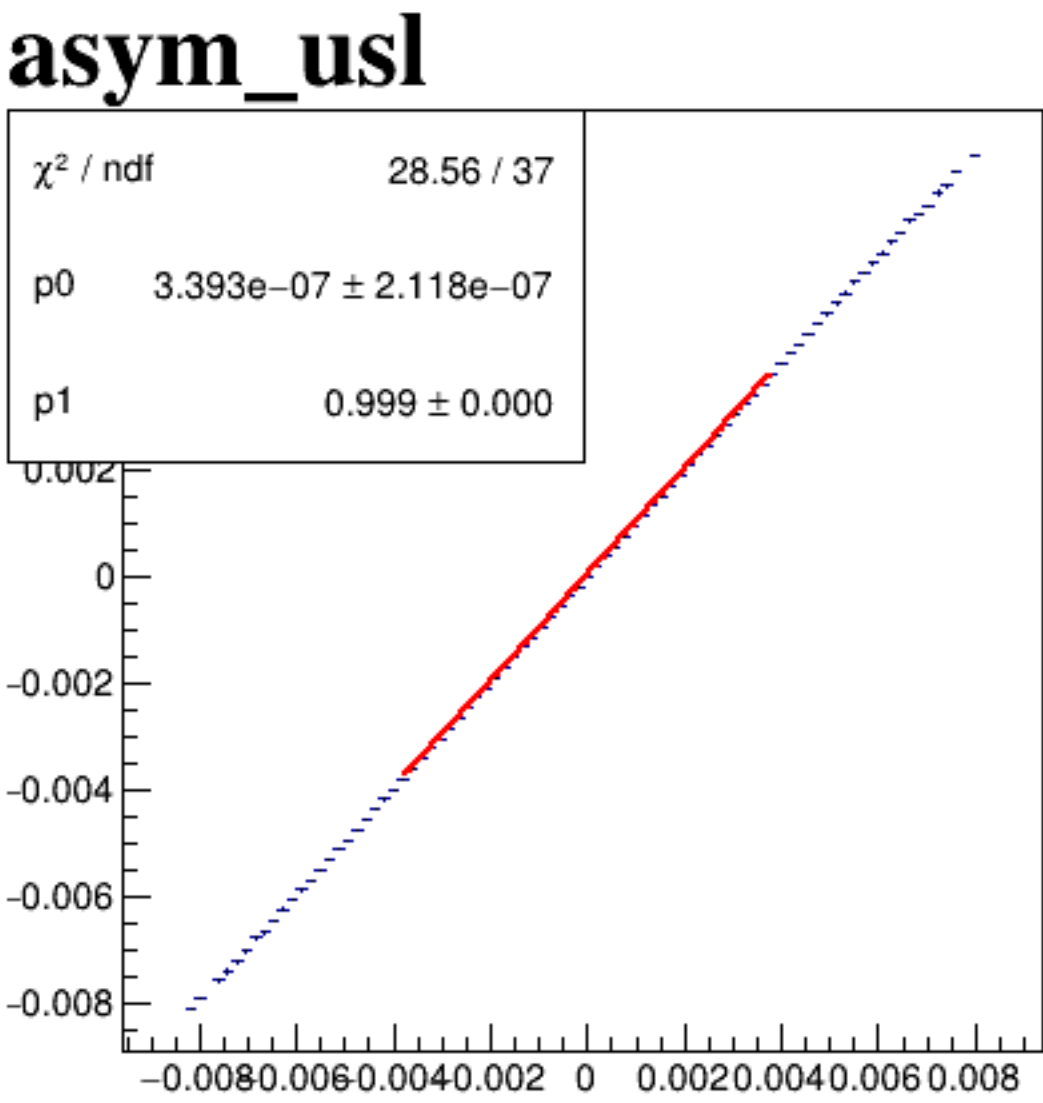
asym_usr



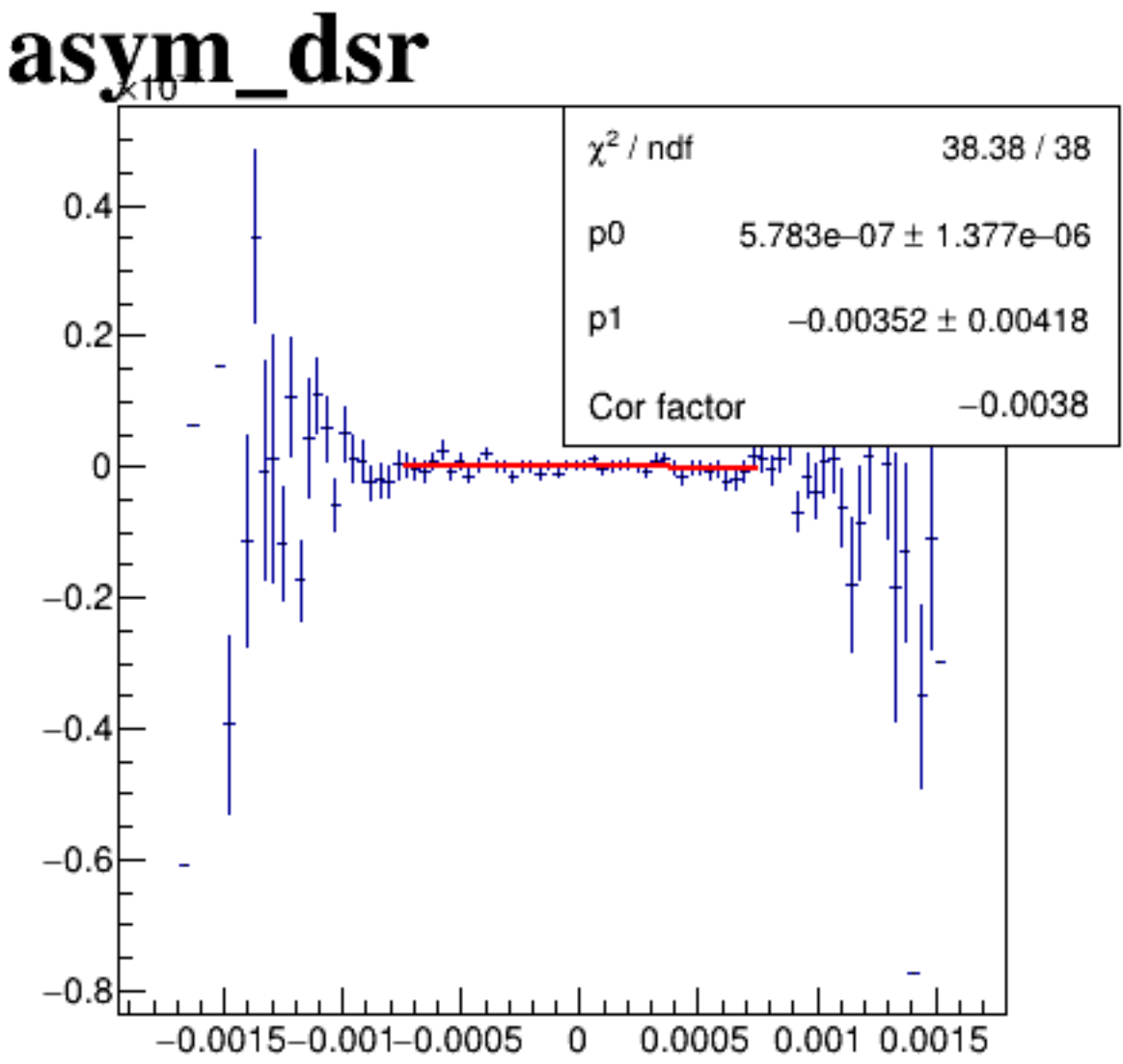
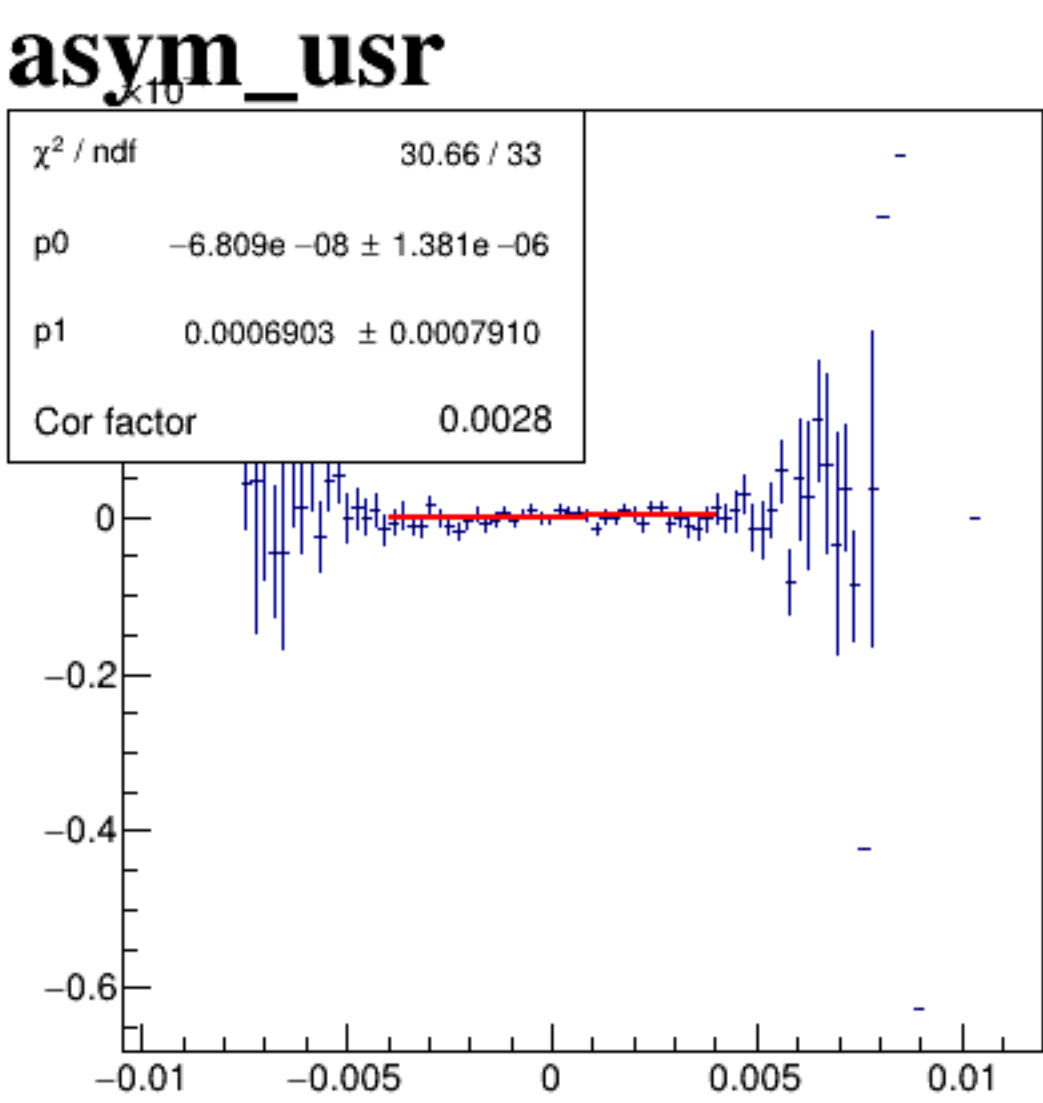
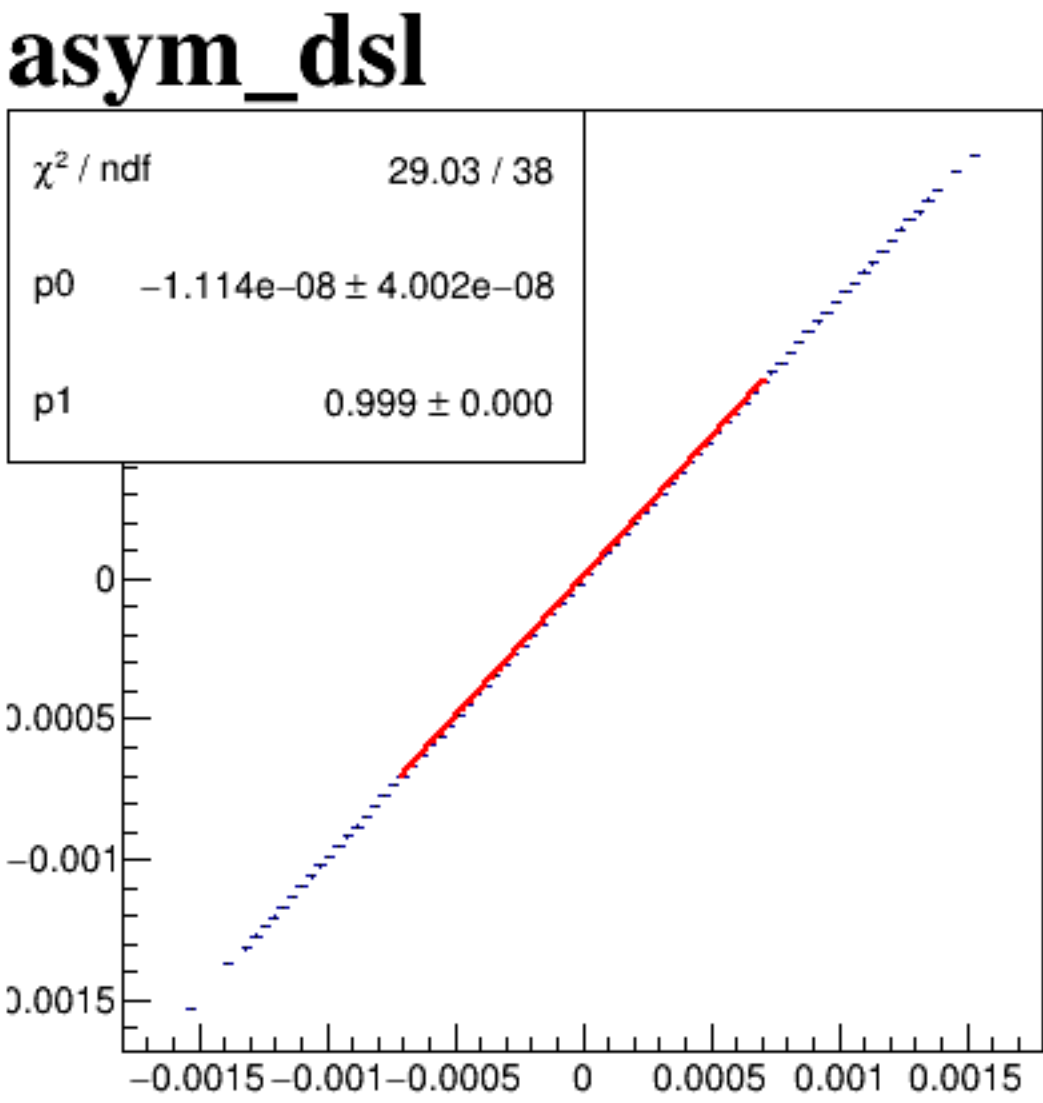
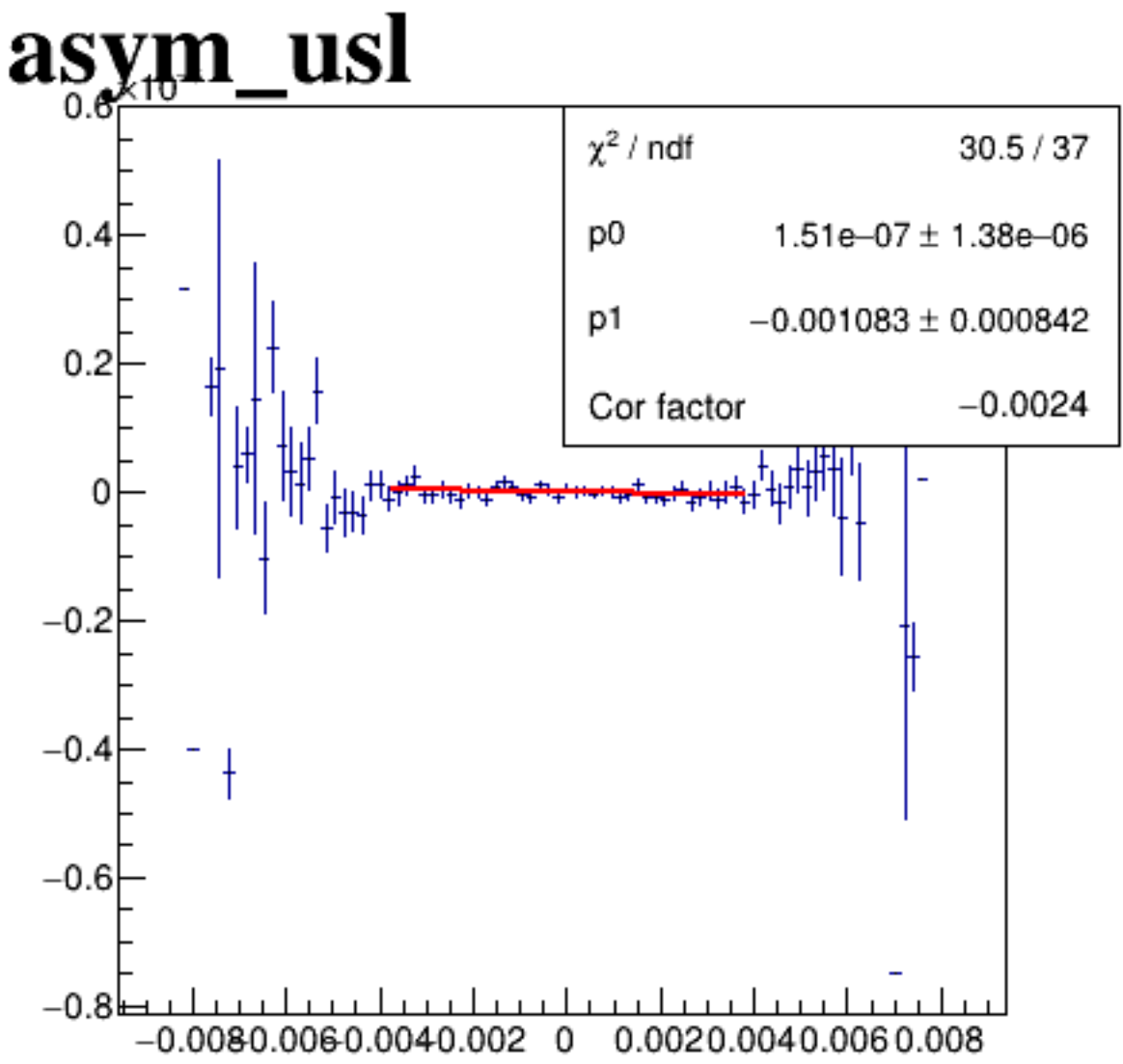
asym_dsr



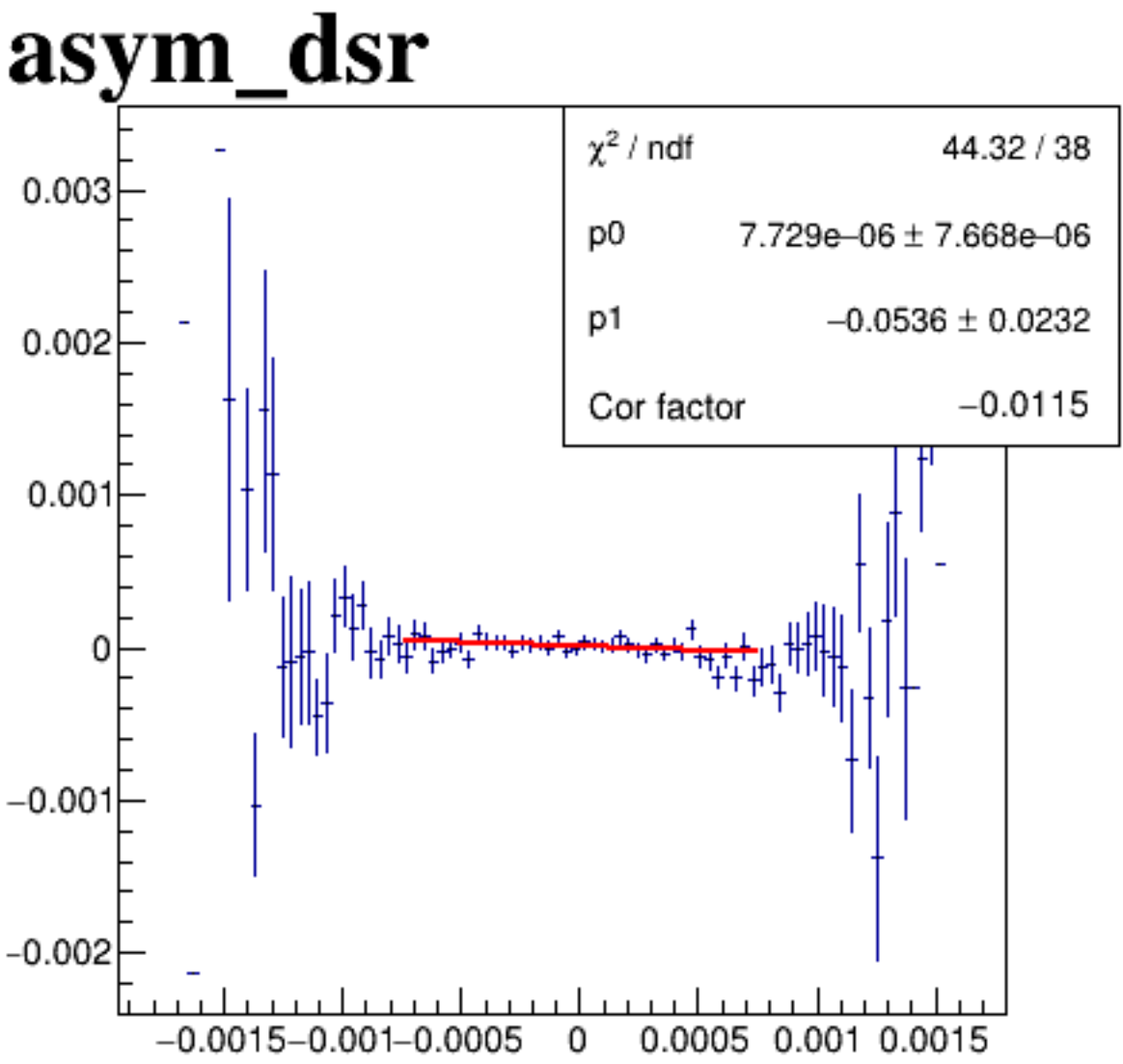
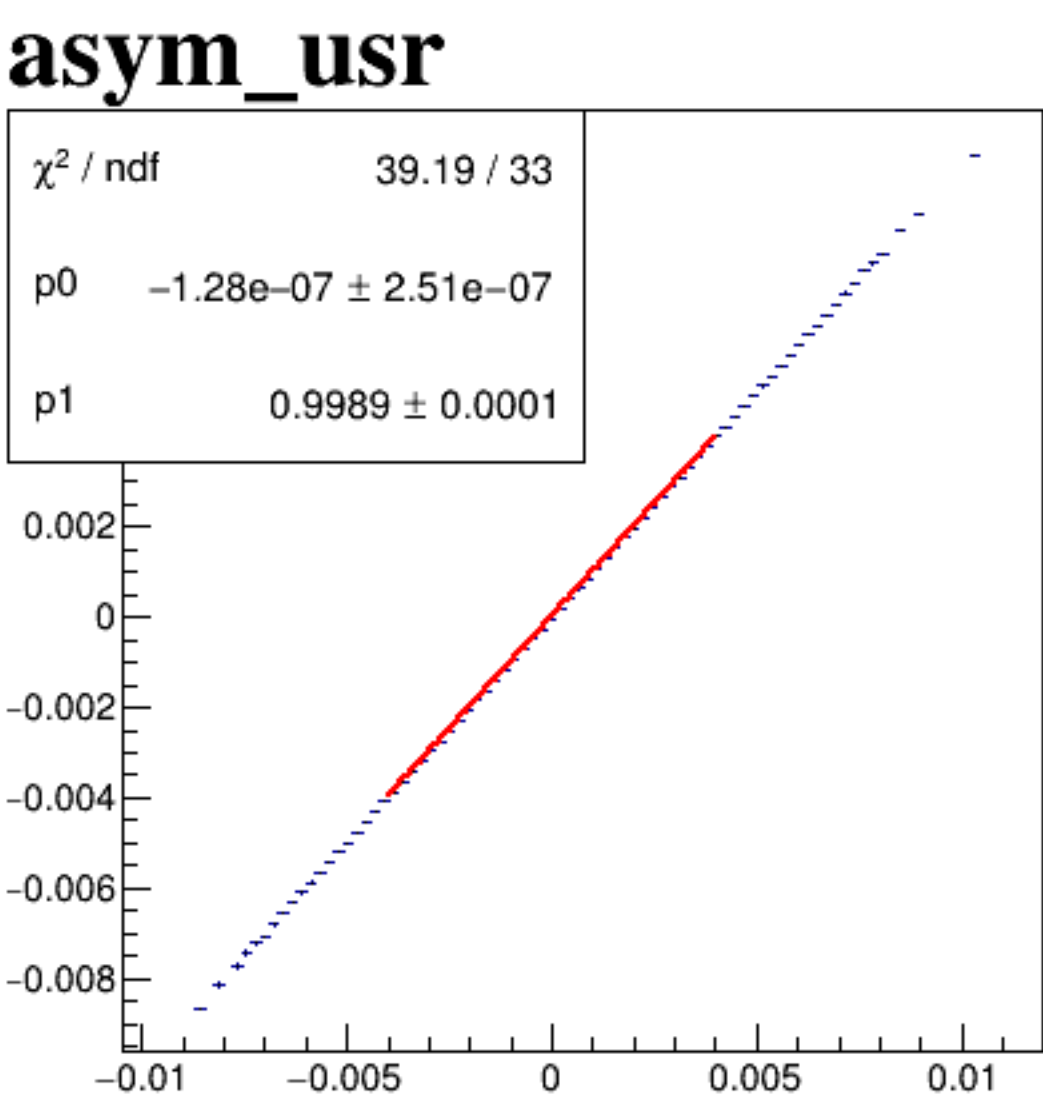
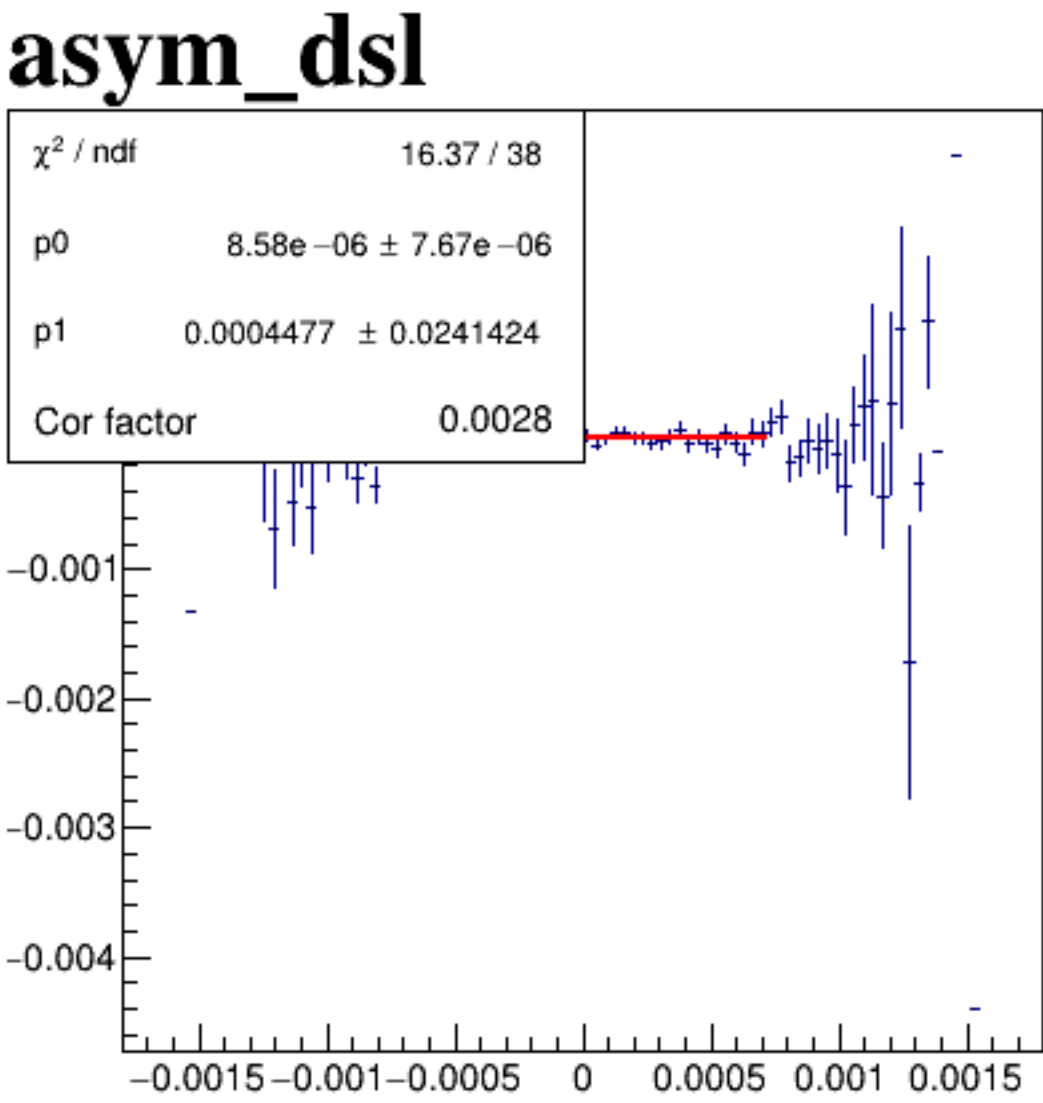
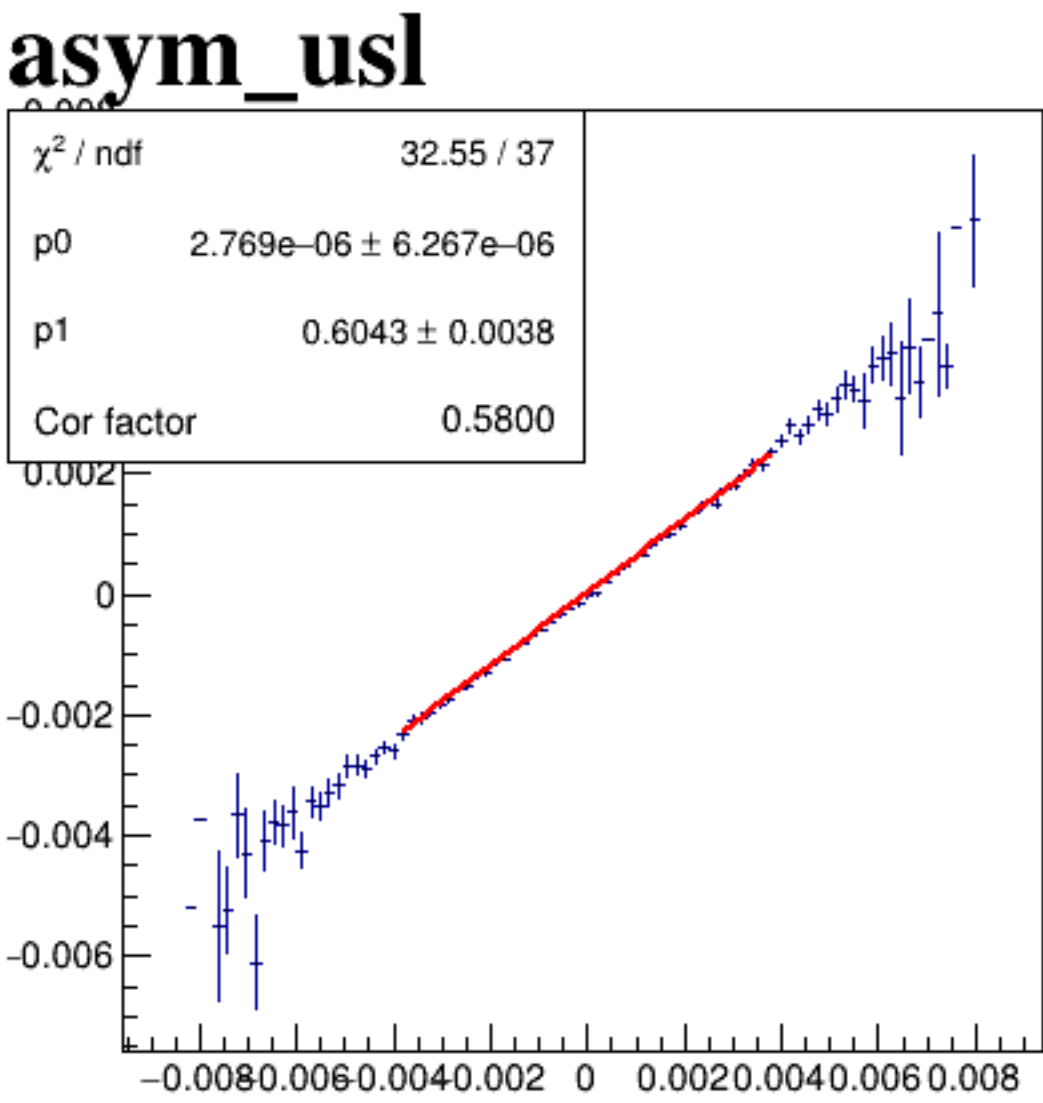
asym_usl



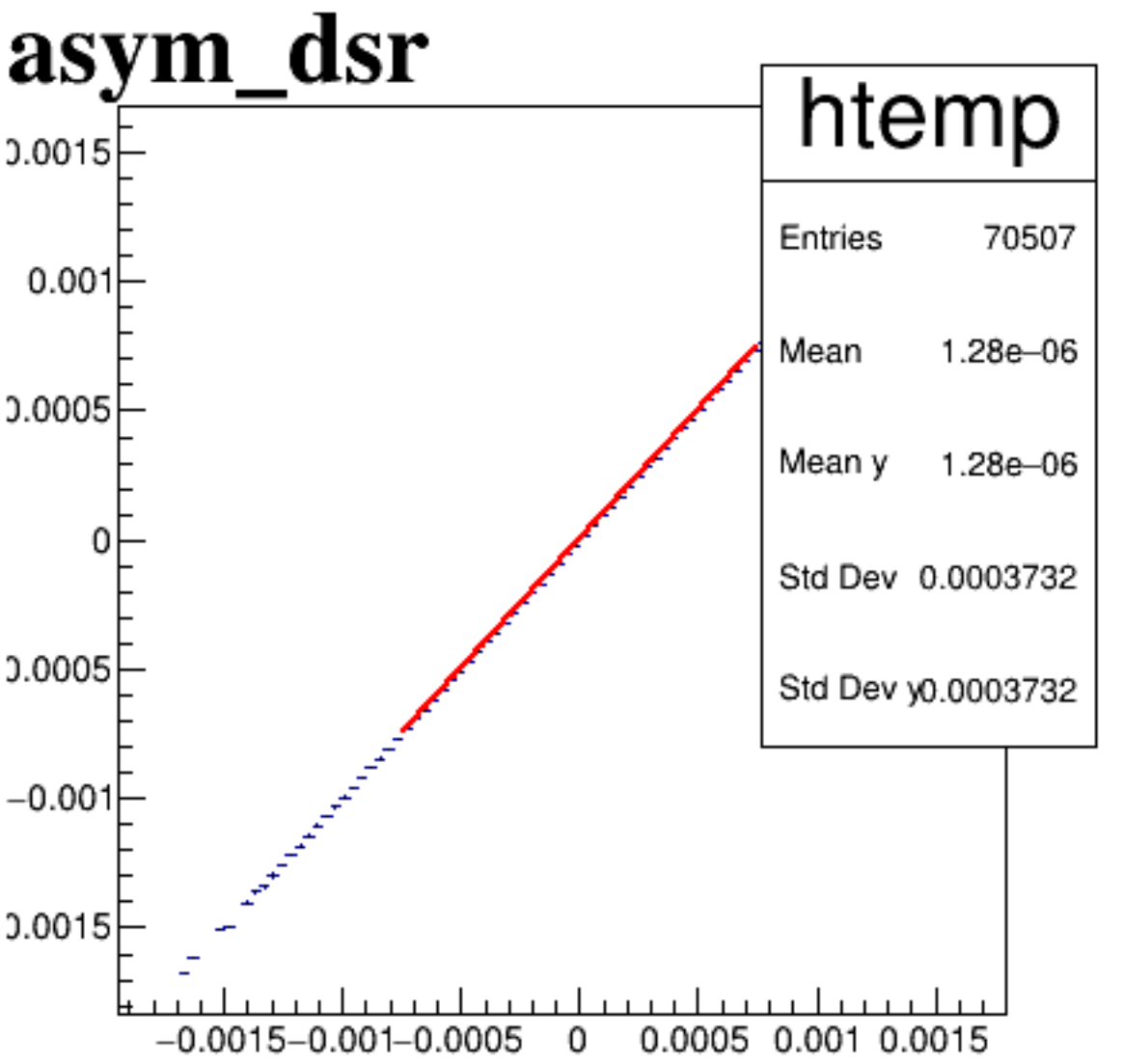
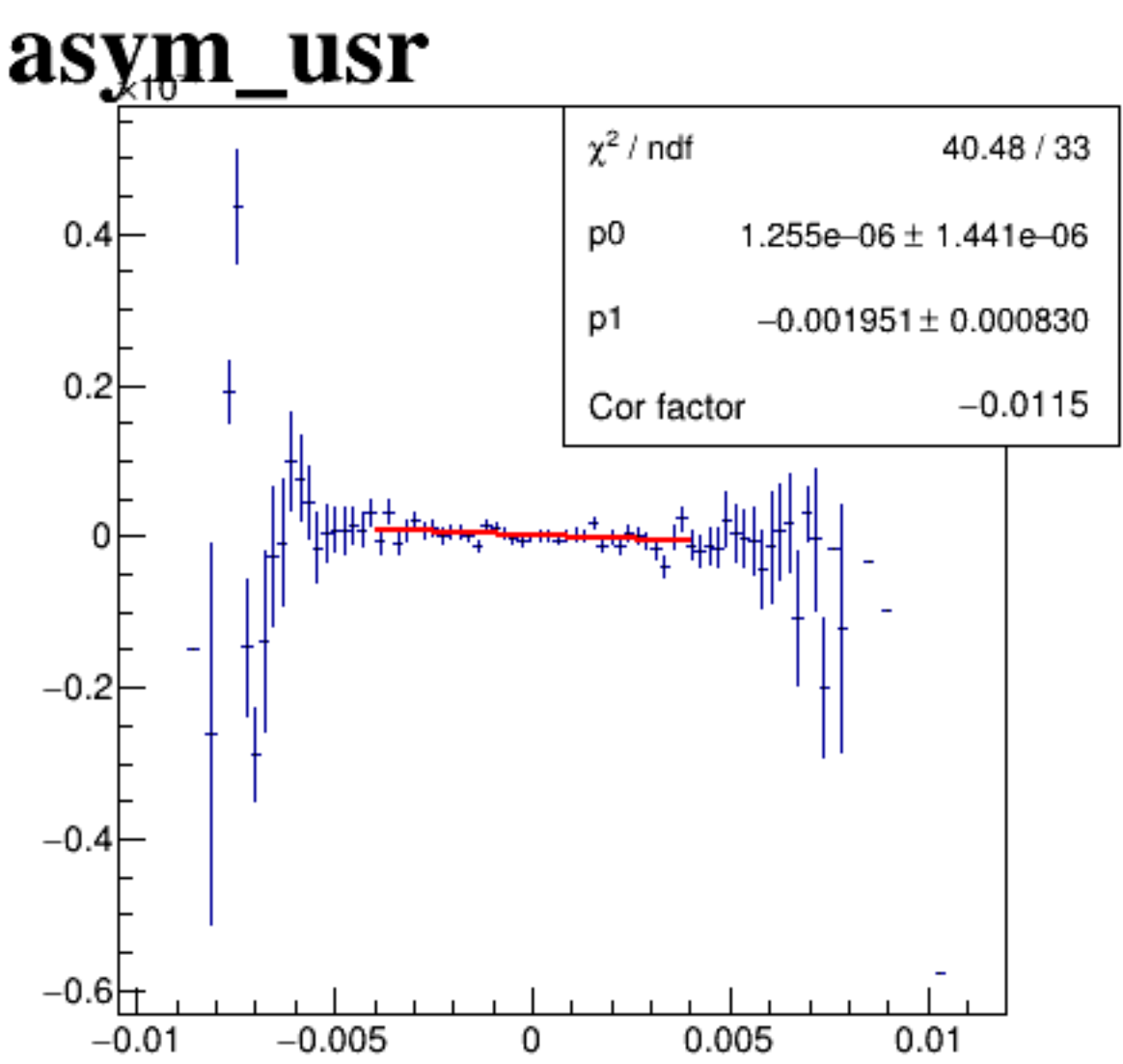
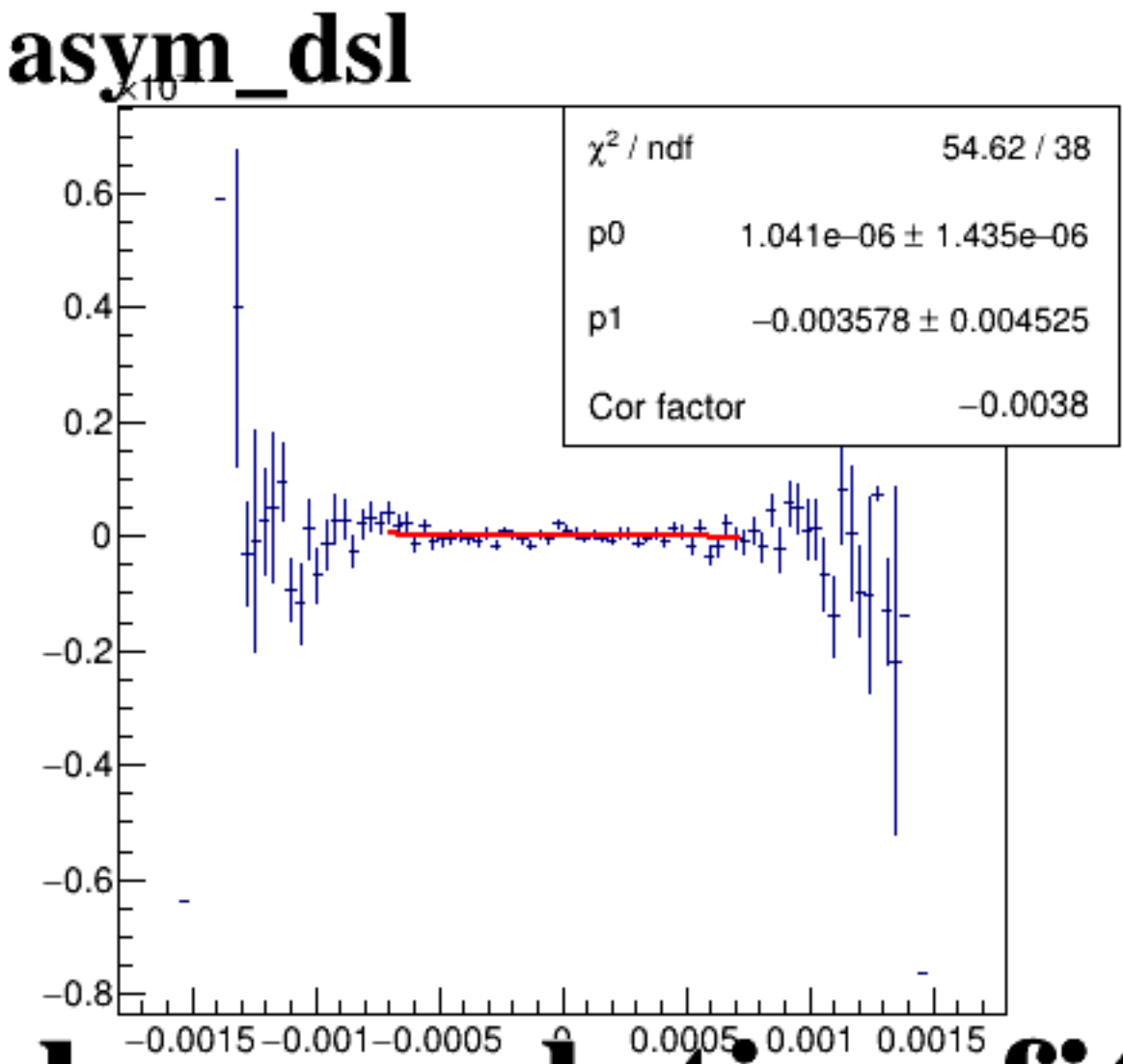
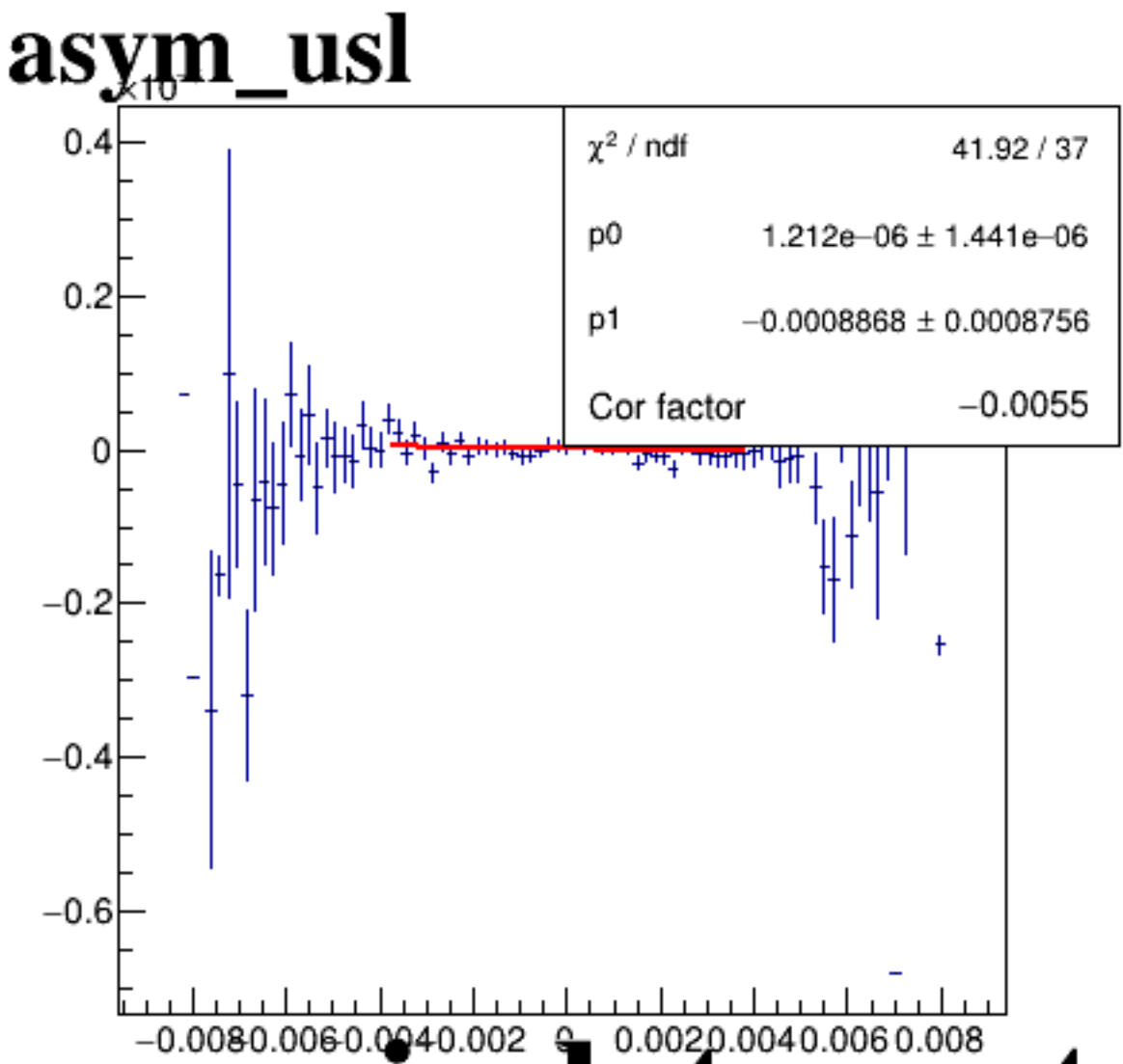
asym_dsl



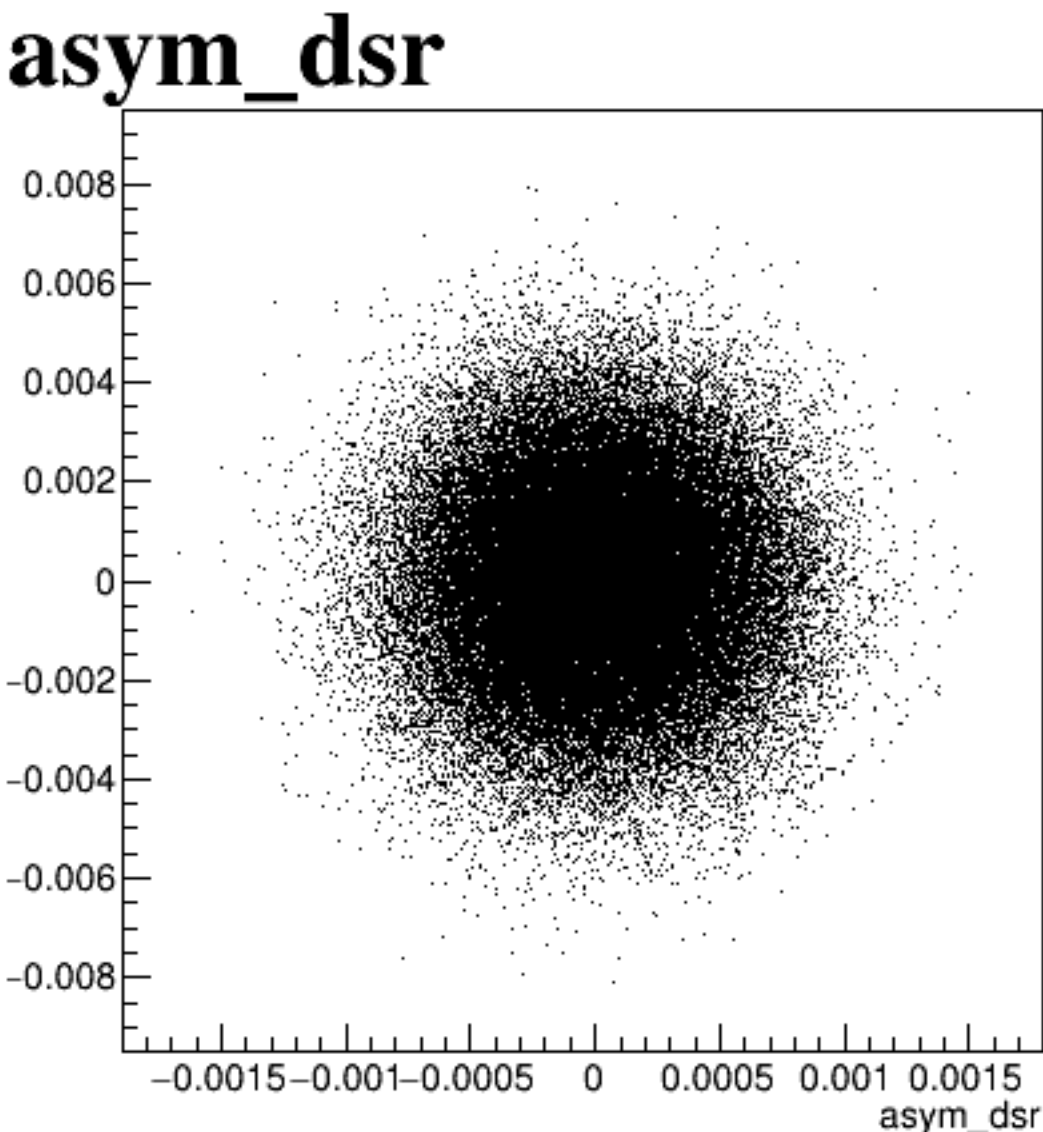
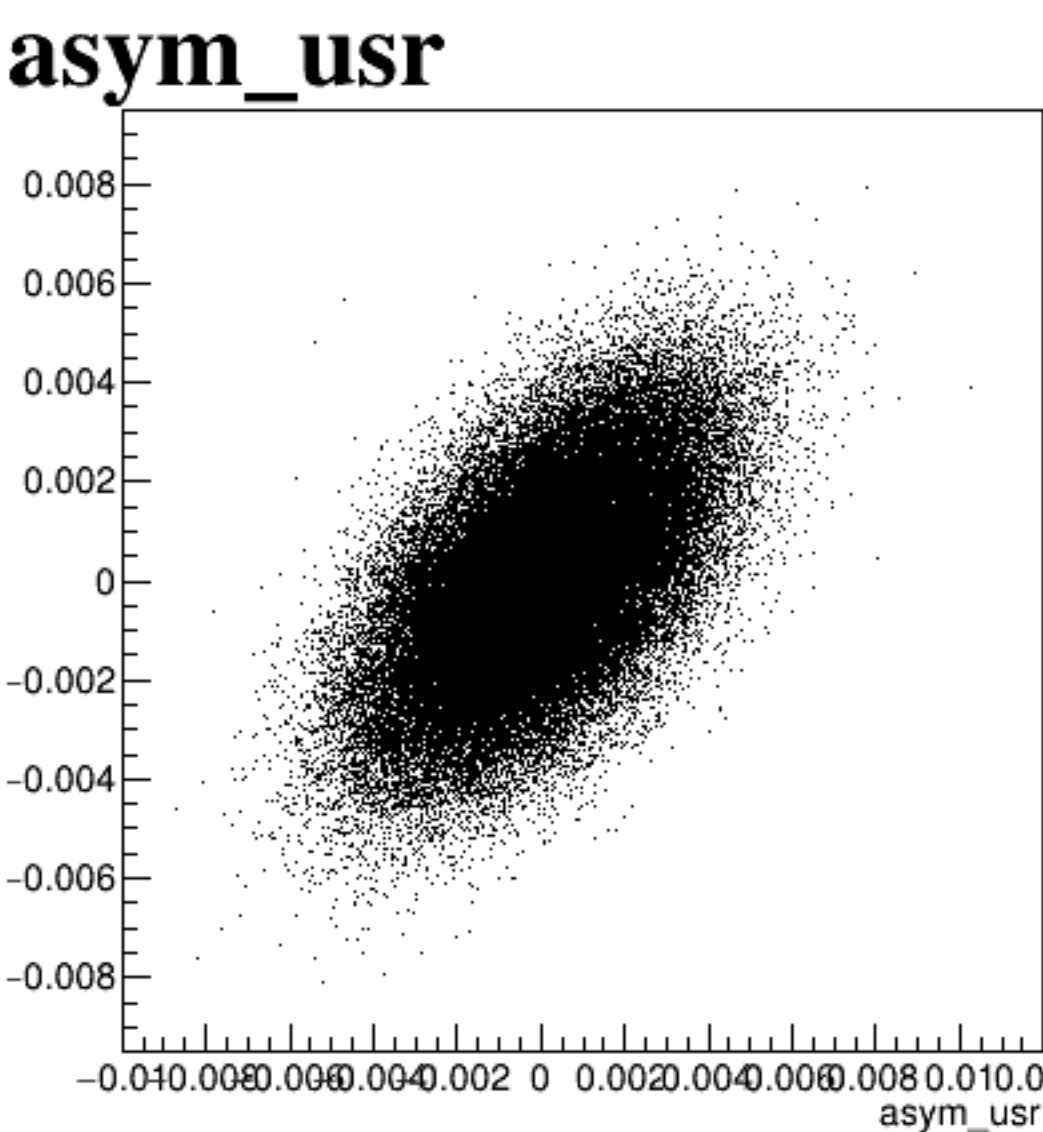
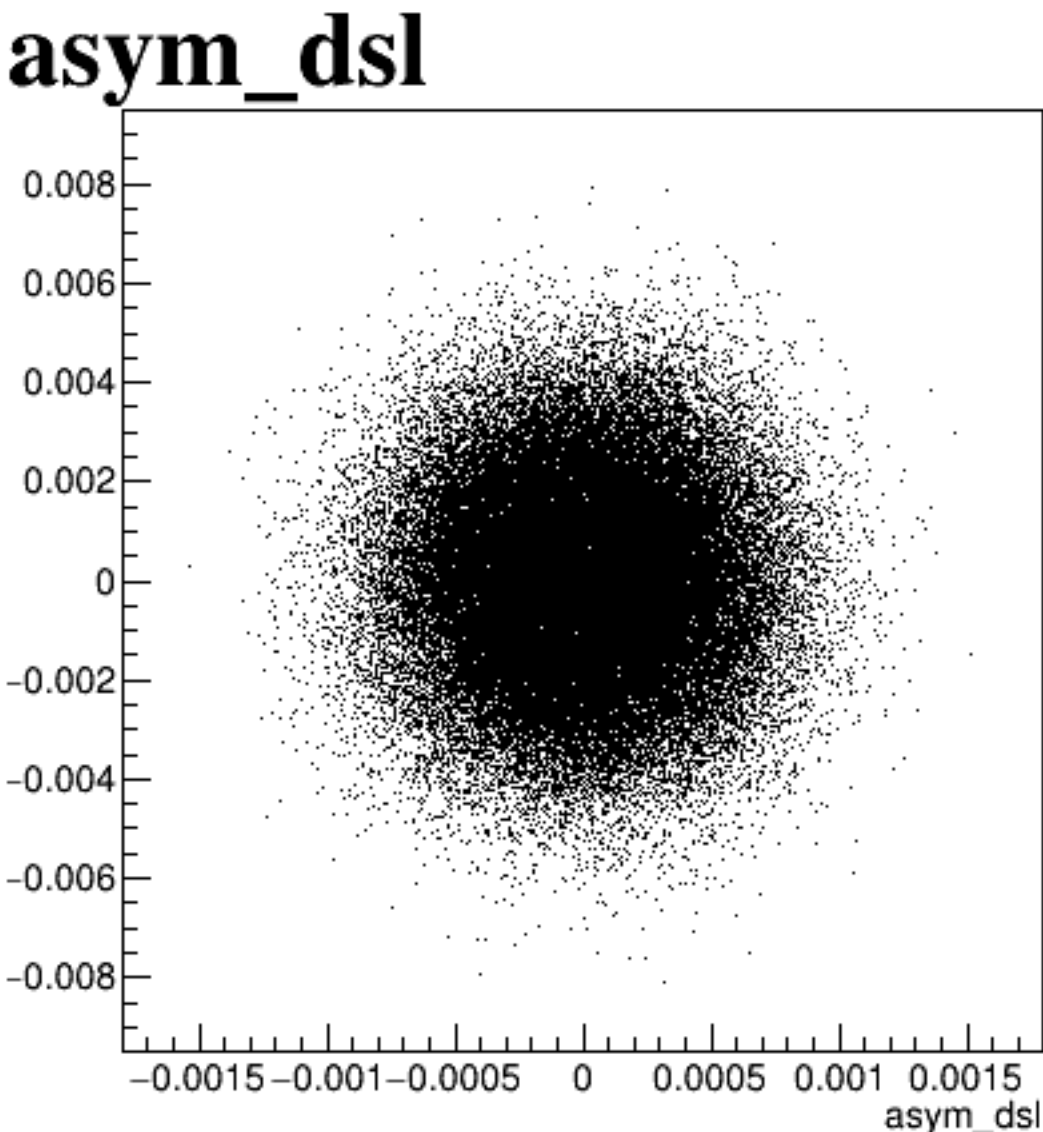
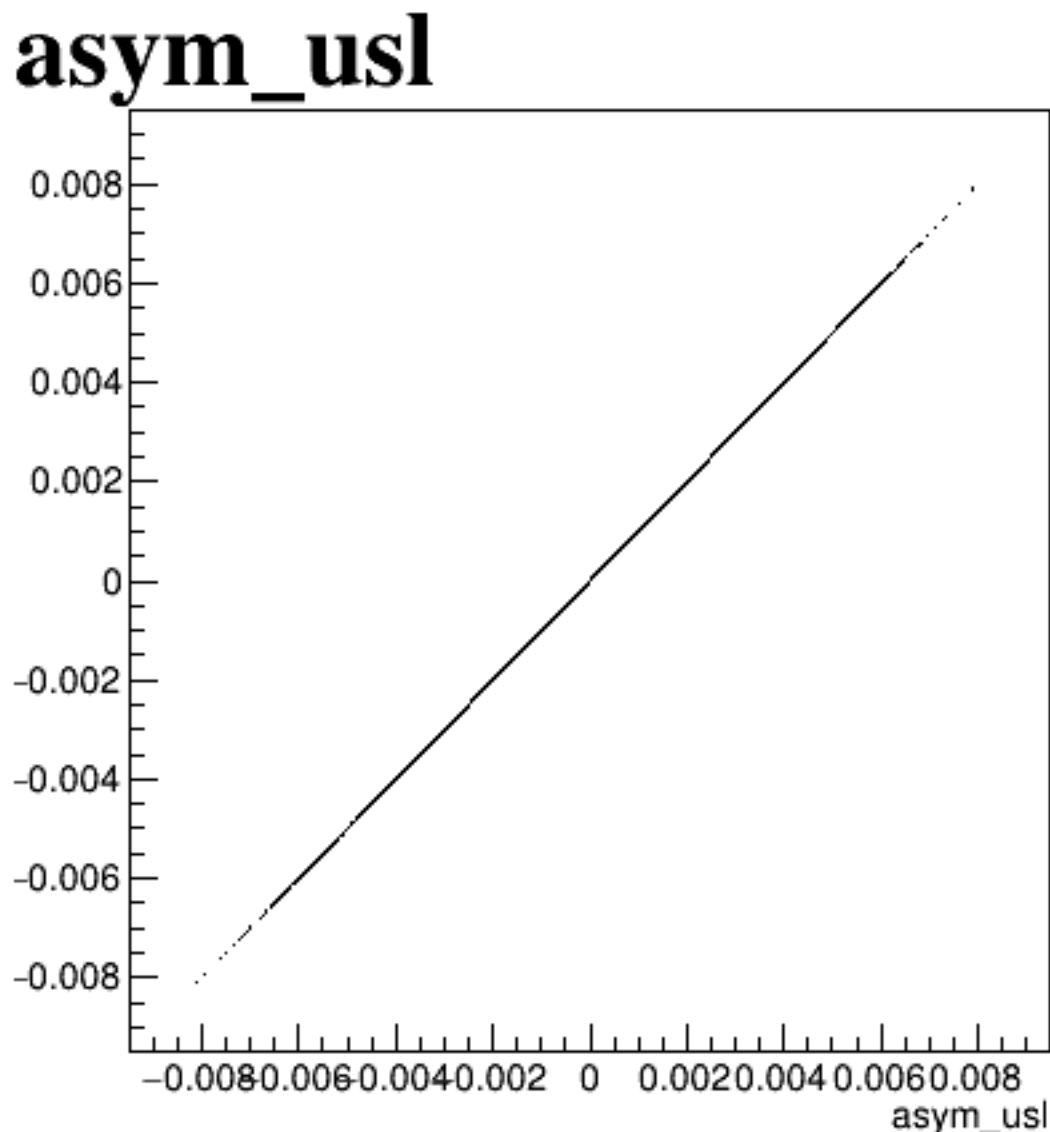
asym_usr



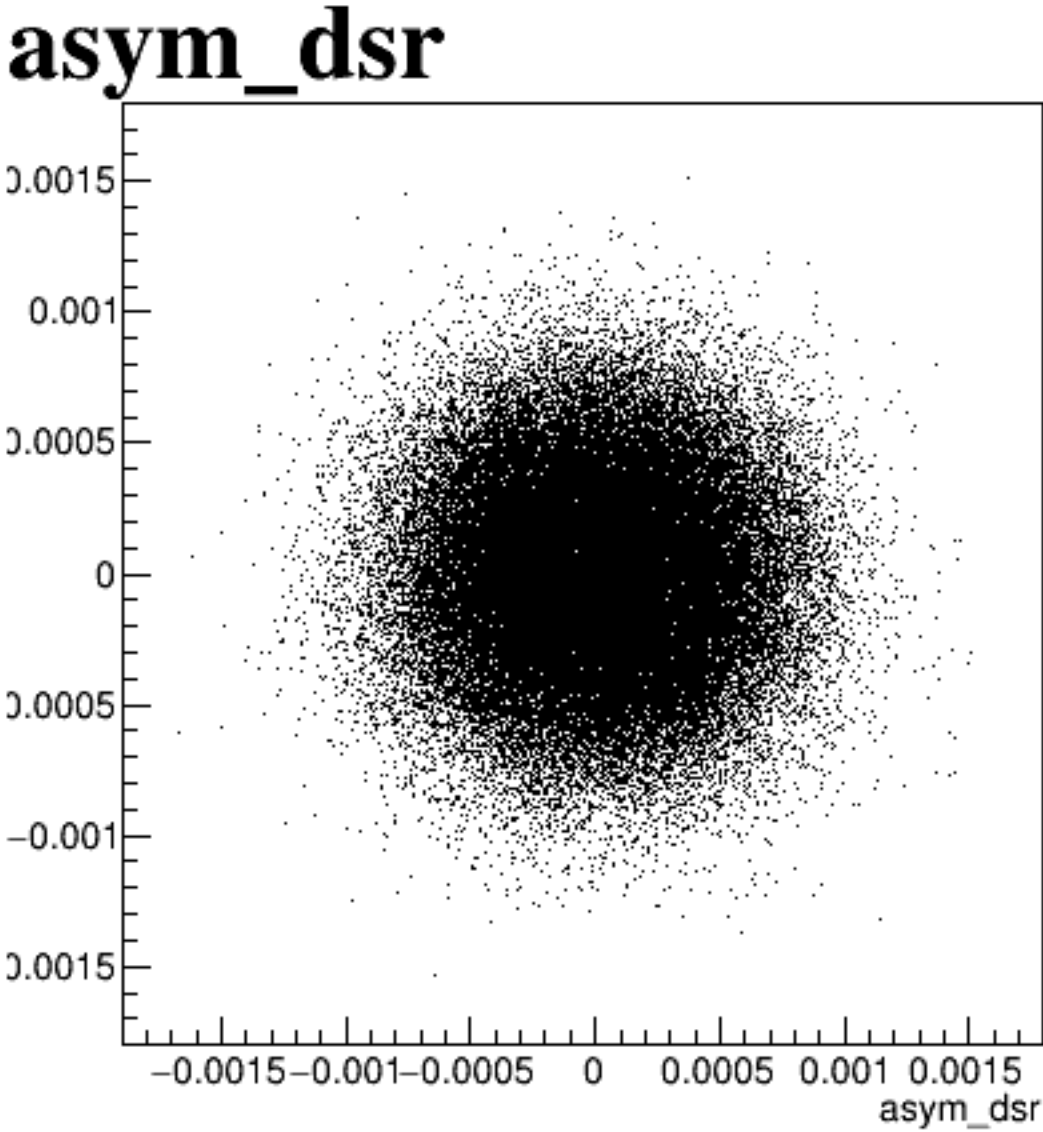
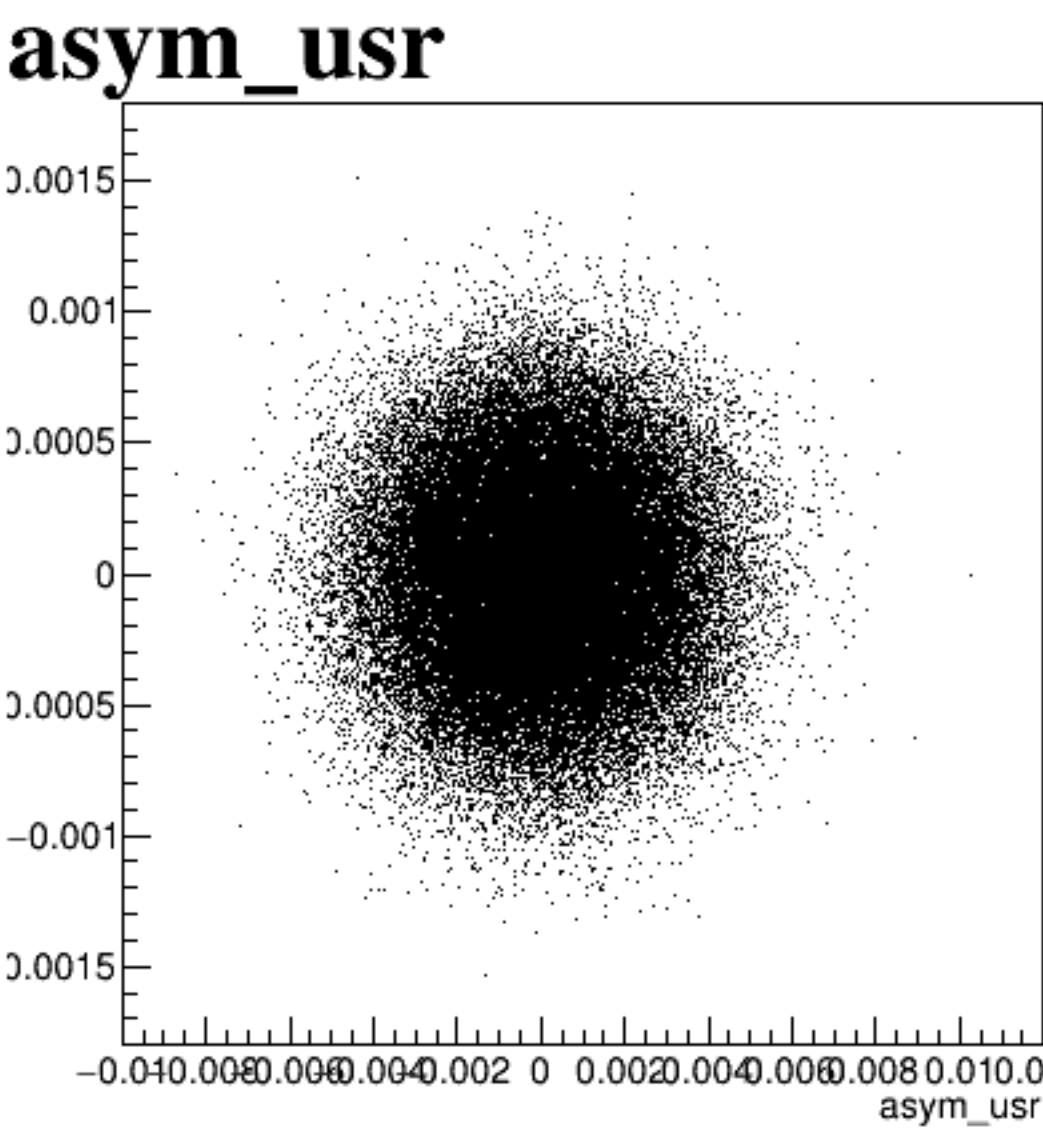
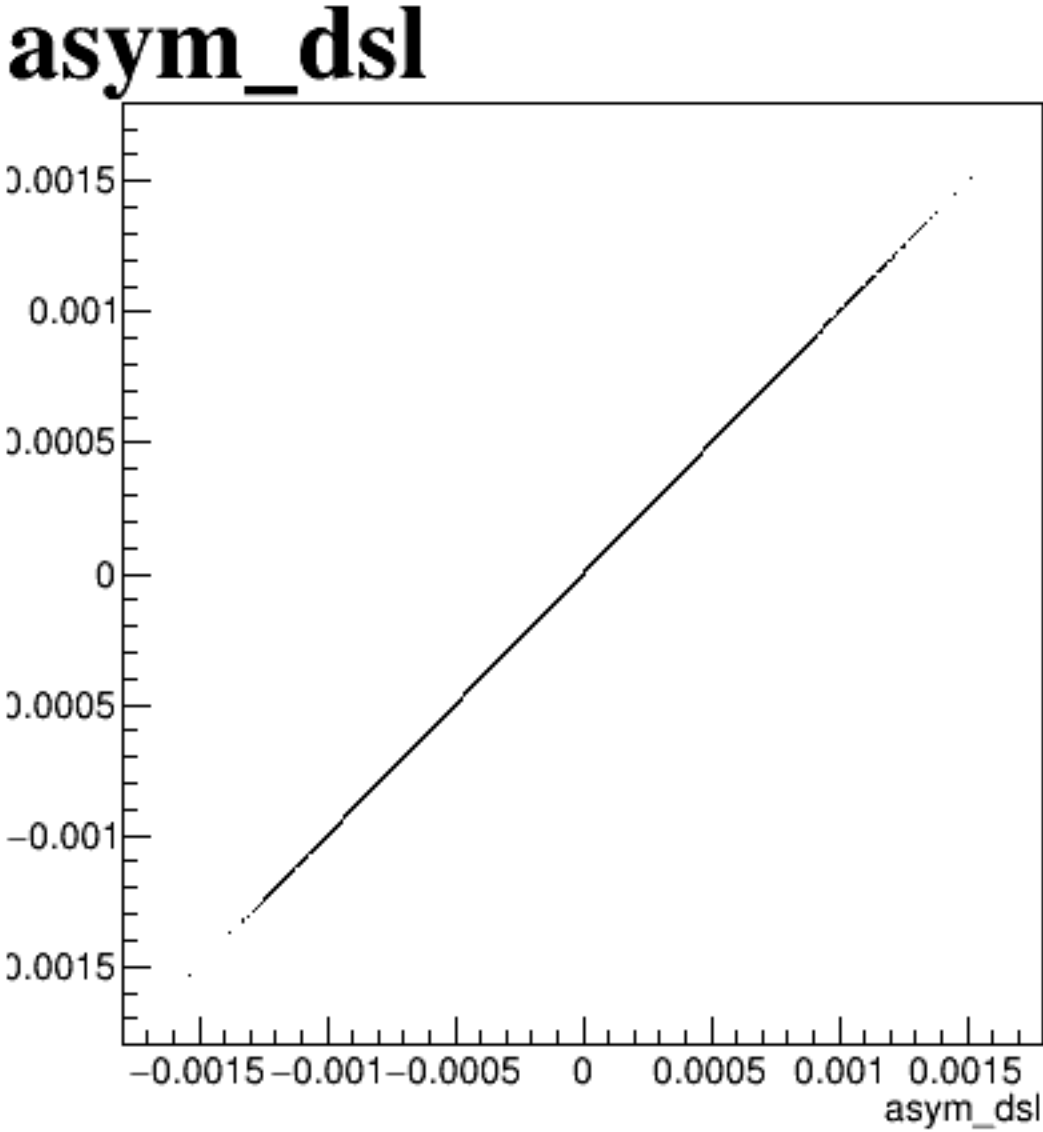
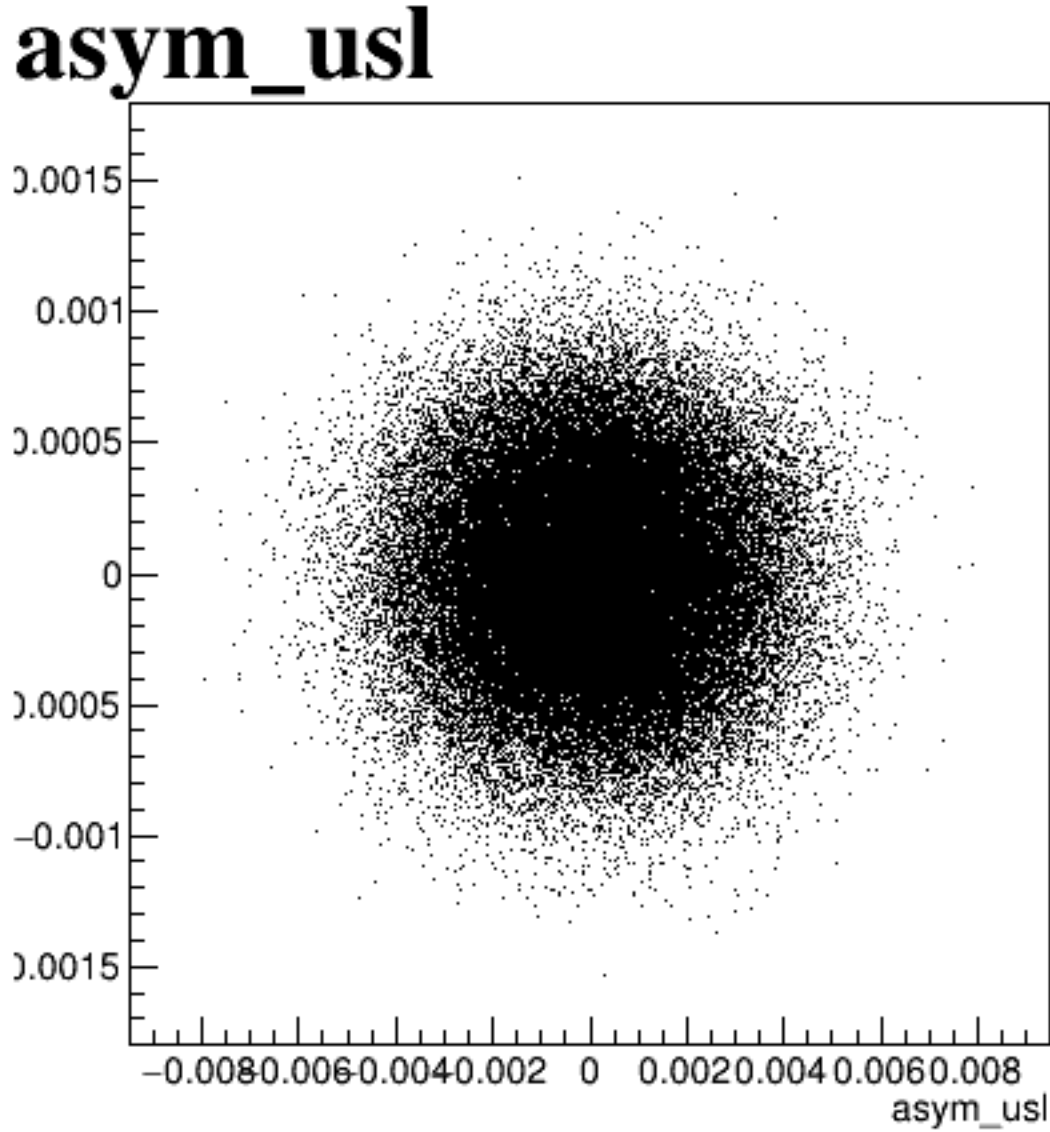
asym_dsr



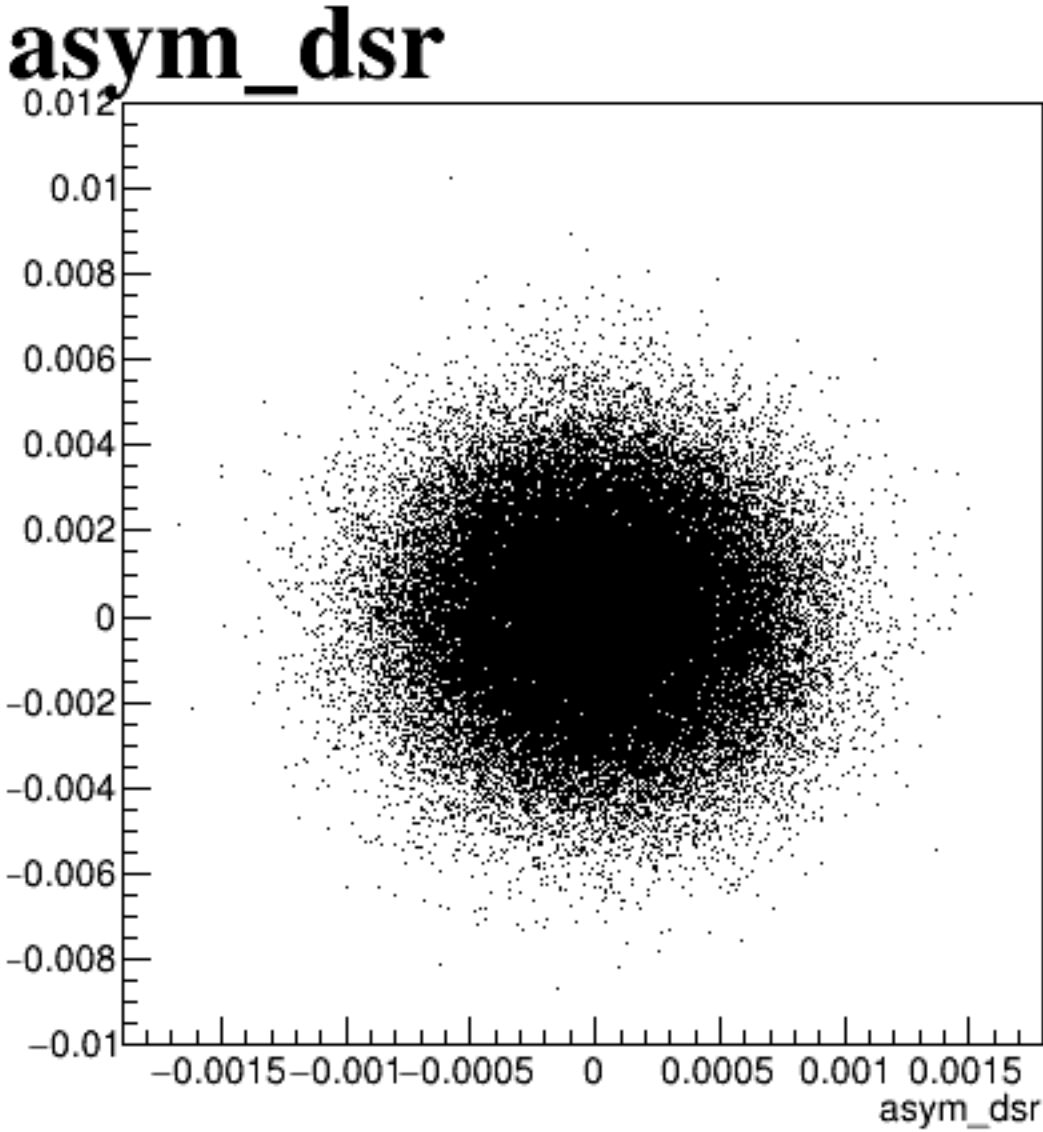
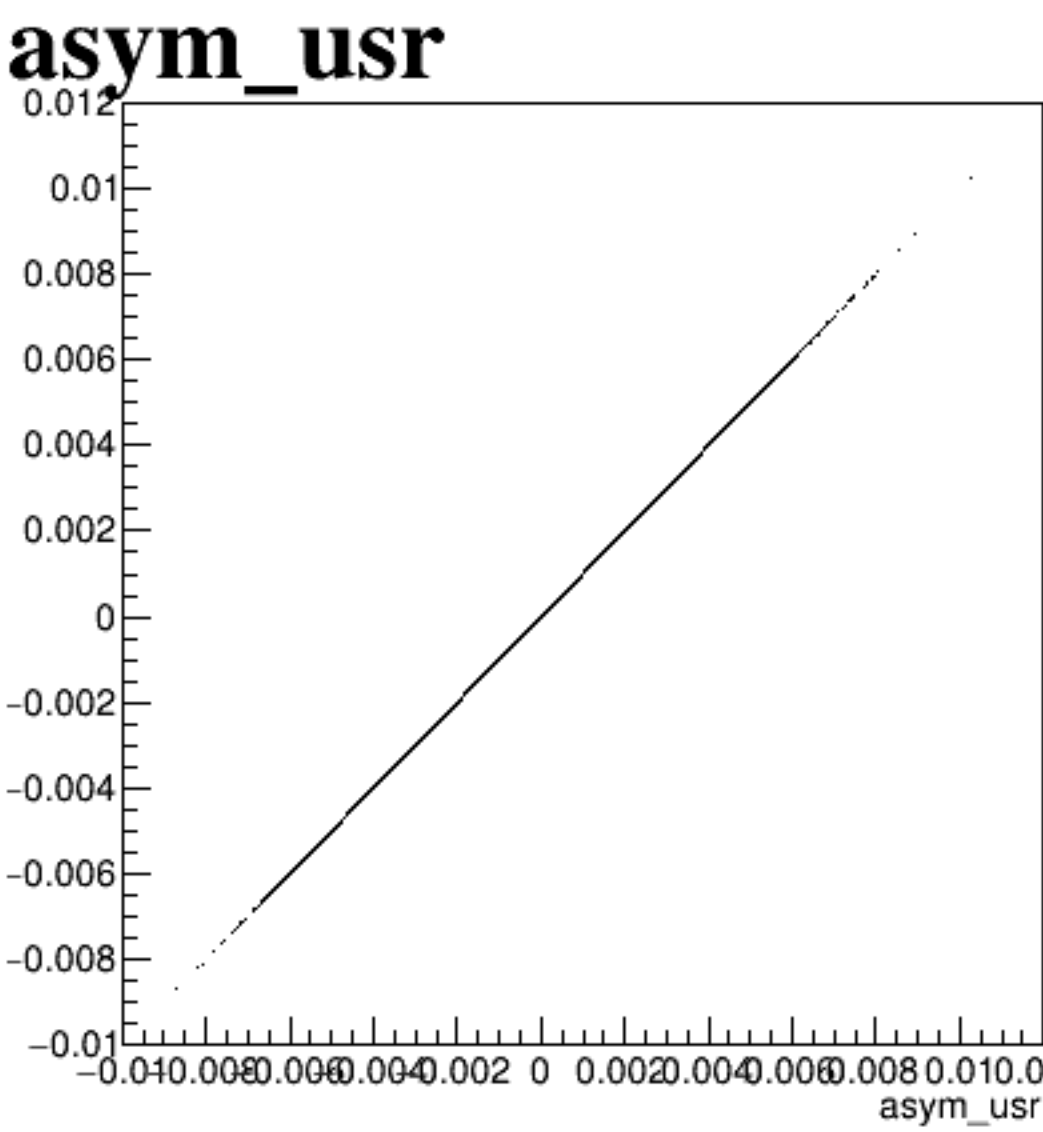
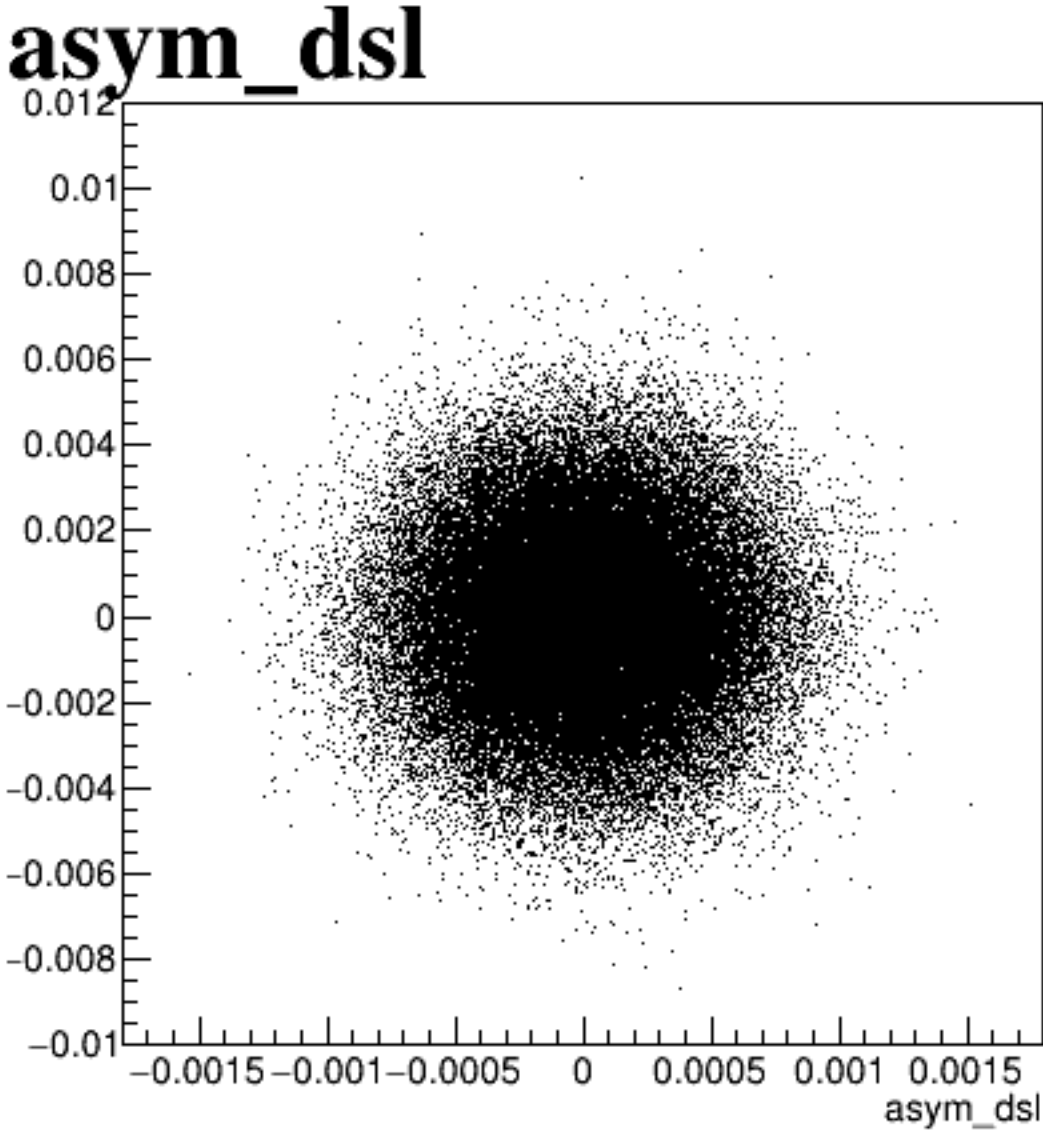
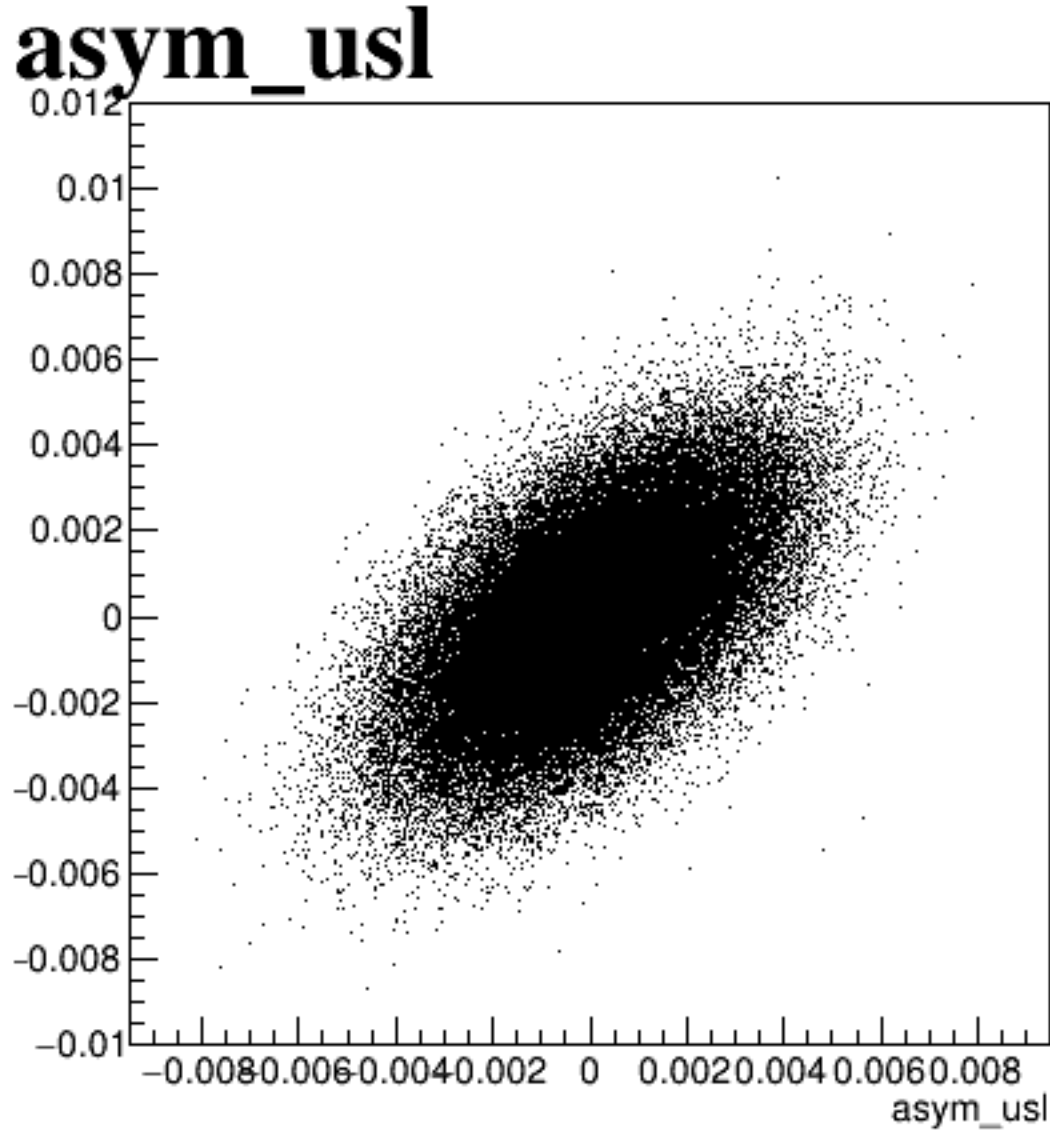
asym_usl



asym_dsl



asym_usr



asym_dsr

