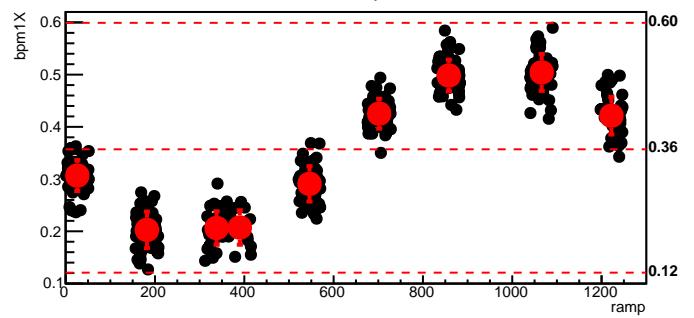
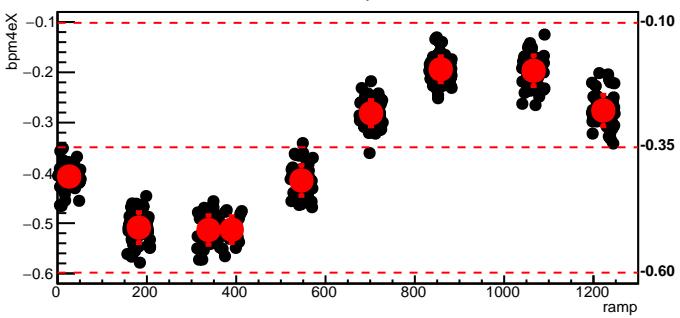


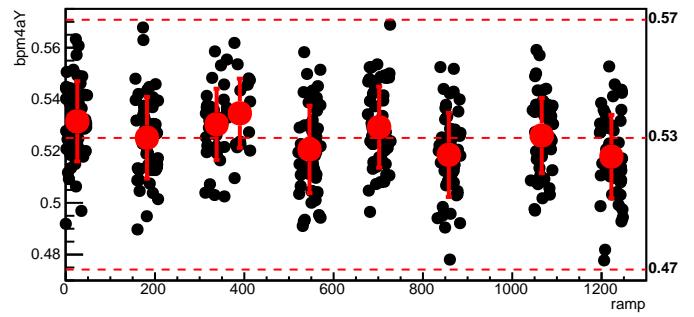
bmod Trim1,cyc=8558



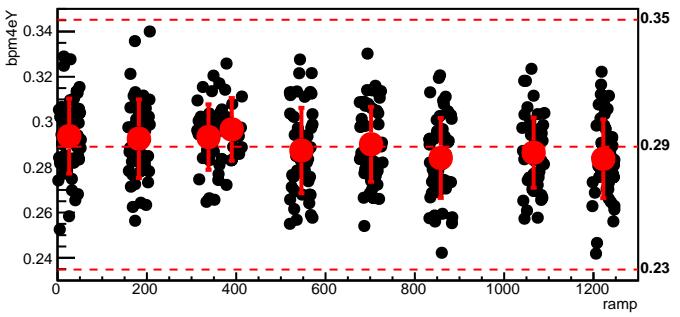
bmod Trim1,cyc=8558



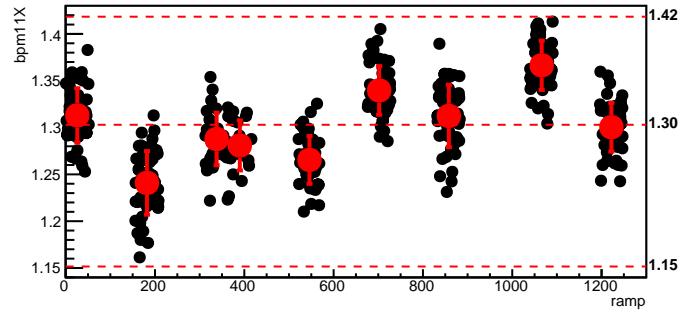
bmod Trim1,cyc=8558



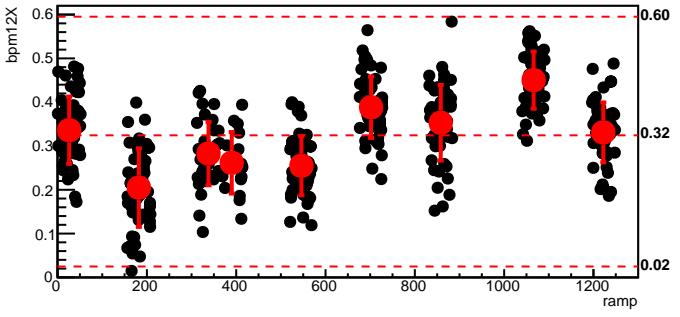
bmod Trim1,cyc=8558



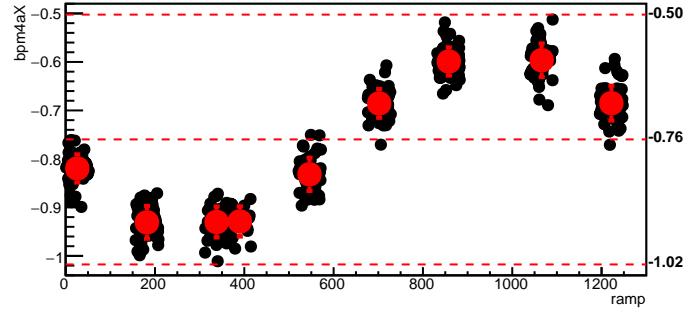
bmod Trim1,cyc=8558



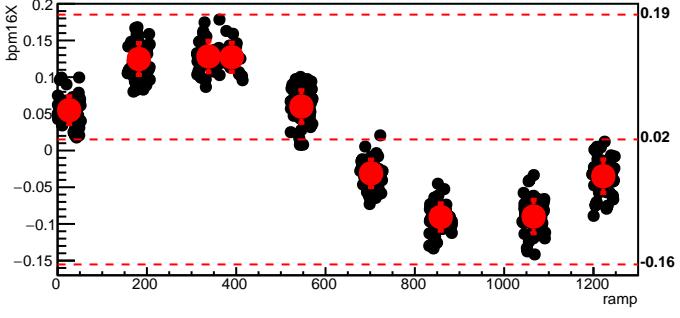
bmod Trim1,cyc=8558



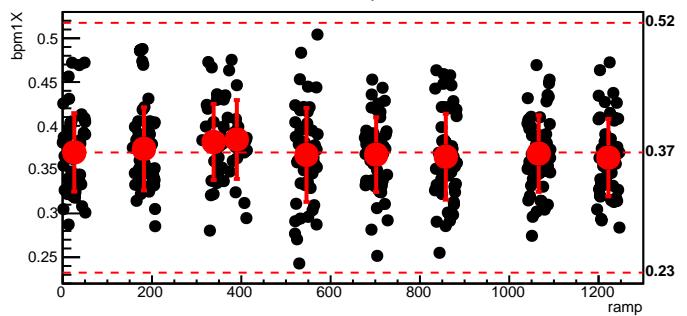
bmod Trim1,cyc=8558



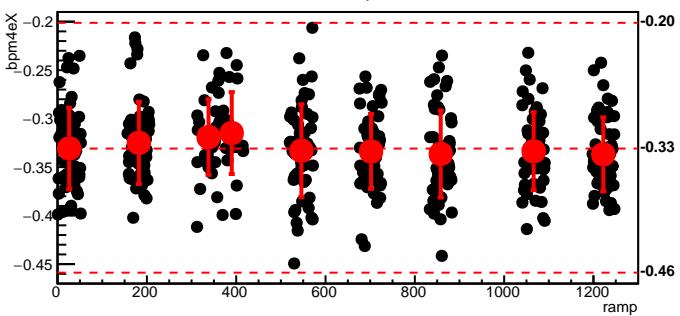
bmod Trim1,cyc=8558



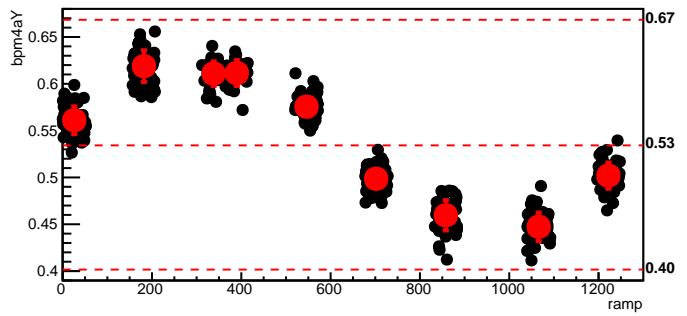
bmod Trim2,cyc=8558



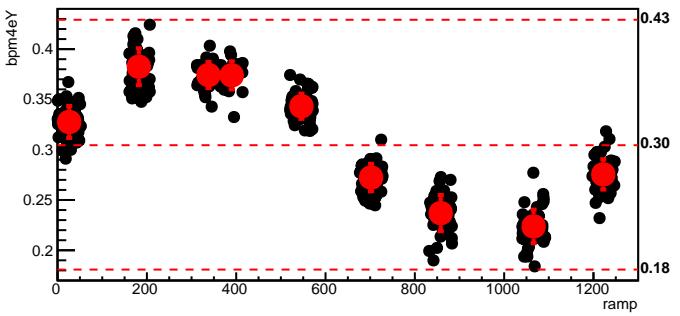
bmod Trim2,cyc=8558



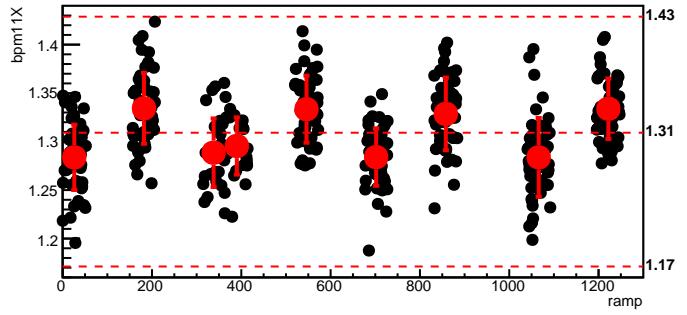
bmod Trim2,cyc=8558



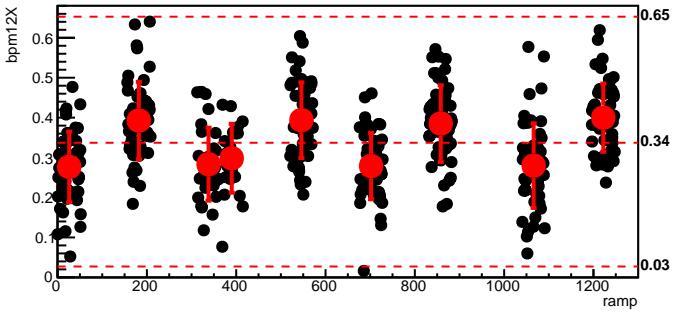
bmod Trim2,cyc=8558



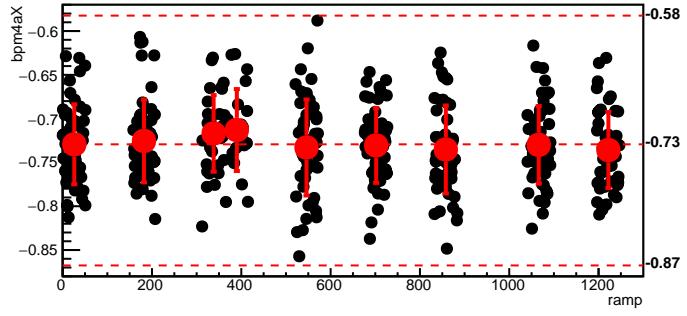
bmod Trim2,cyc=8558



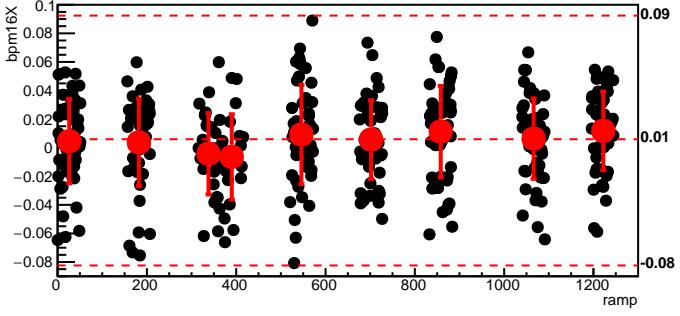
bmod Trim2,cyc=8558



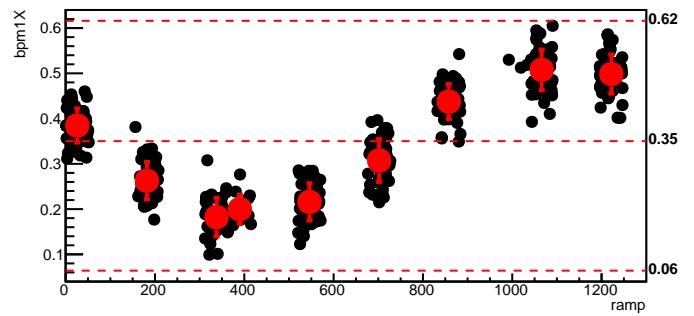
bmod Trim2,cyc=8558



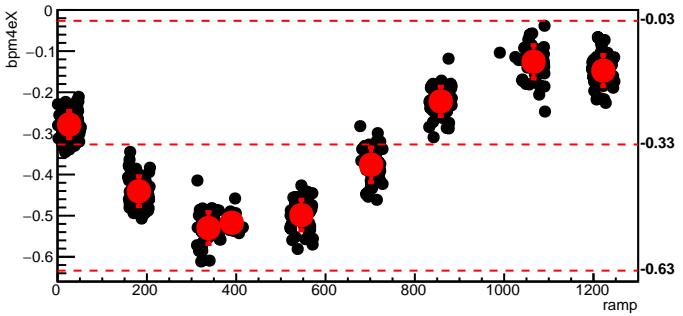
bmod Trim2,cyc=8558



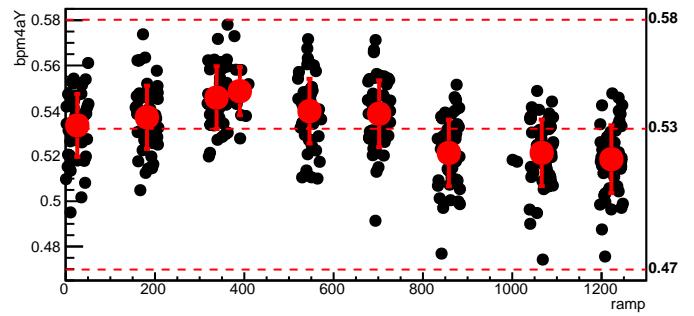
bmod Trim3,cyc=8558



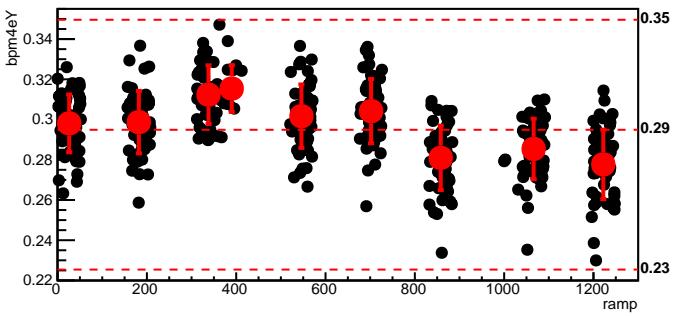
bmod Trim3,cyc=8558



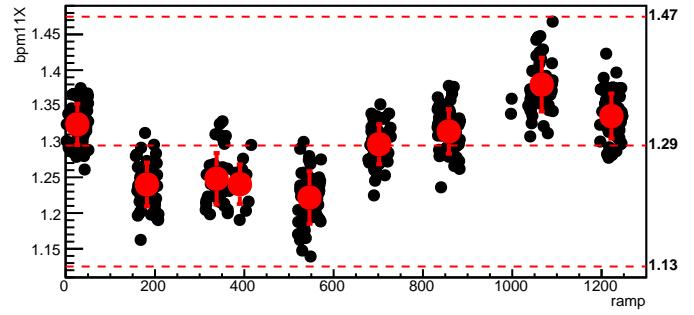
bmod Trim3,cyc=8558



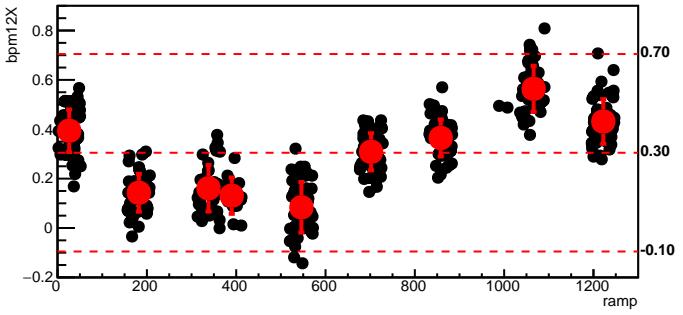
bmod Trim3,cyc=8558



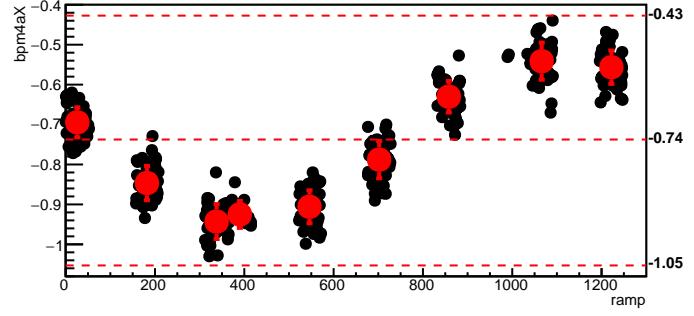
bmod Trim3,cyc=8558



bmod Trim3,cyc=8558



bmod Trim3,cyc=8558



bmod Trim3,cyc=8558

