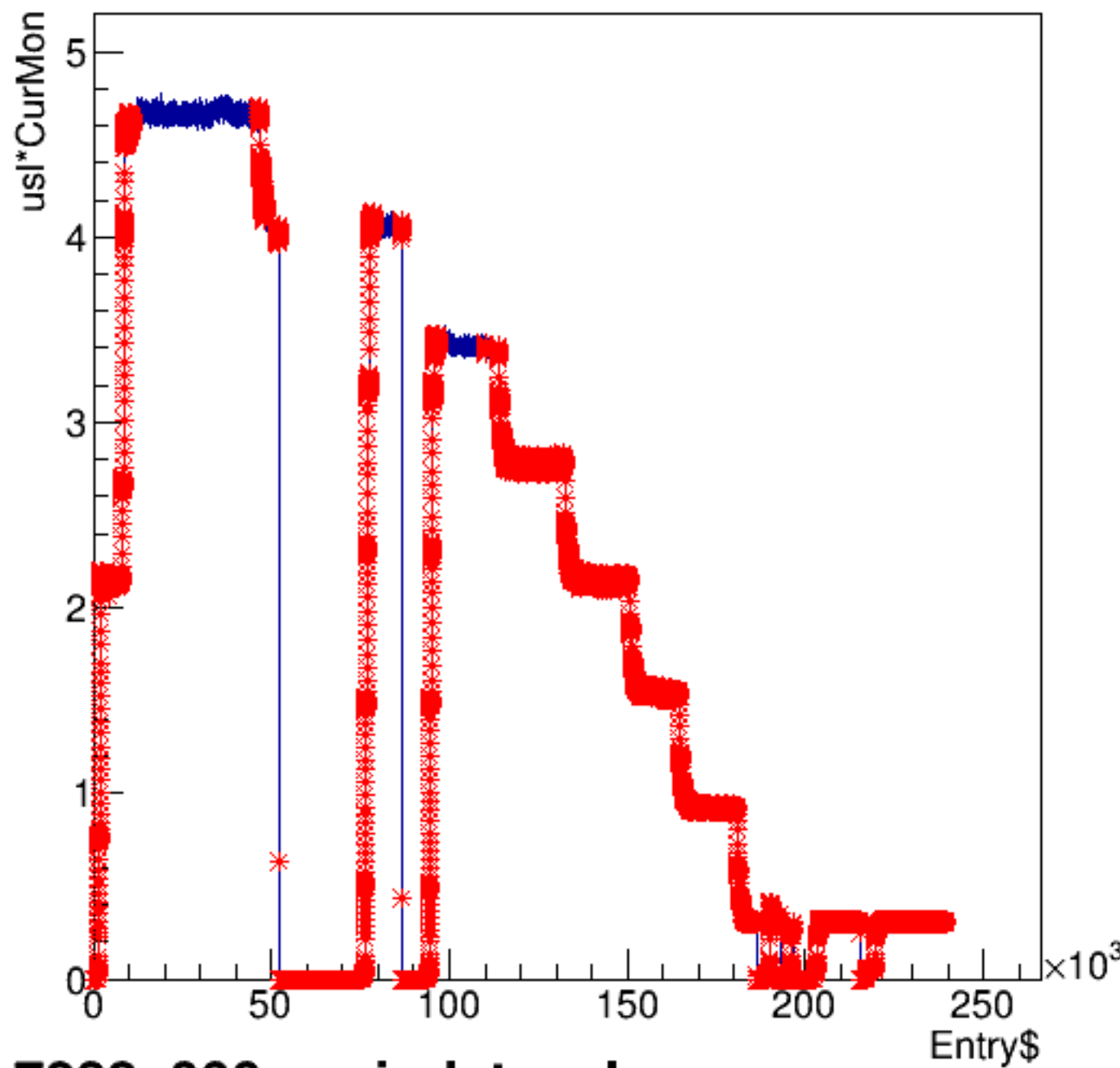
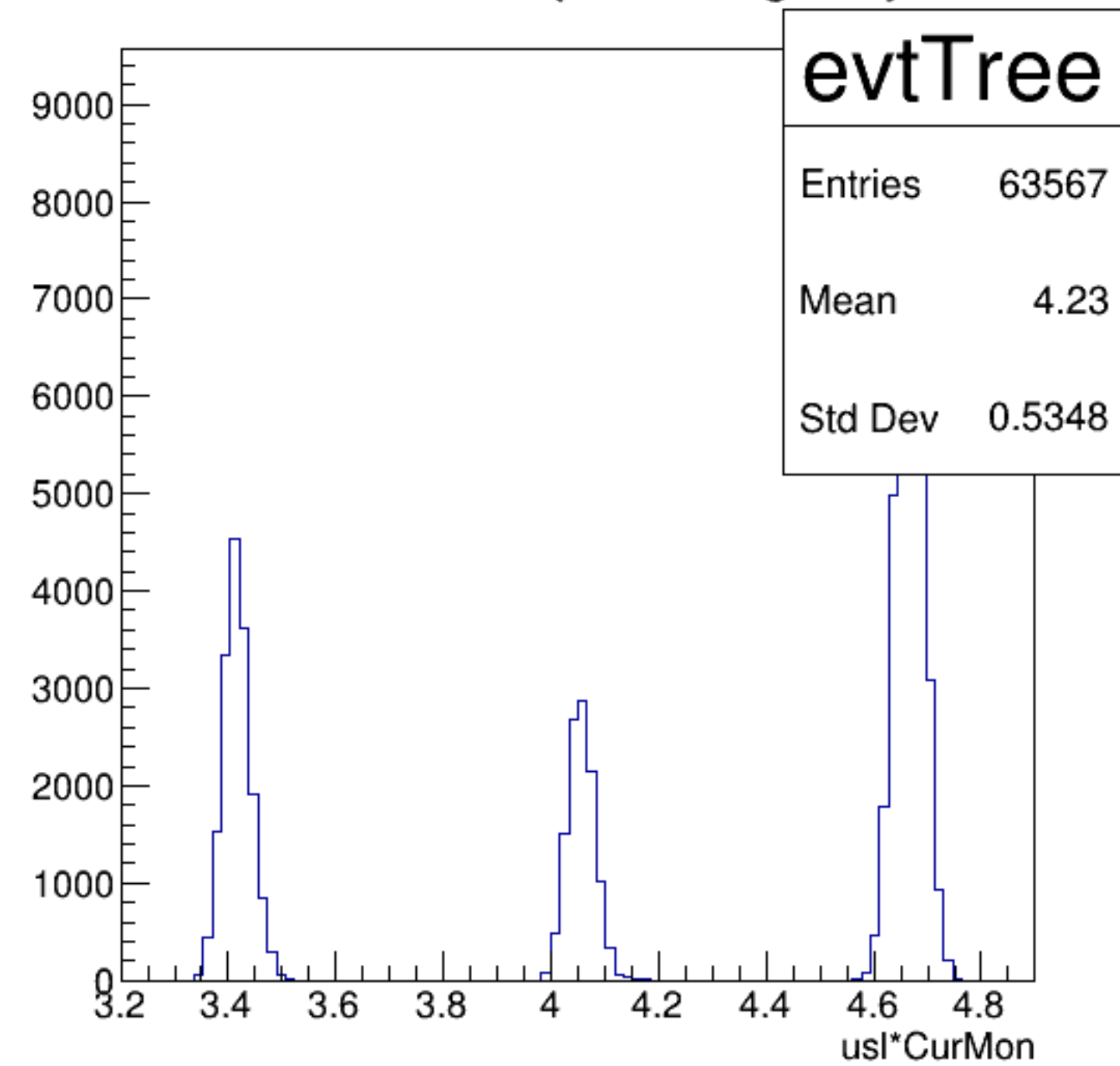


usl*CurMon:Entry\$

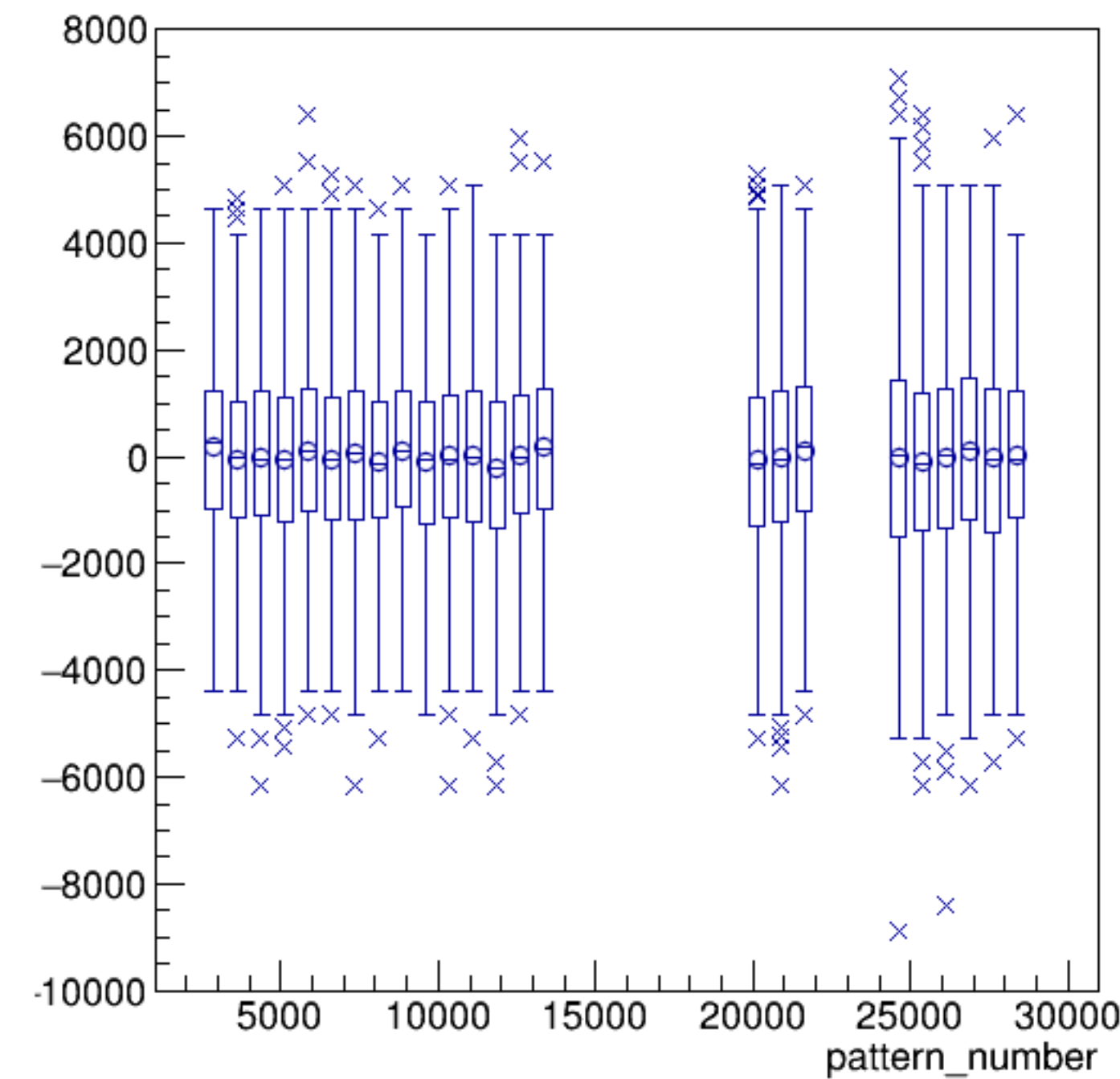


run7282_000_maindet_usl.png

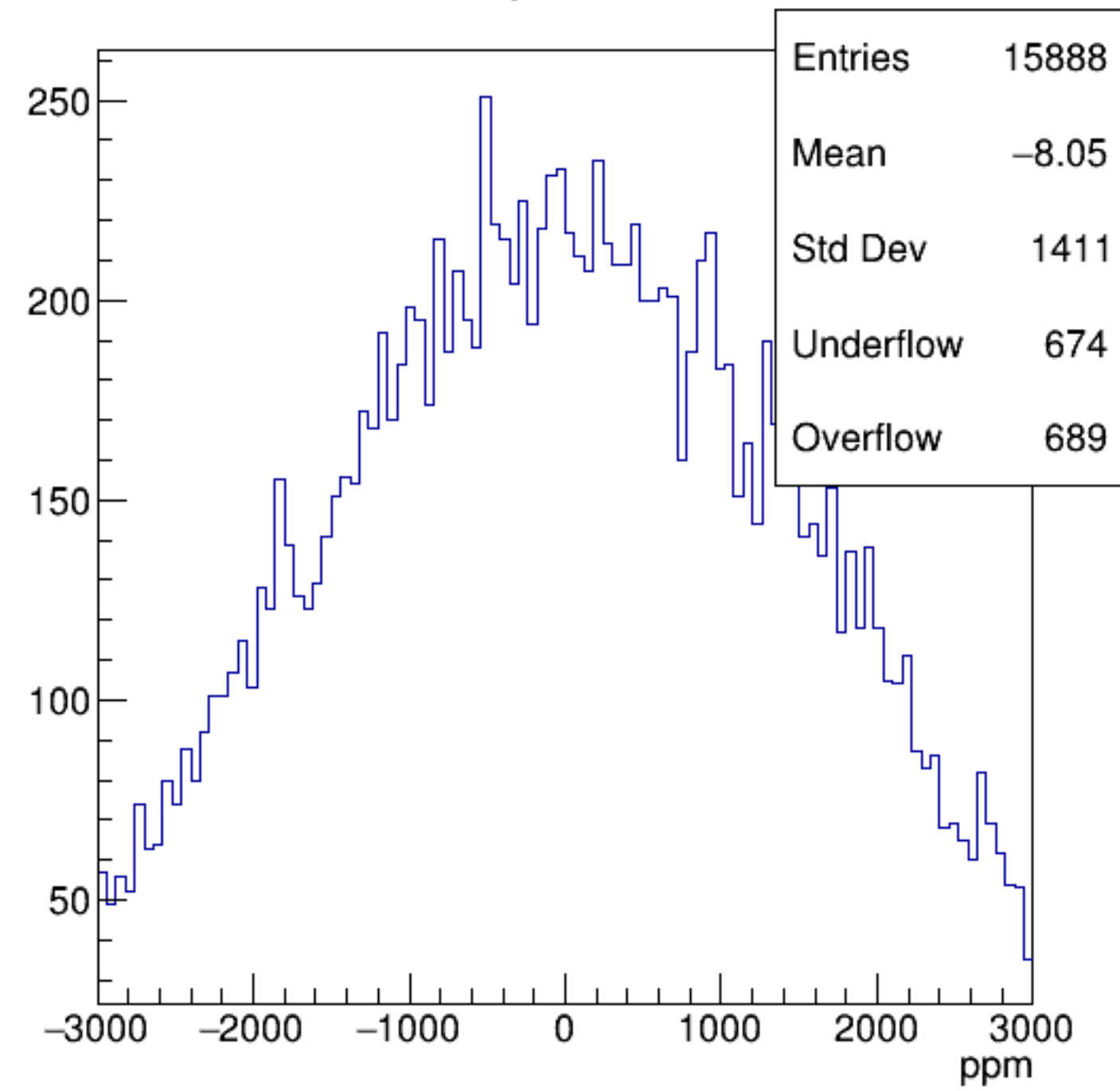
usl*CurMon {ErrorFlag==0}

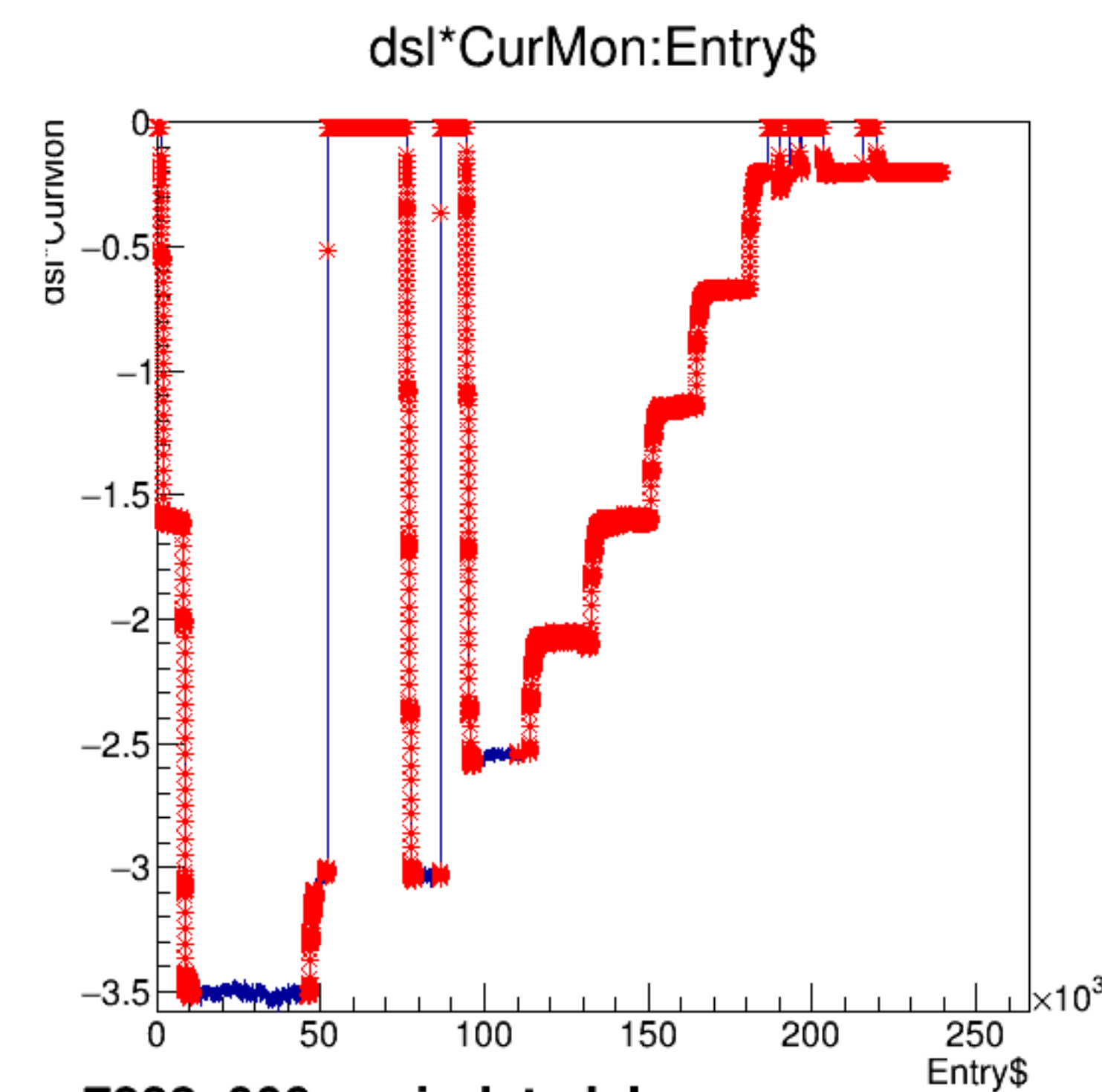


asym_usl/ppm:pattern_number {ErrorFlag==0}

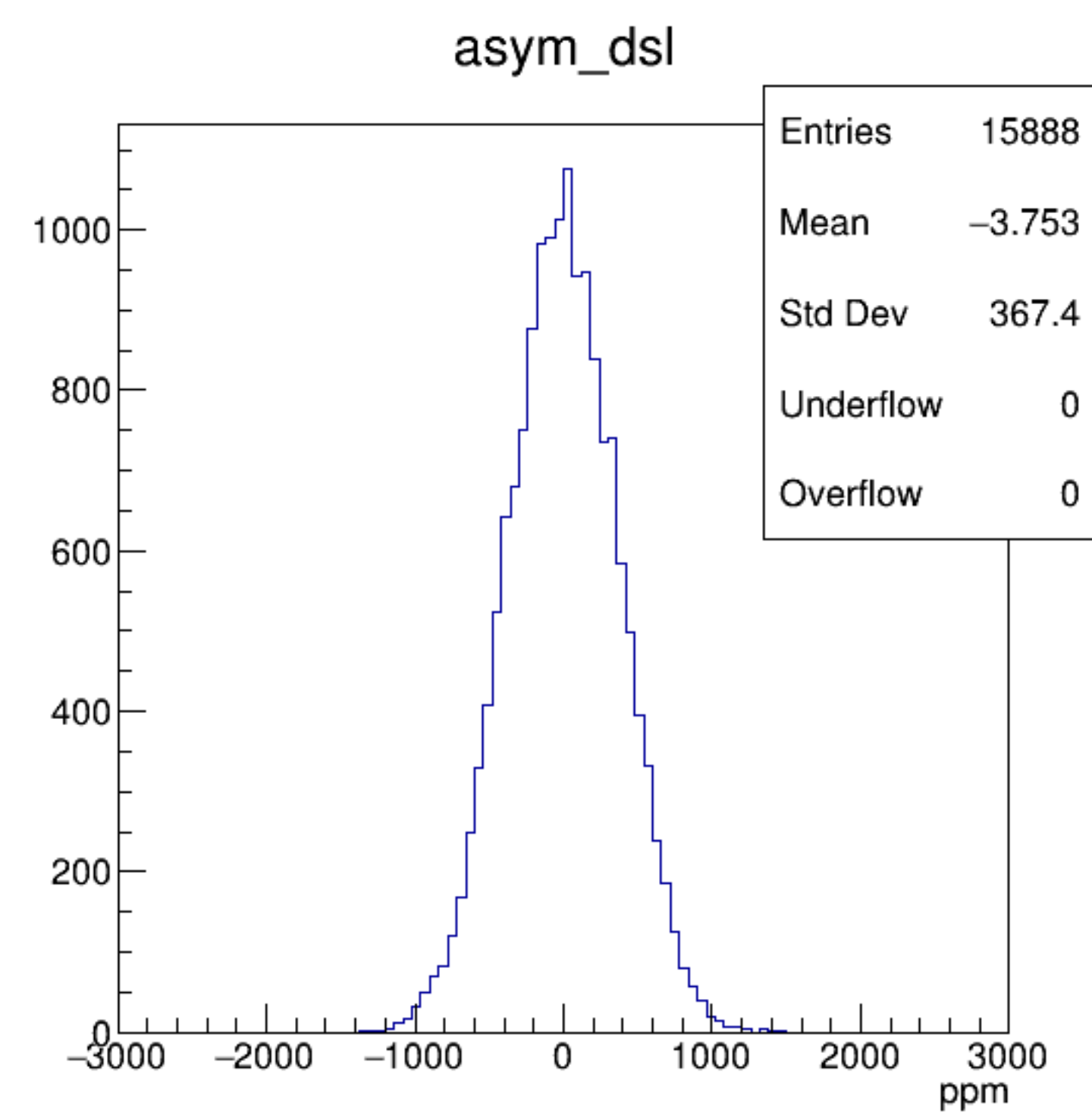
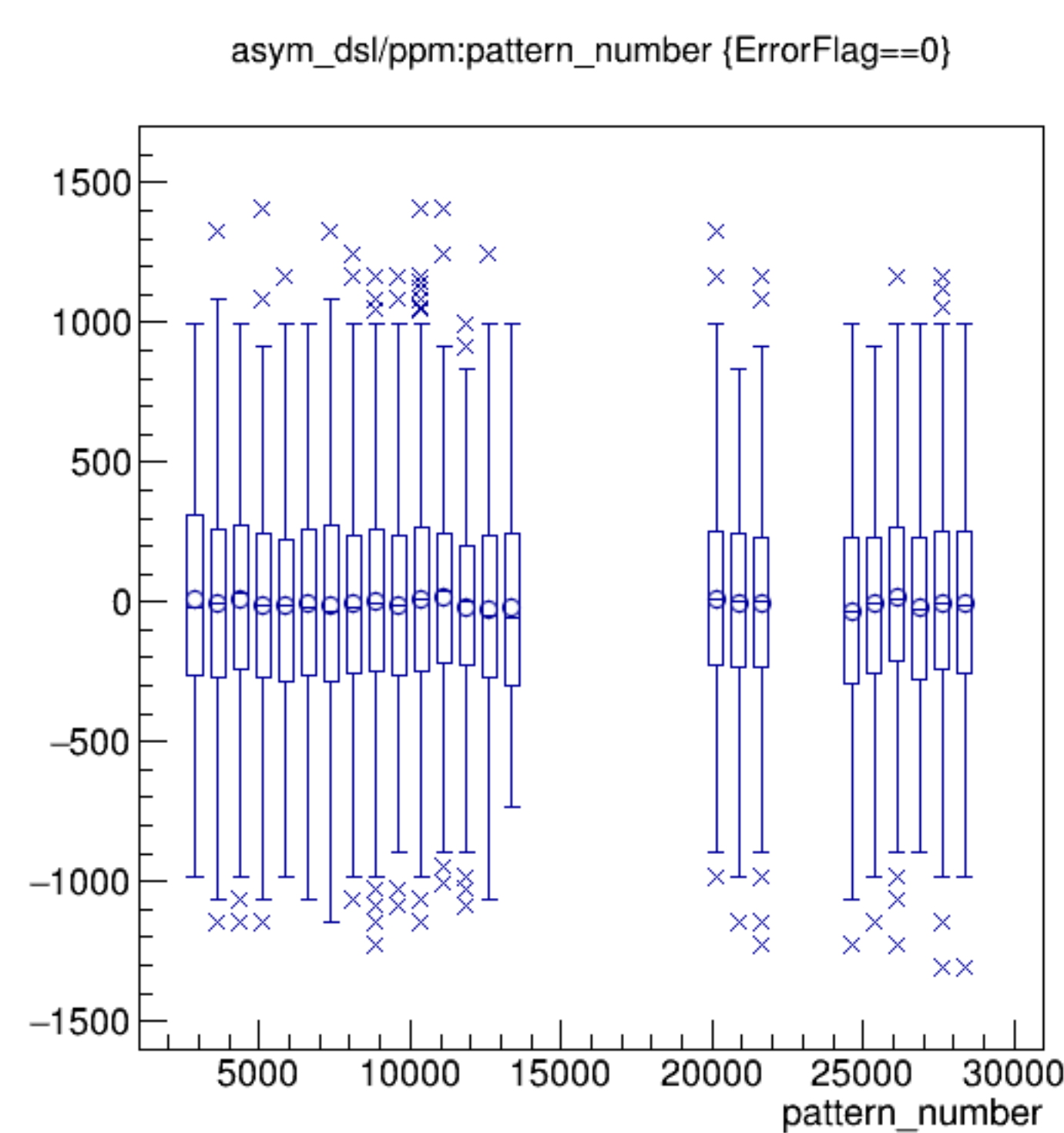
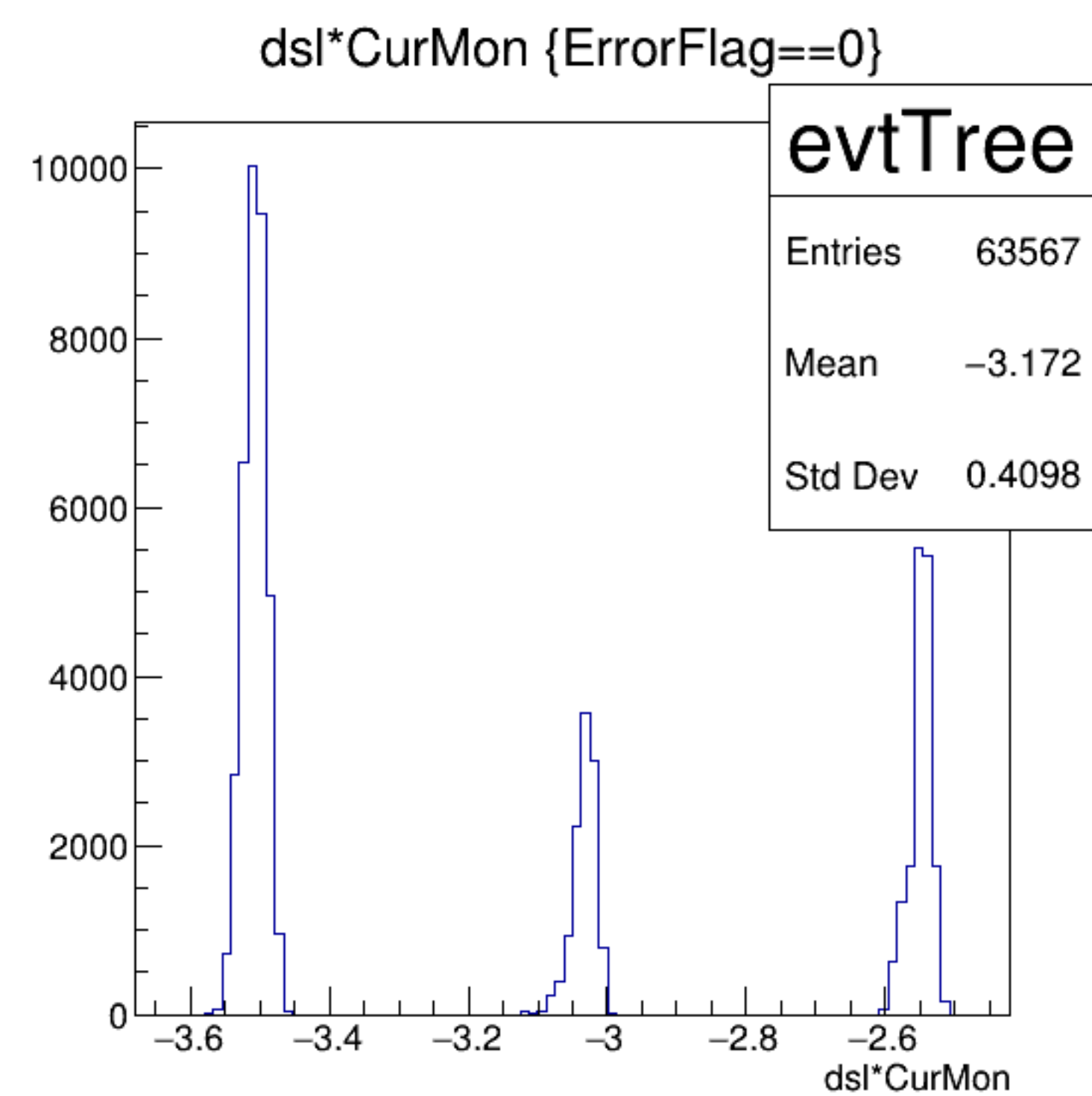


asym_usl

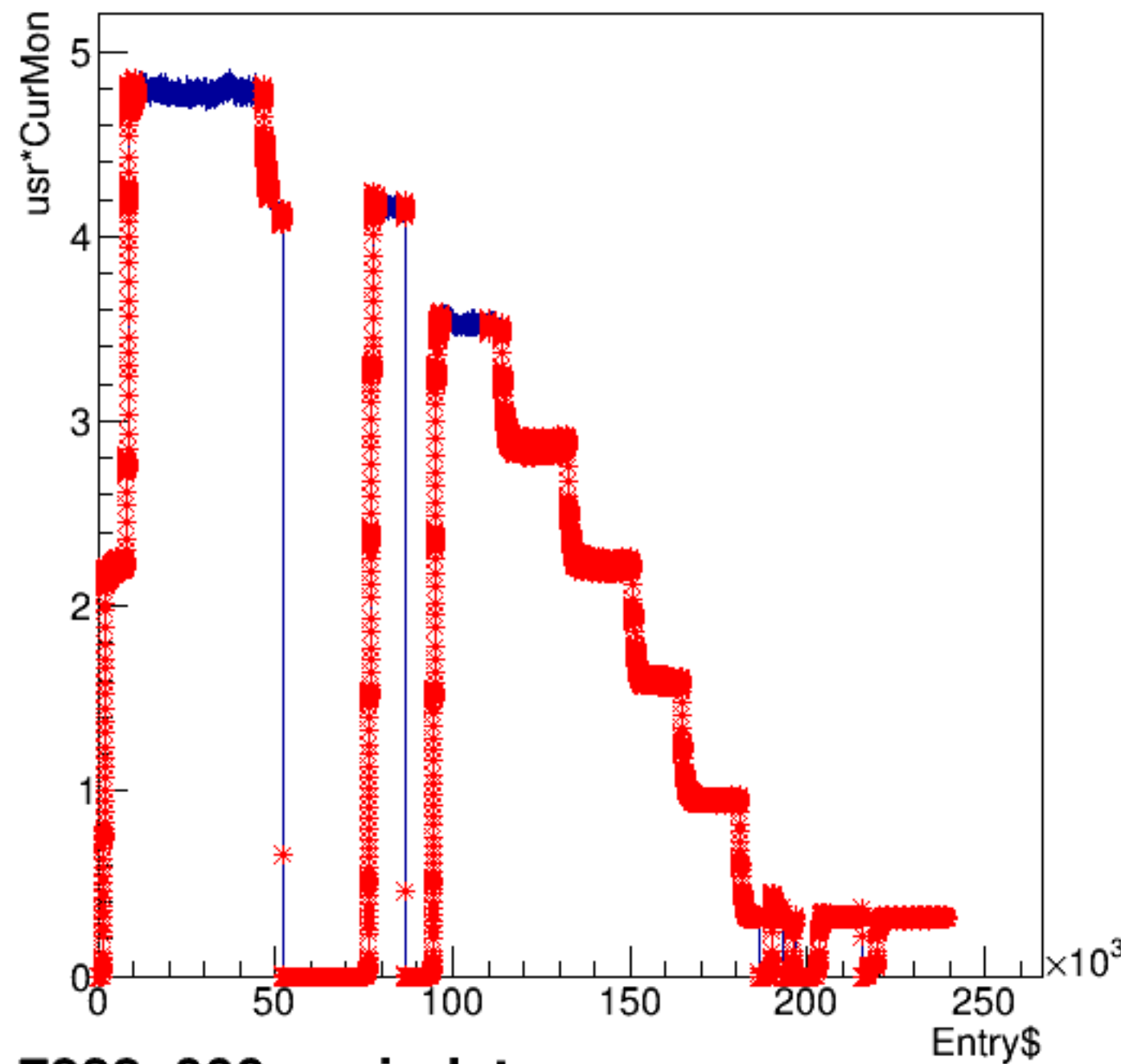




run7282_000_maindet_dsl.png

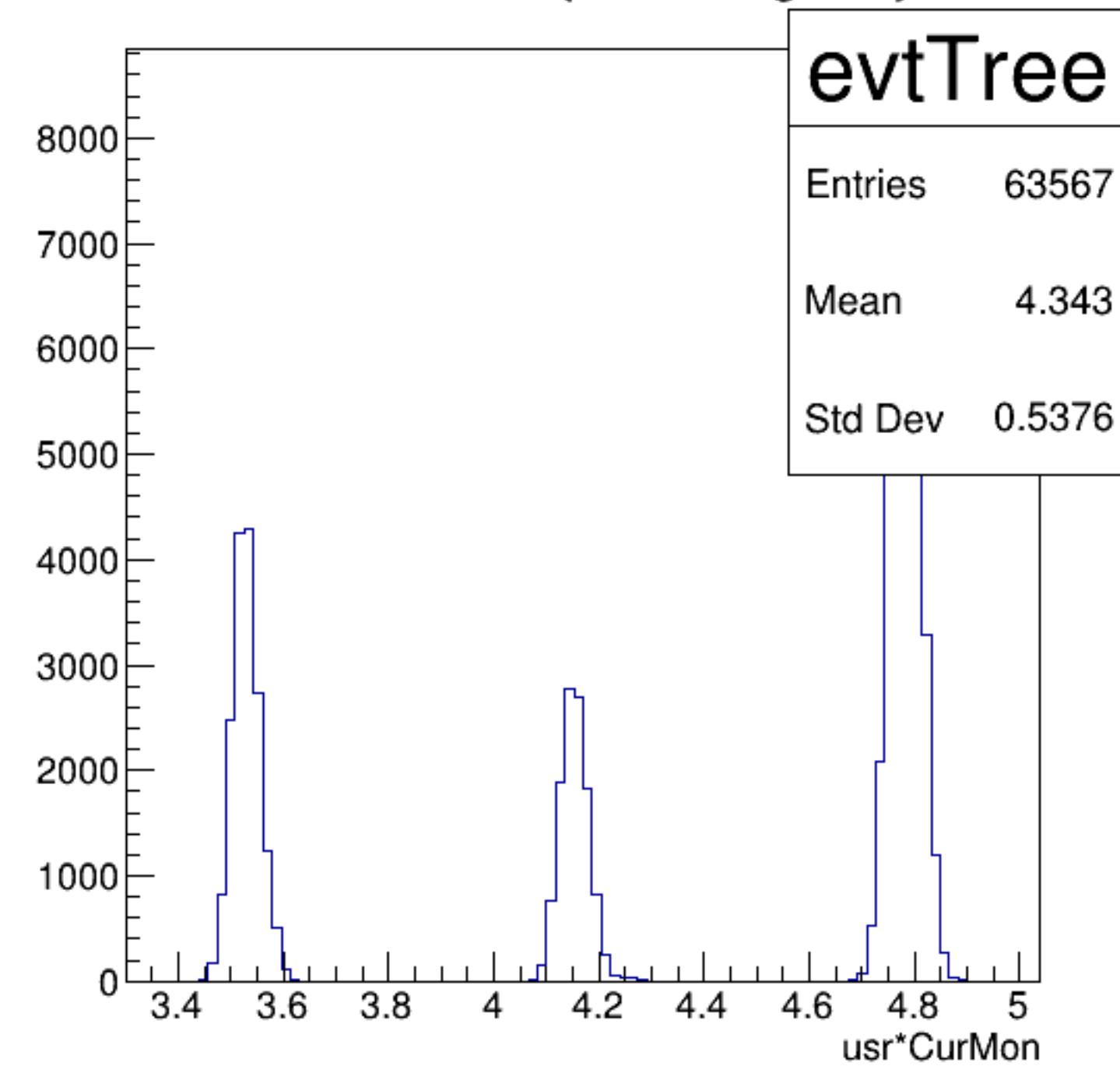


usr*CurMon:Entry\$



run7282_000_maindet_usr.png

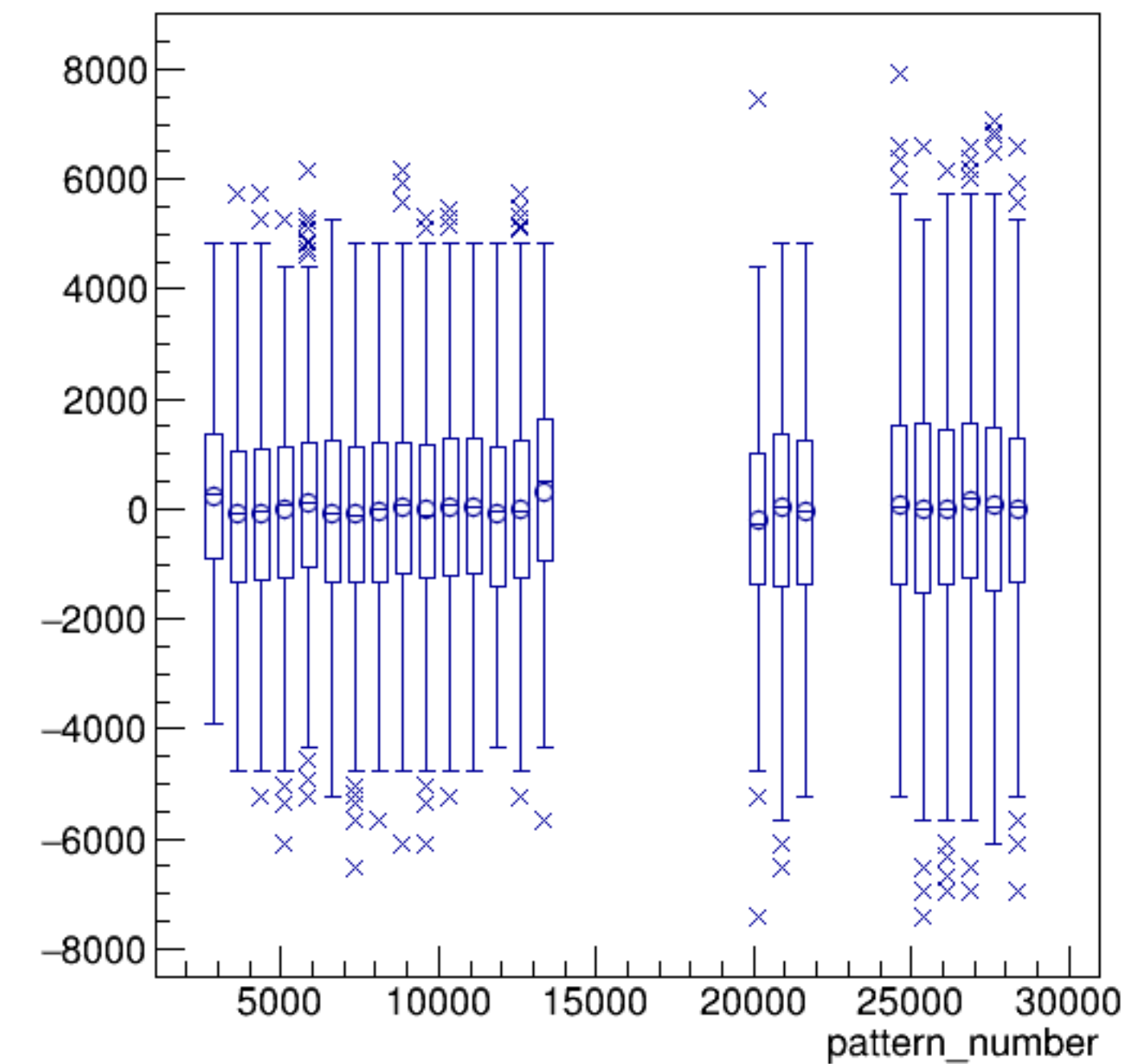
usr*CurMon {ErrorFlag==0}



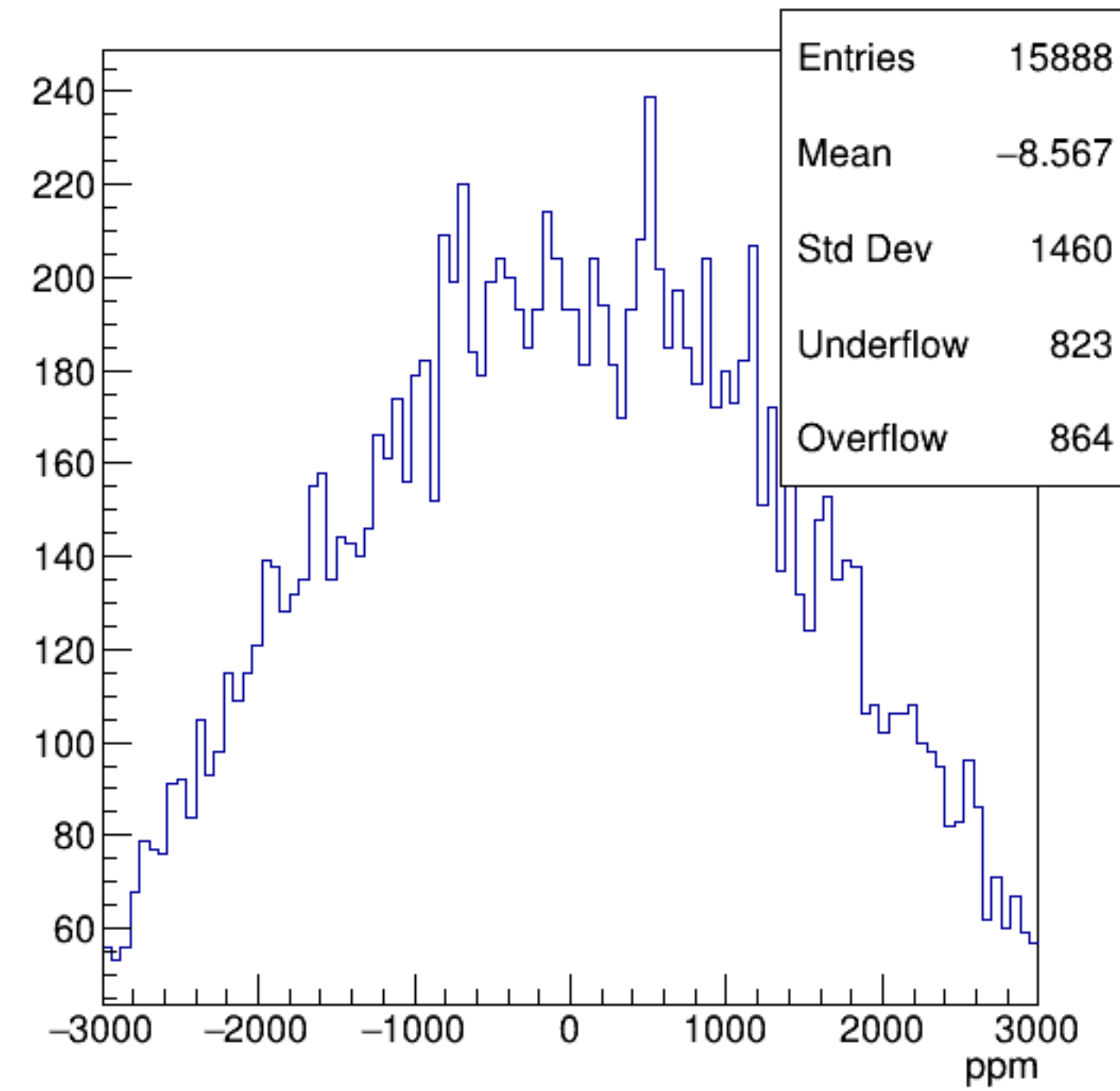
evtTree

Entries	63567
Mean	4.343
Std Dev	0.5376

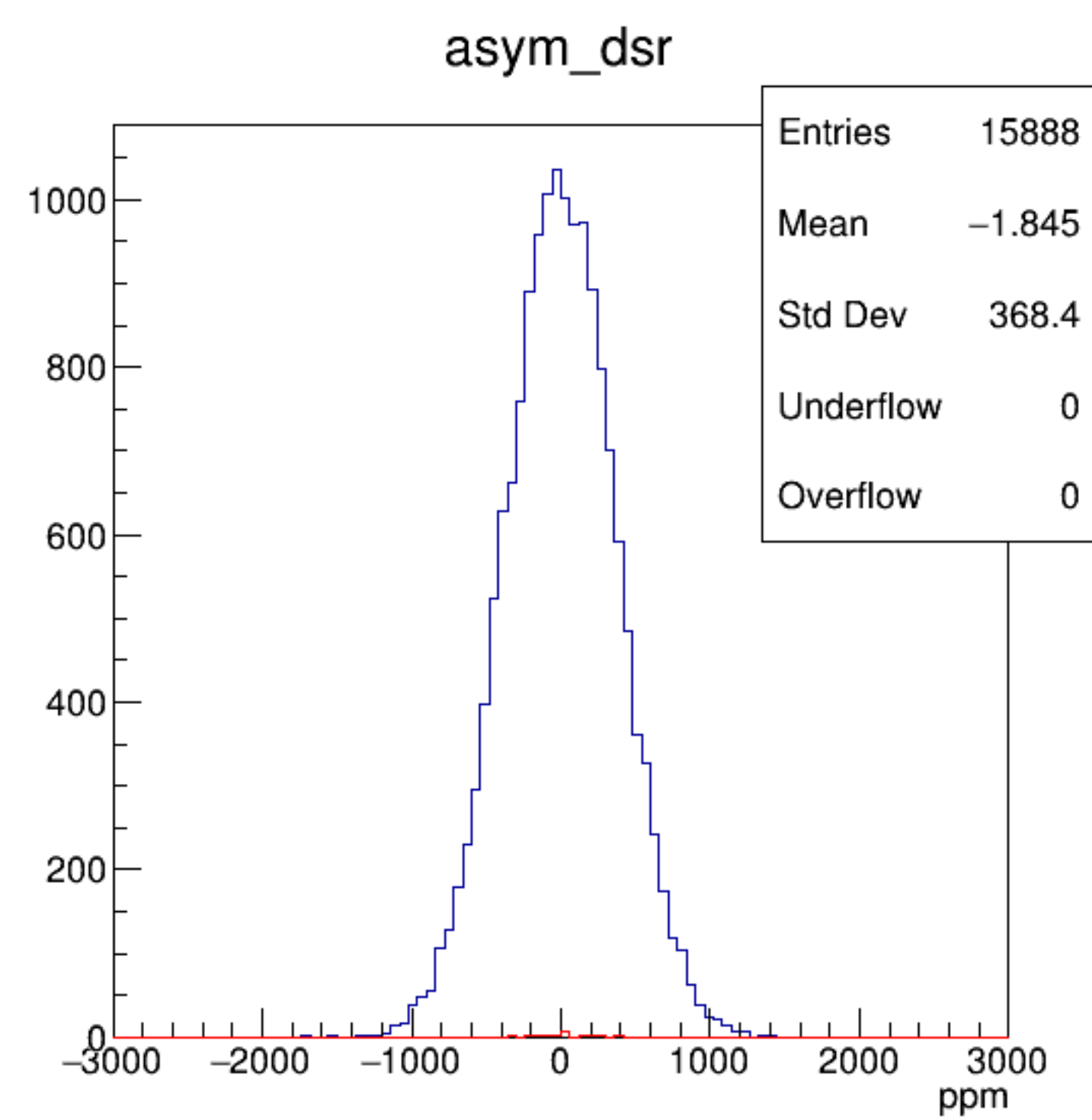
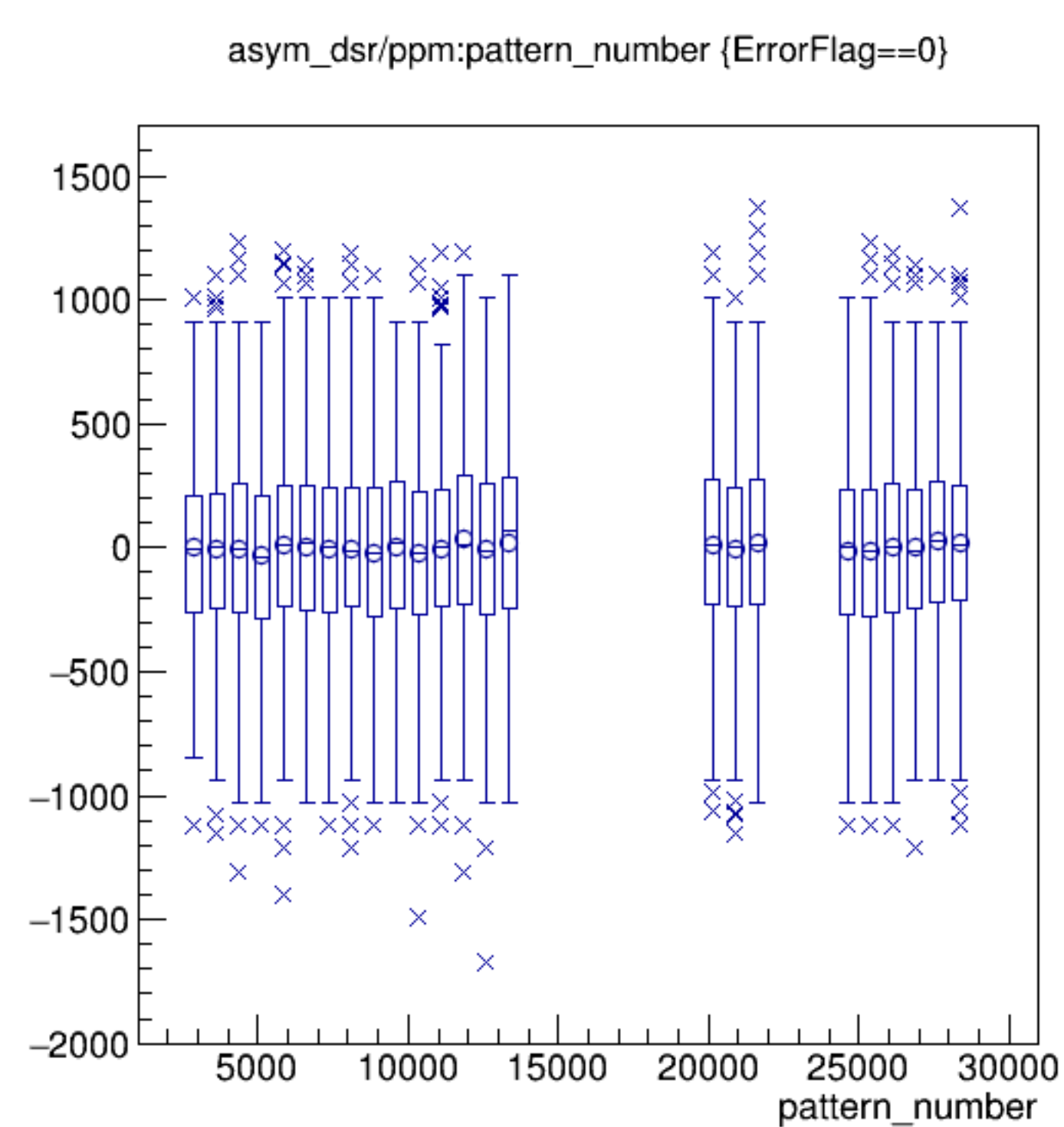
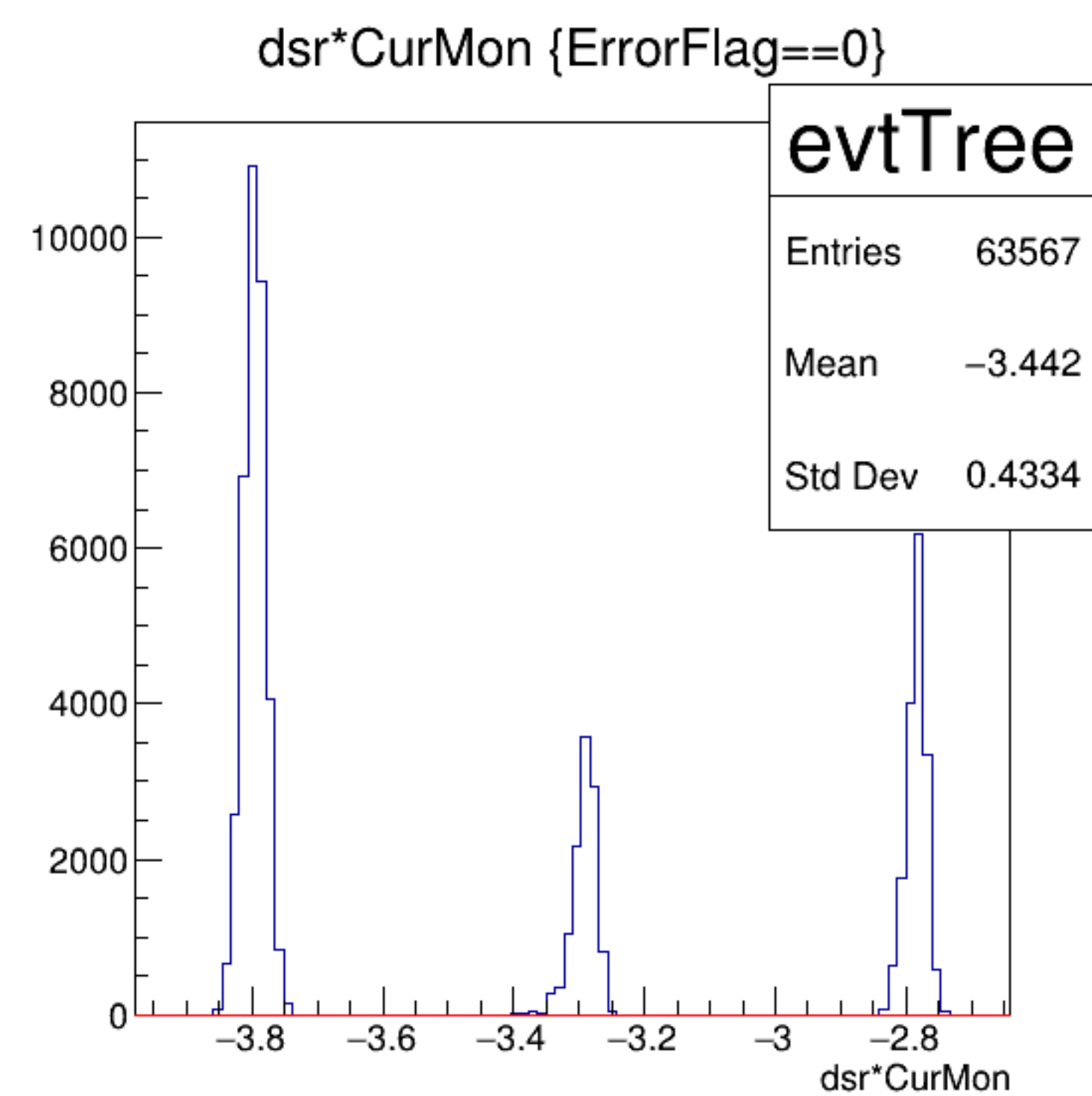
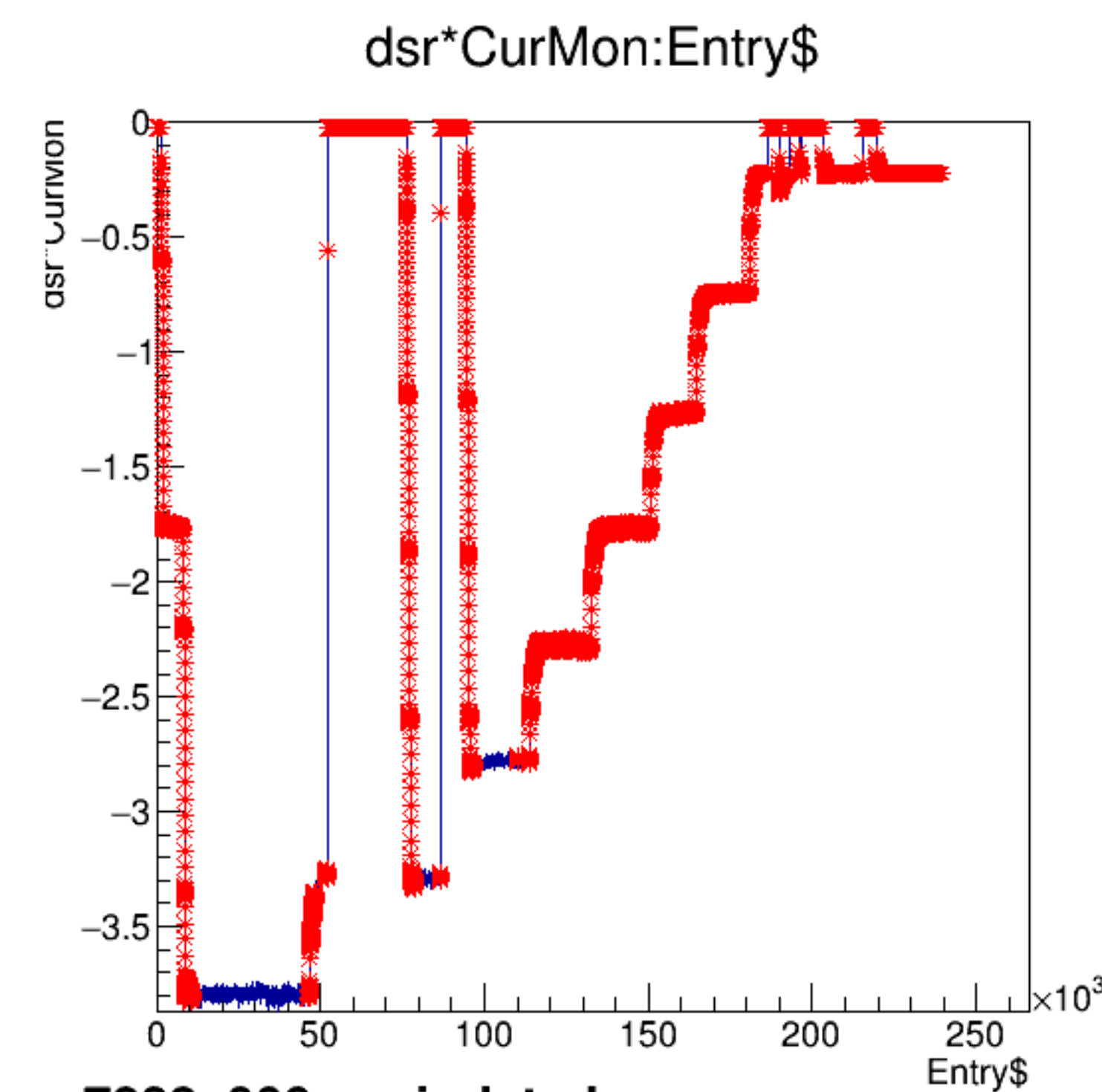
asym_usr/ppm:pattern_number {ErrorFlag==0}



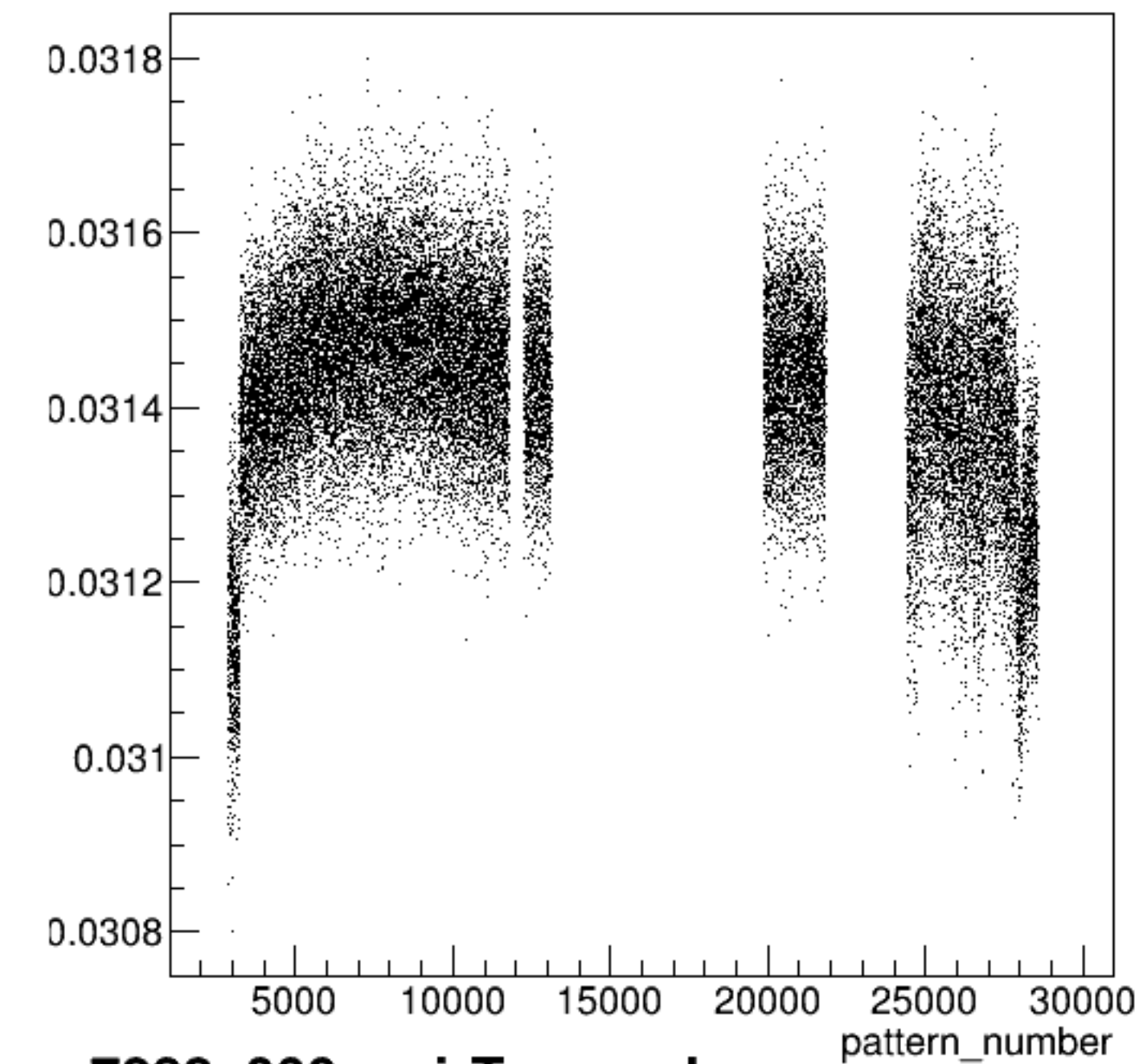
asym_usr



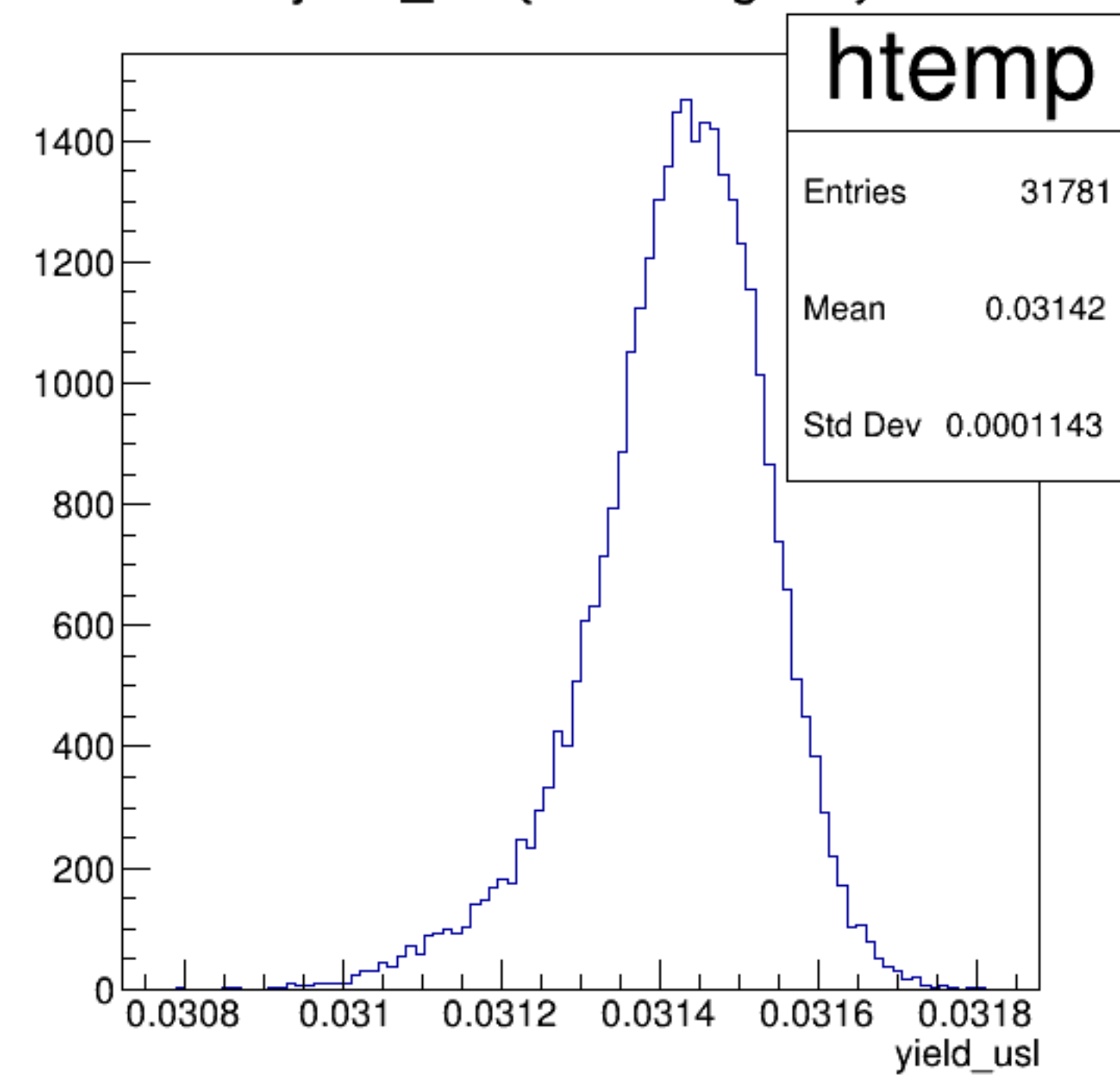
Entries	15888
Mean	-8.567
Std Dev	1460
Underflow	823
Overflow	864



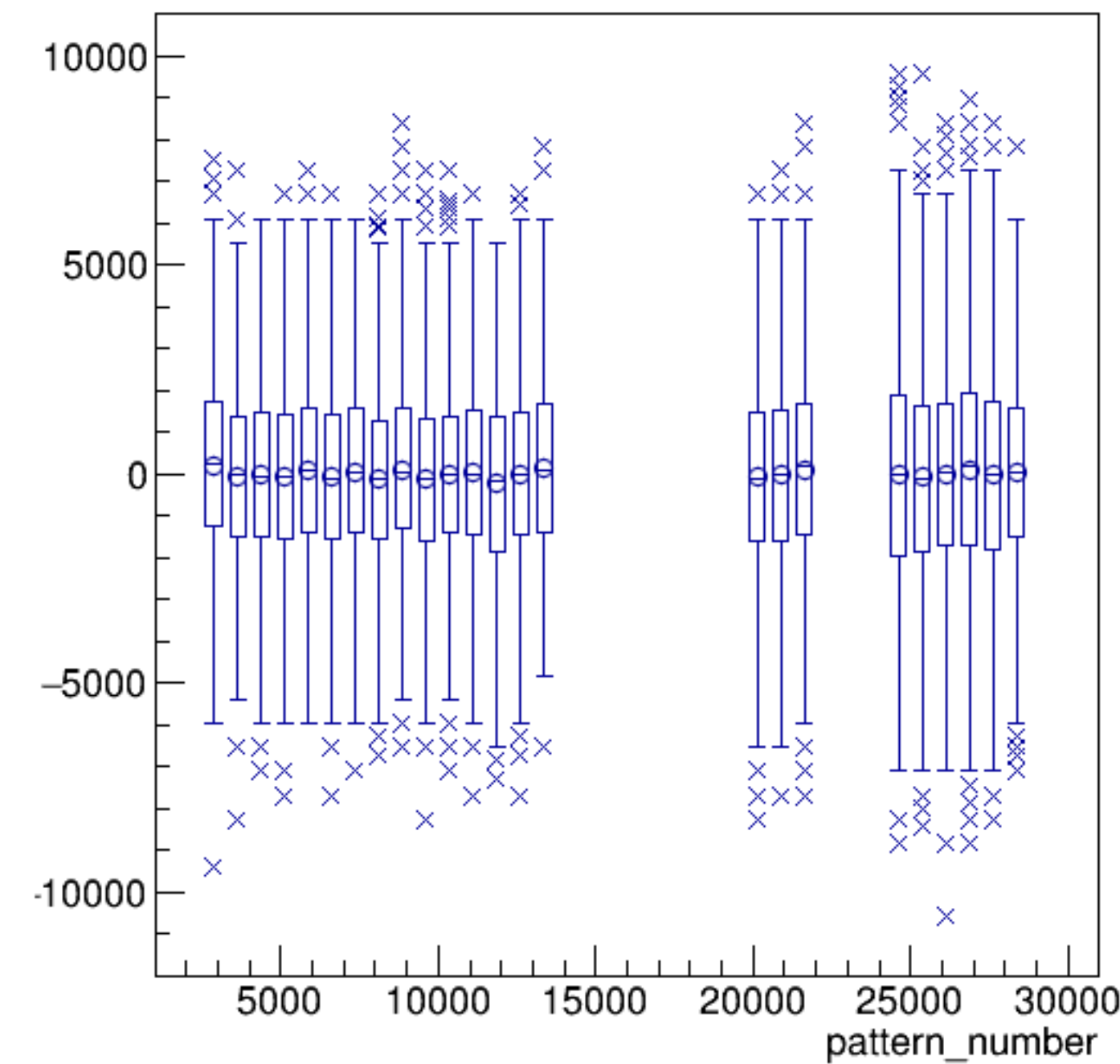
yield_usl:pattern_number {ErrorFlag==0}



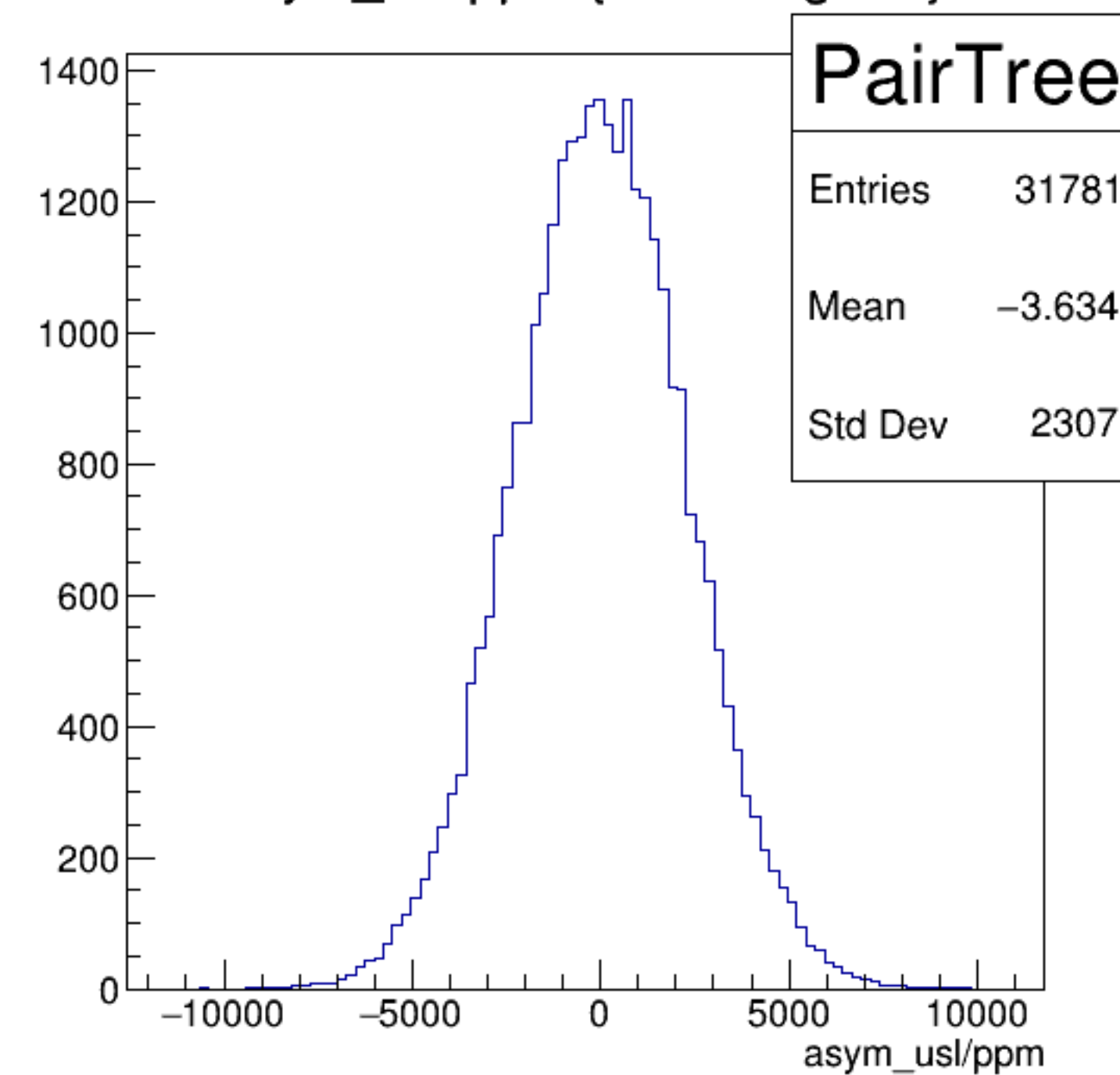
yield_usl {ErrorFlag==0}



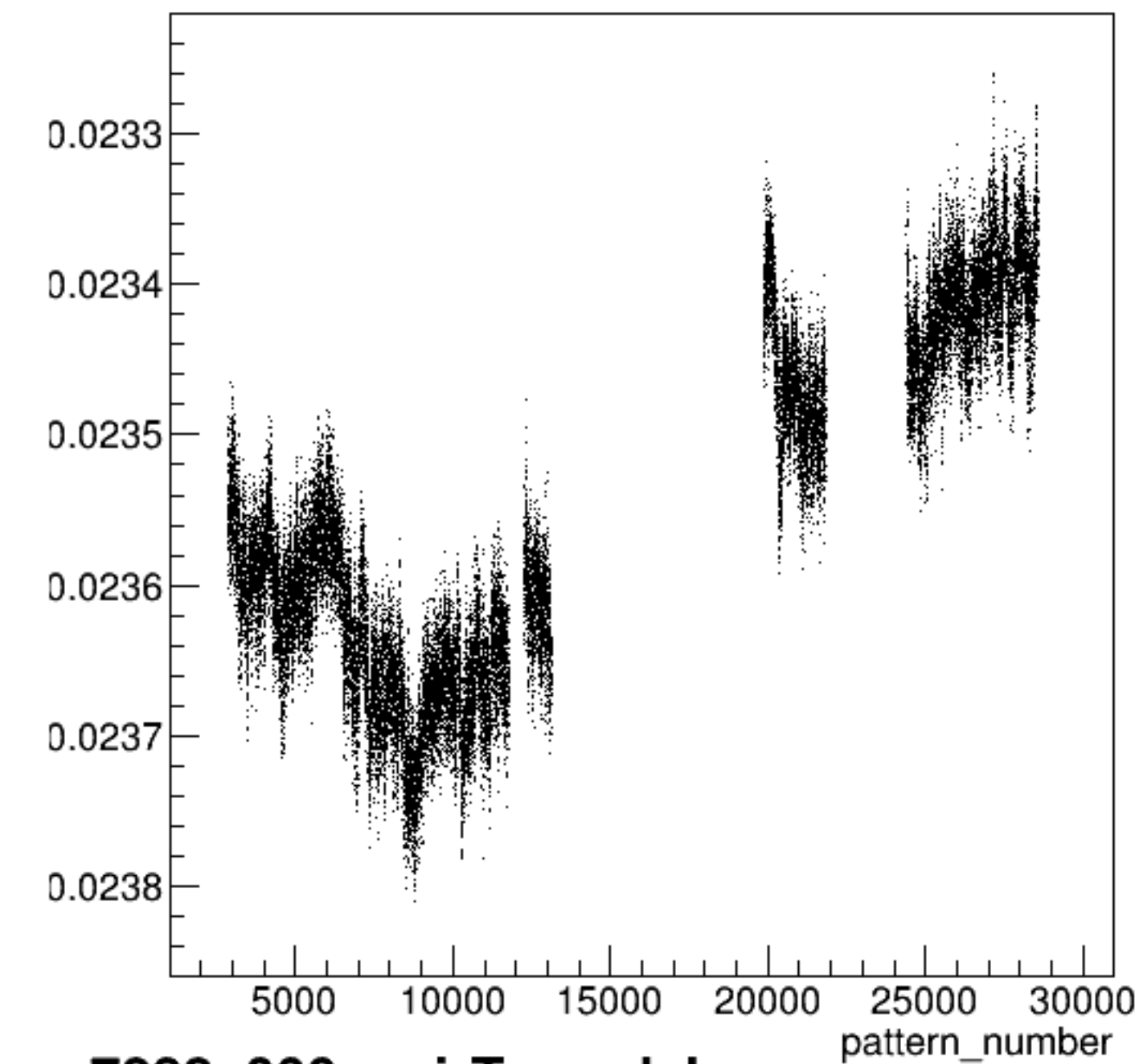
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl/ppm {ErrorFlag==0}

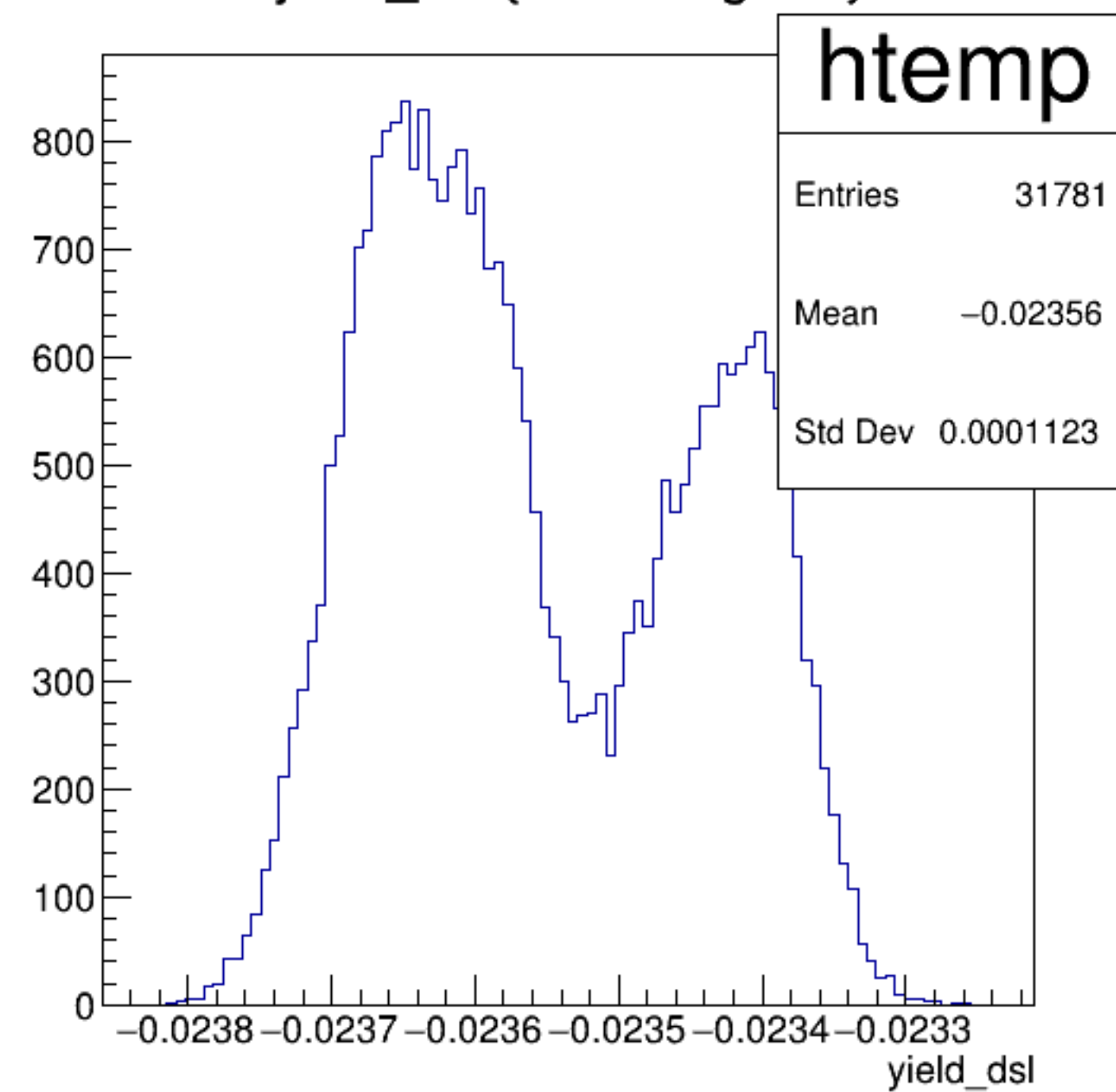


yield_dsl:pattern_number {ErrorFlag==0}

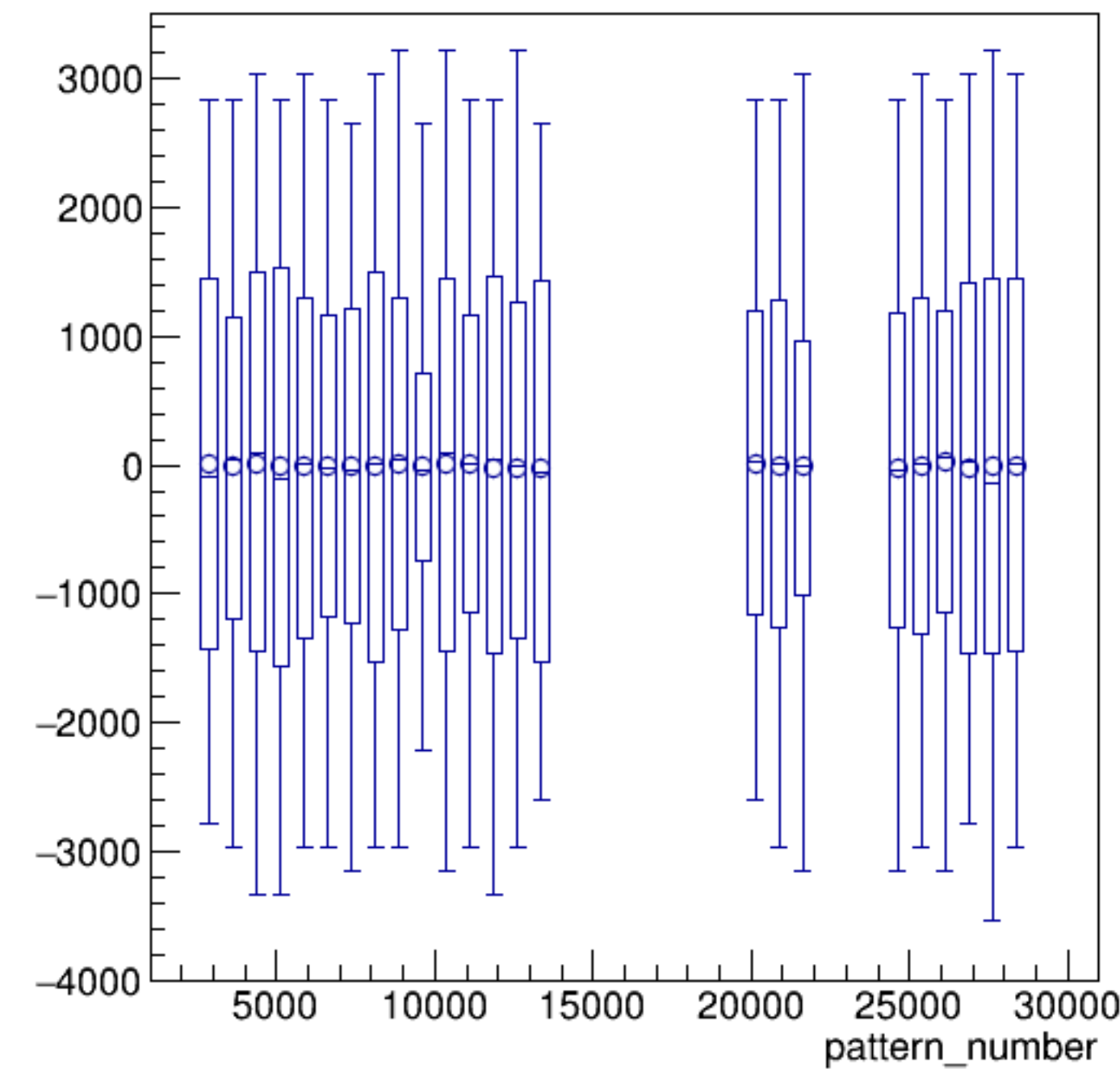


run7282_000_pairTree_dsl.png

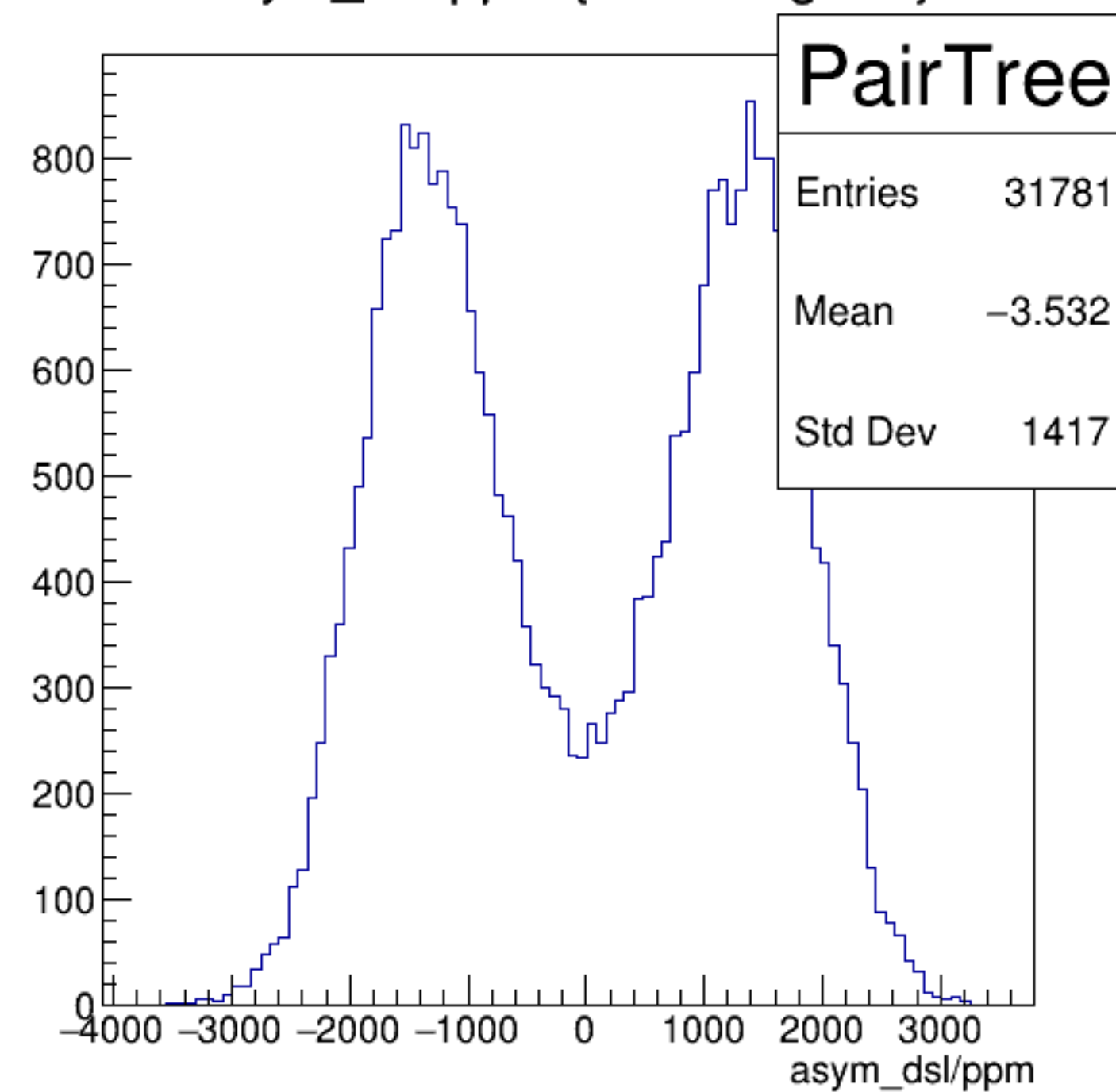
yield_dsl {ErrorFlag==0}

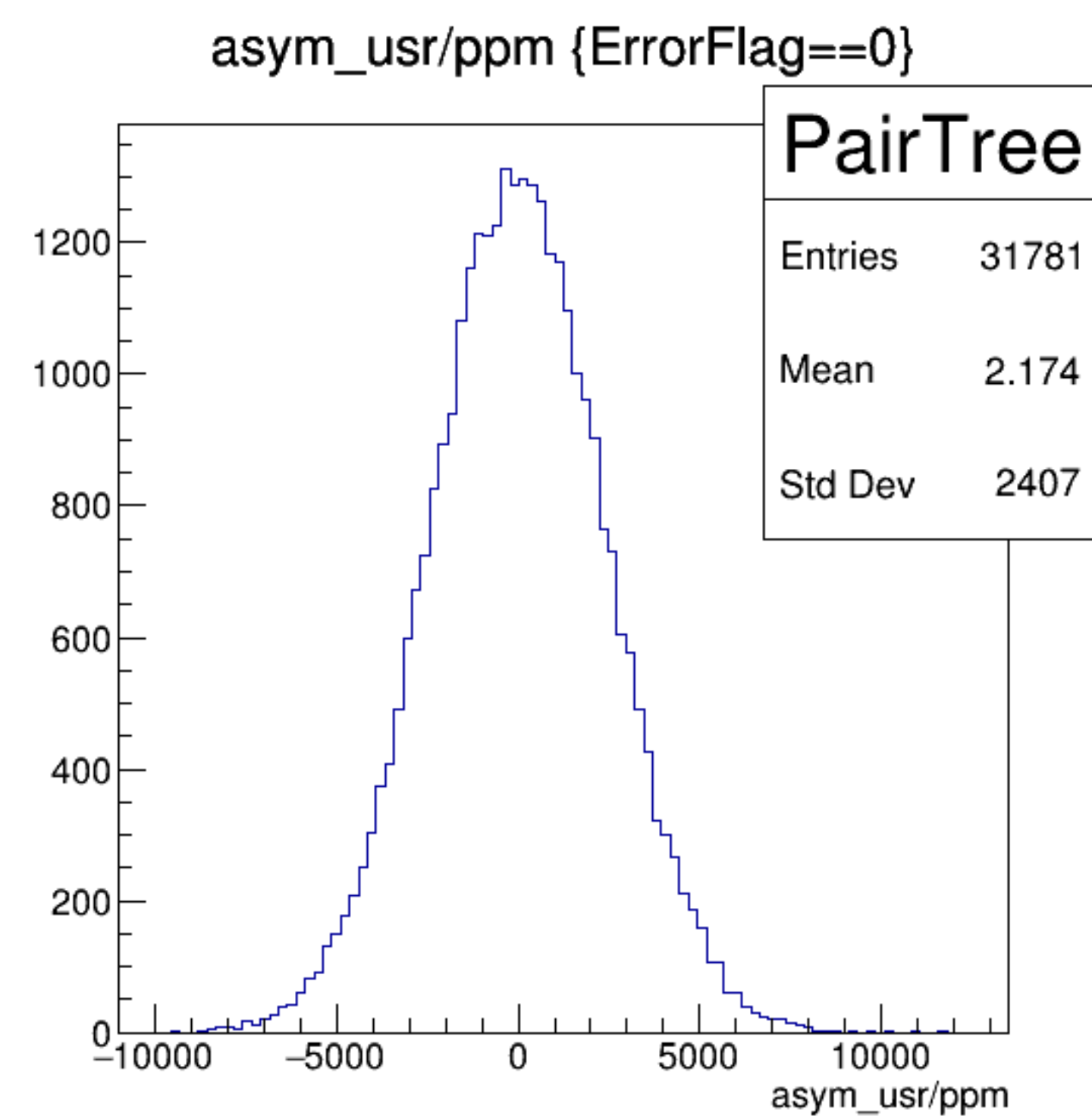
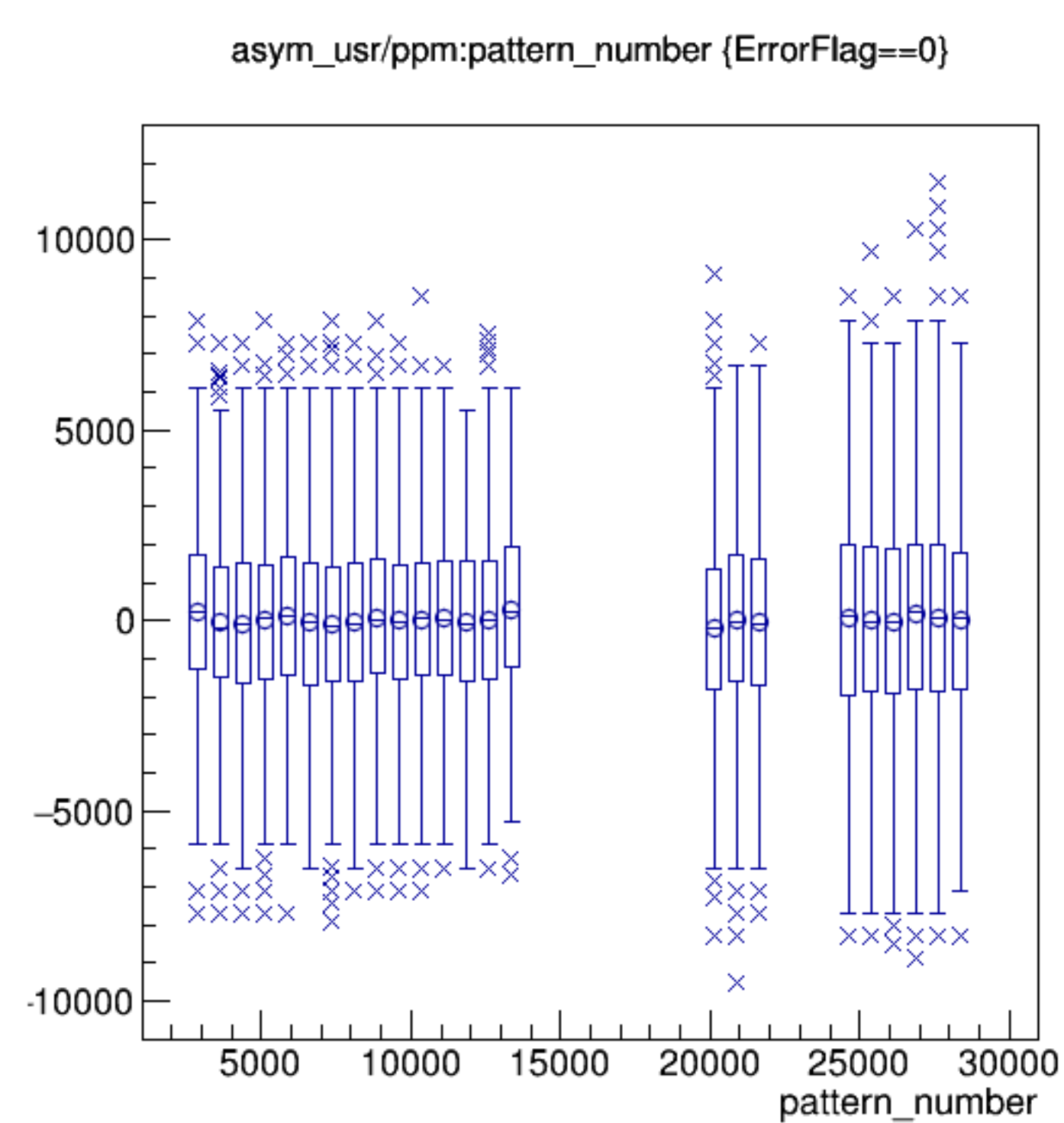
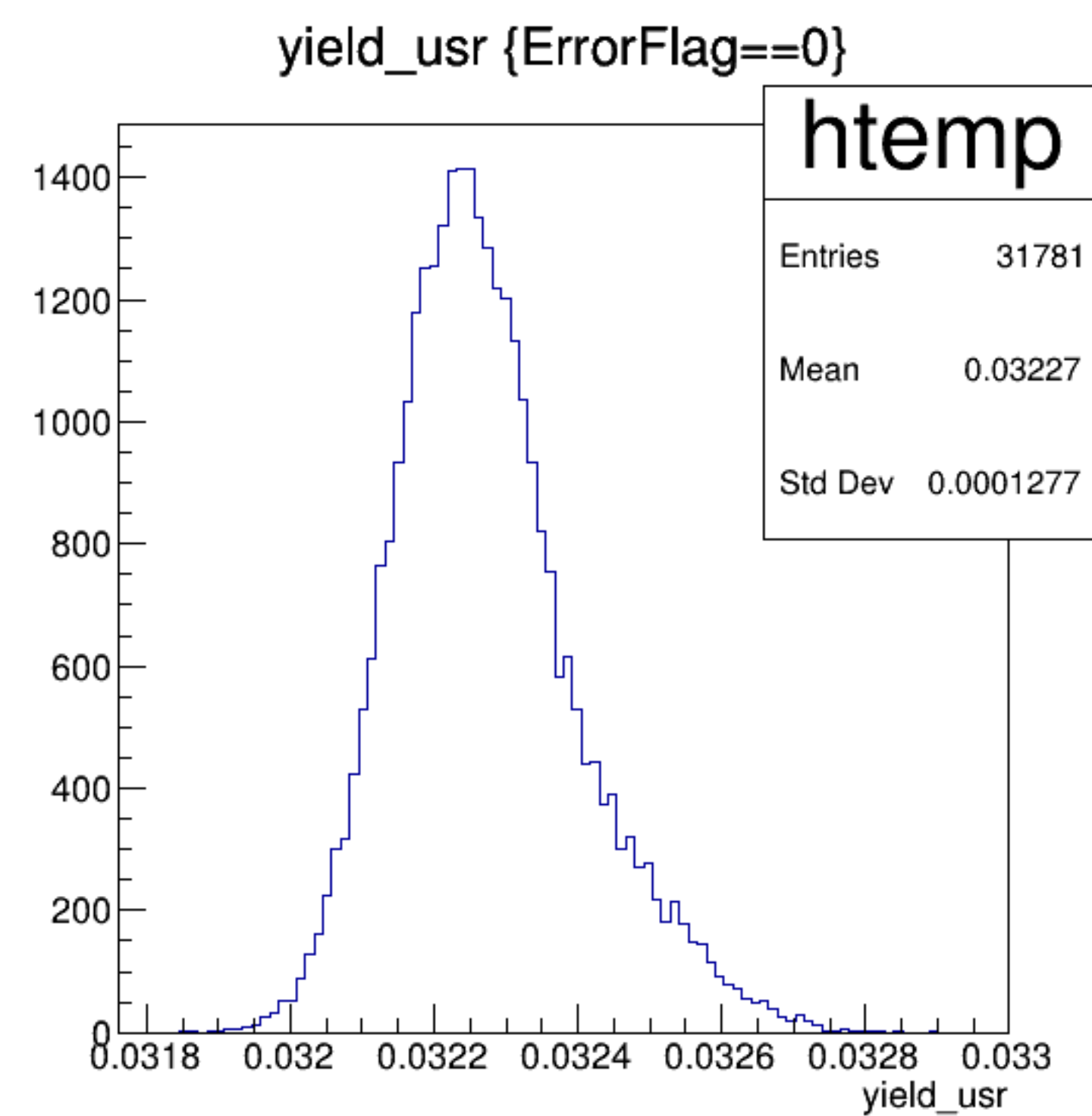
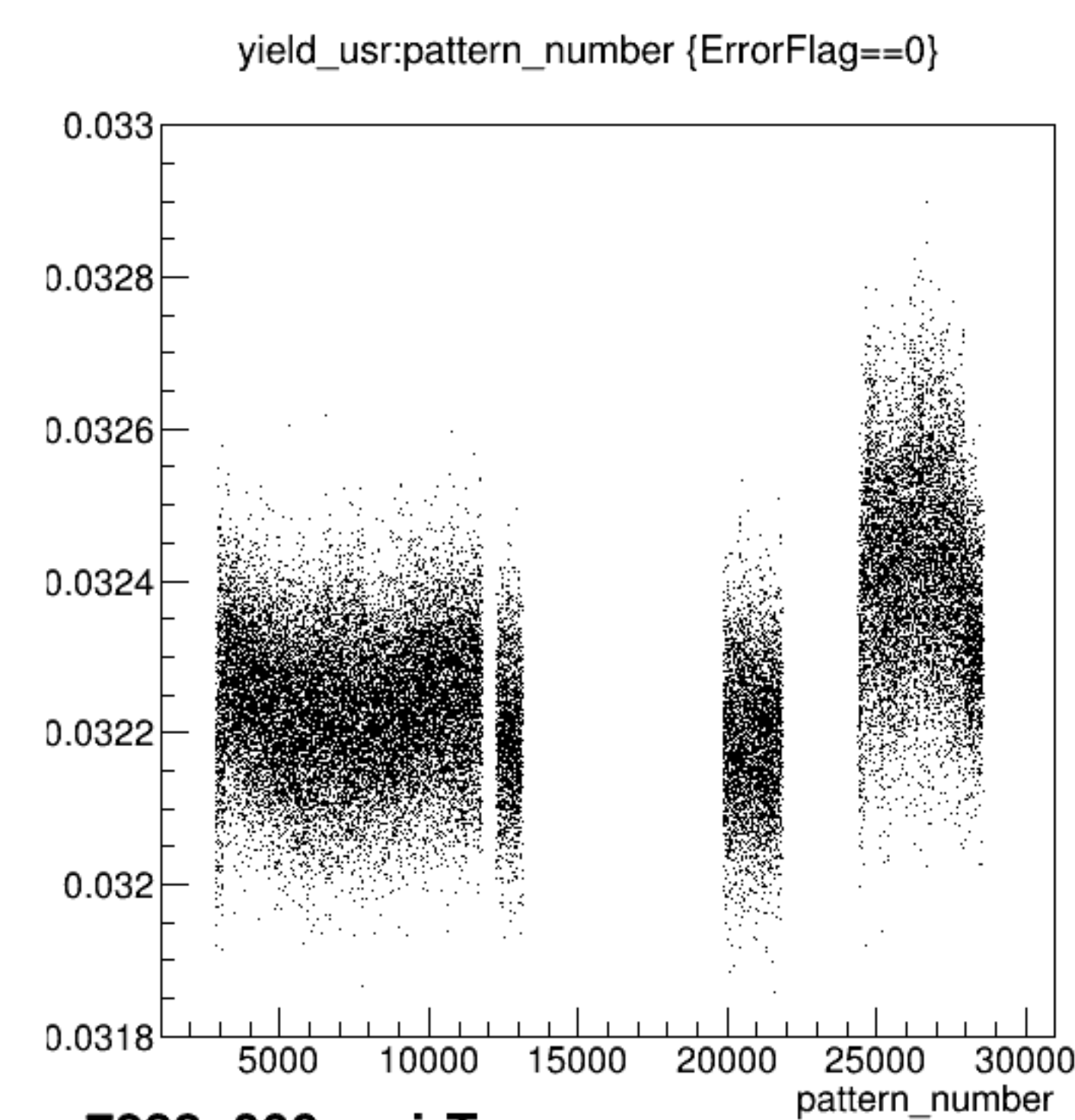


asym_dsl/ppm:pattern_number {ErrorFlag==0}

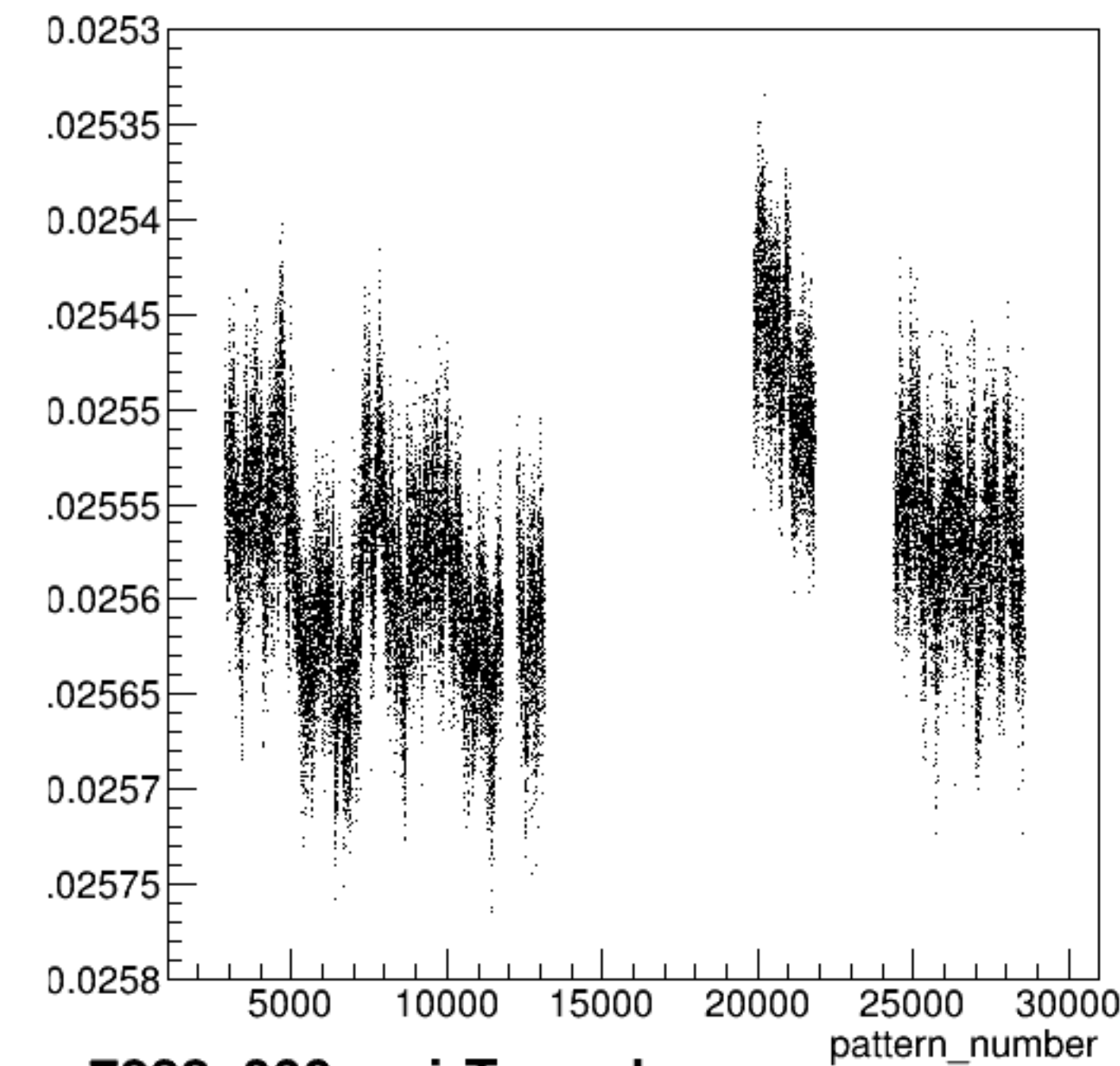


asym_dsl/ppm {ErrorFlag==0}



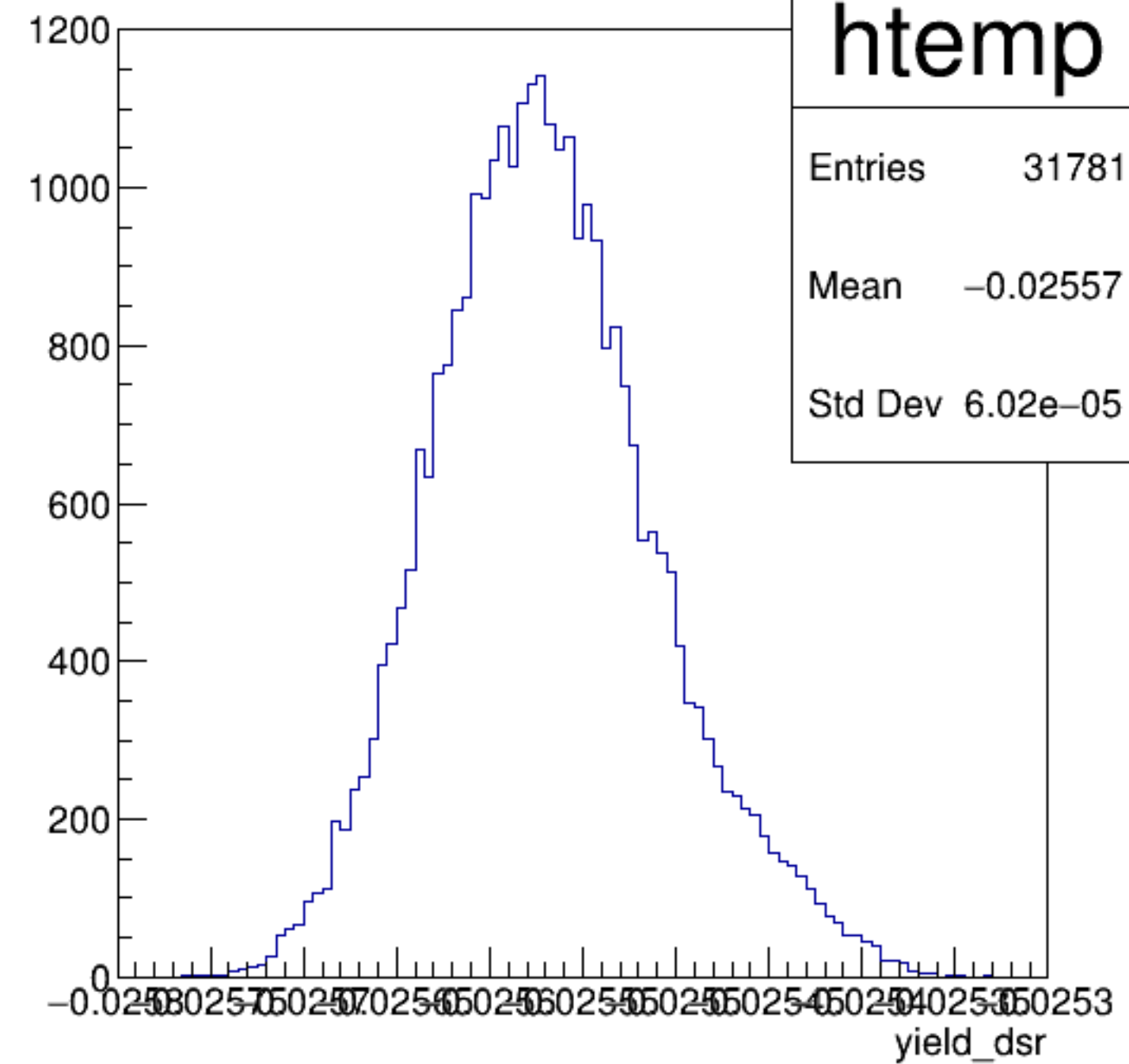


yield_dsr:pattern_number {ErrorFlag==0}

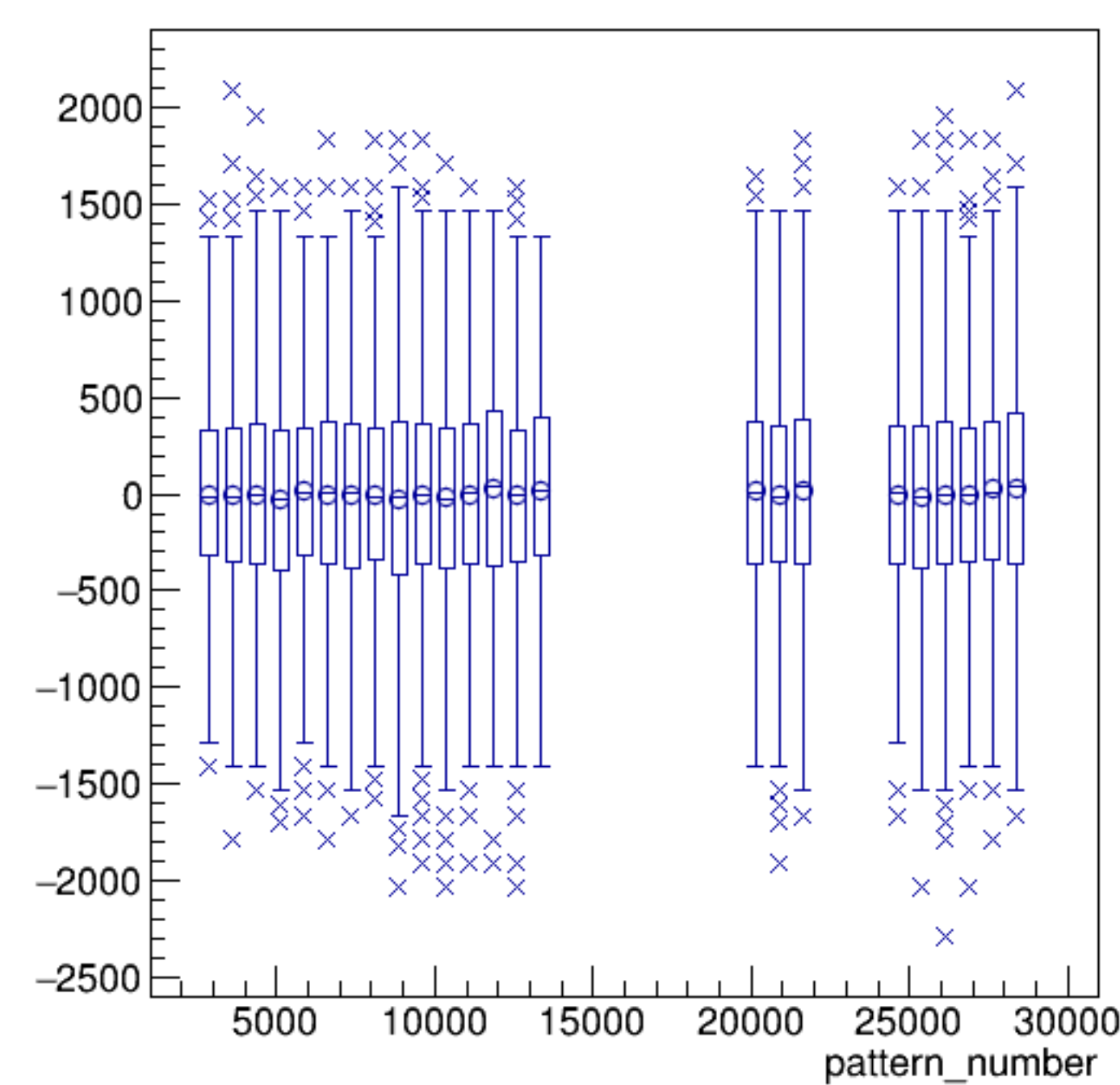


run7282_000_pairTree_dsr.png

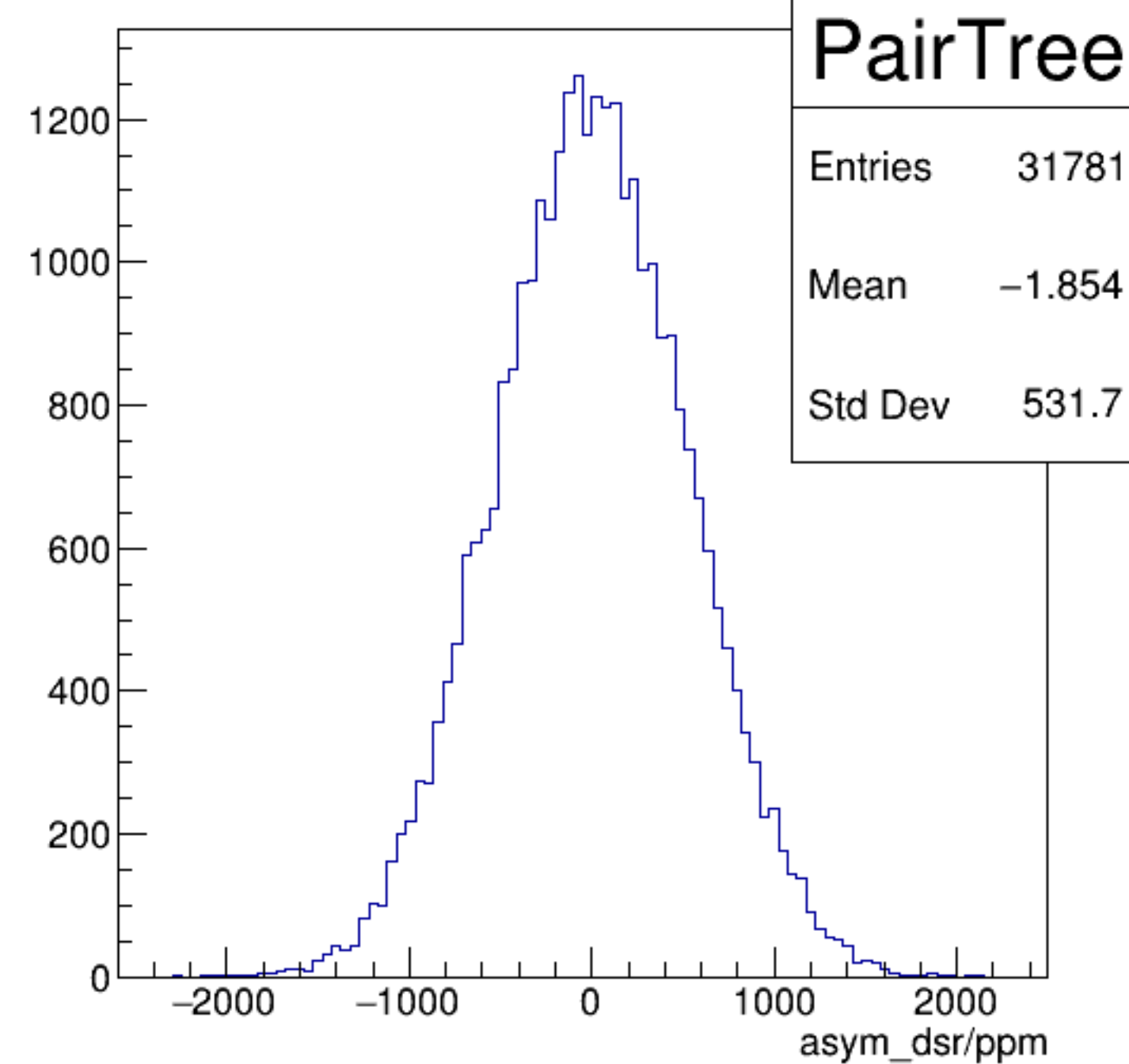
yield_dsr {ErrorFlag==0}



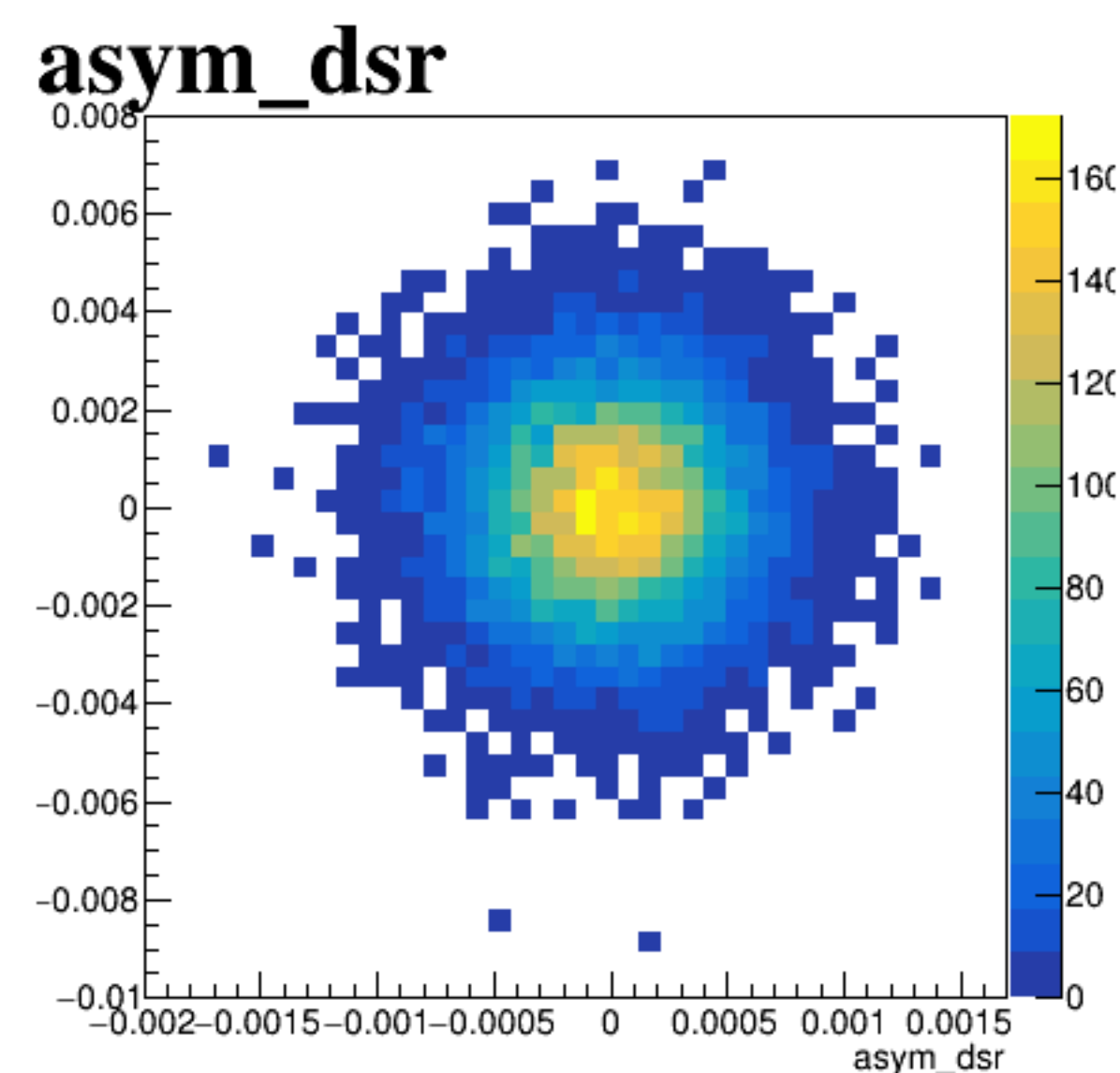
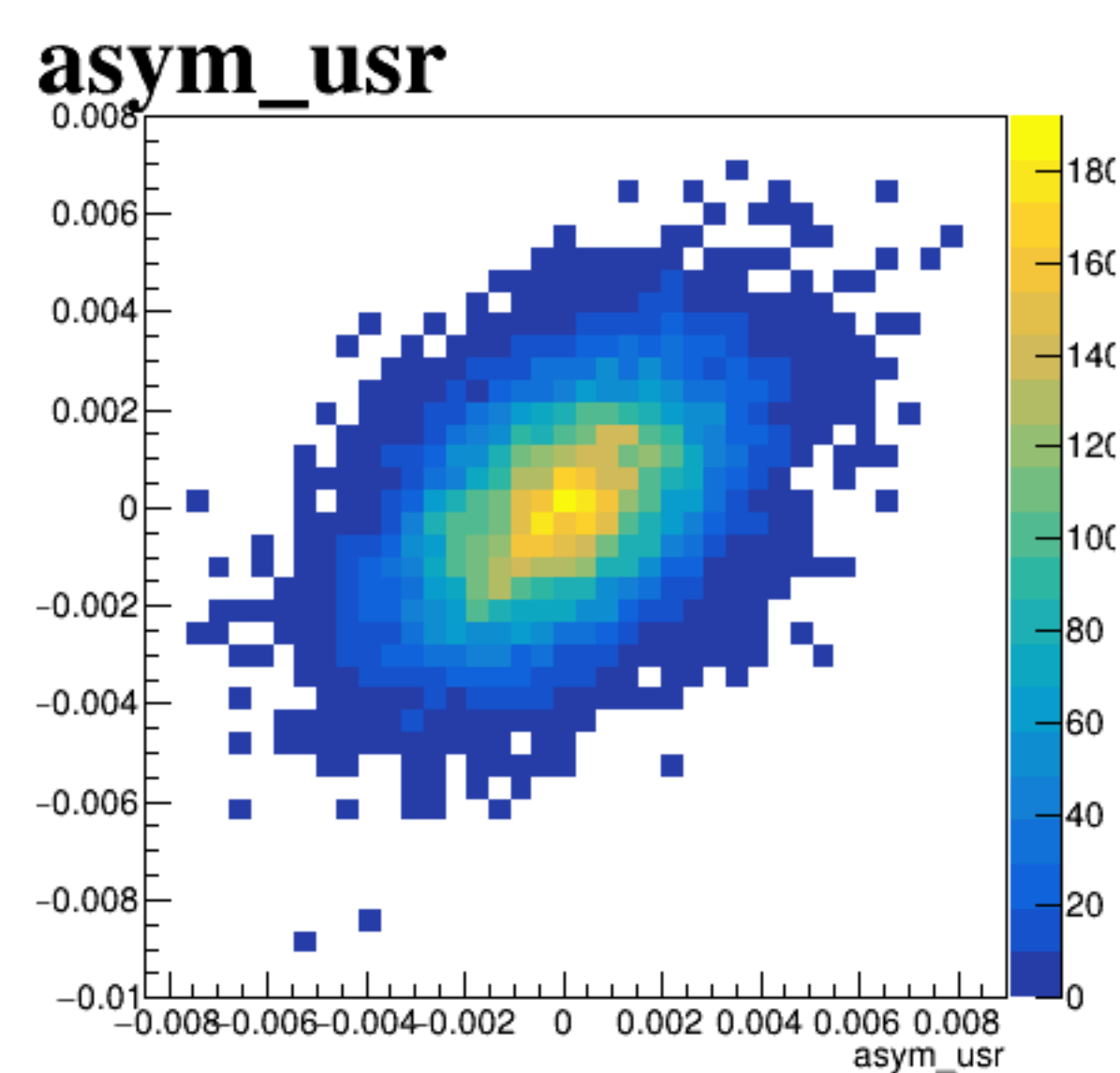
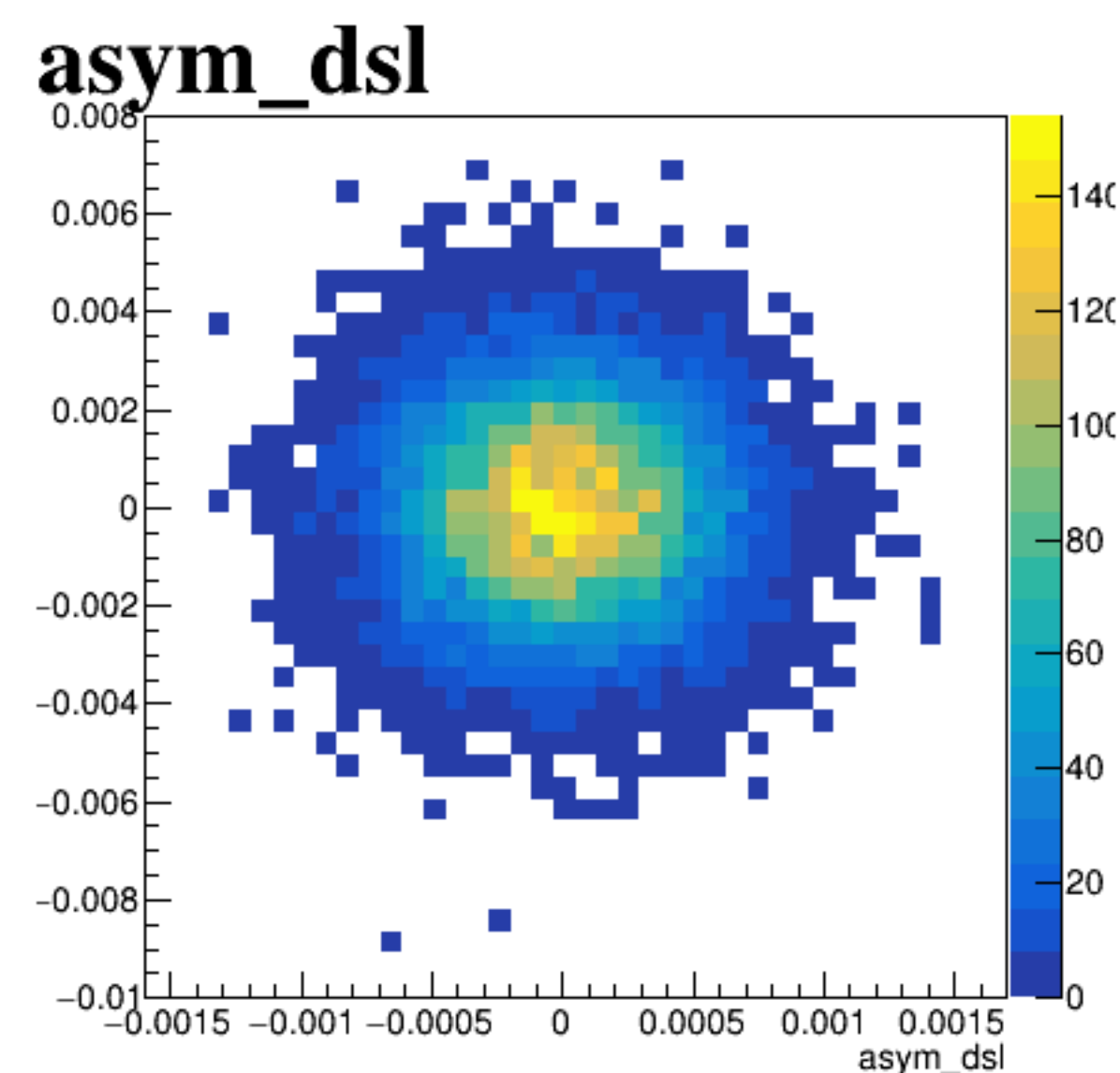
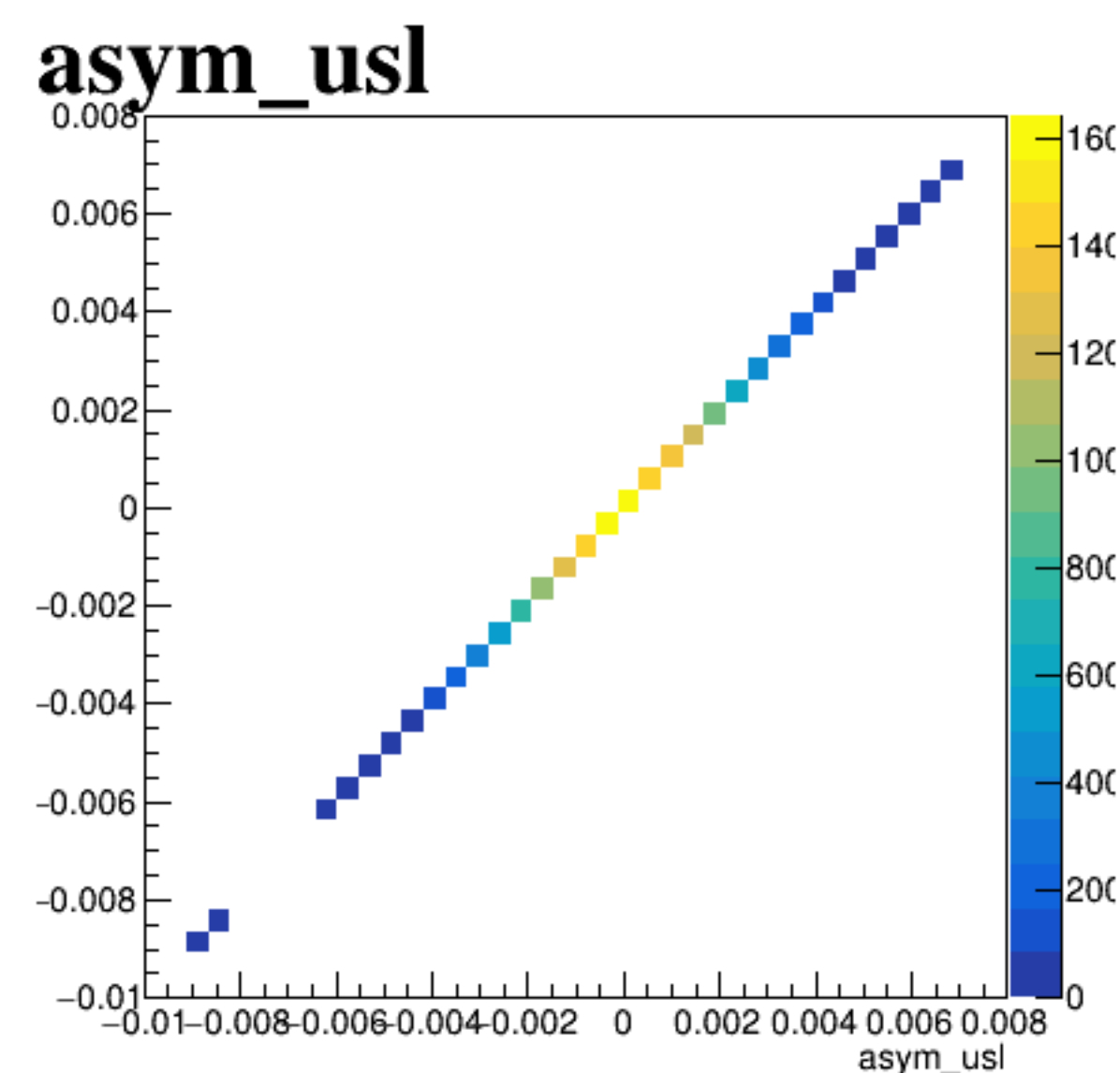
asym_dsr/ppm:pattern_number {ErrorFlag==0}



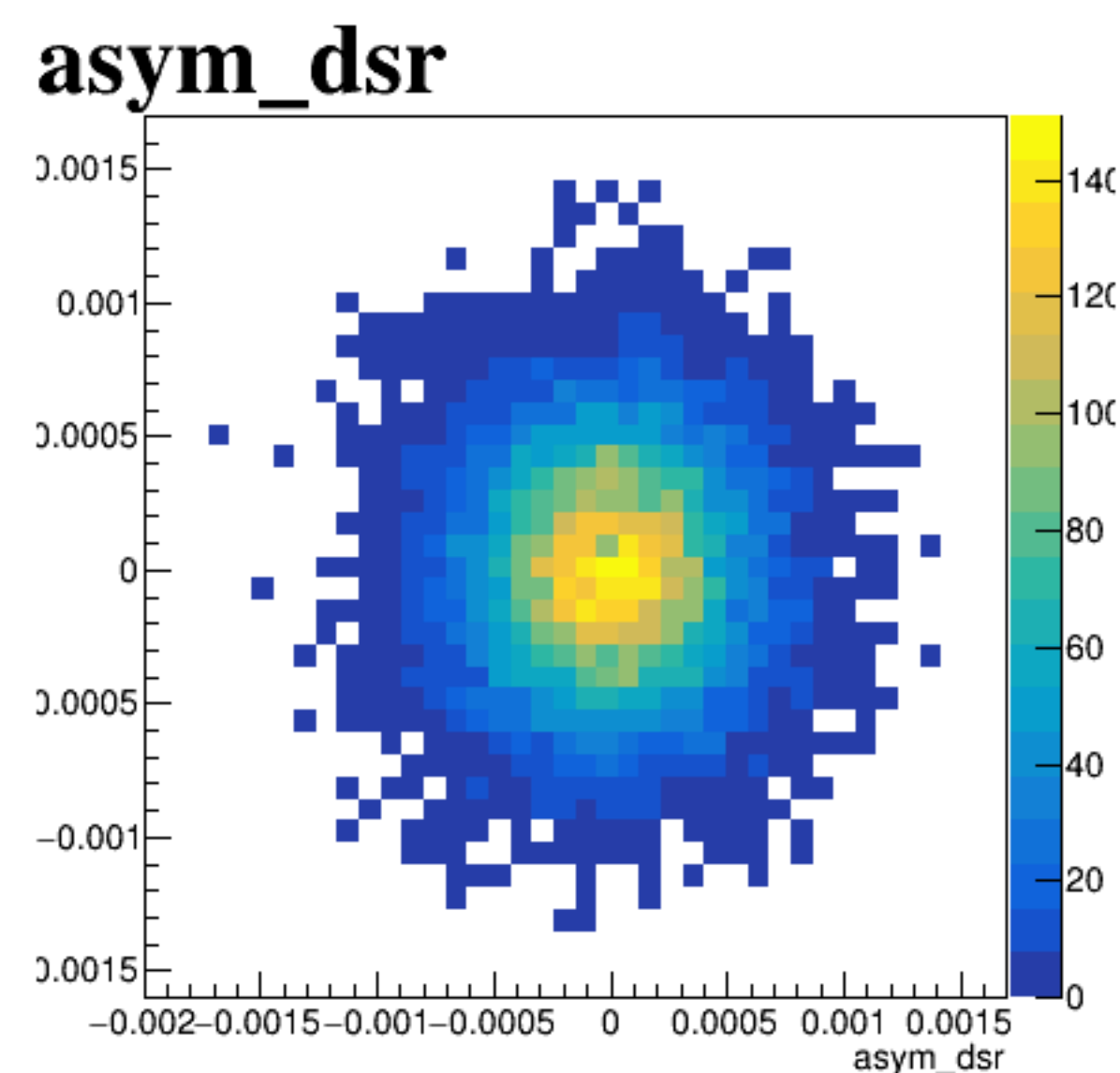
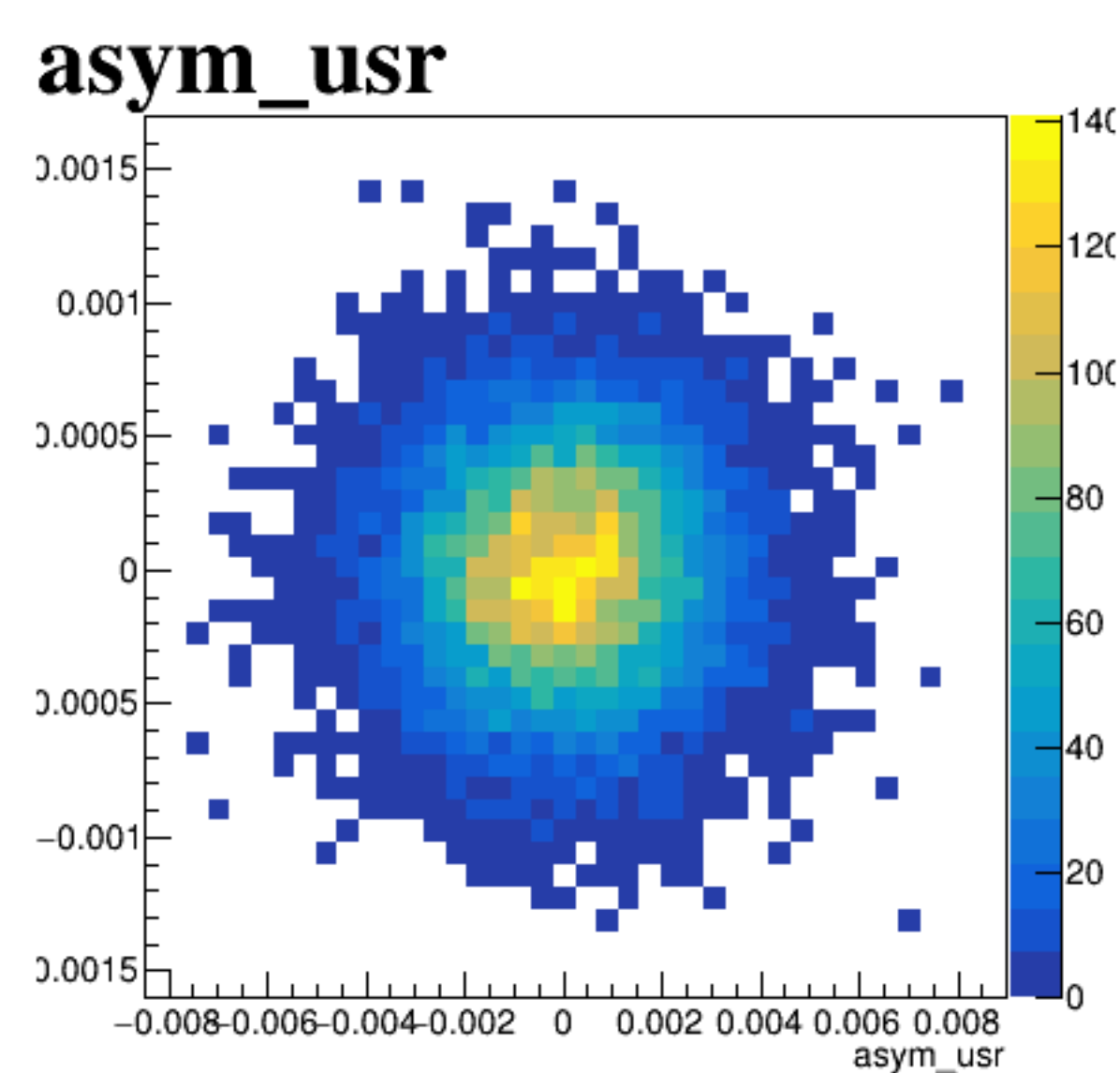
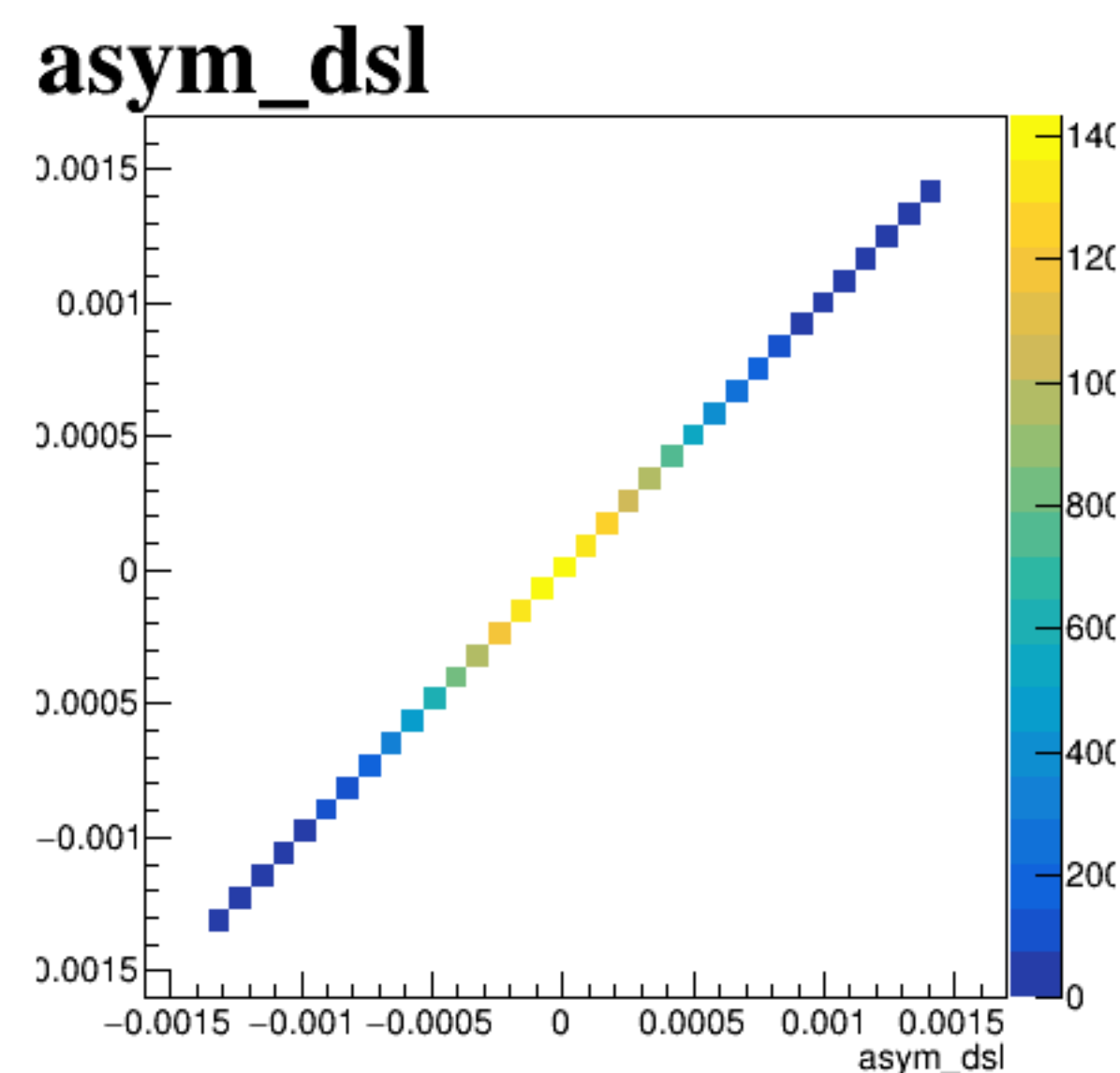
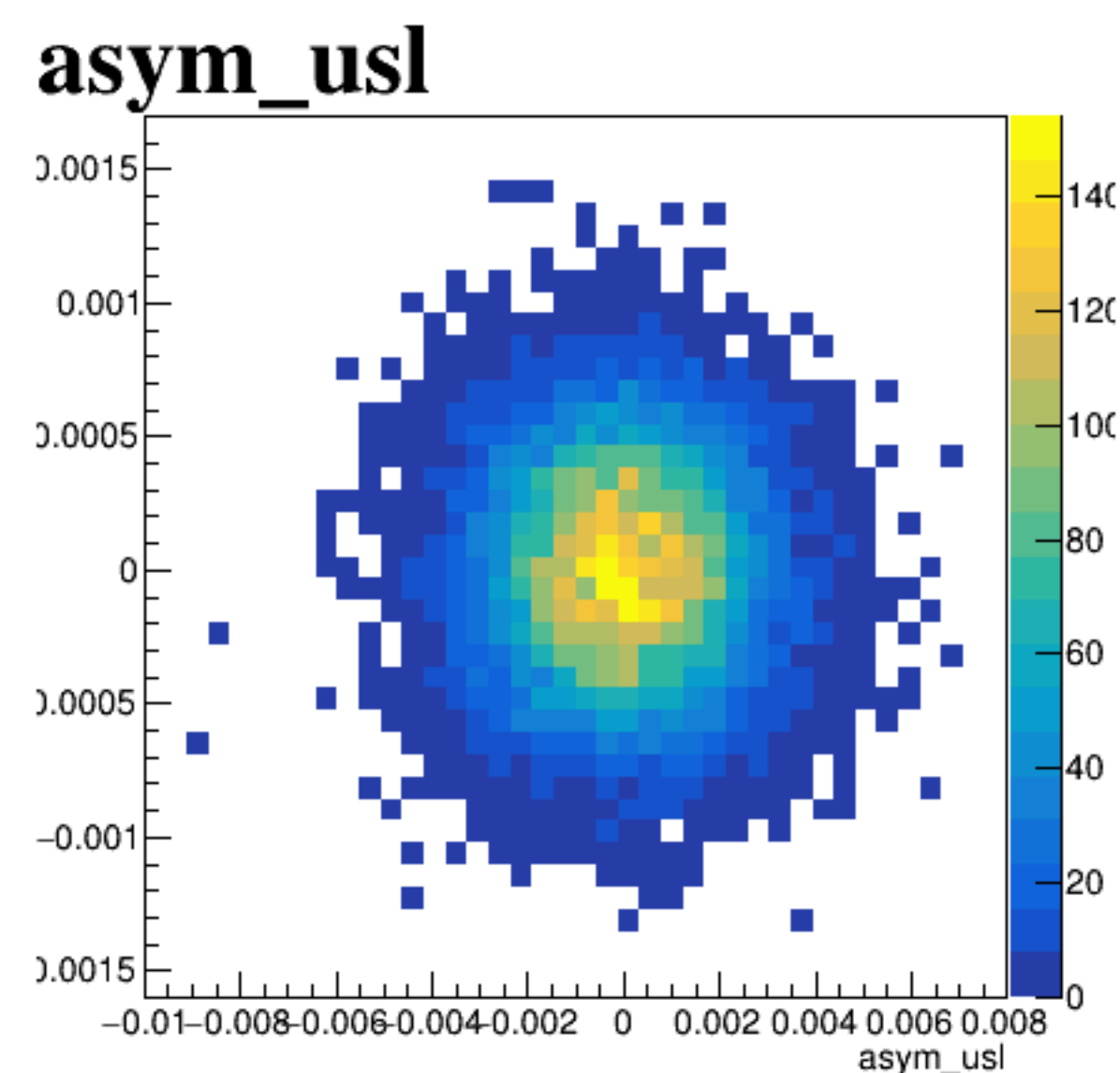
asym_dsr/ppm {ErrorFlag==0}



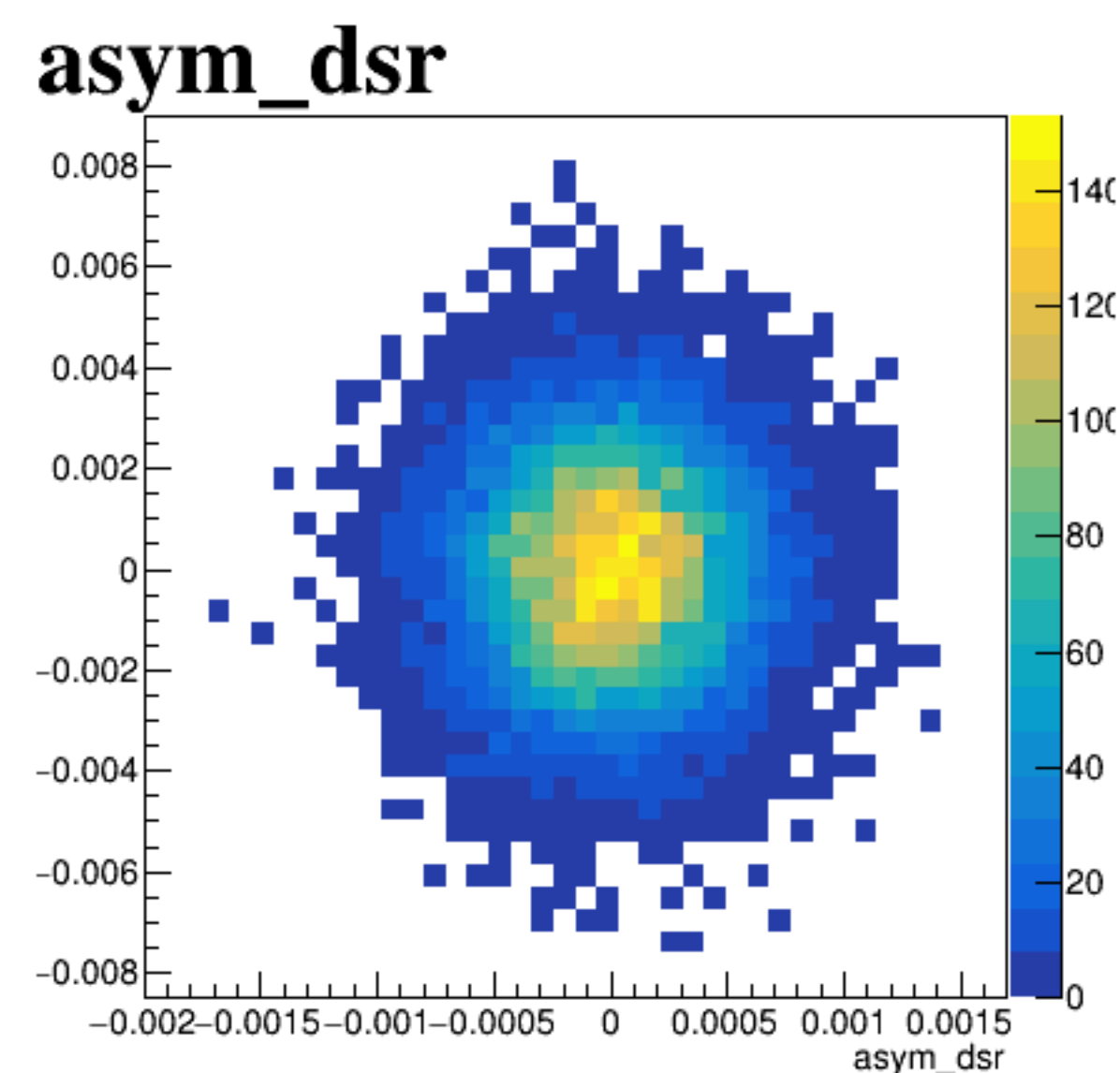
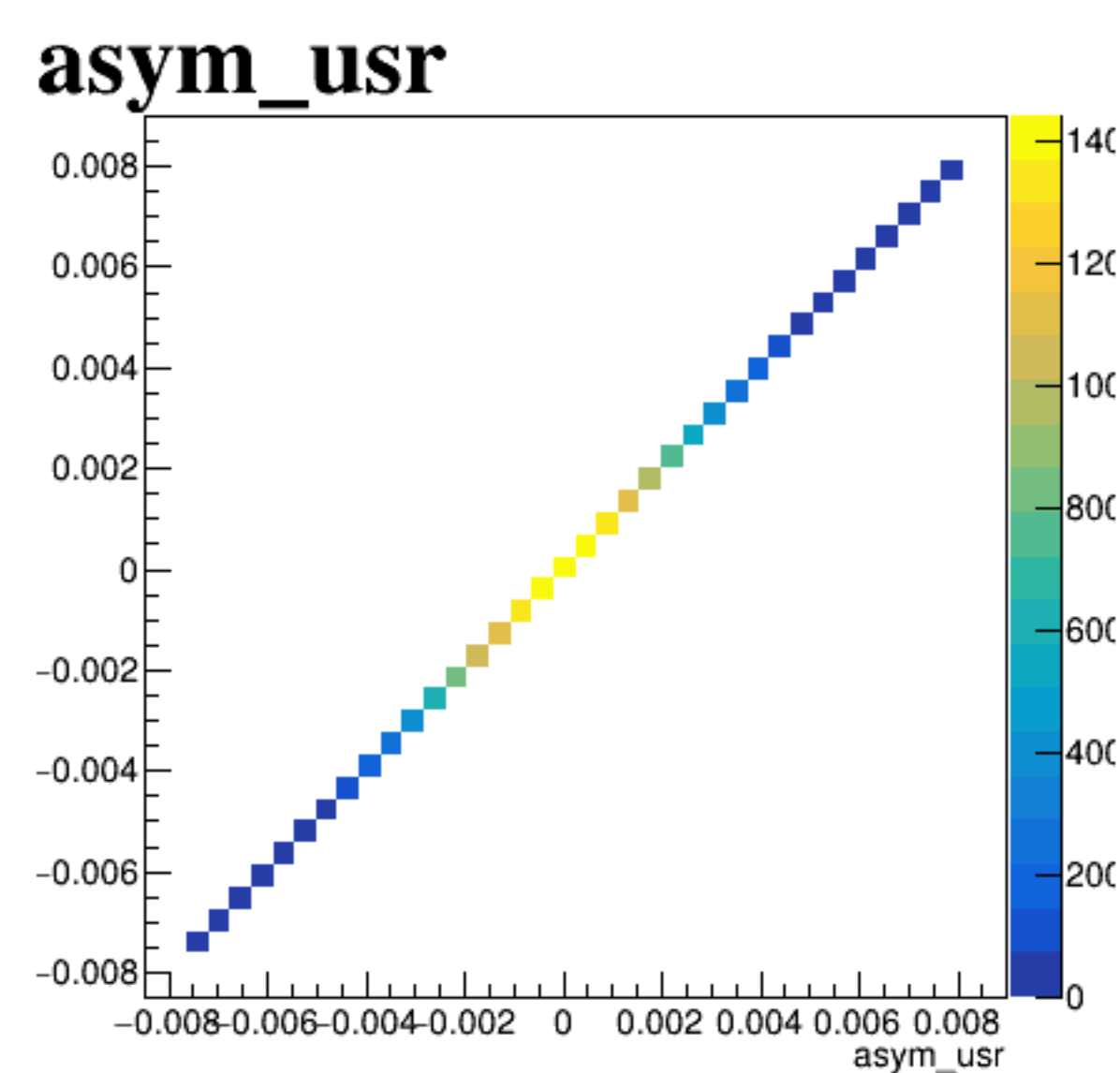
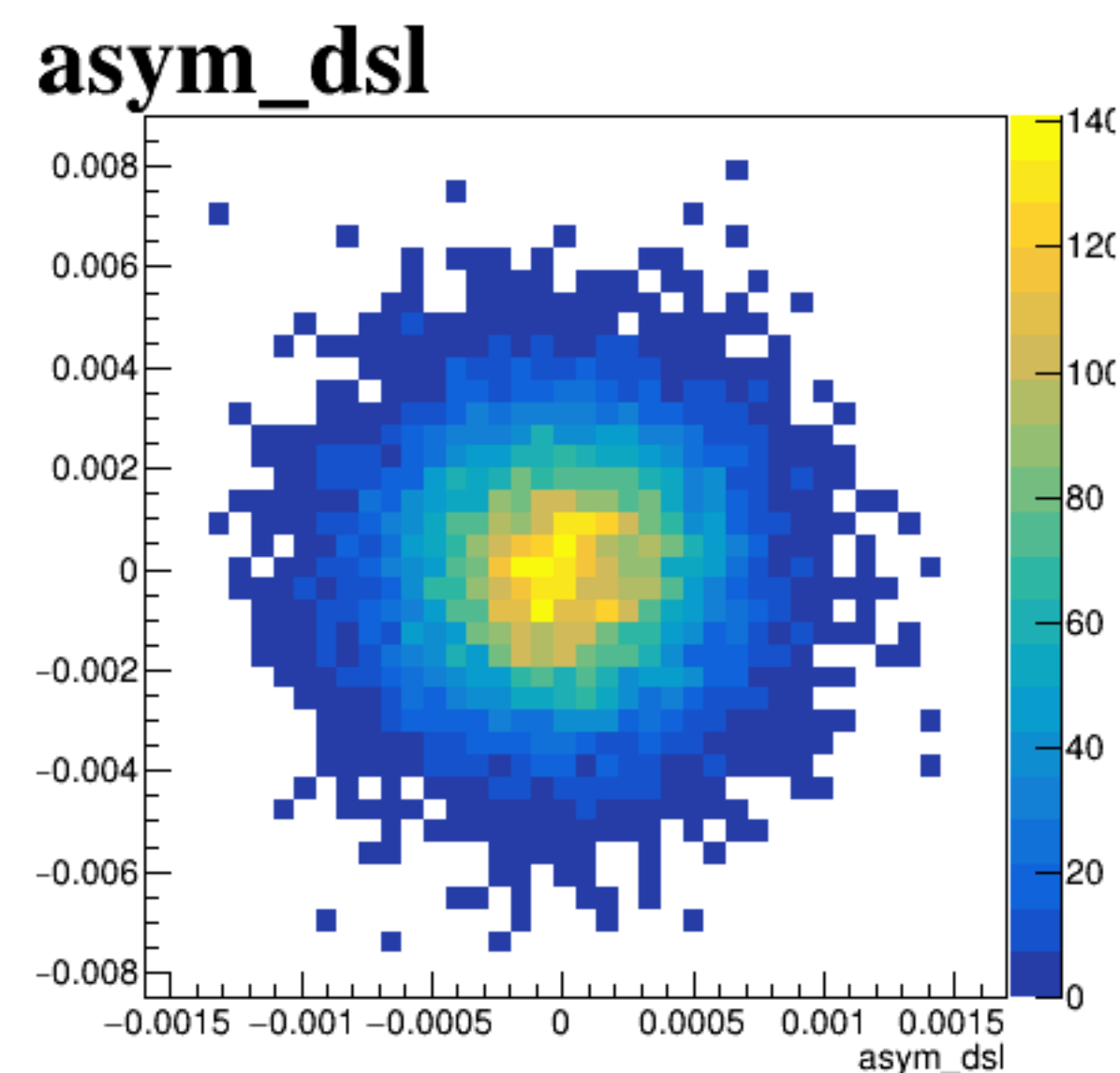
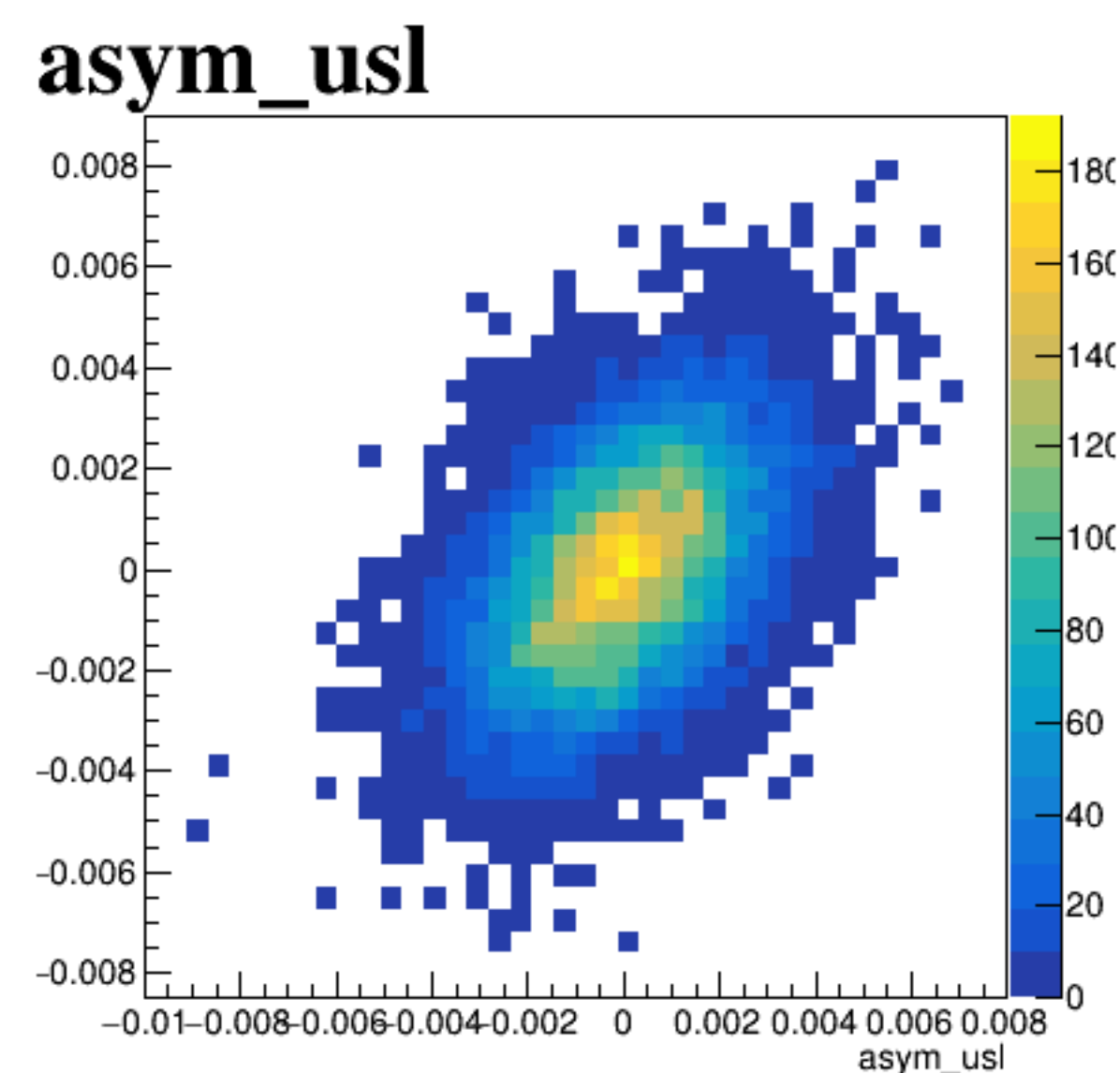
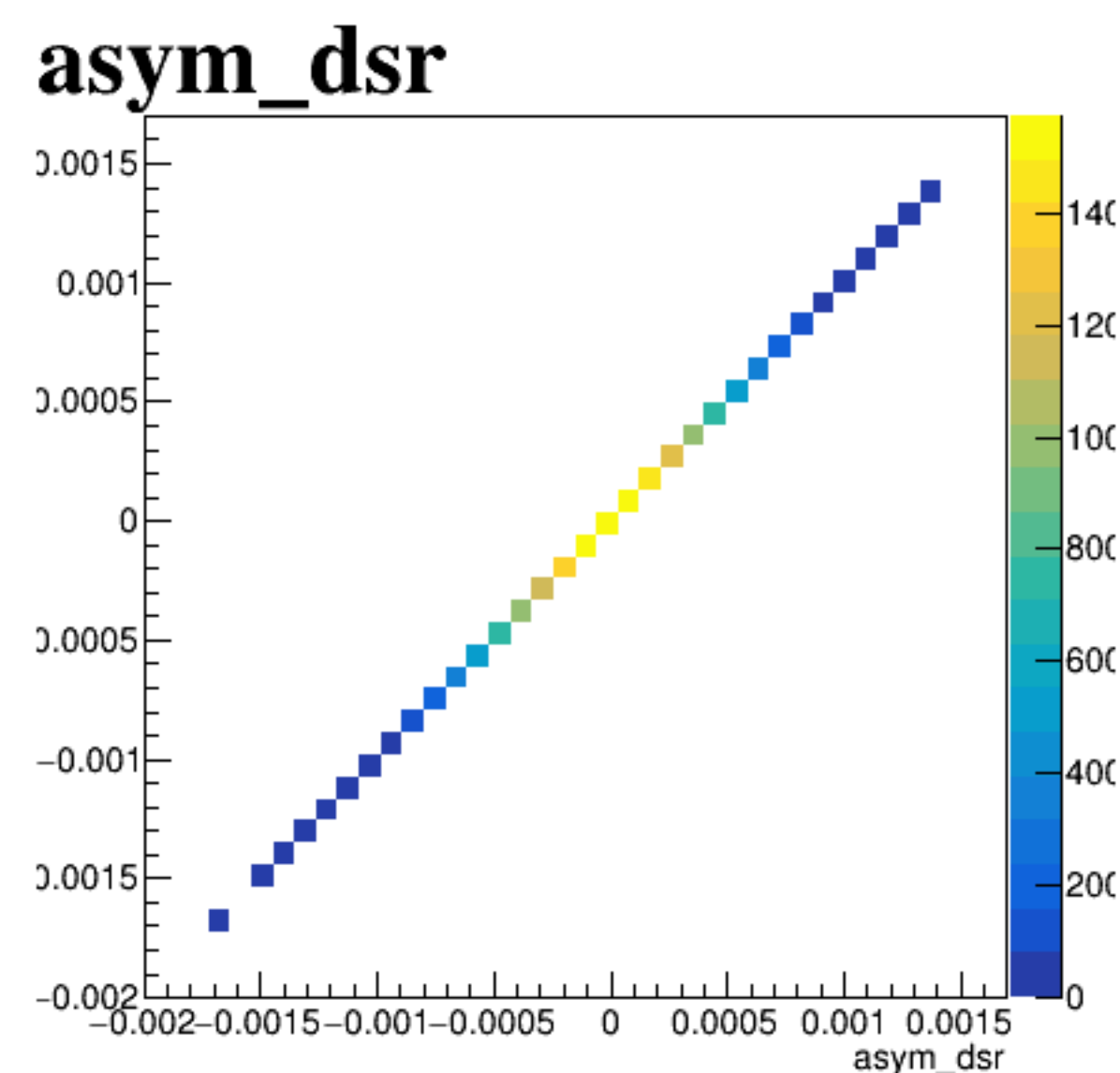
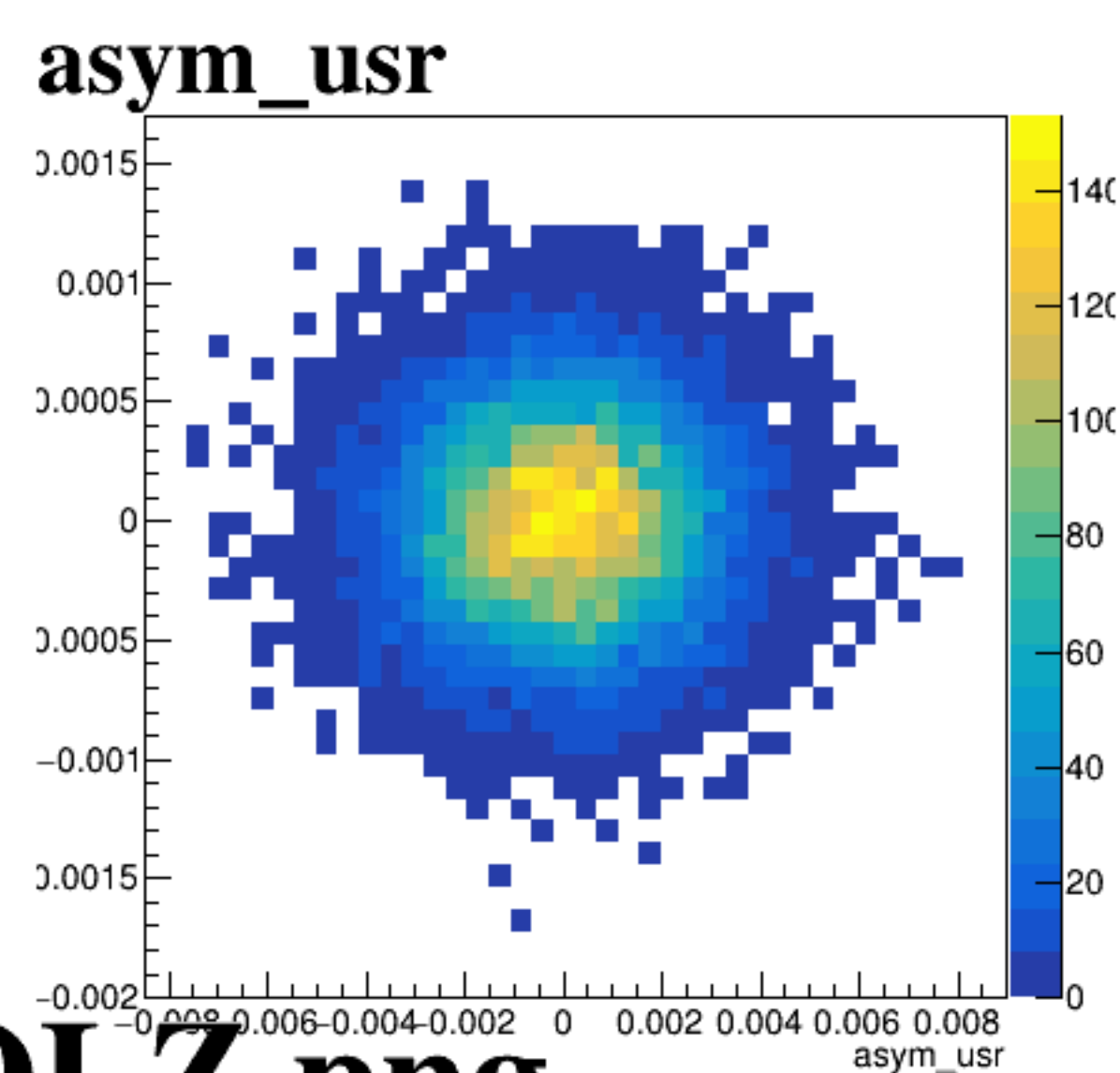
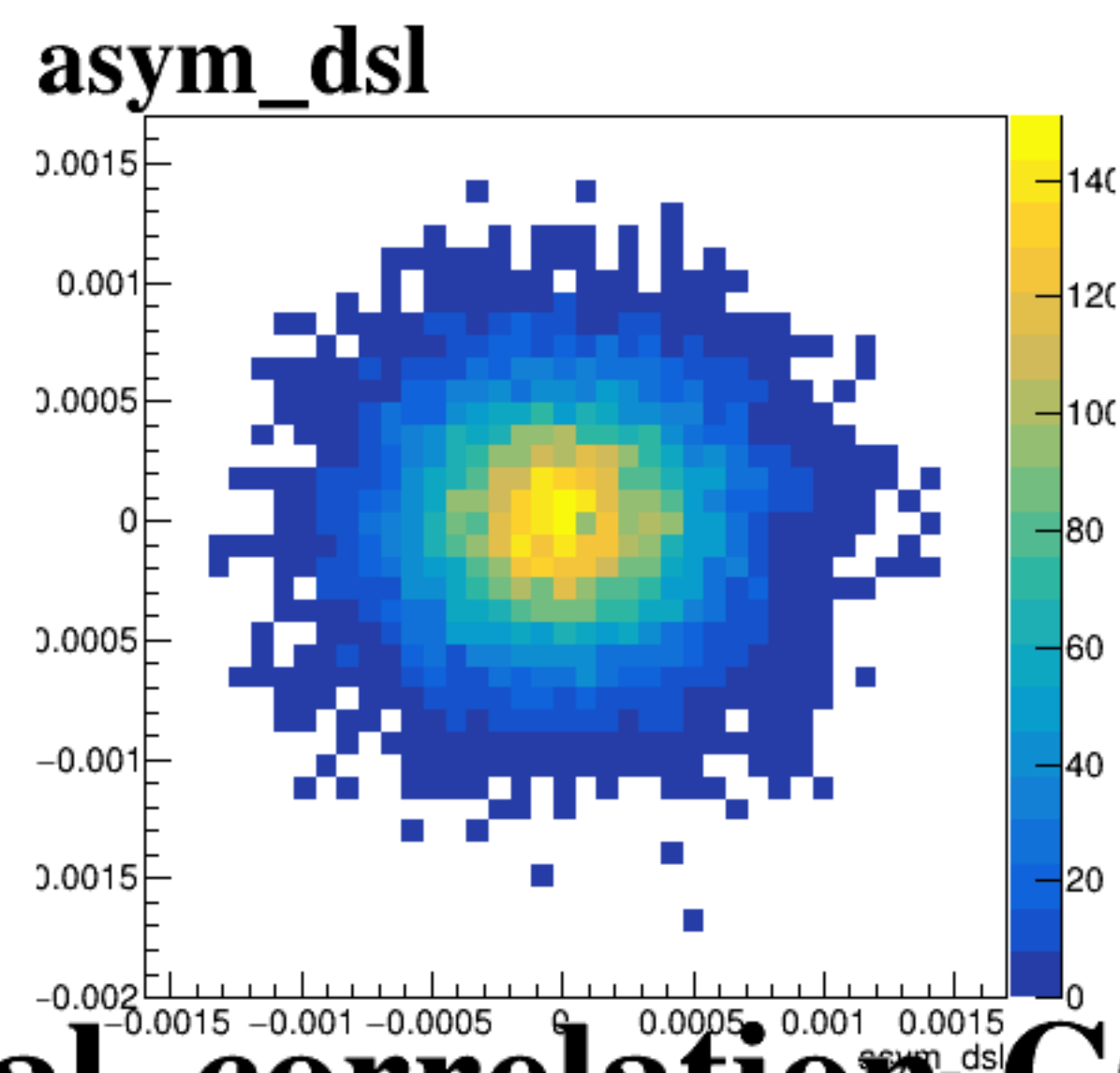
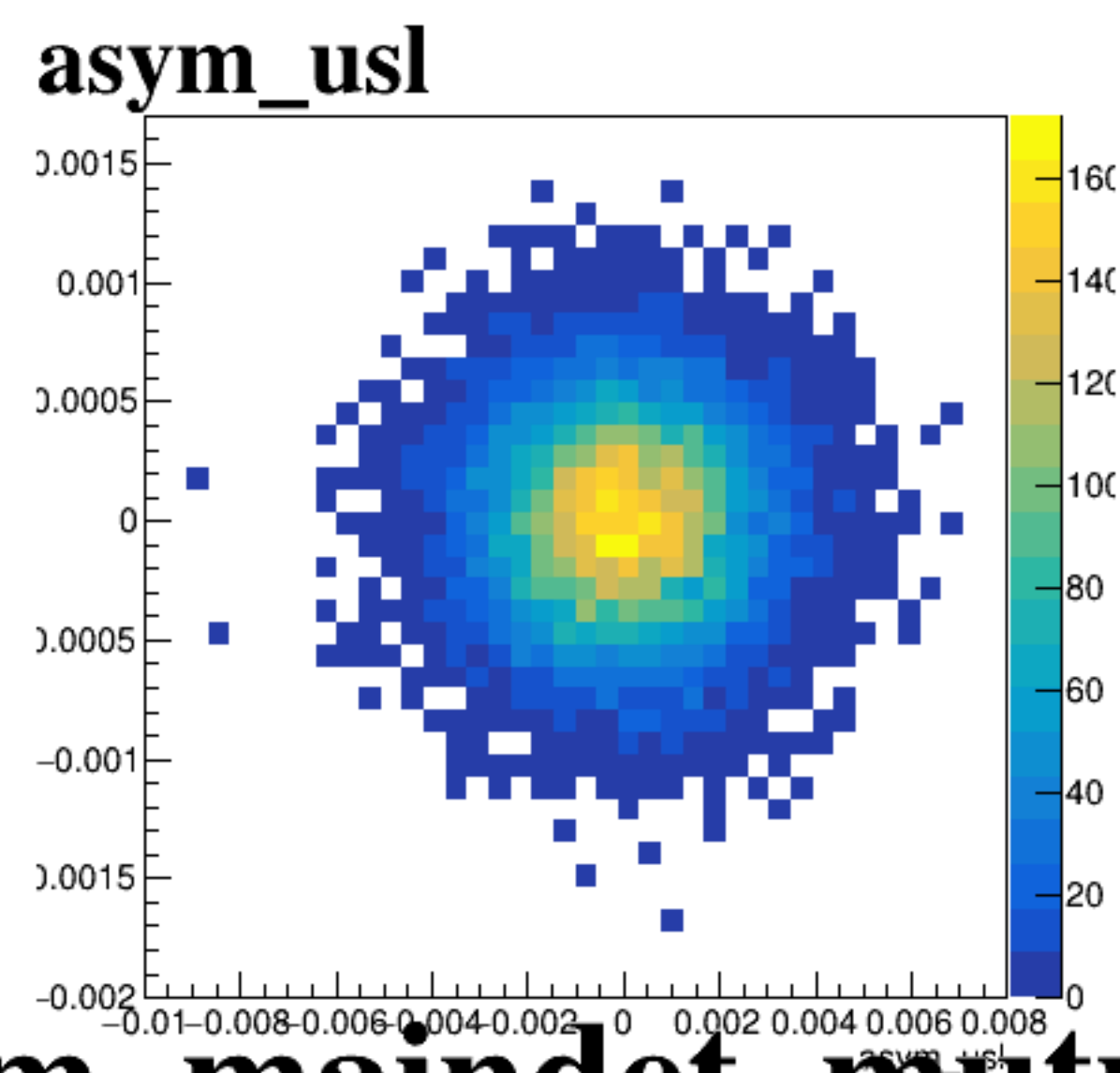
asym_usl



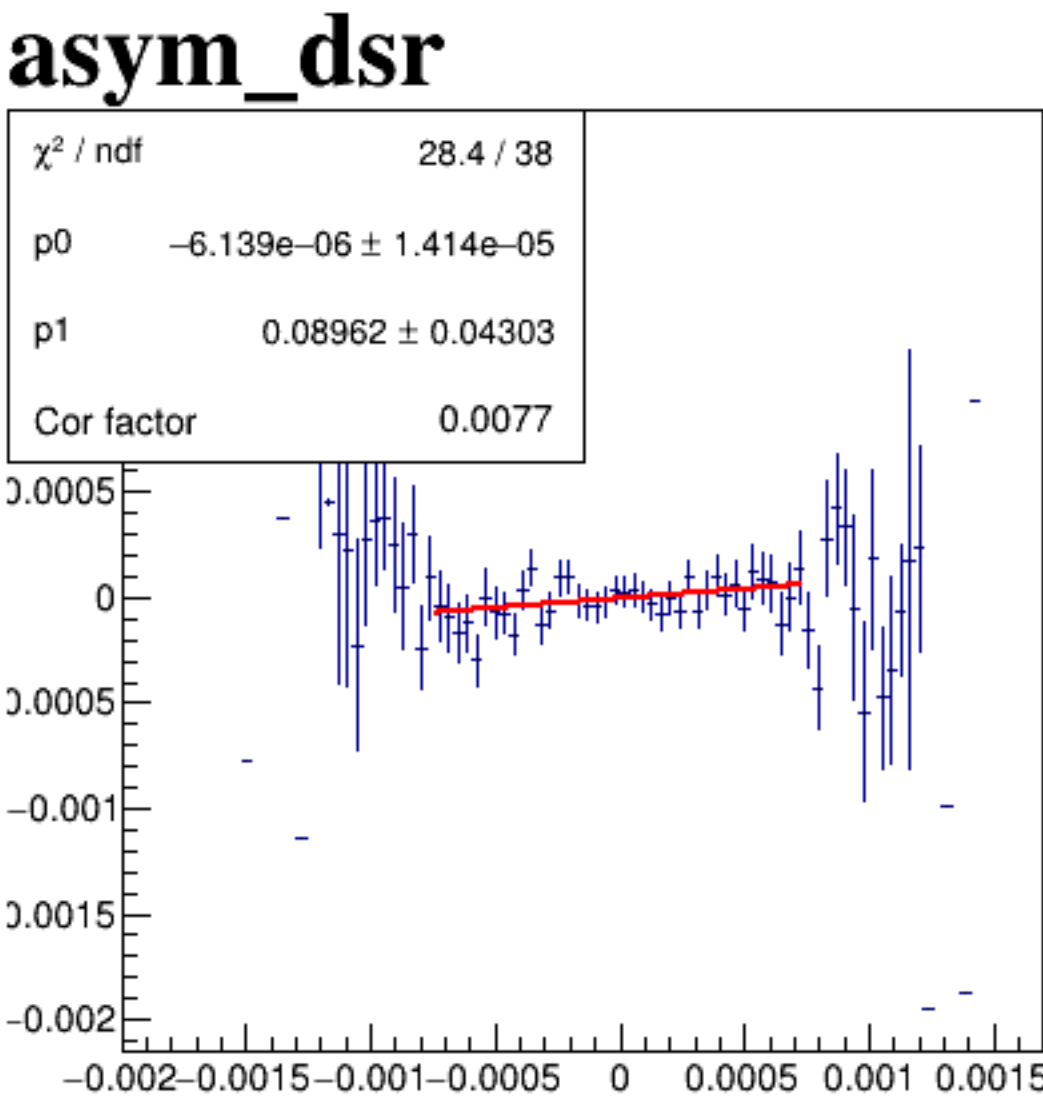
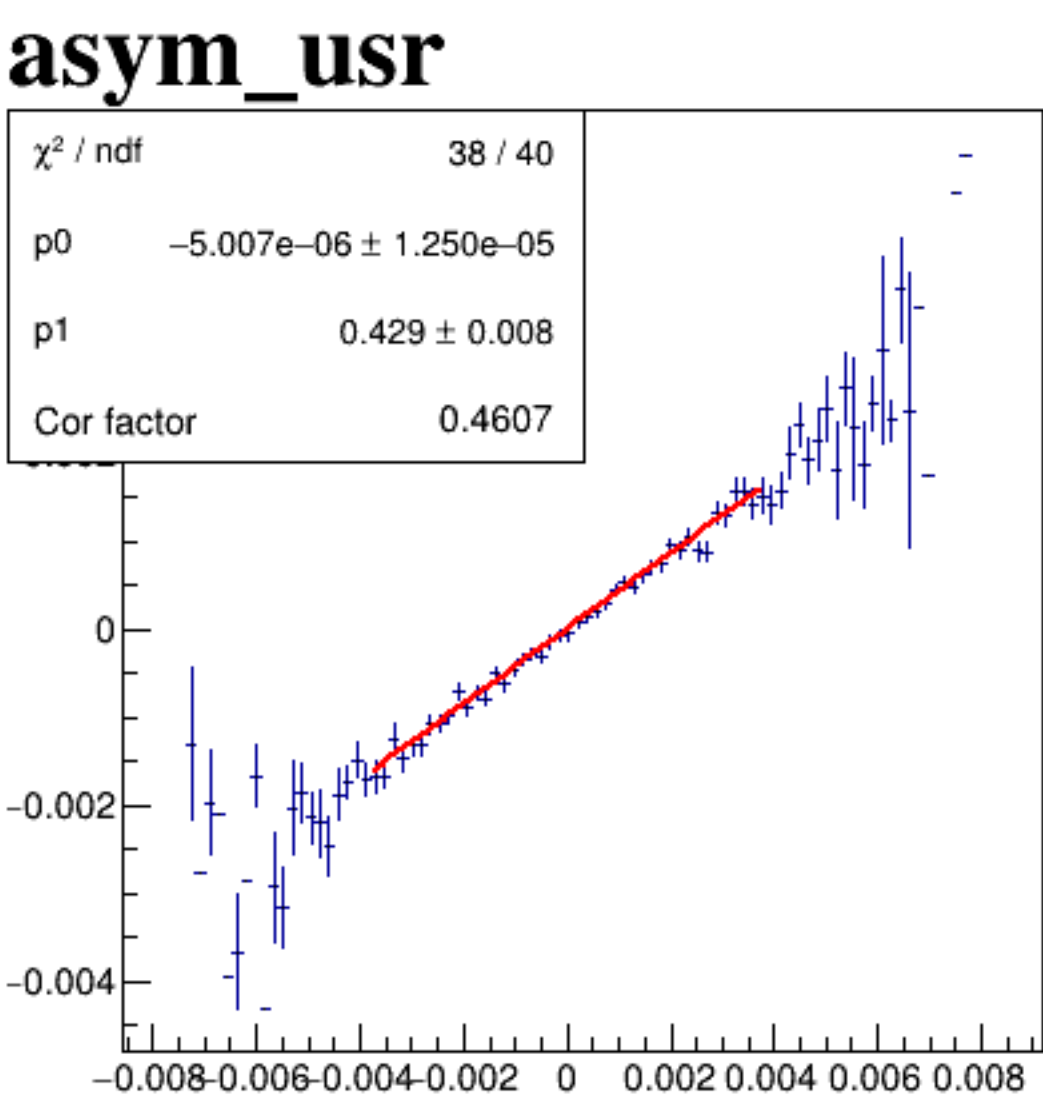
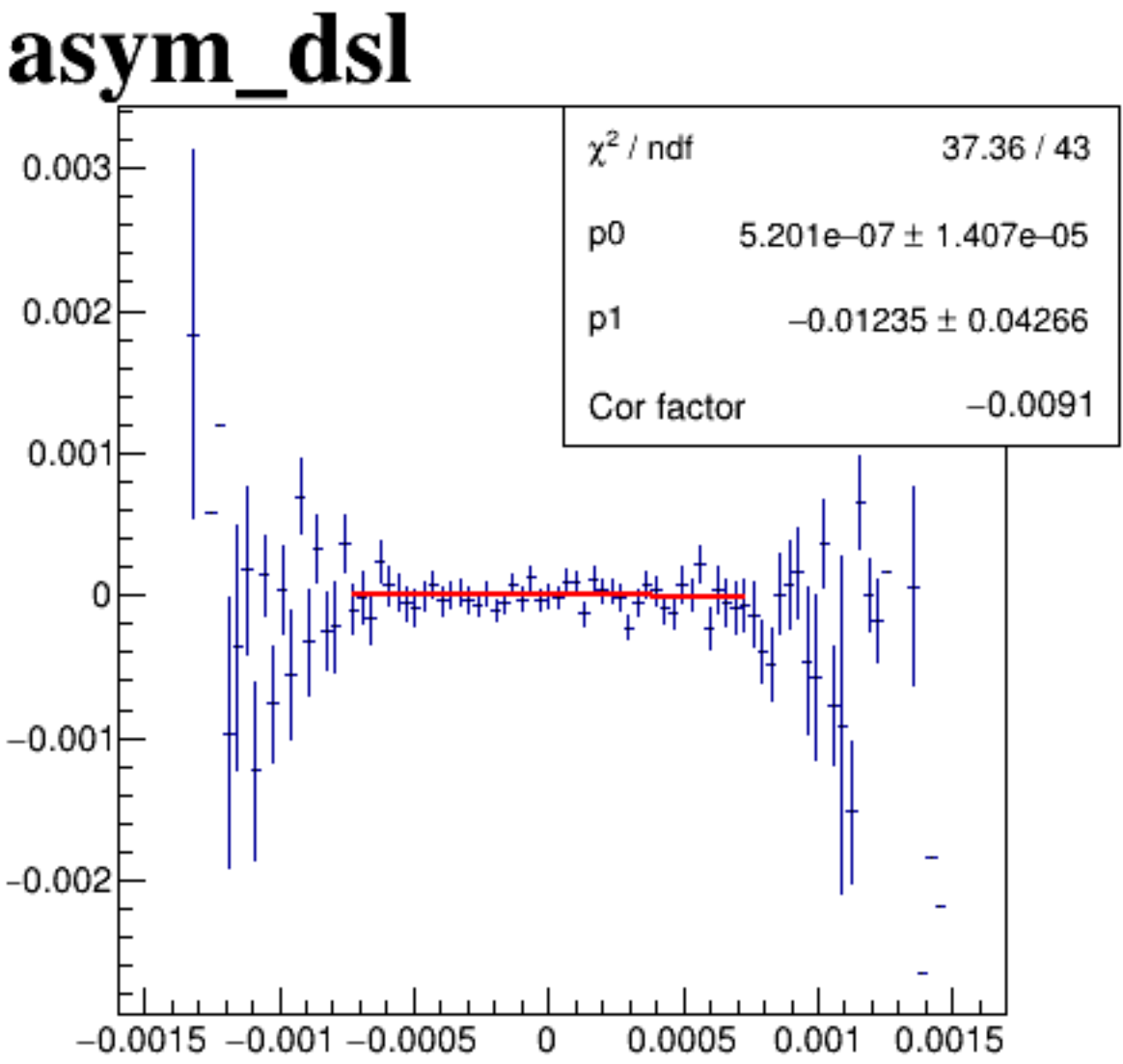
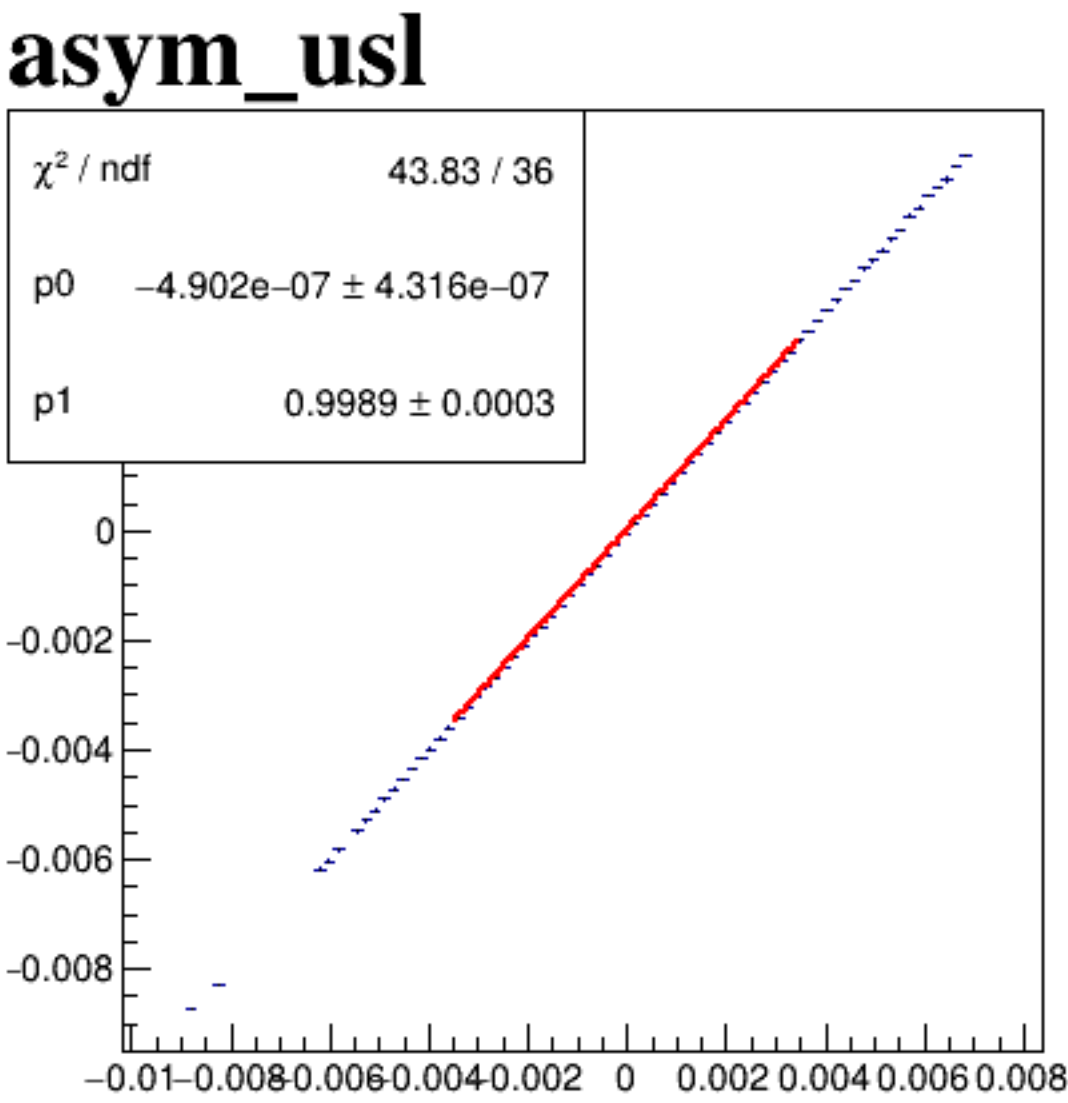
asym_dsl



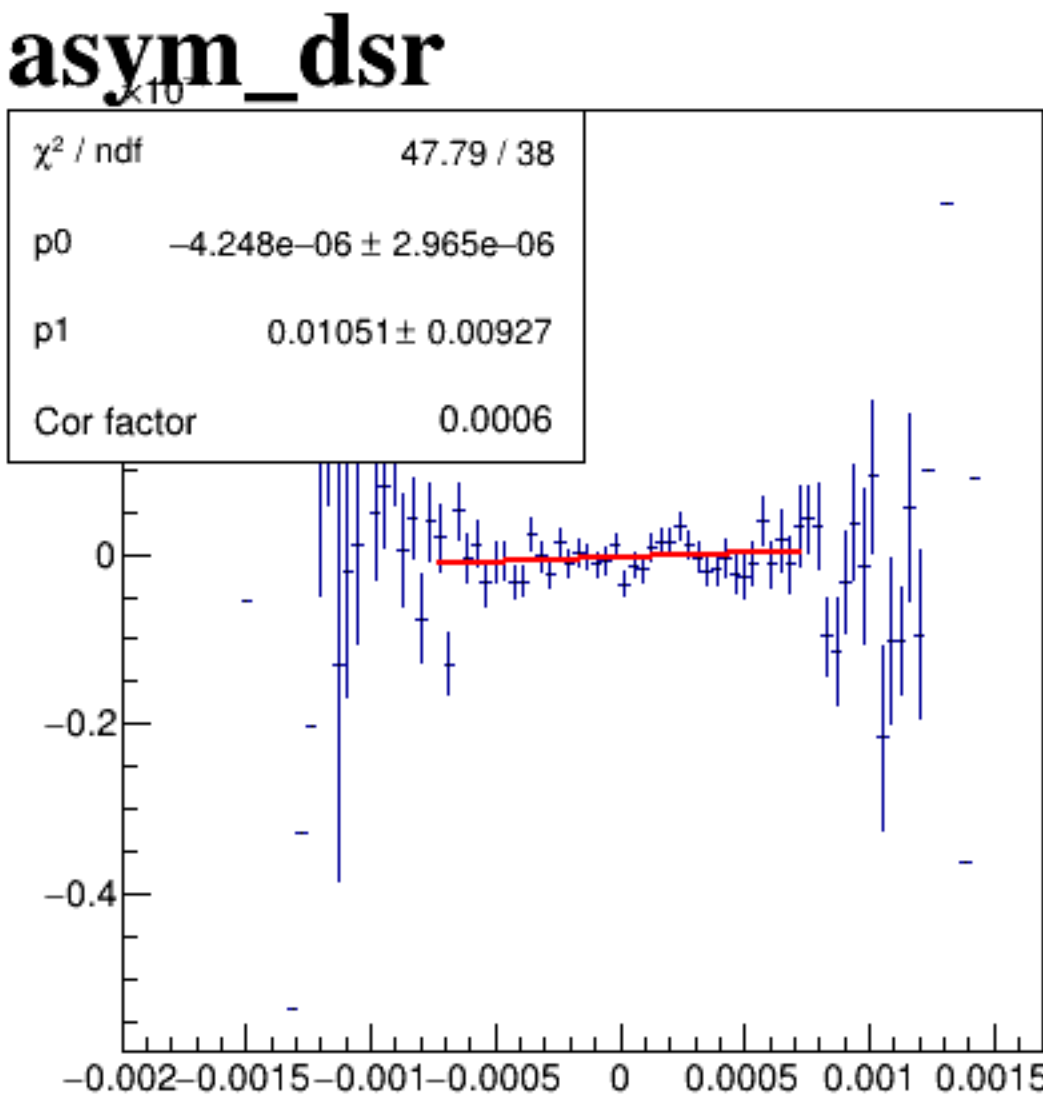
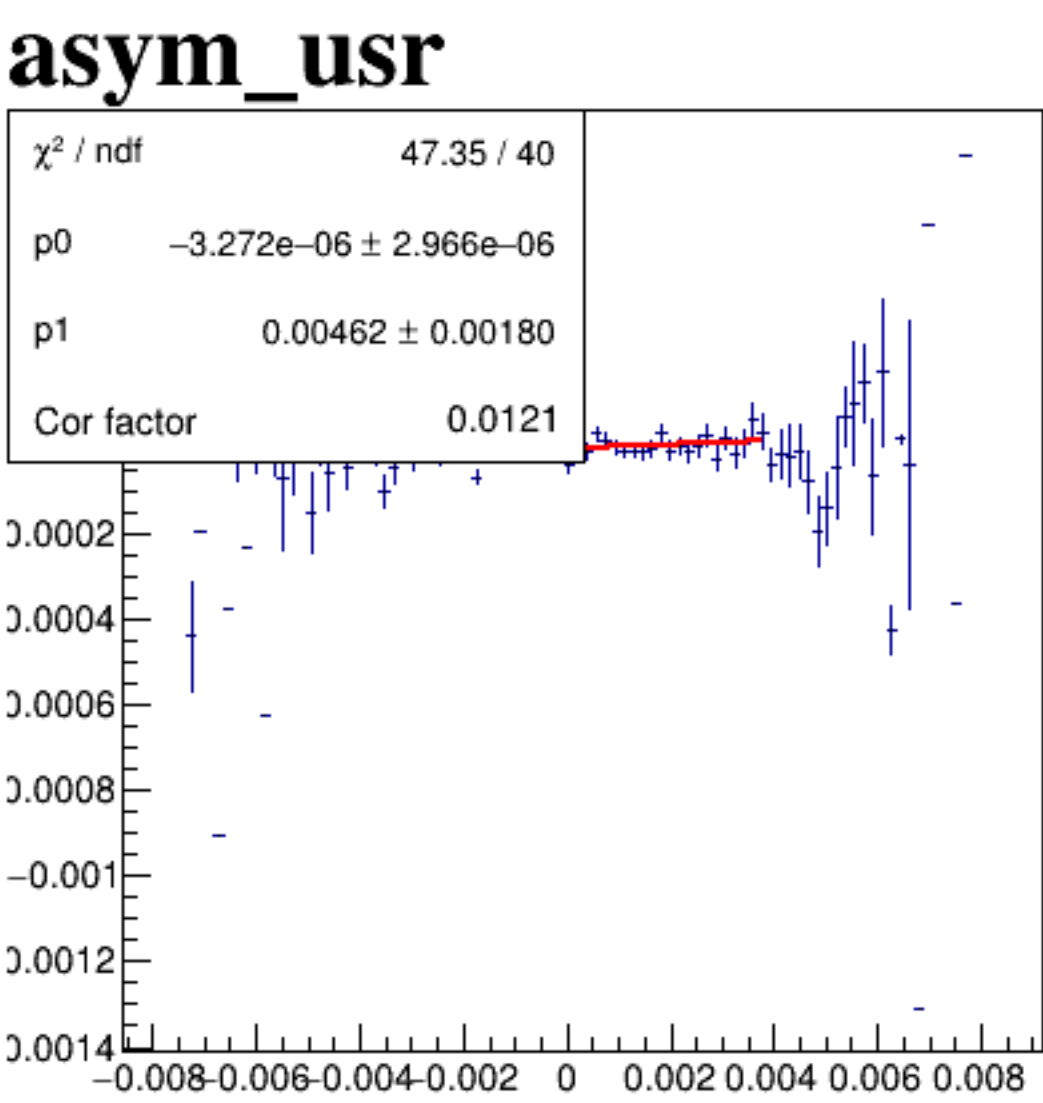
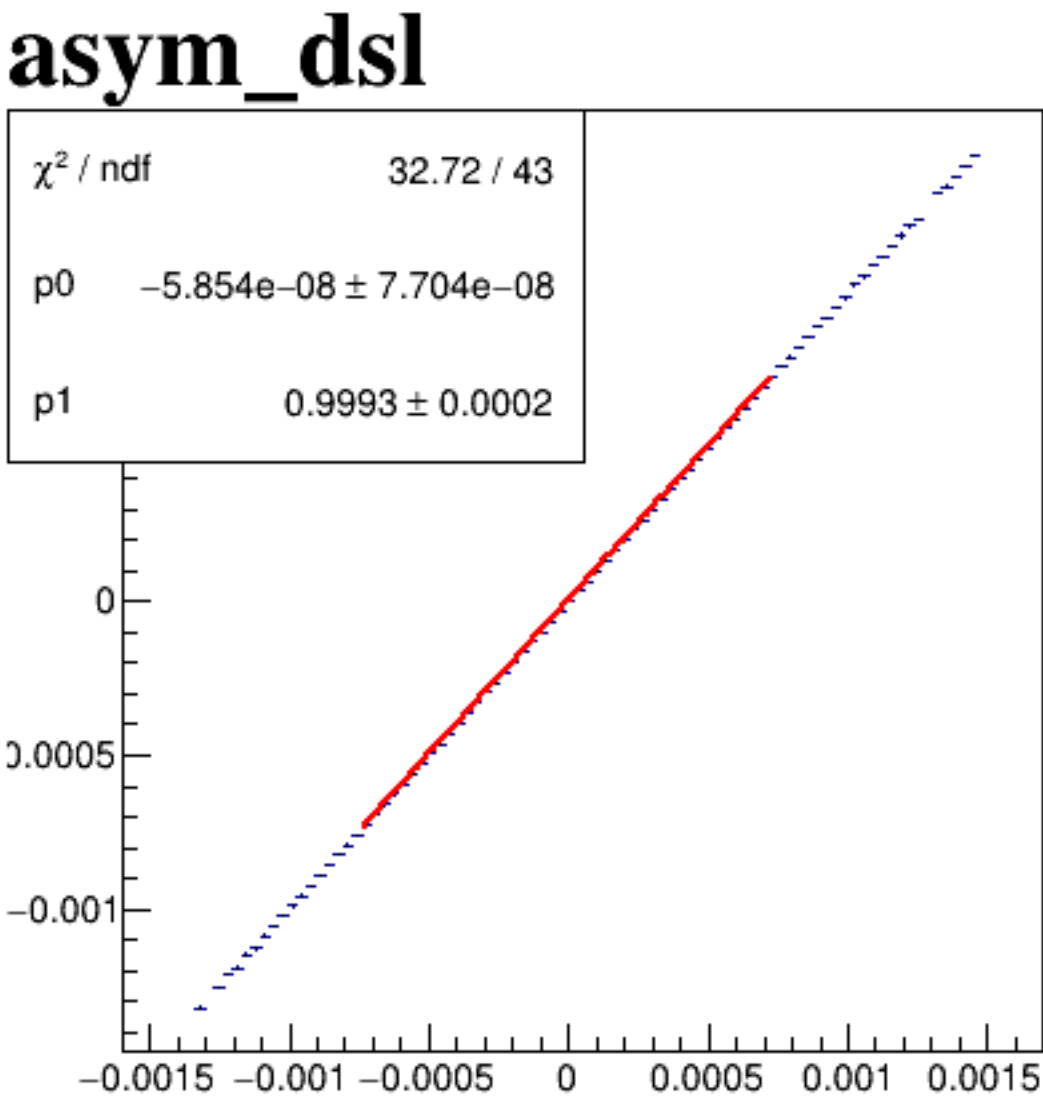
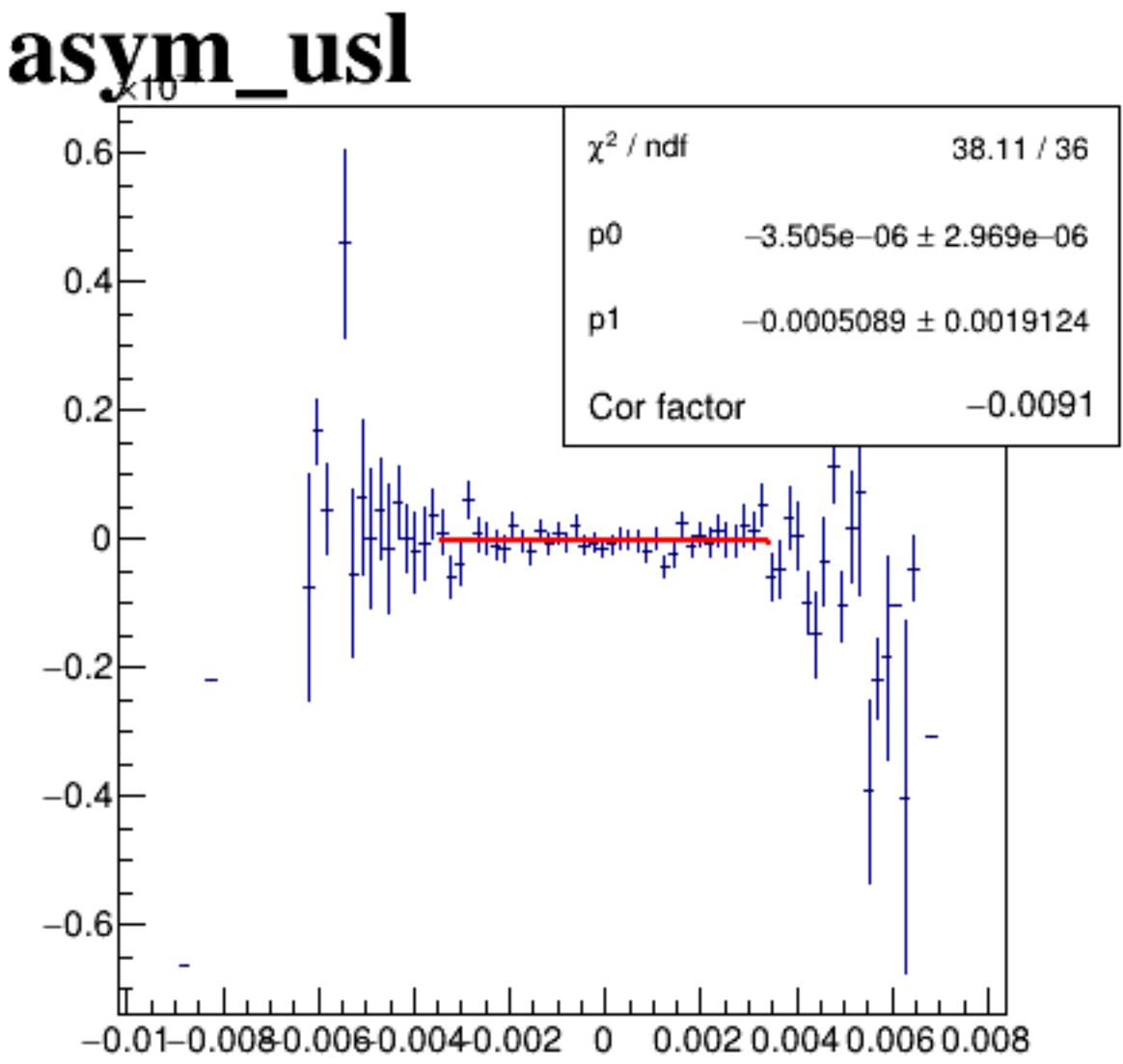
asym_usr

**asym_dsr**

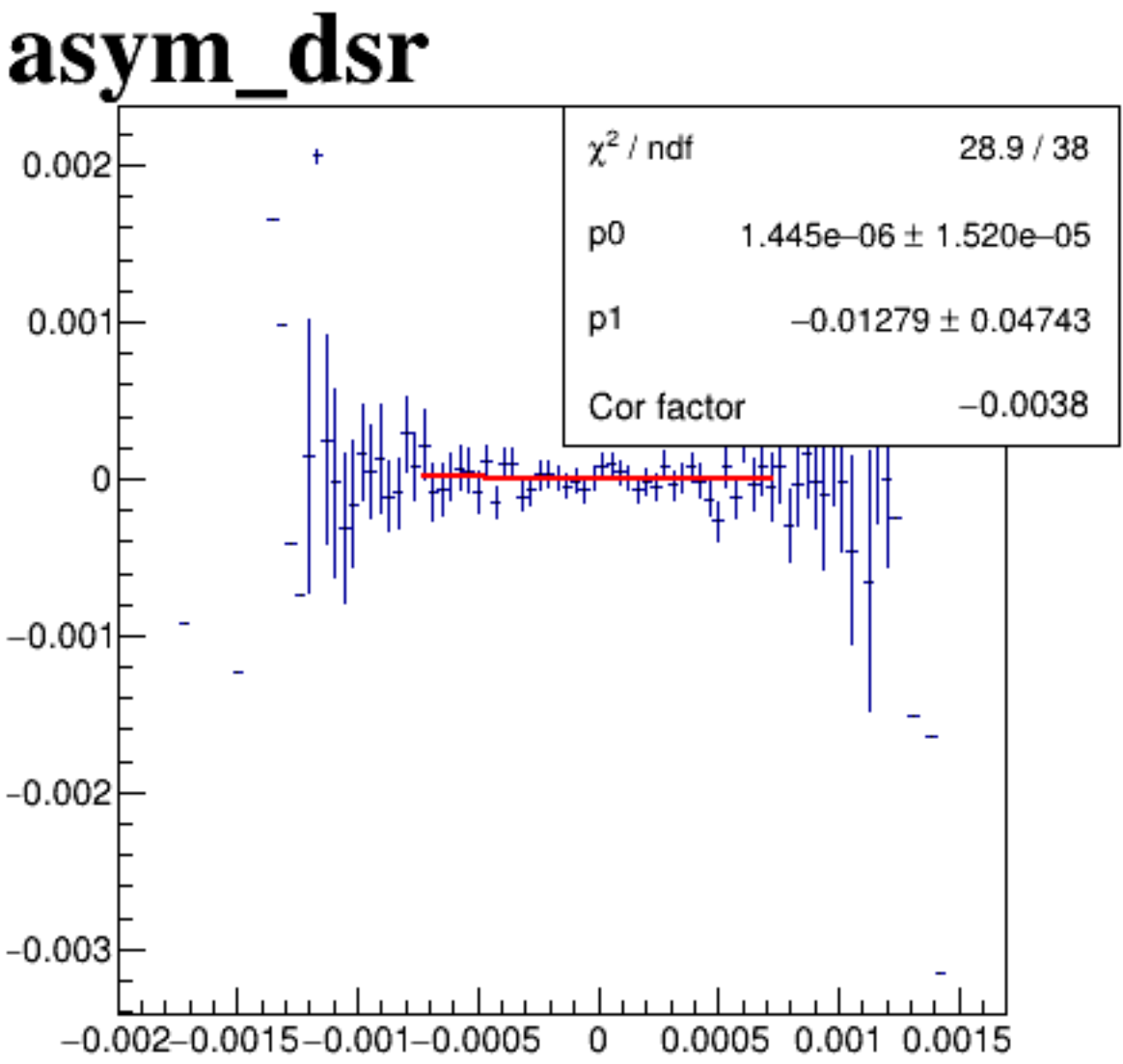
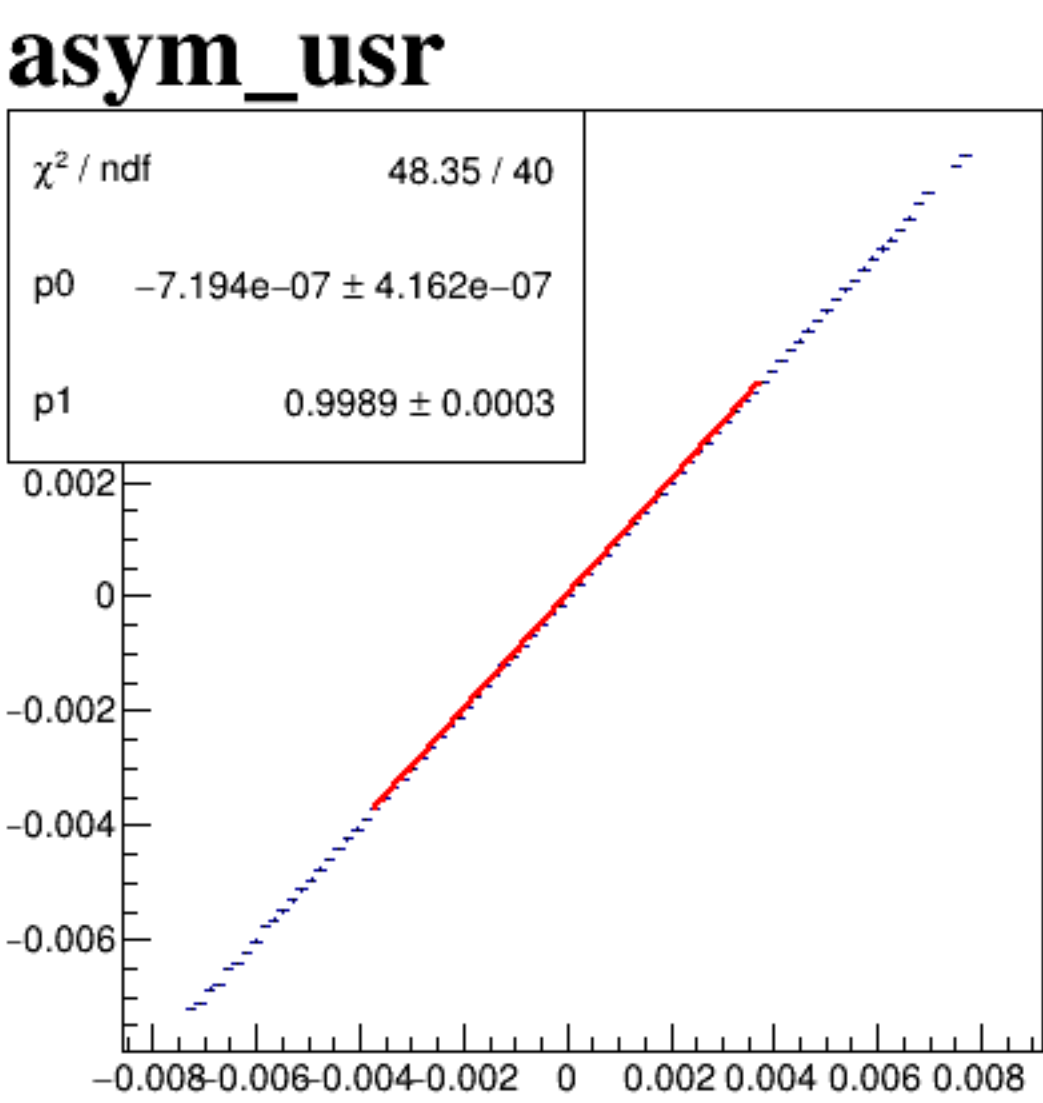
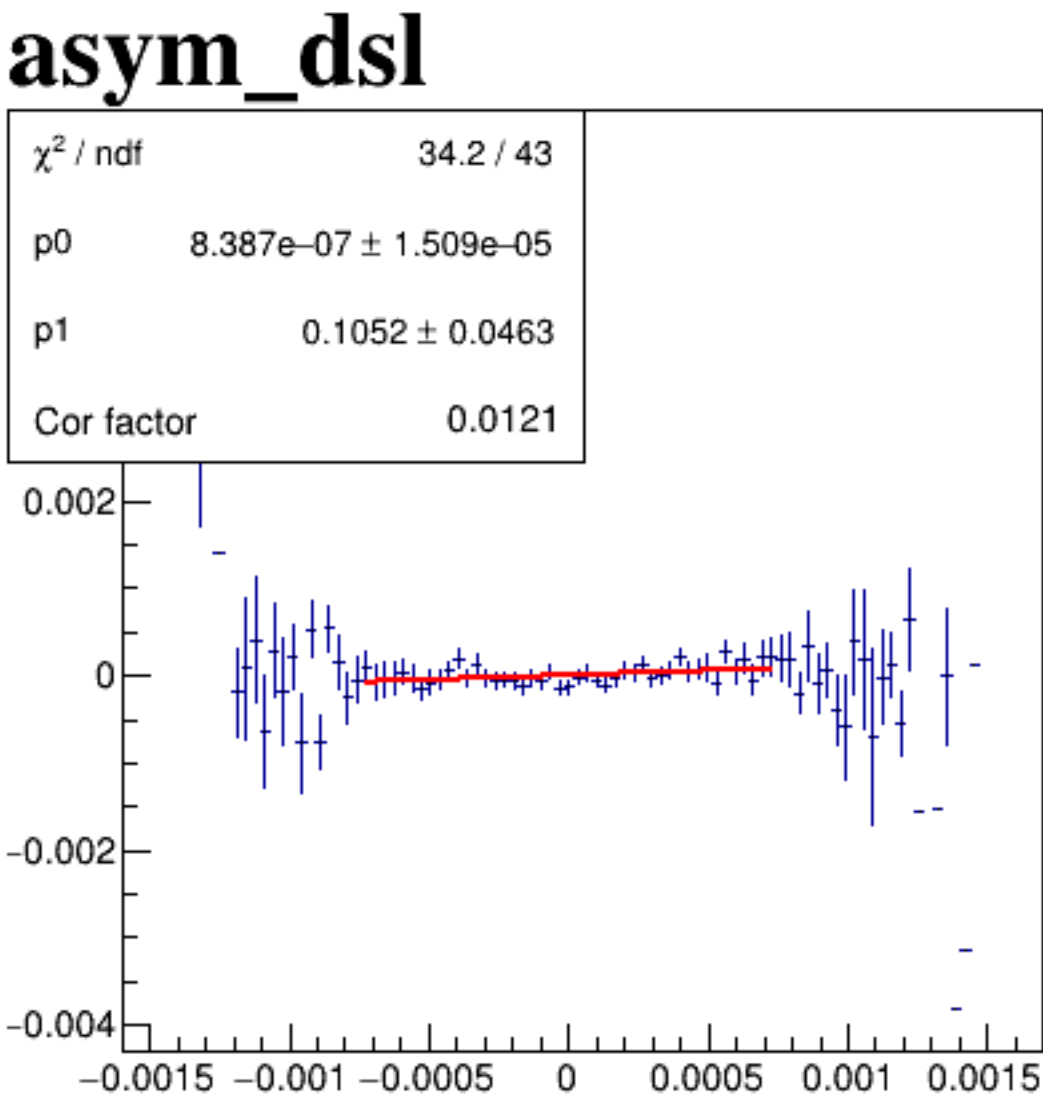
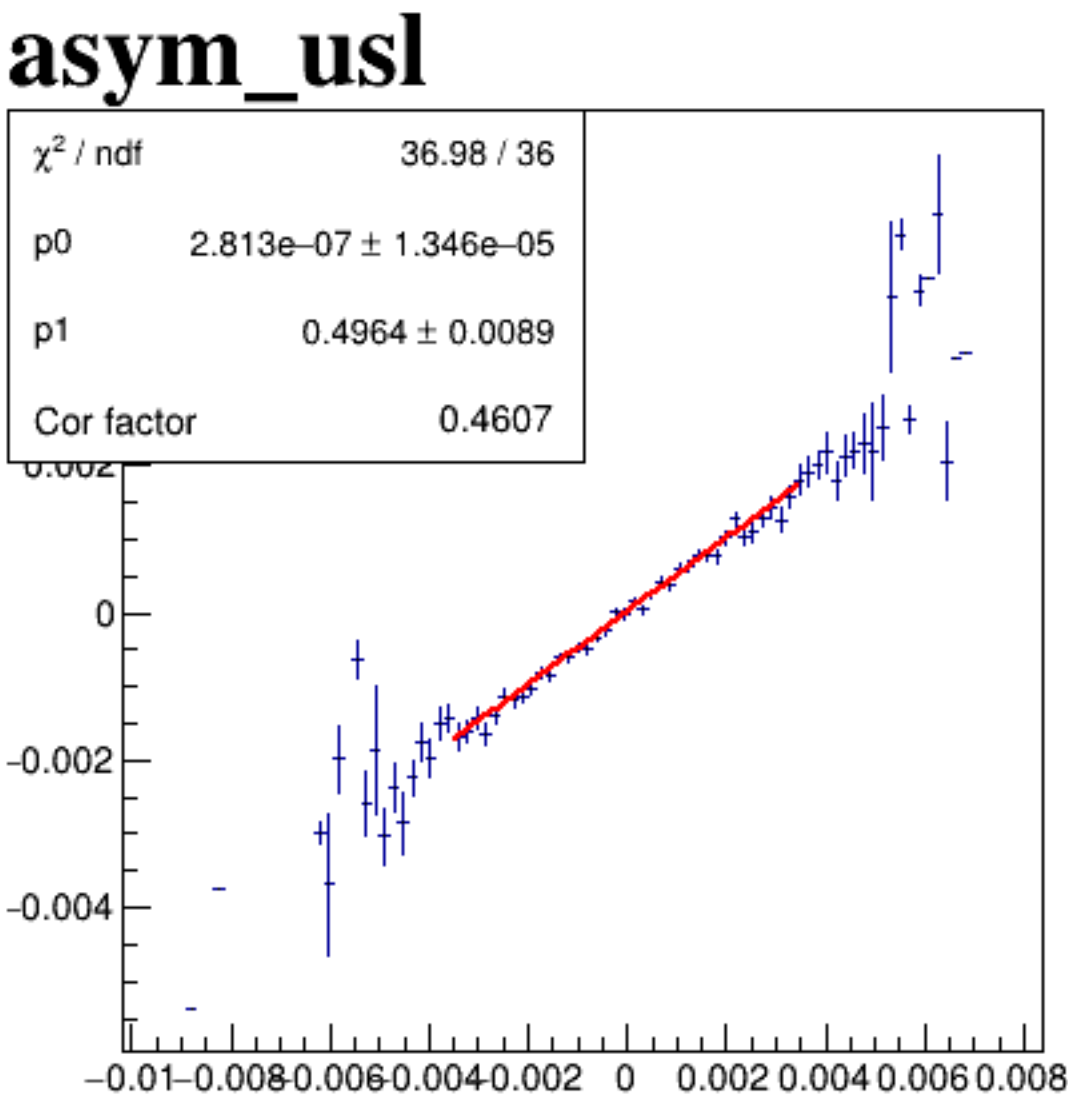
asym_usl



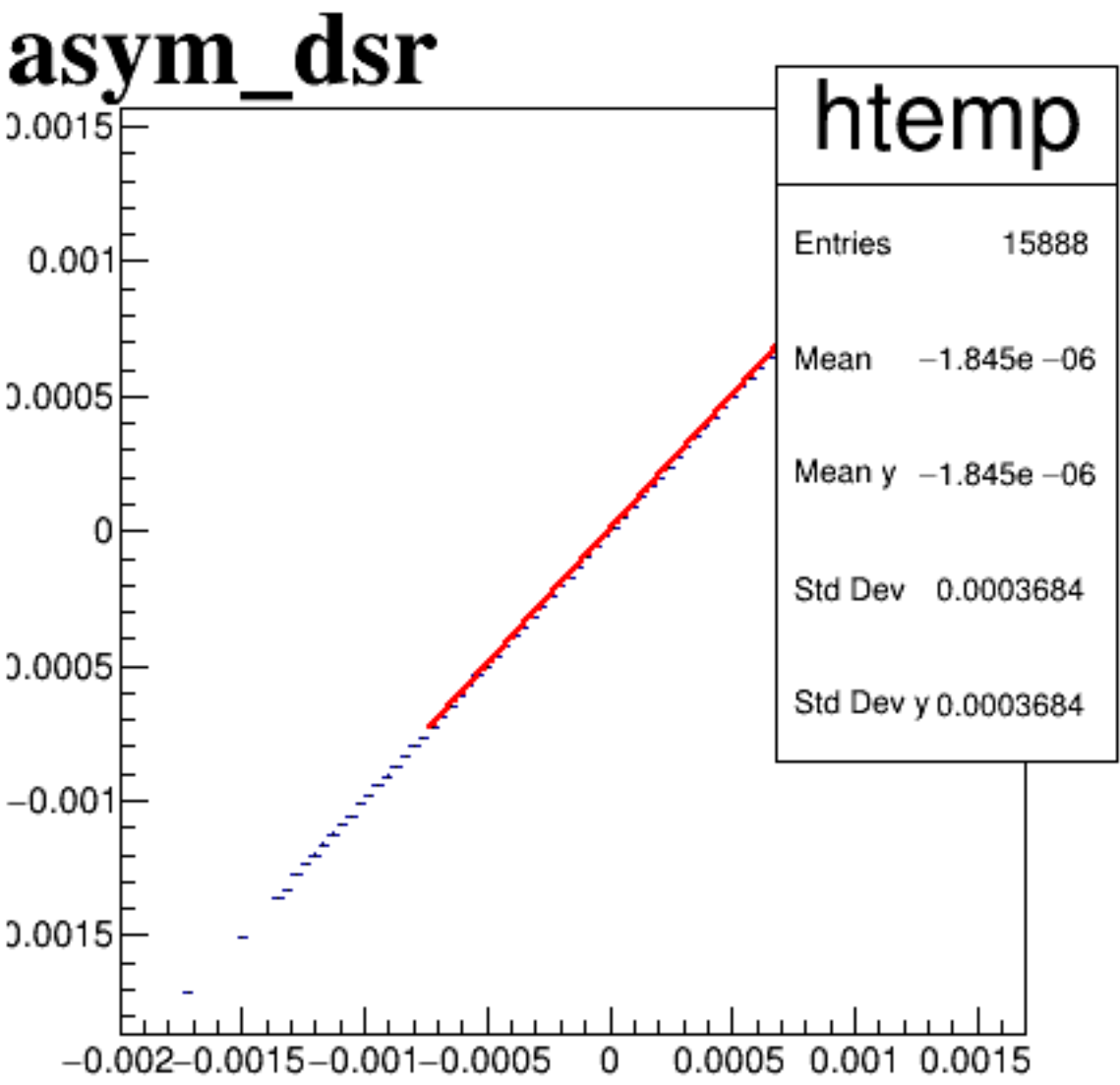
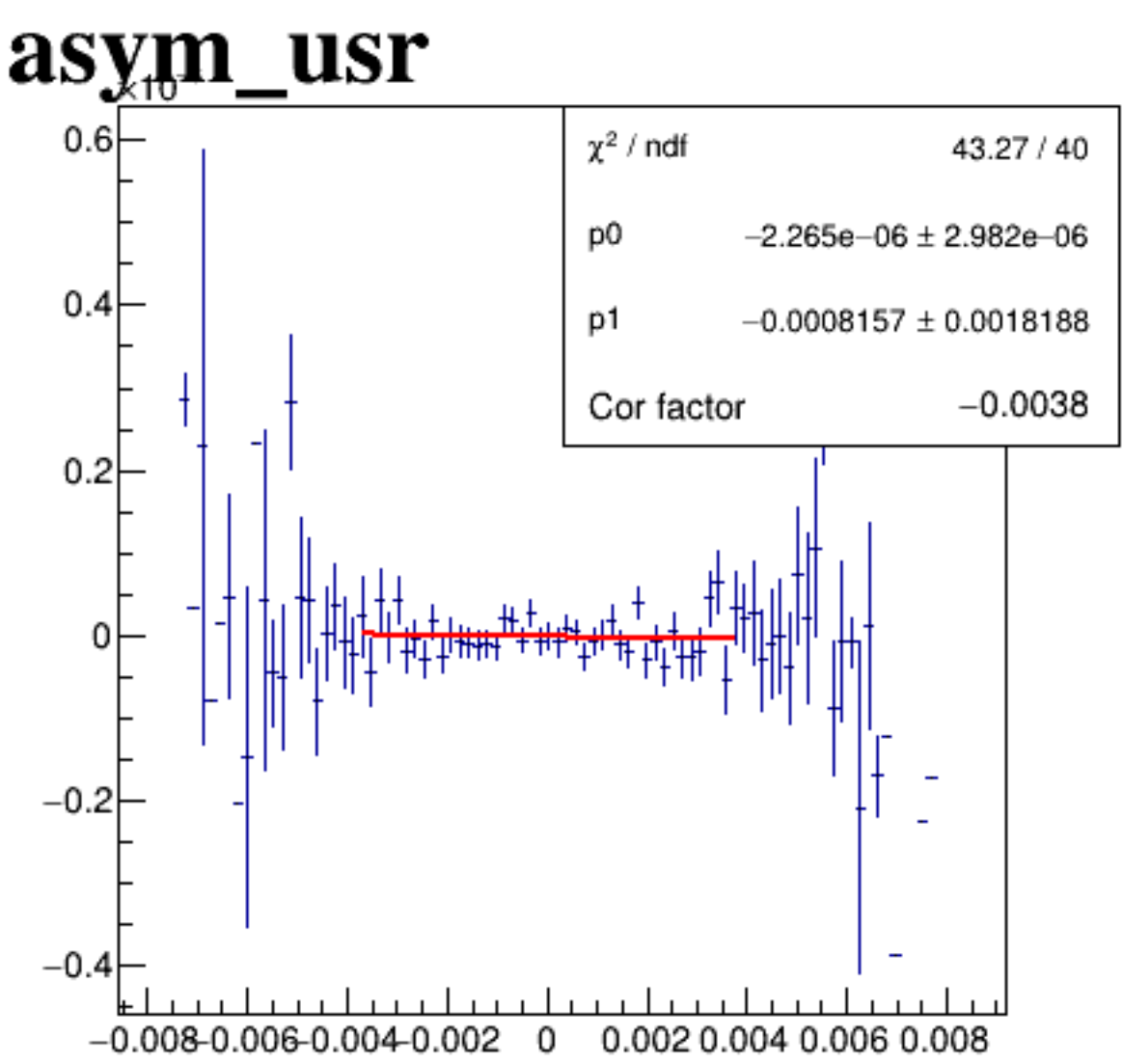
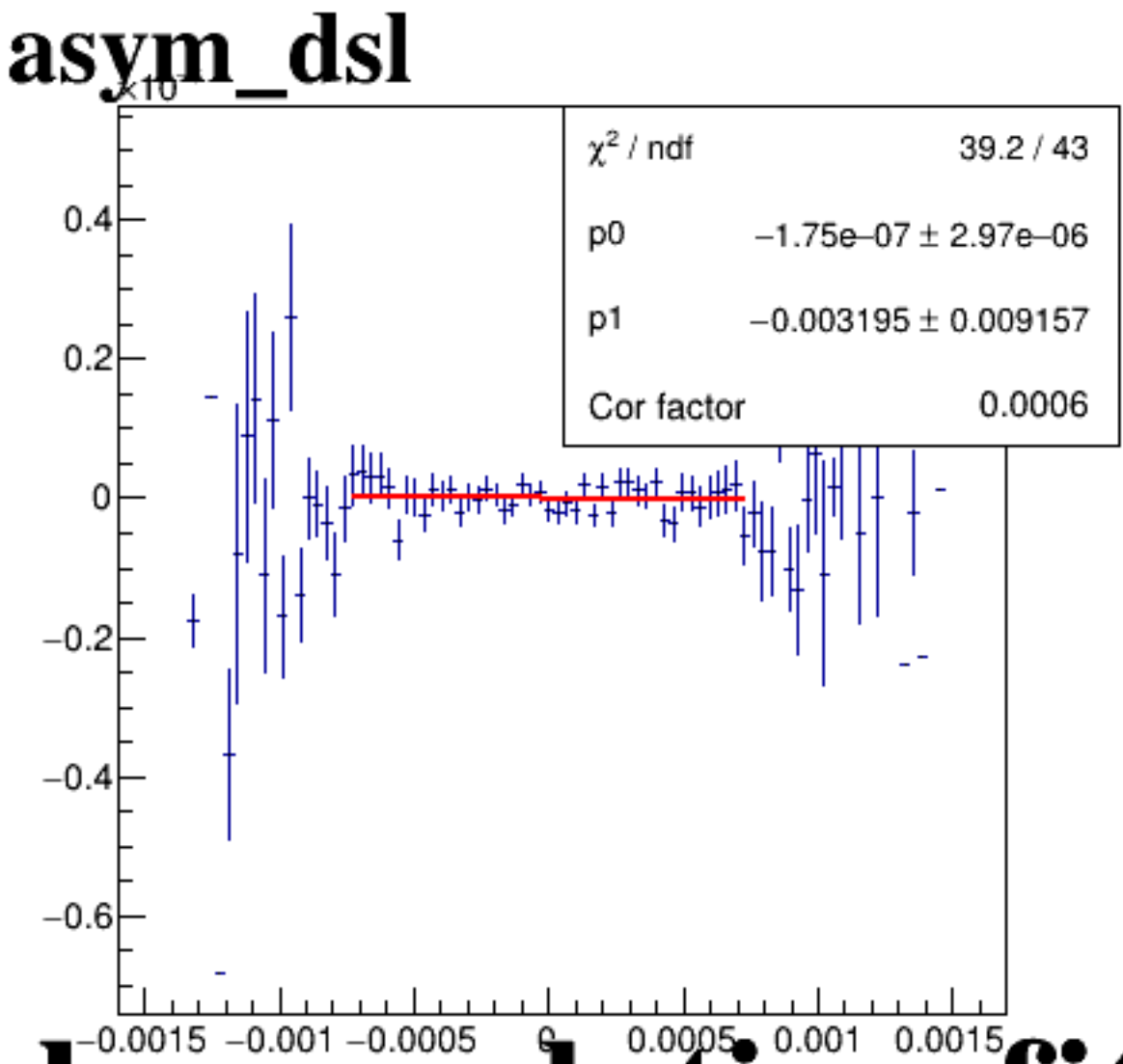
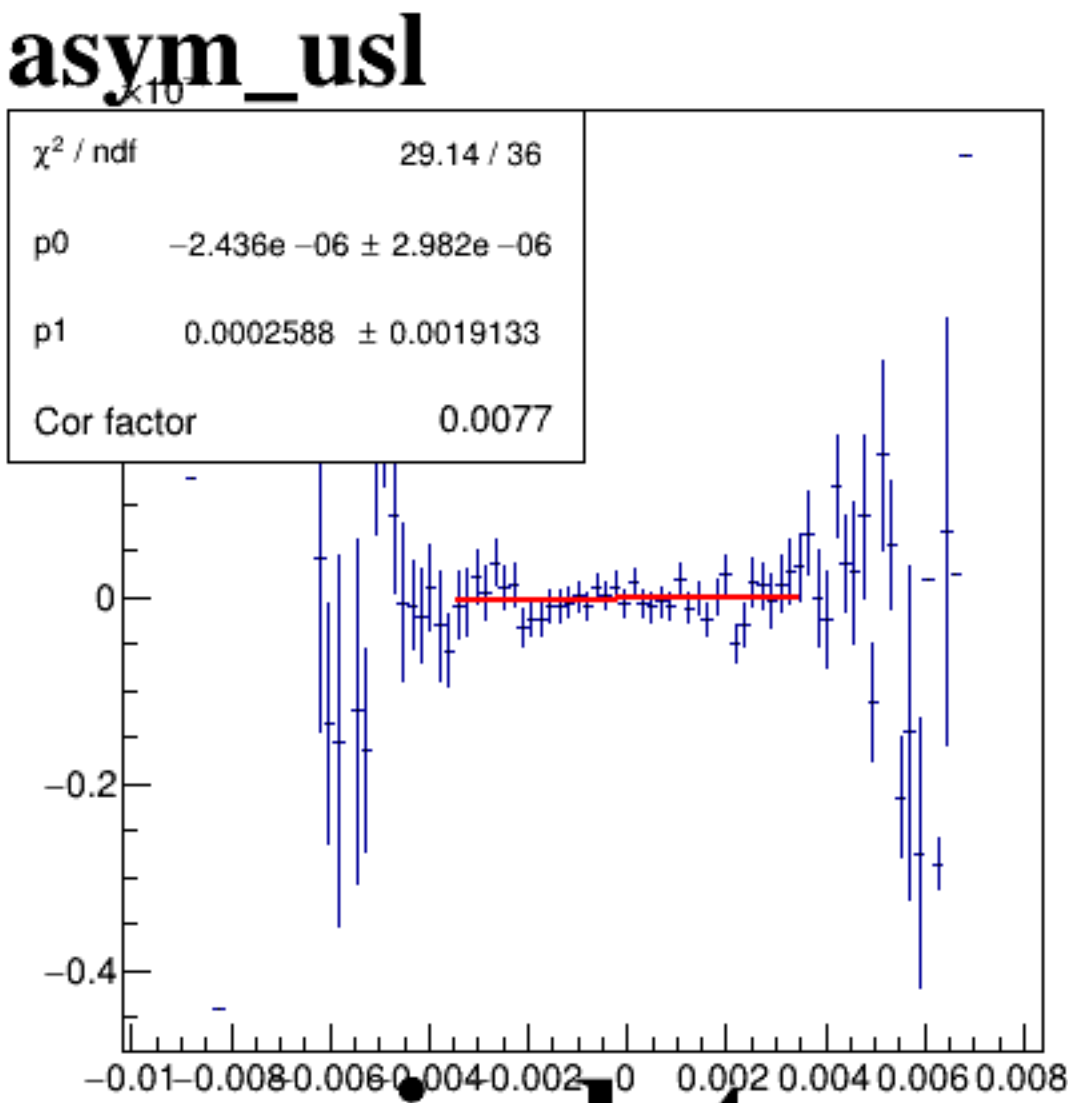
asym_dsl



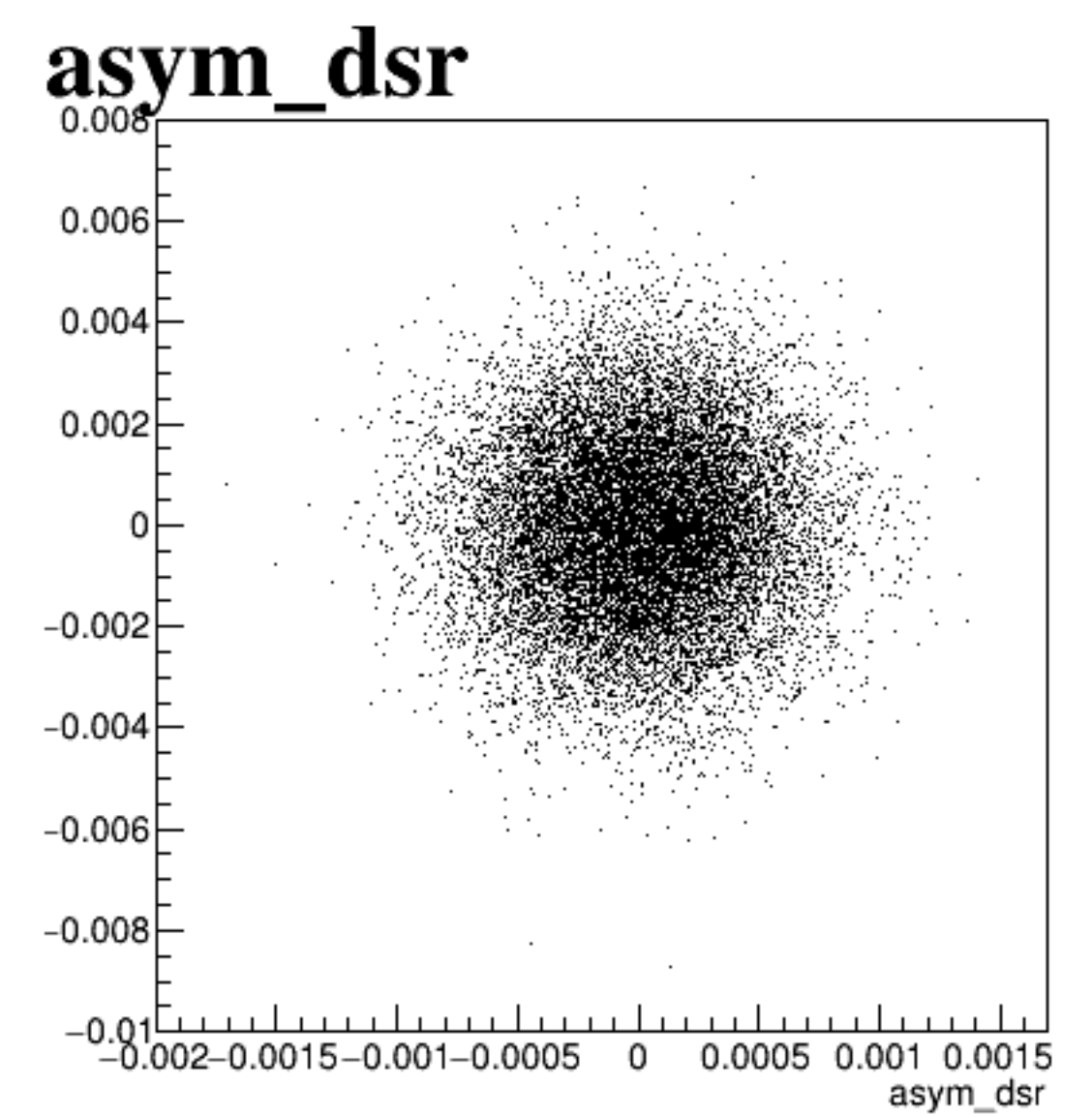
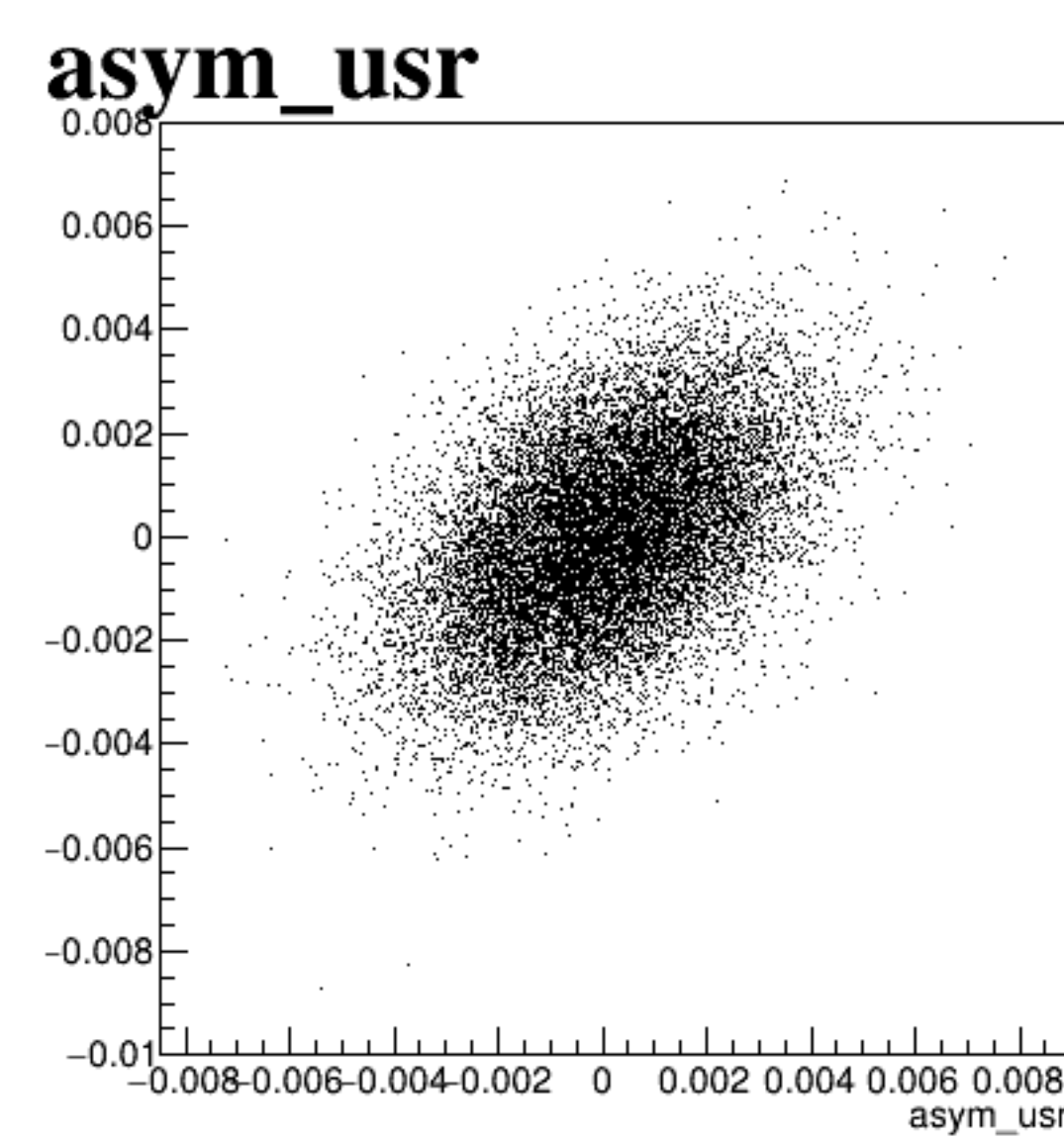
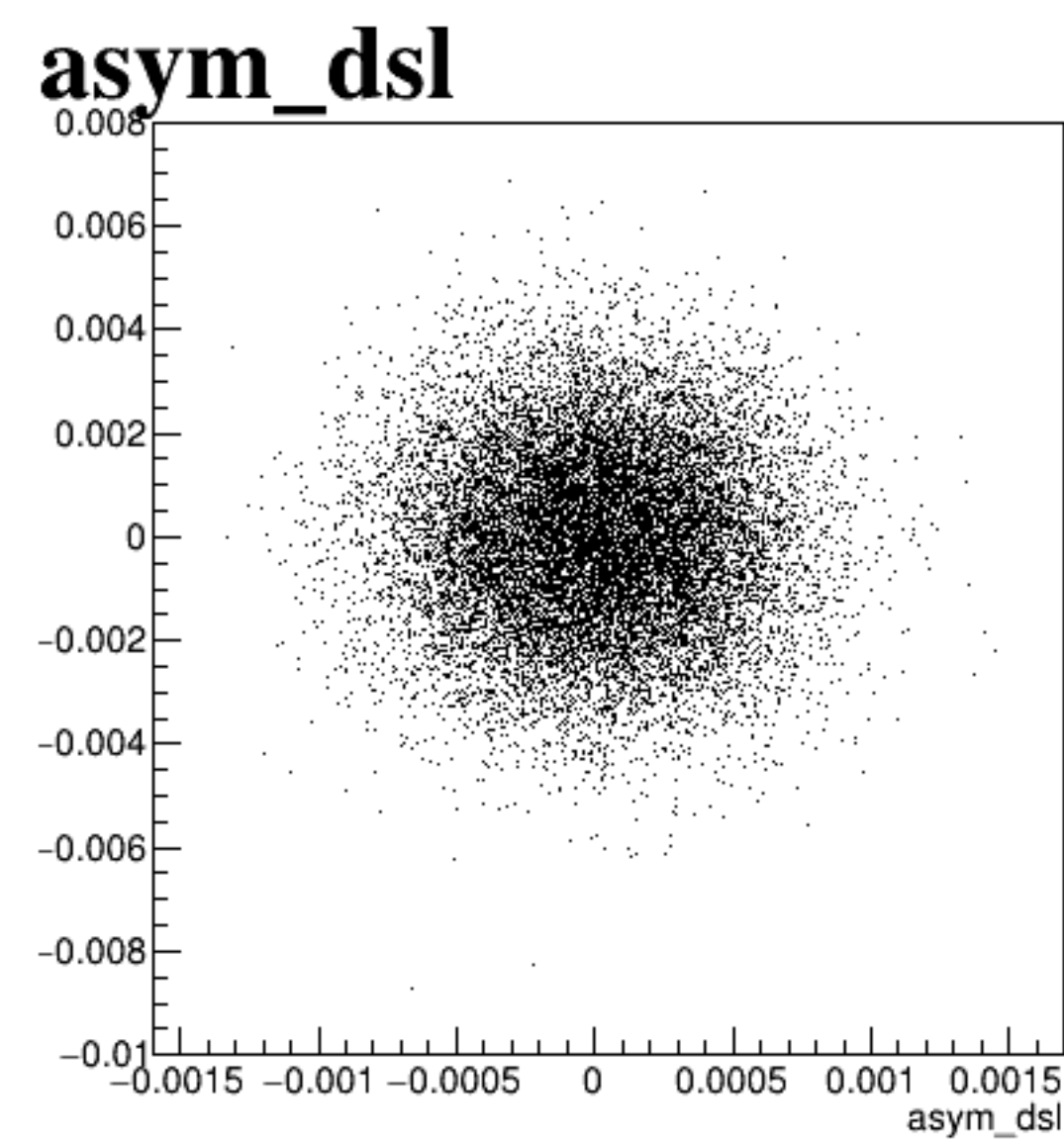
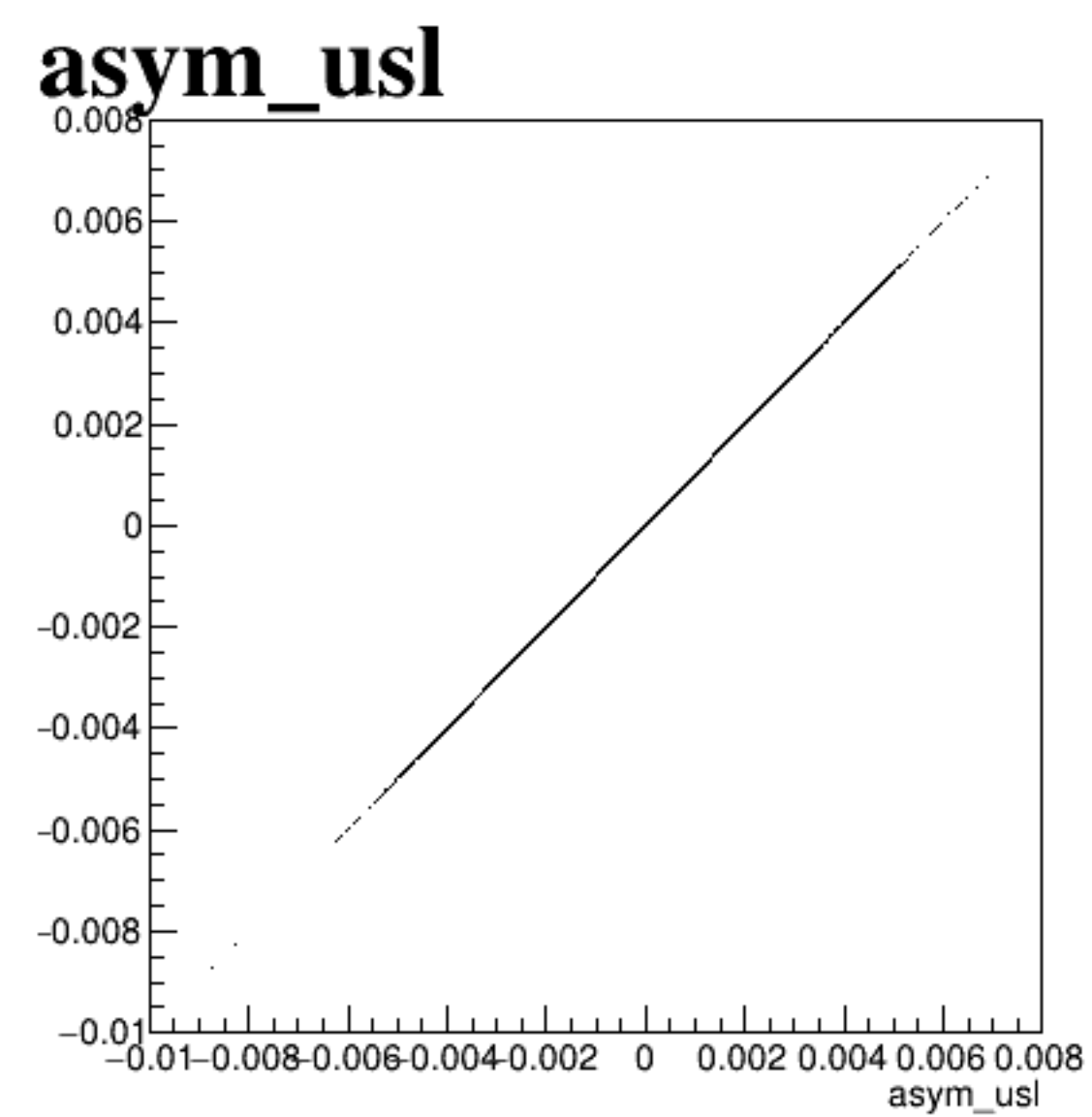
asym_usr



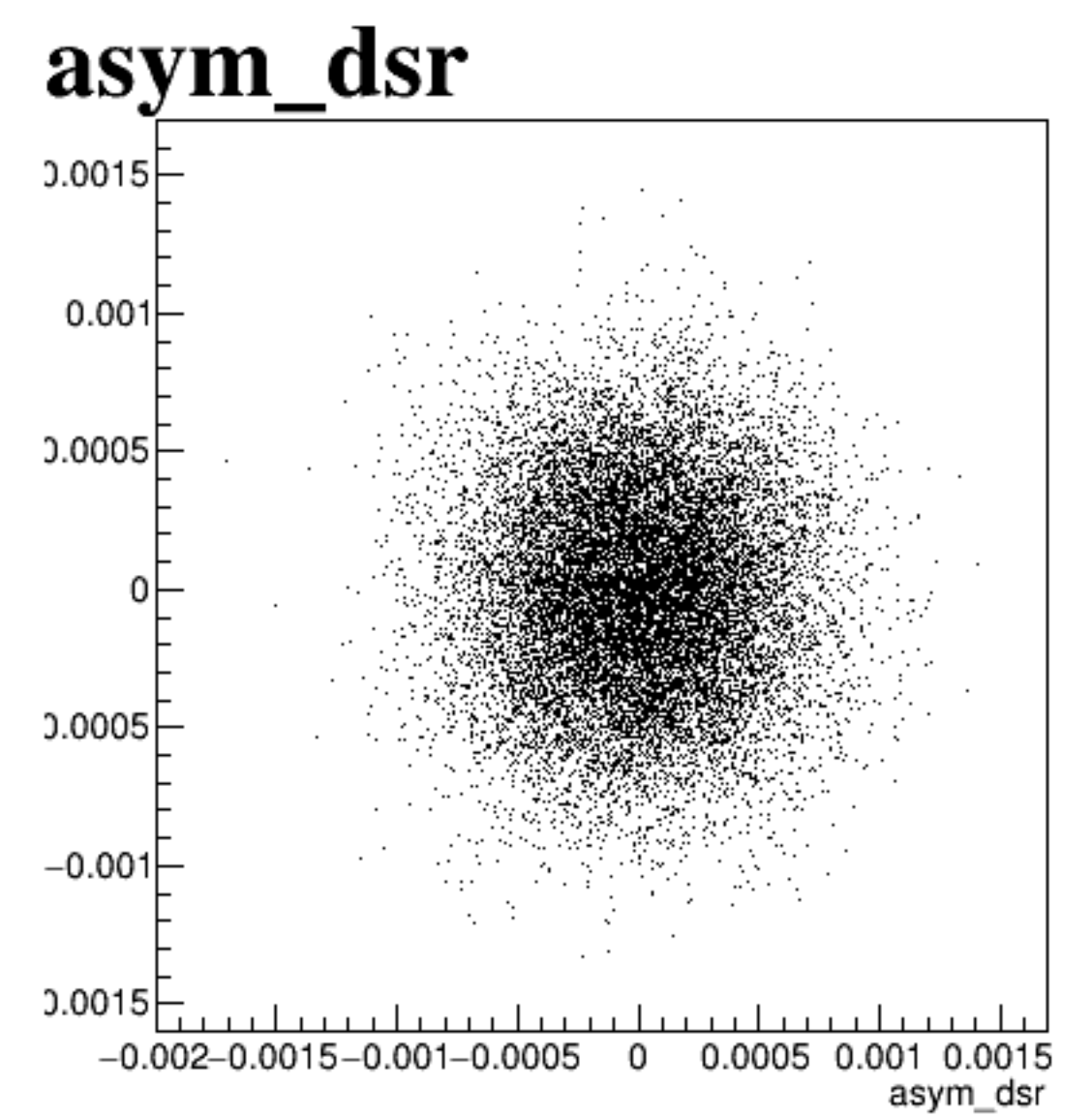
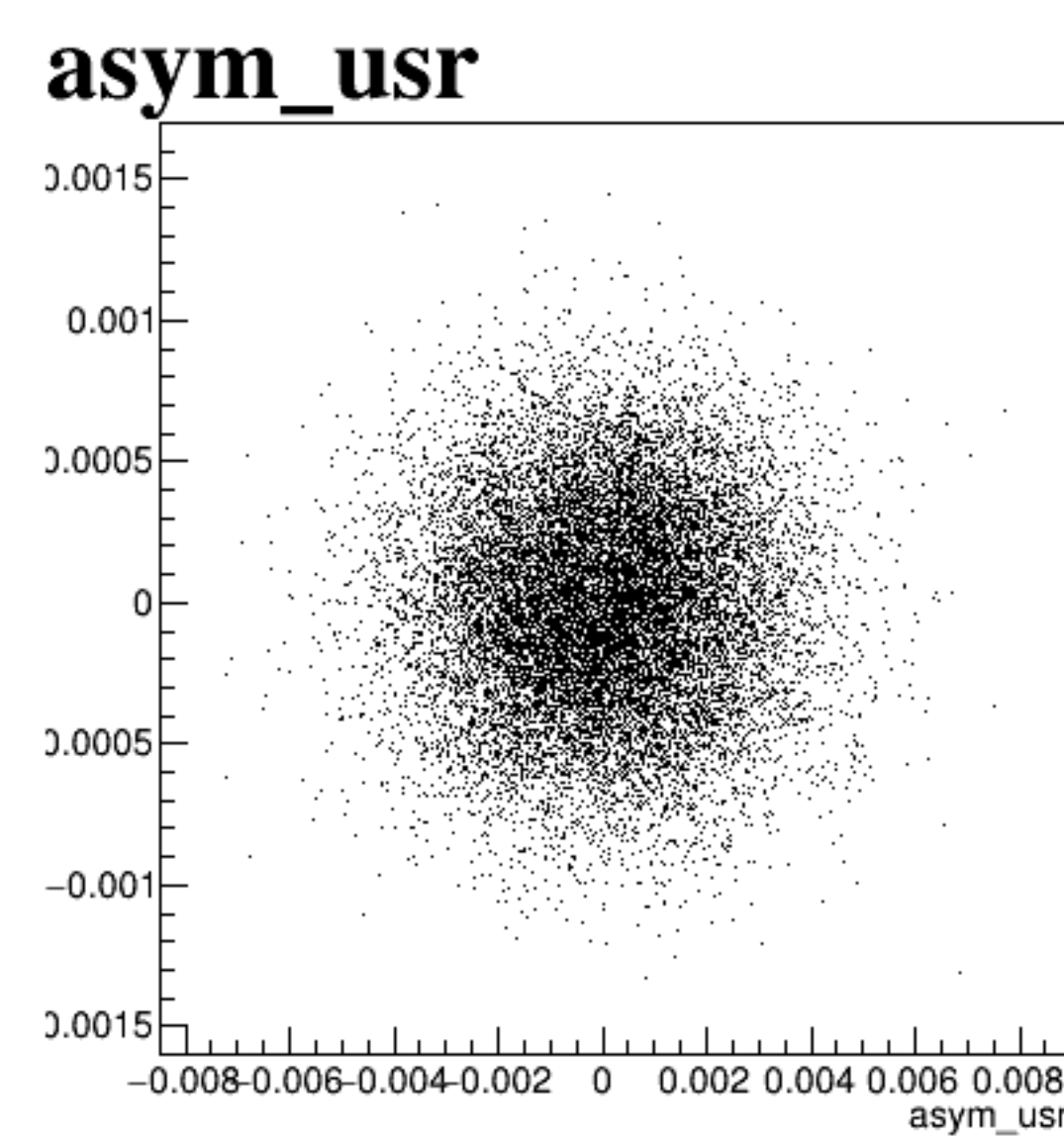
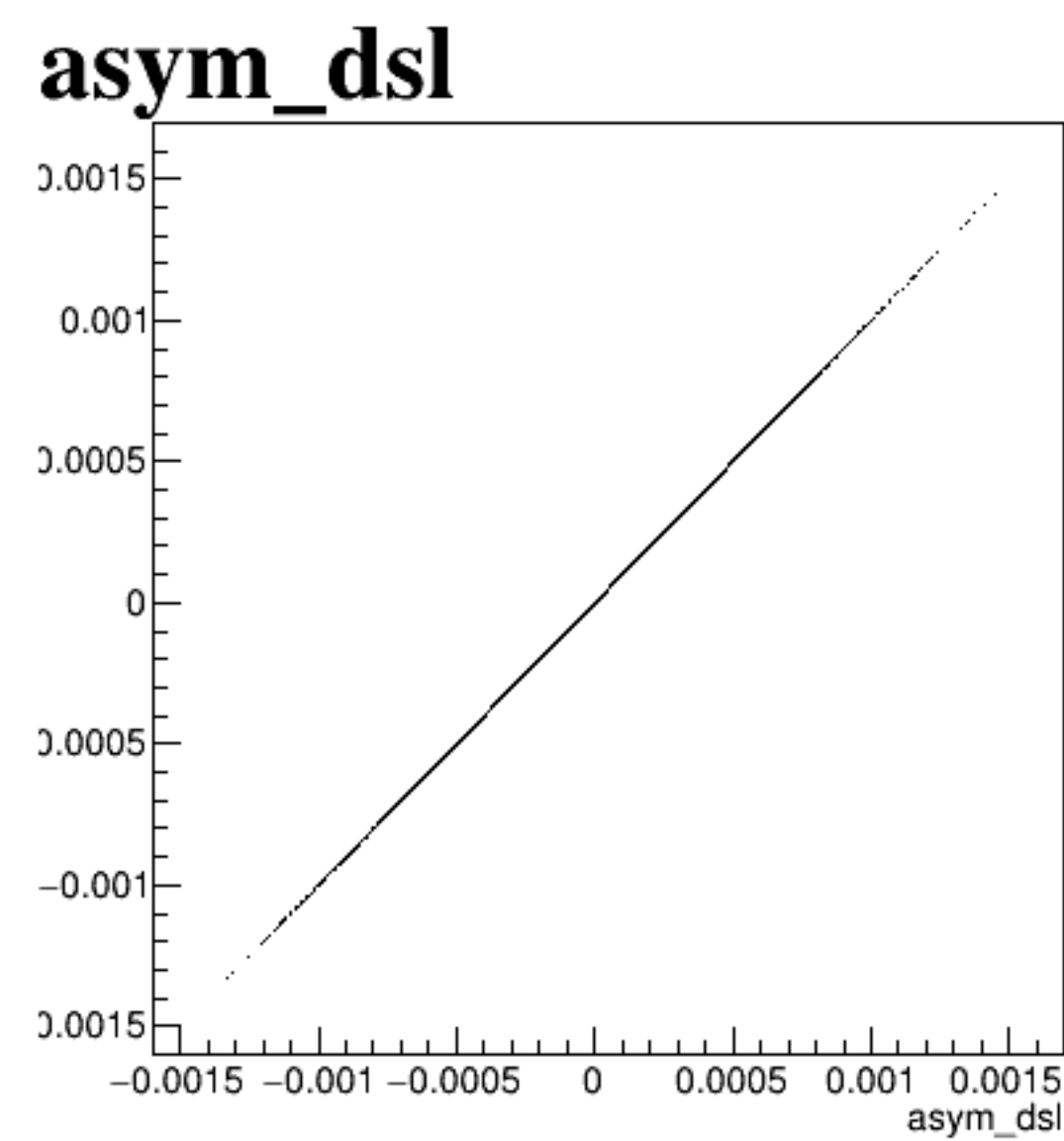
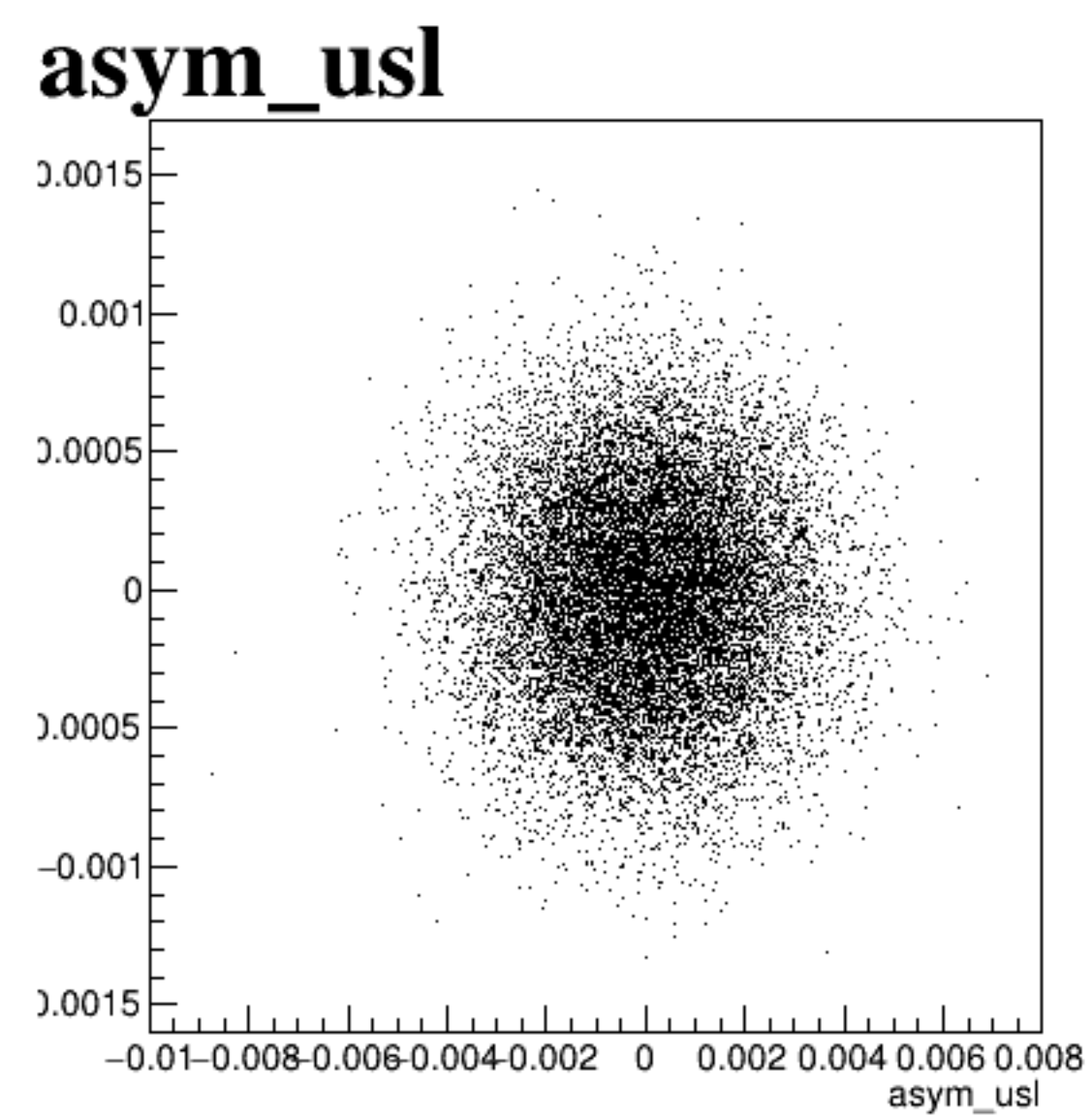
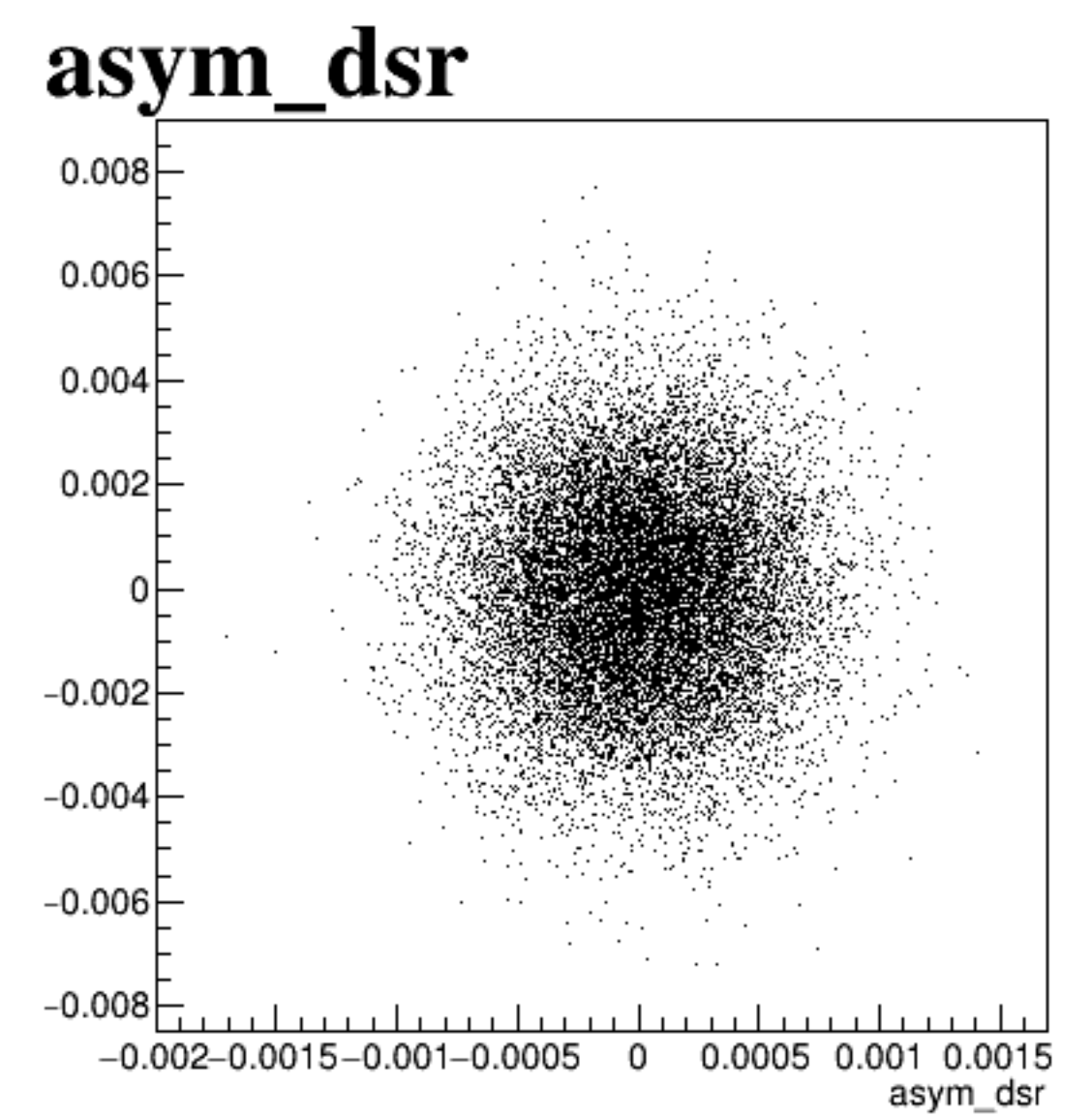
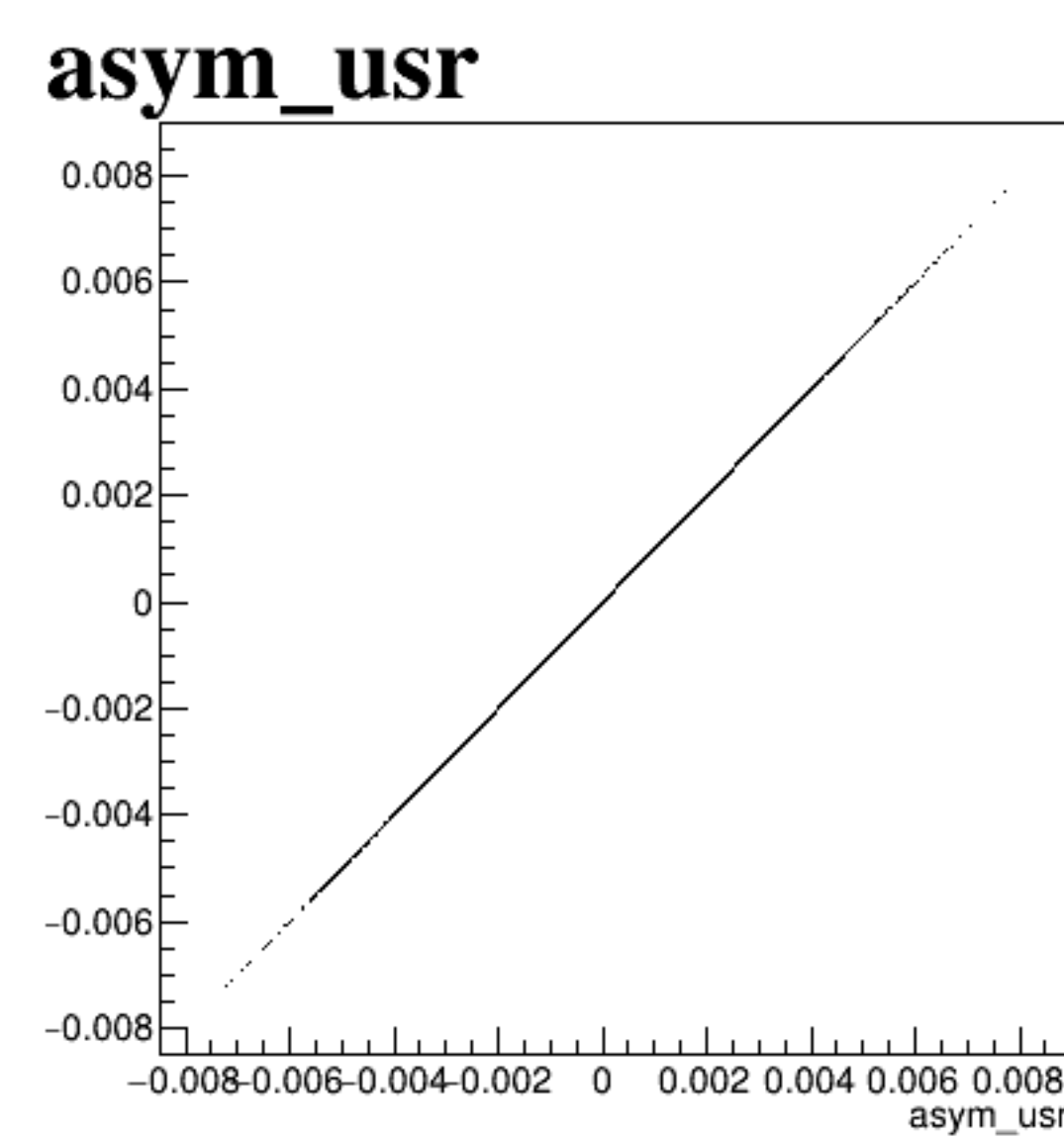
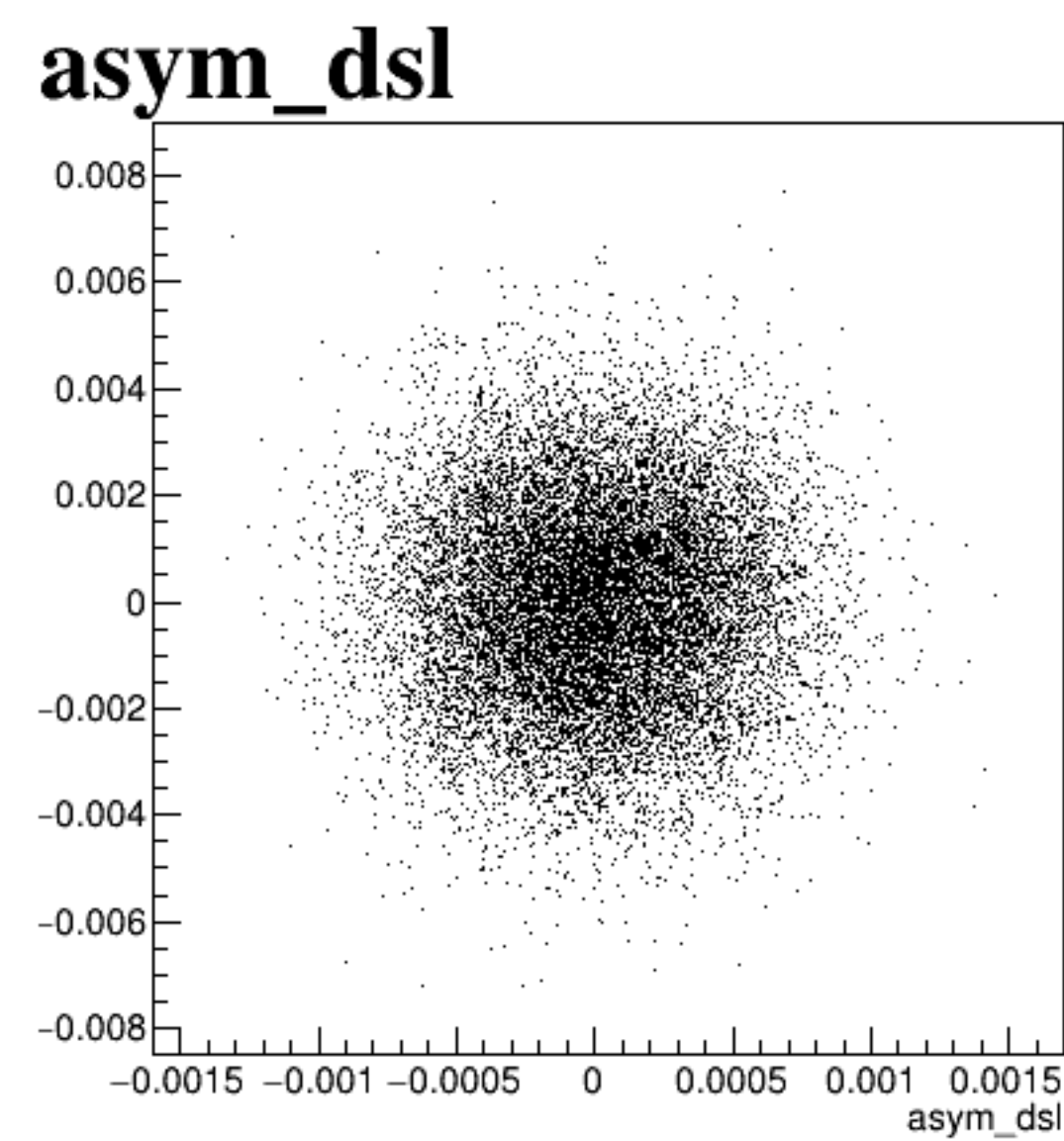
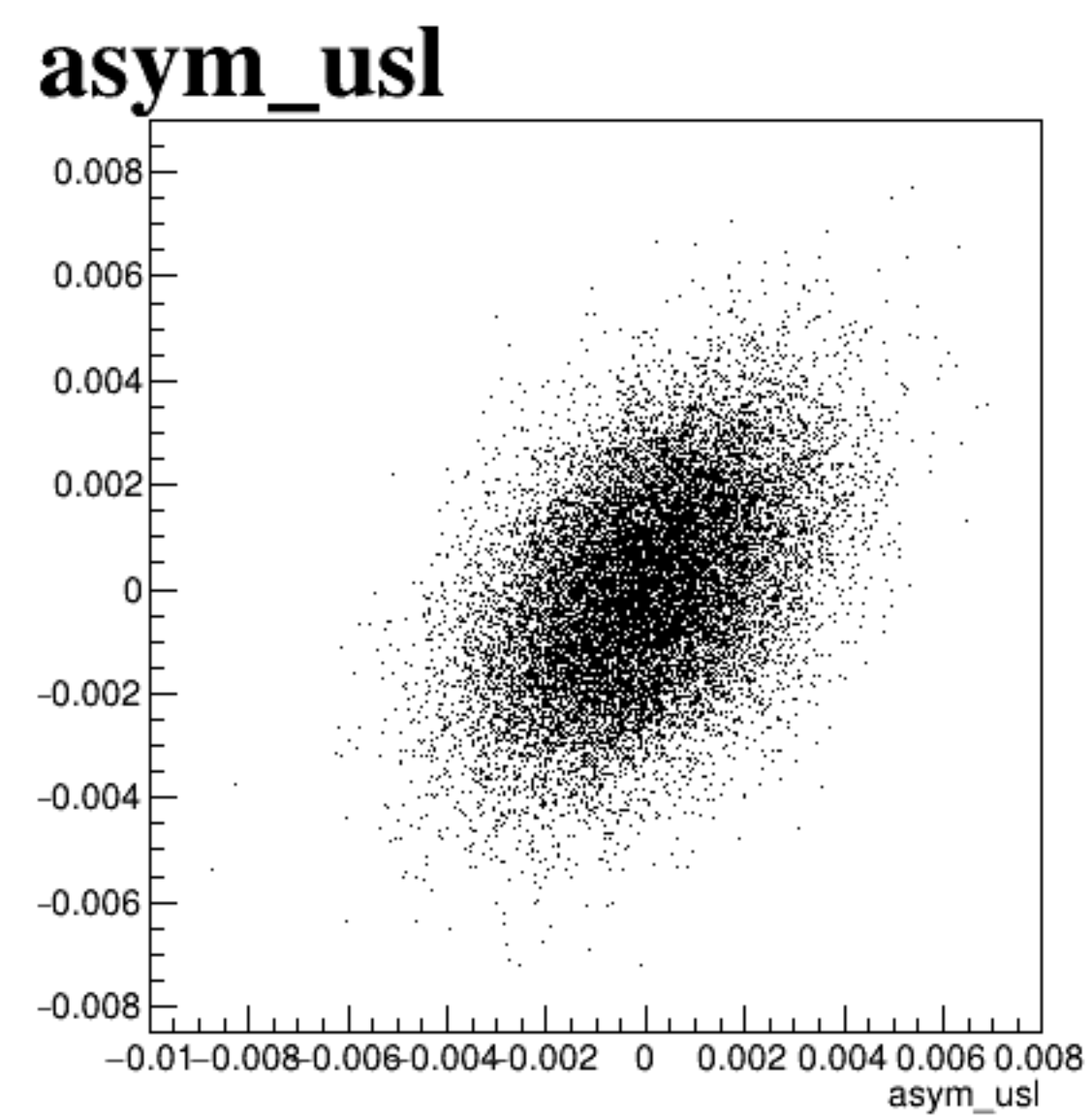
asym_dsr



asym_usl



asym_dsl

**asym_usr****asym_dsr**