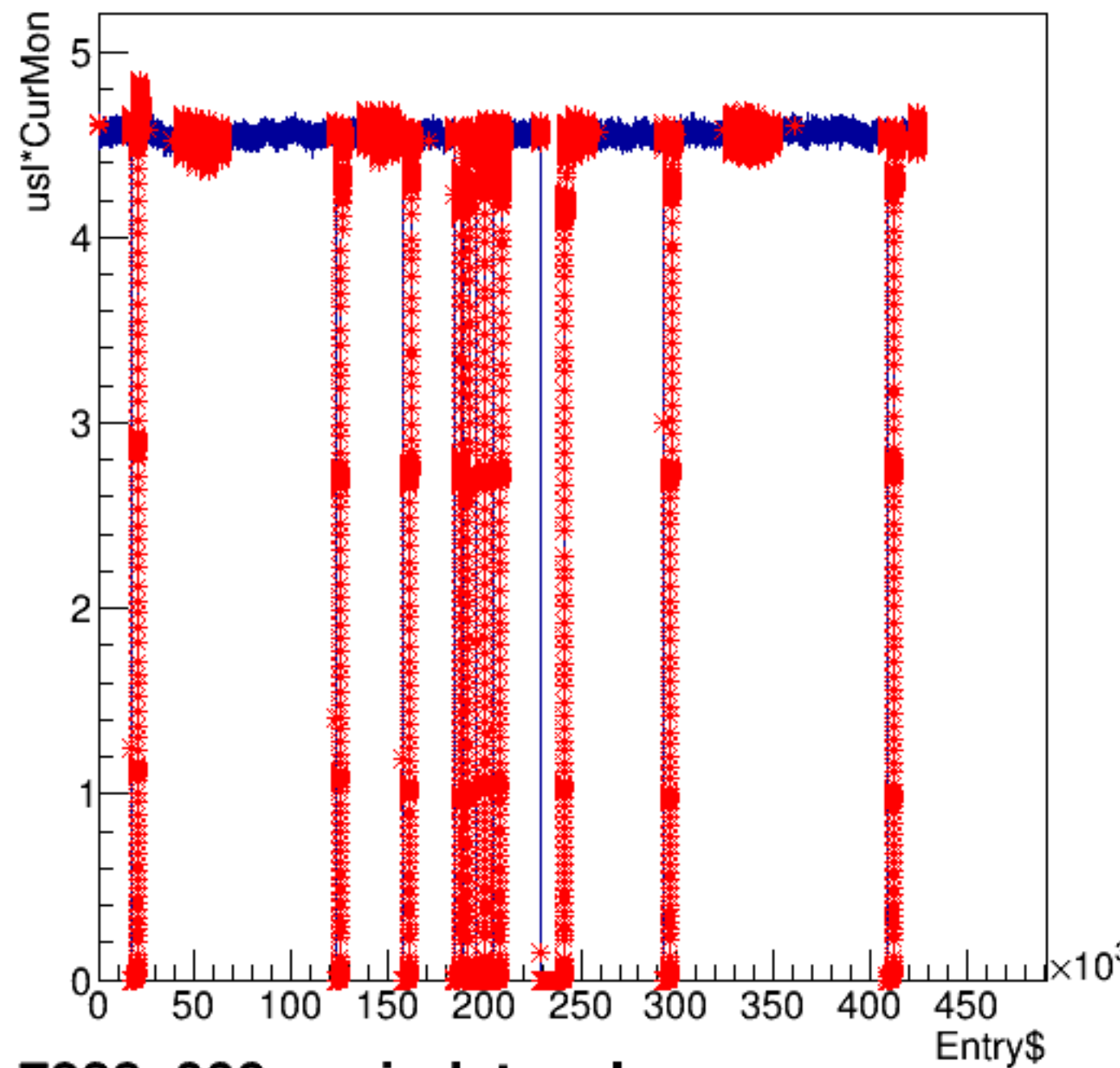
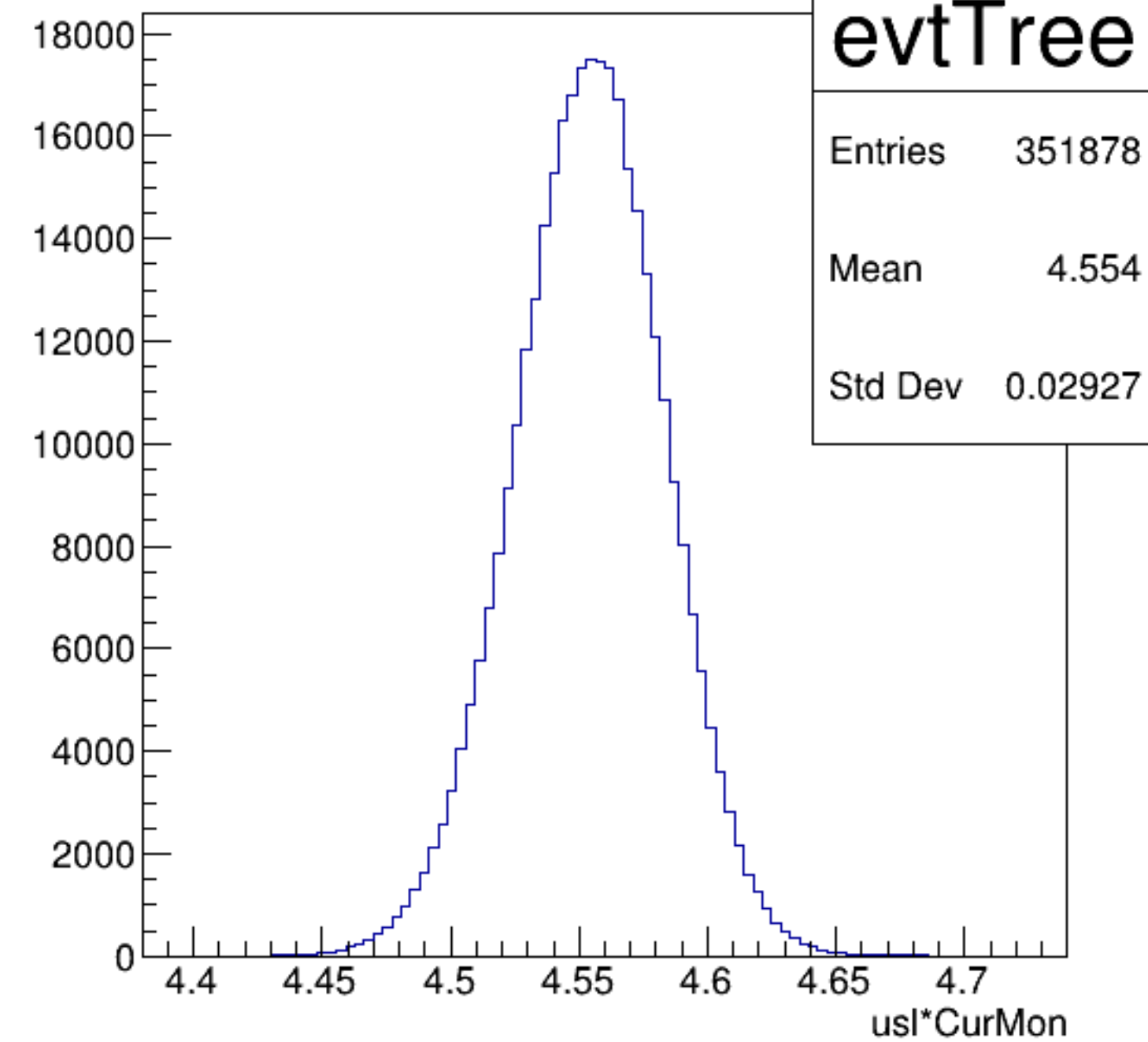


usl*CurMon:Entry\$



run7288_000_maindet_usl.png

usl*CurMon {ErrorFlag==0}



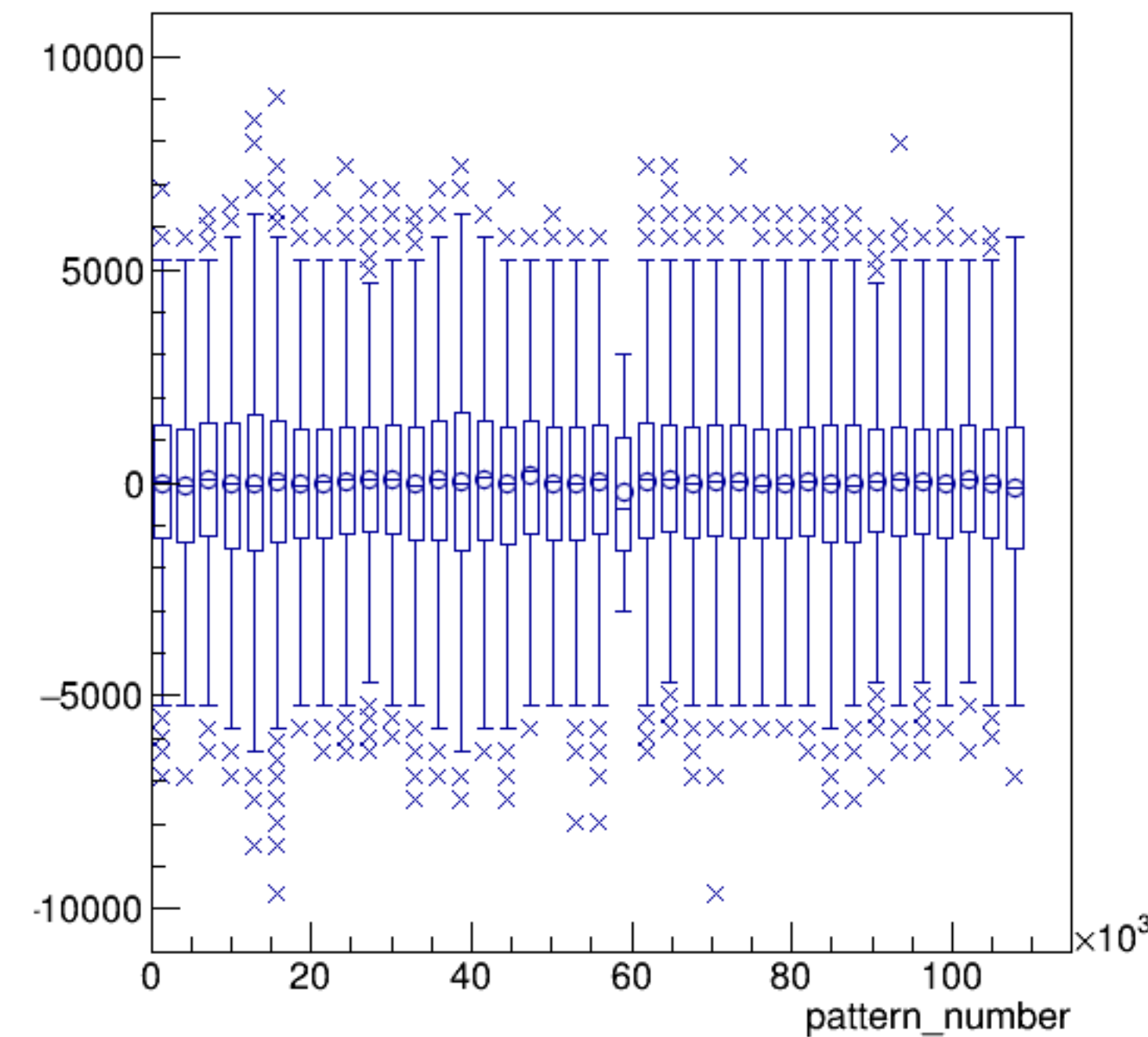
evtTree

Entries 351878

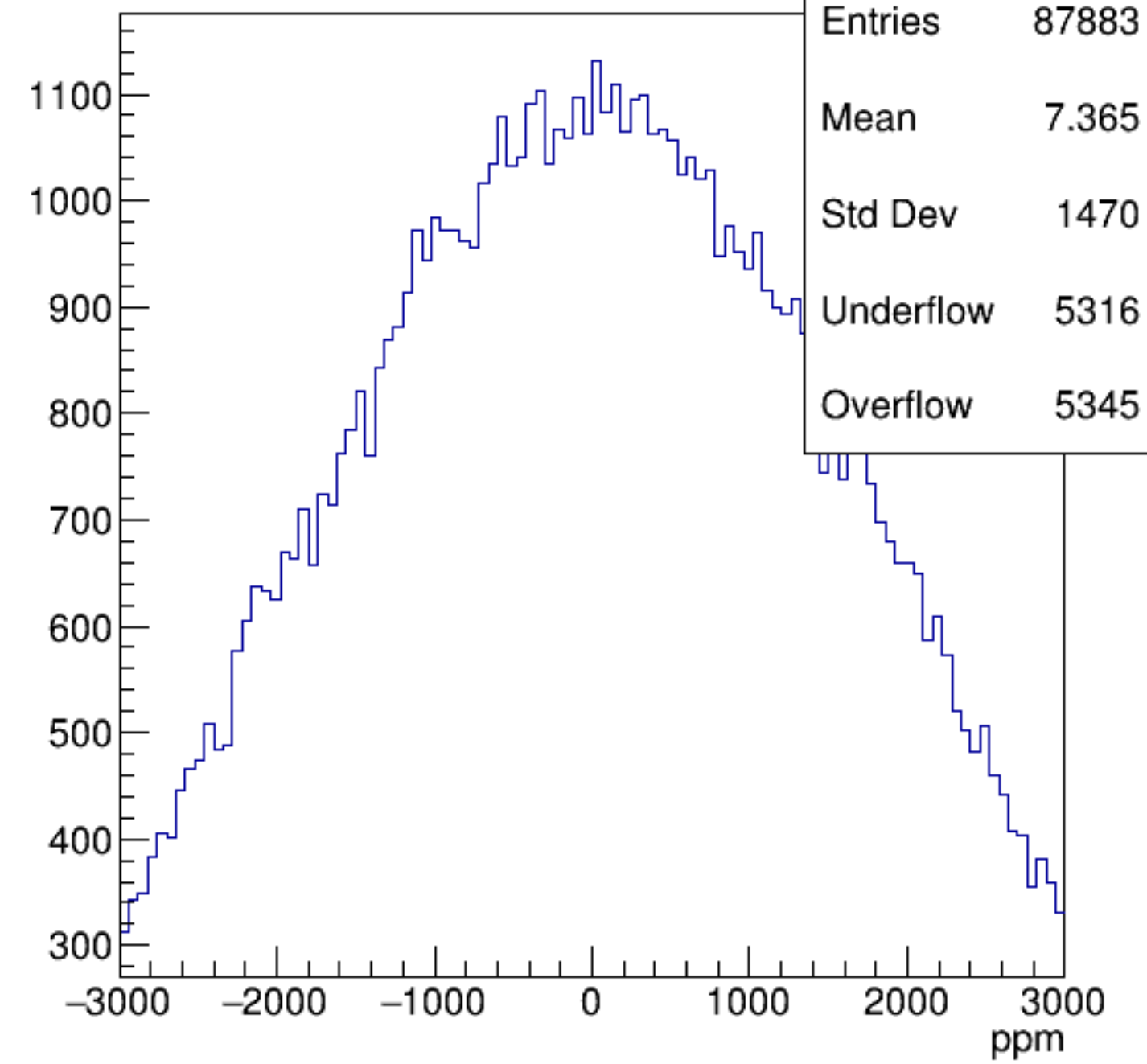
Mean 4.554

Std Dev 0.02927

asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl



Entries 87883

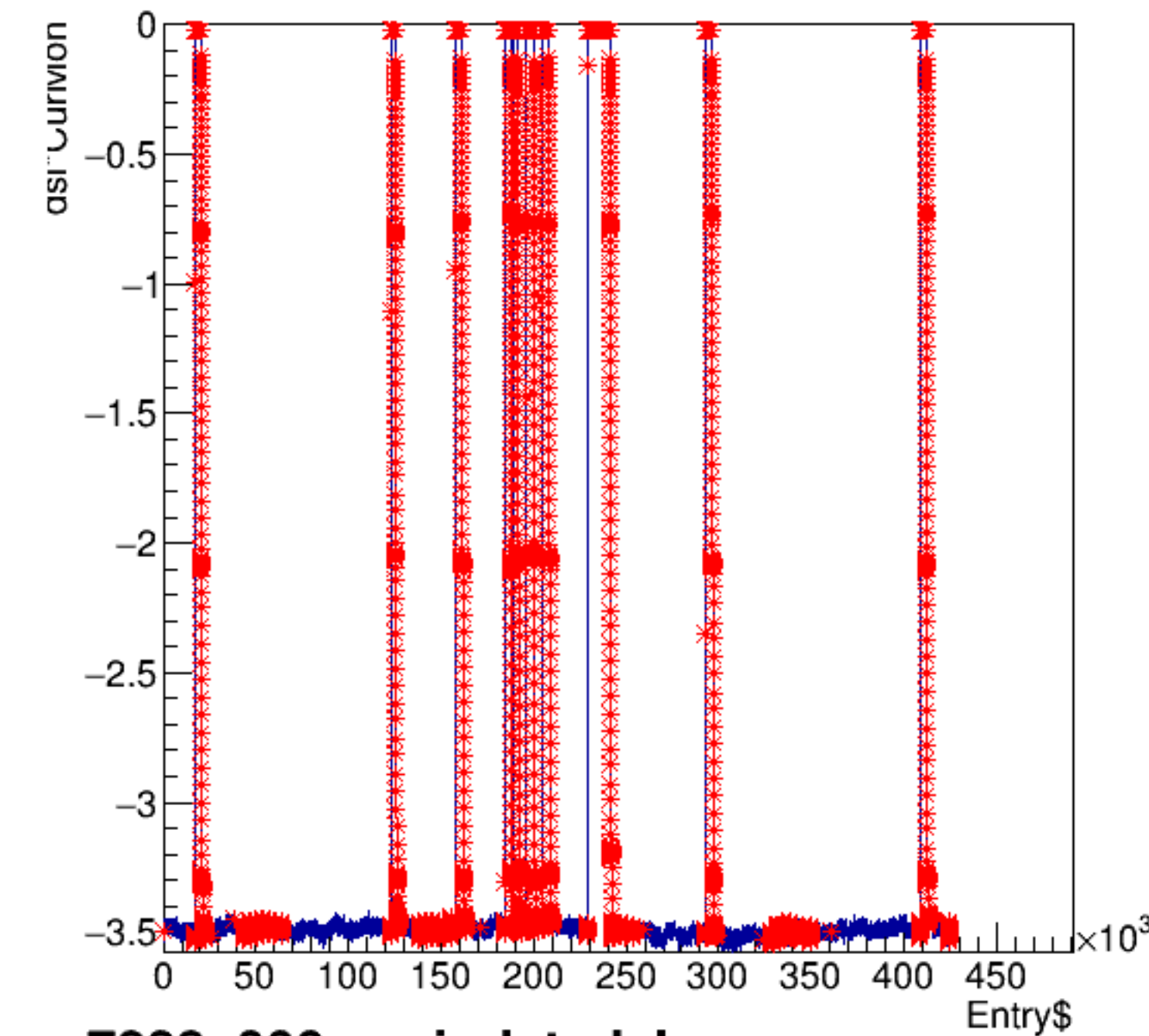
Mean 7.365

Std Dev 1470

Underflow 5316

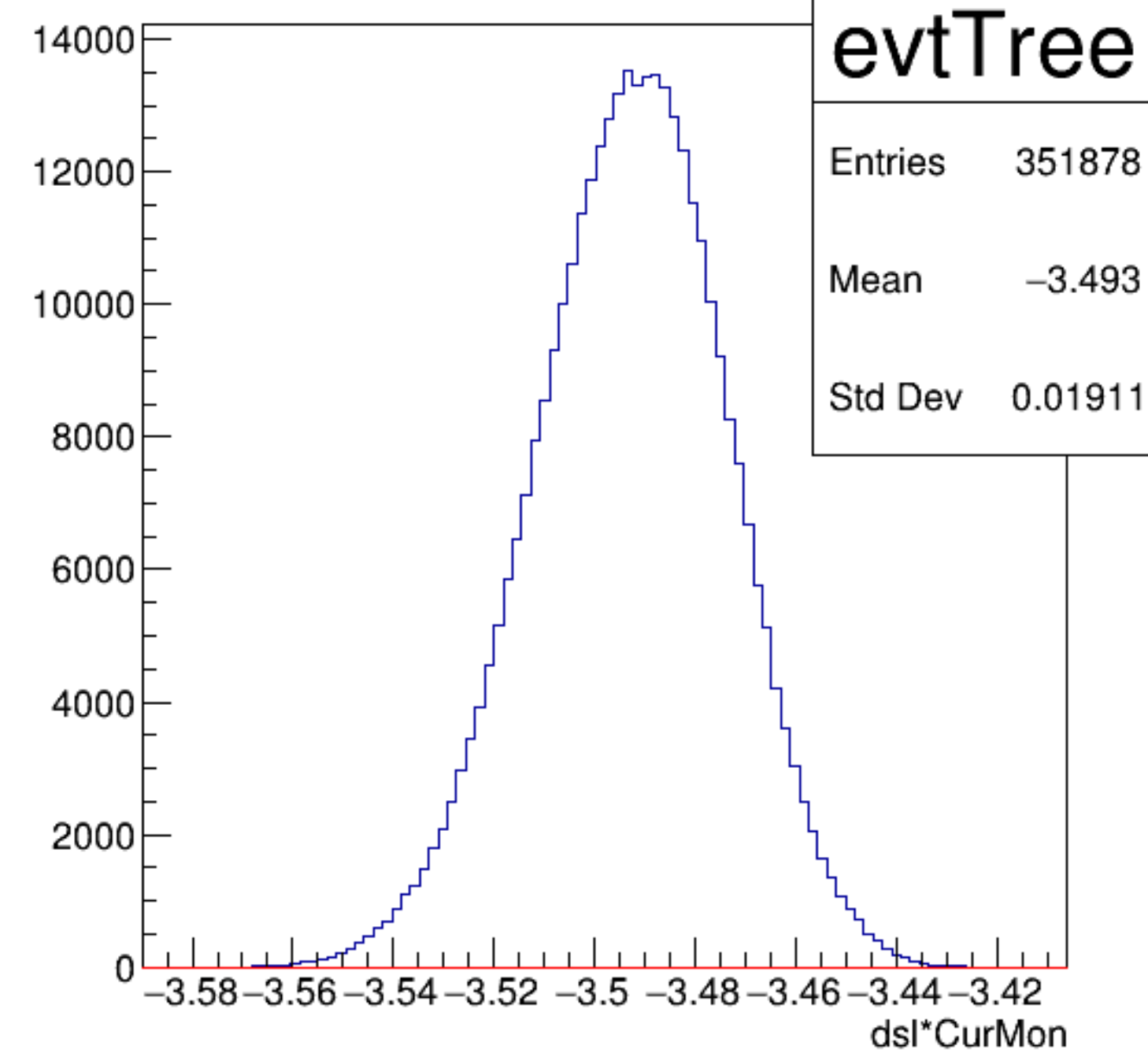
Overflow 5345

dsl*CurMon:Entry\$



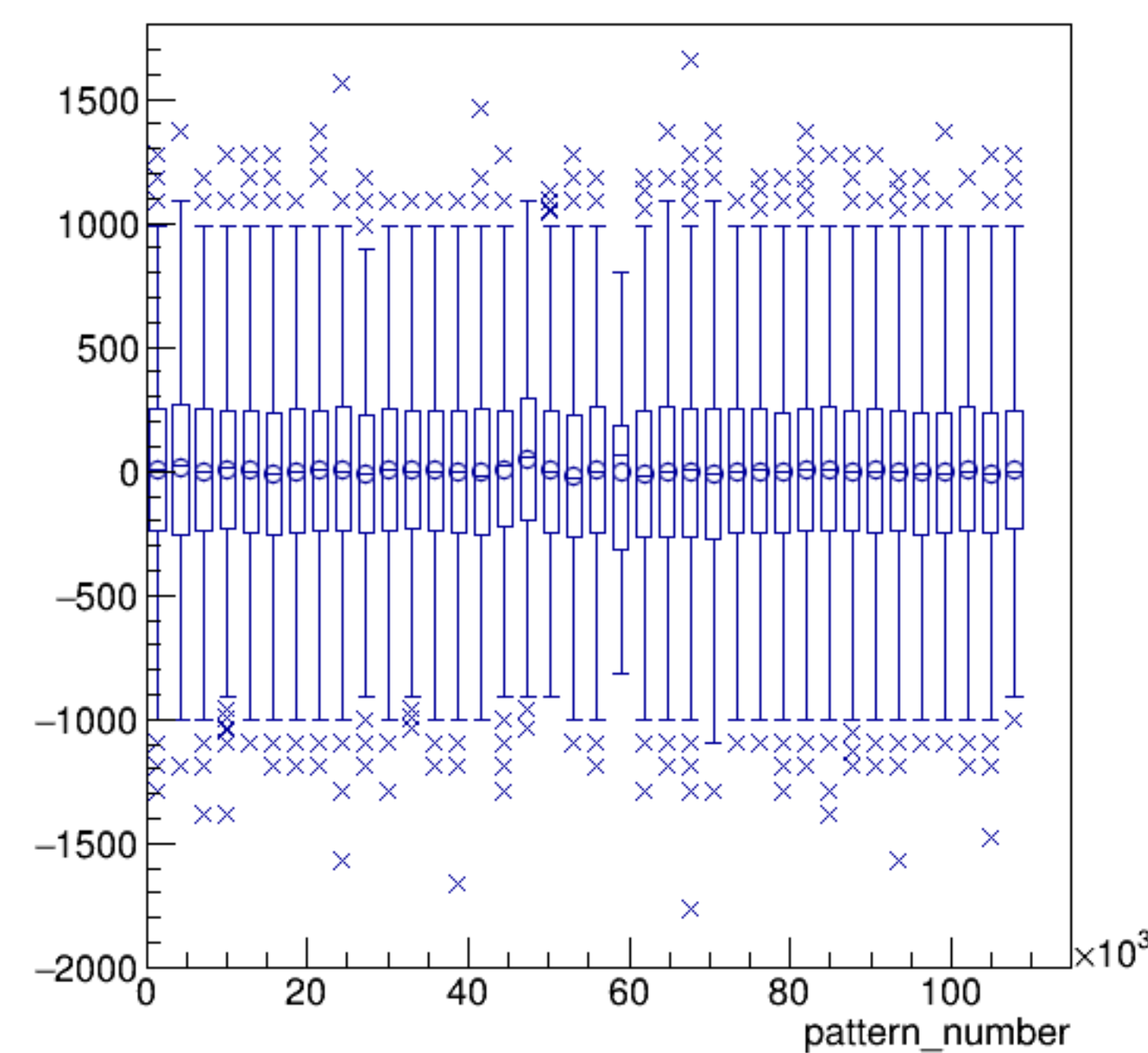
run7288_000_maindet_dsl.png

dsl*CurMon {ErrorFlag==0}

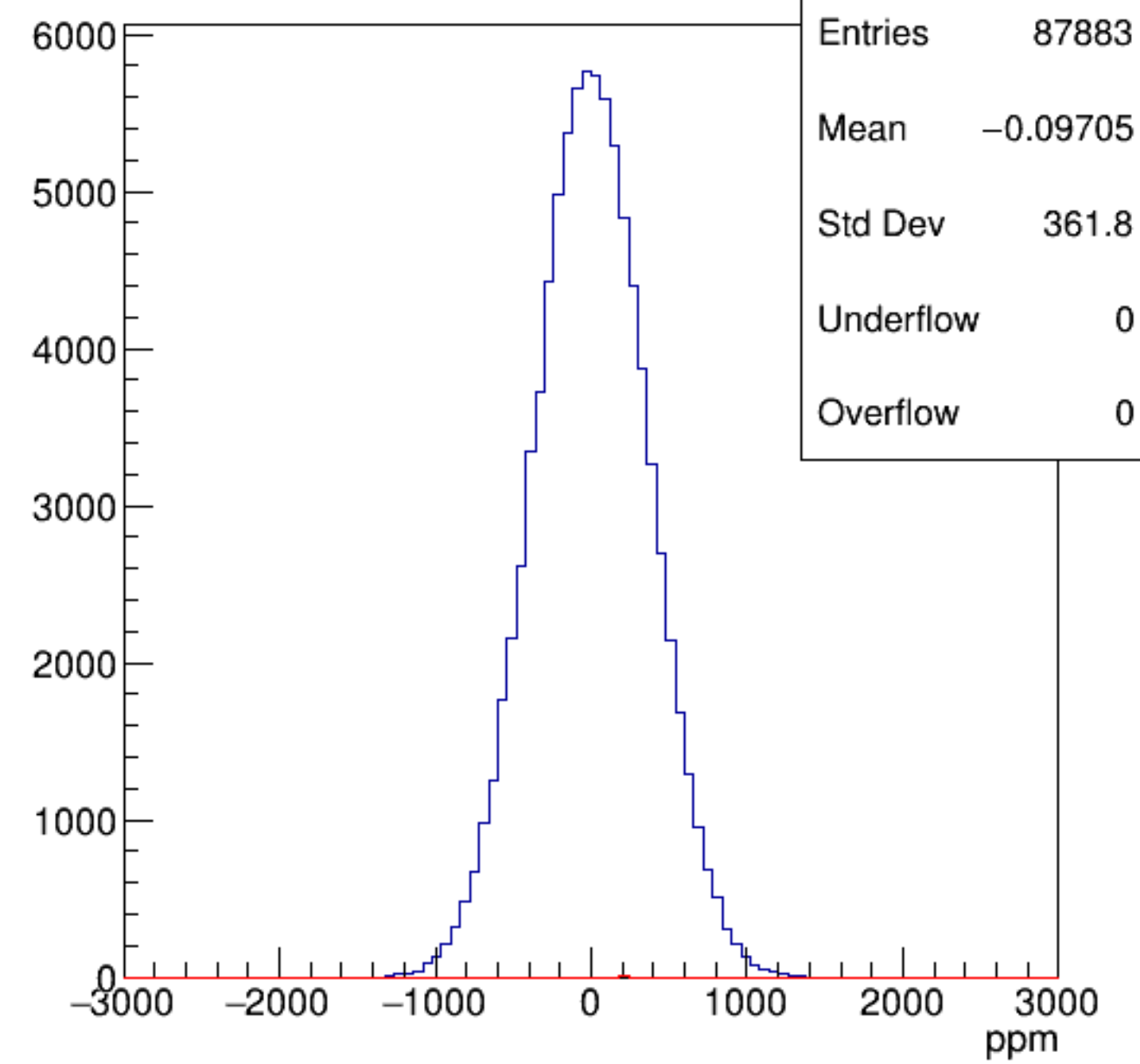


evtTree	
Entries	351878
Mean	-3.493
Std Dev	0.01911

asym_dsl/ppm:pattern_number {ErrorFlag==0}

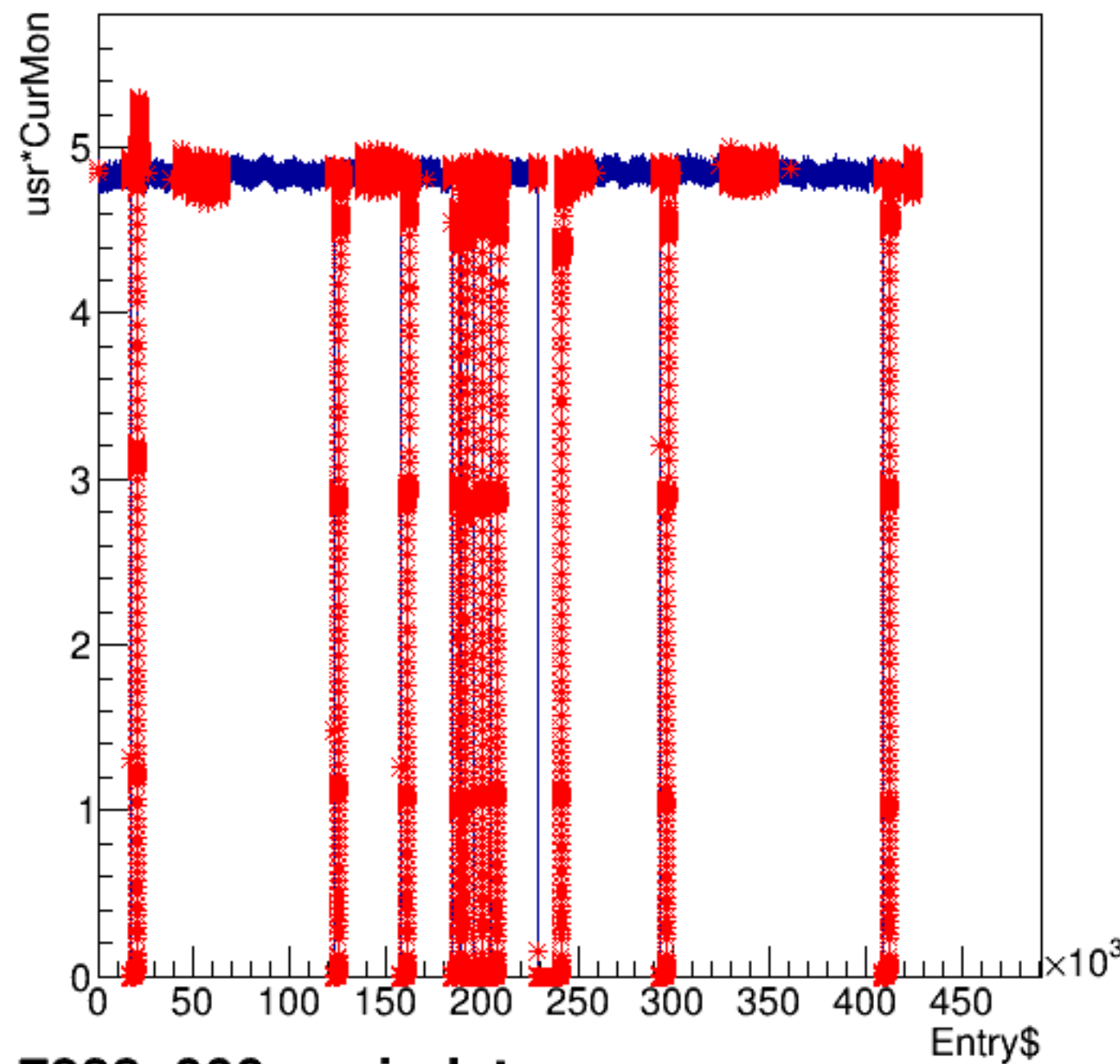


asym_dsl



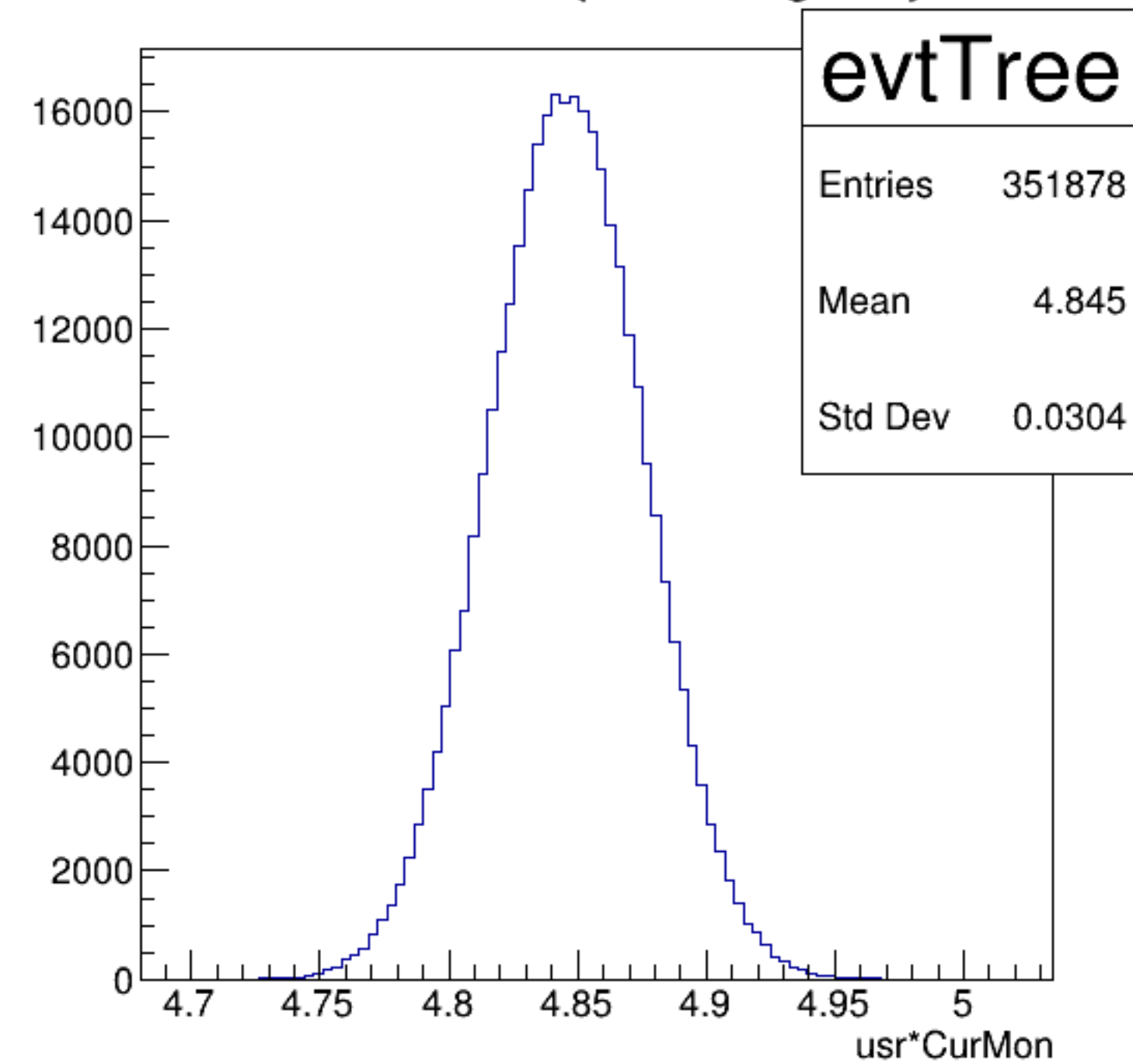
Entries	87883
Mean	-0.09705
Std Dev	361.8
Underflow	0
Overflow	0

usr*CurMon:Entry\$

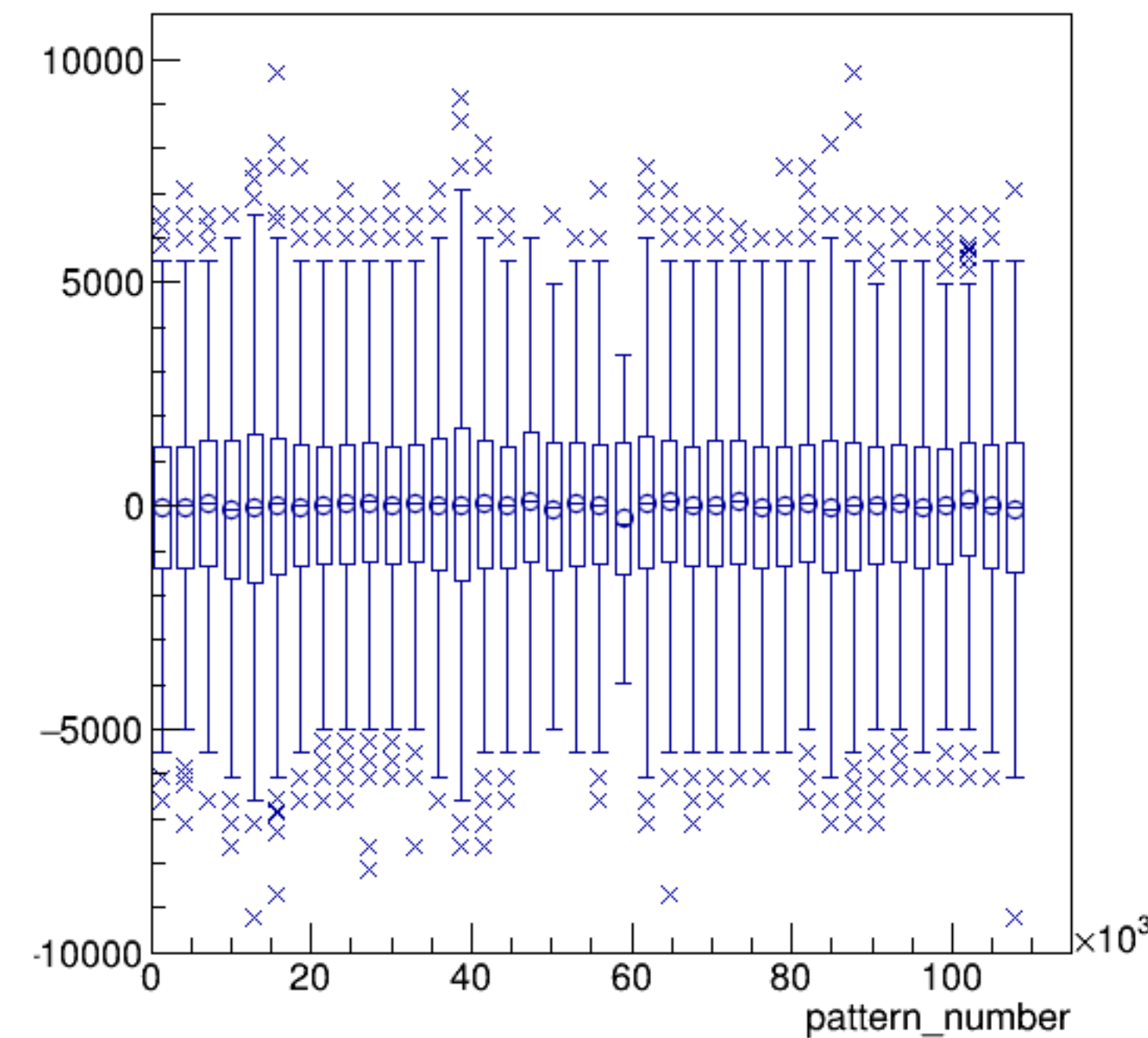


run7288_000_maindet_usr.png

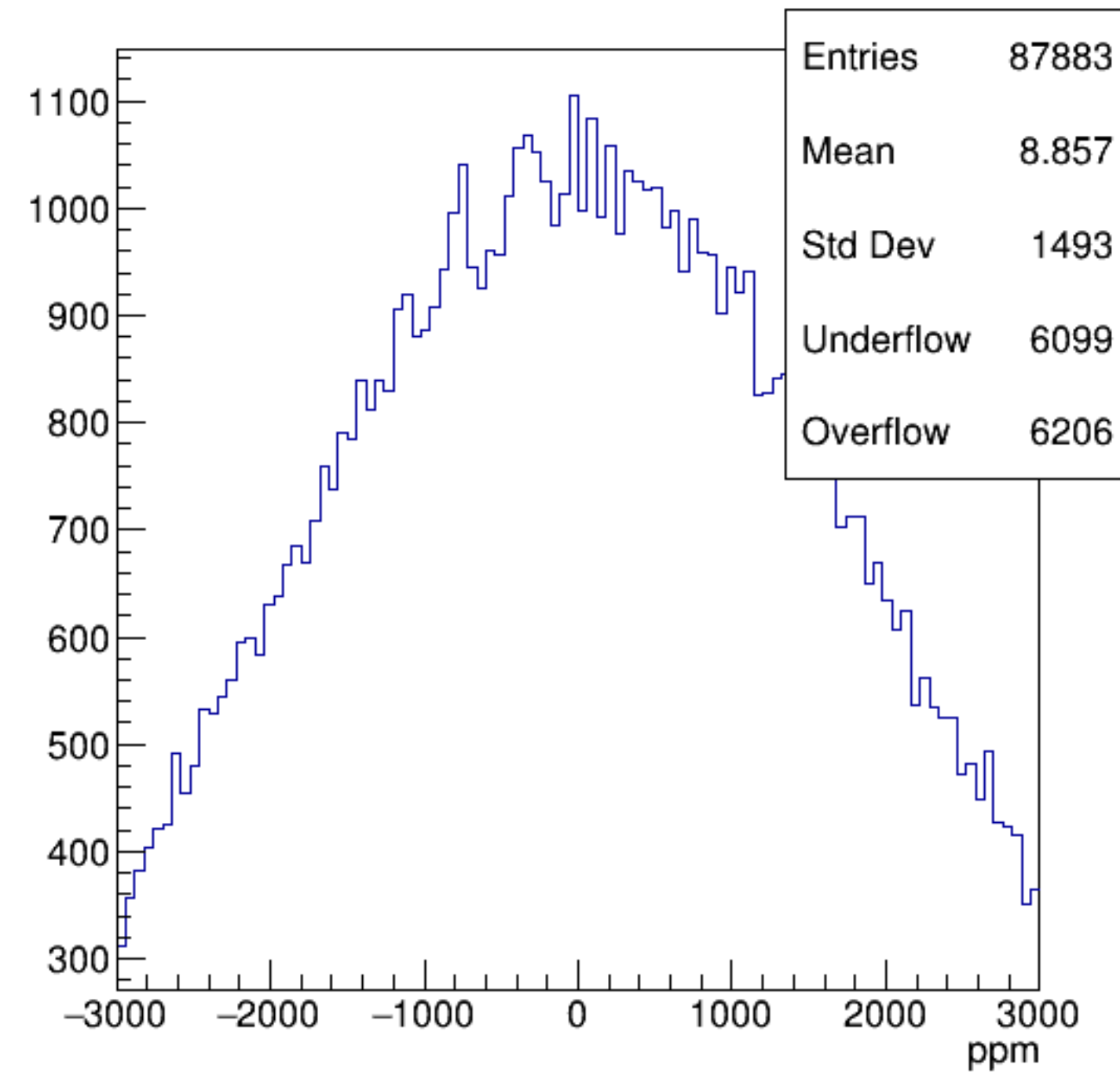
usr*CurMon {ErrorFlag==0}



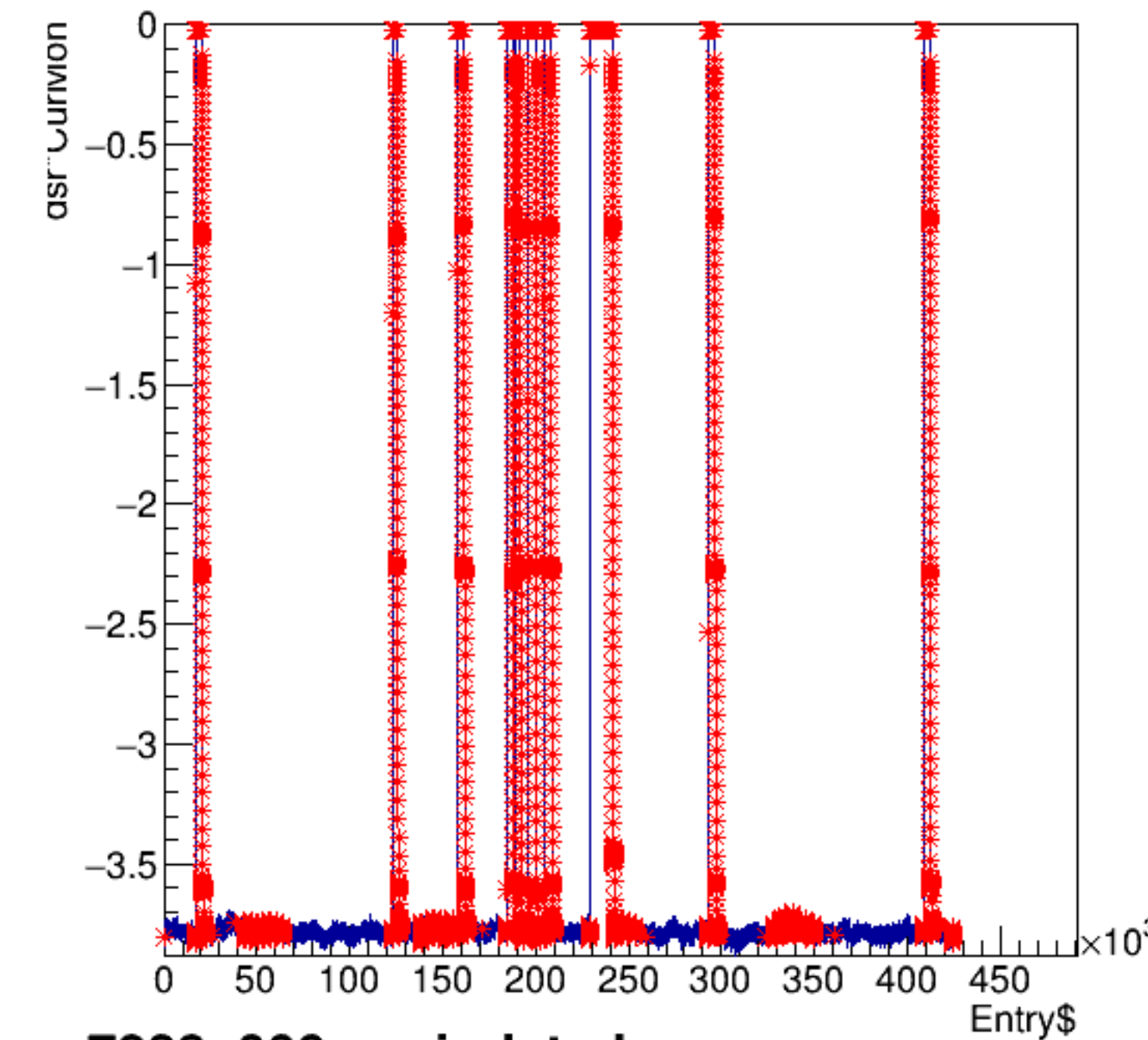
asym_usr/ppm:pattern_number {ErrorFlag==0}



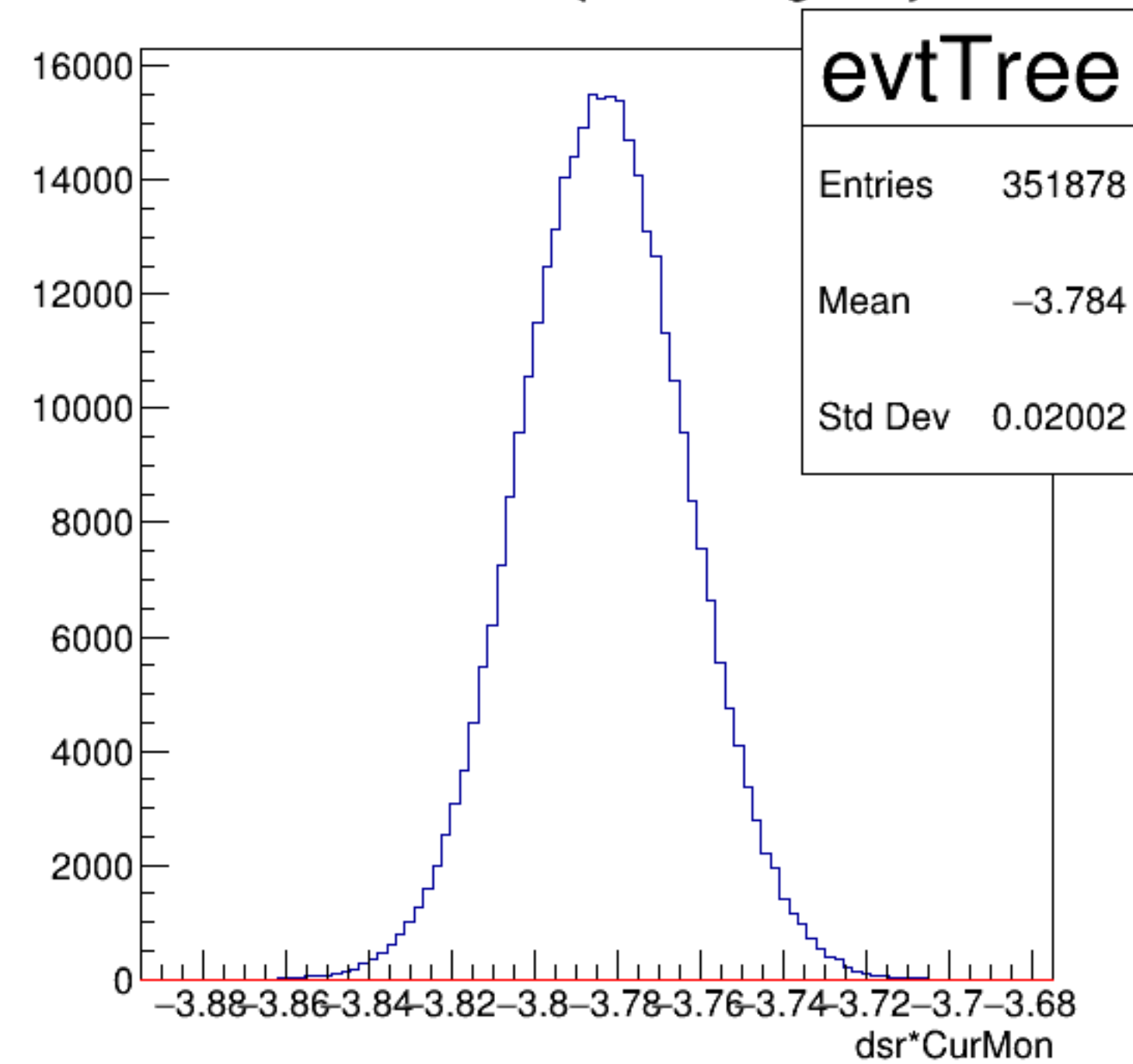
asym_usr



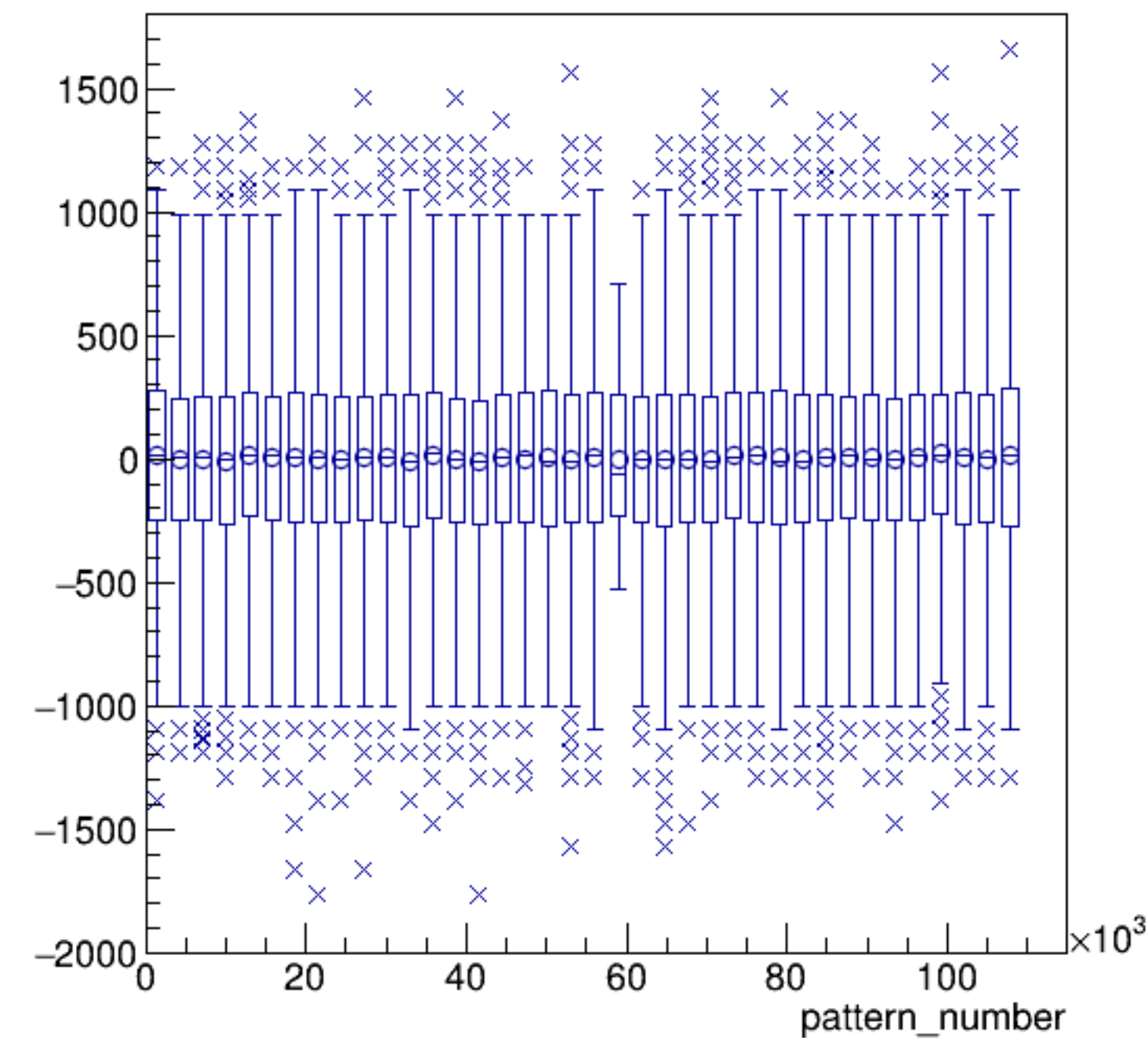
dsr*CurMon:Entry\$



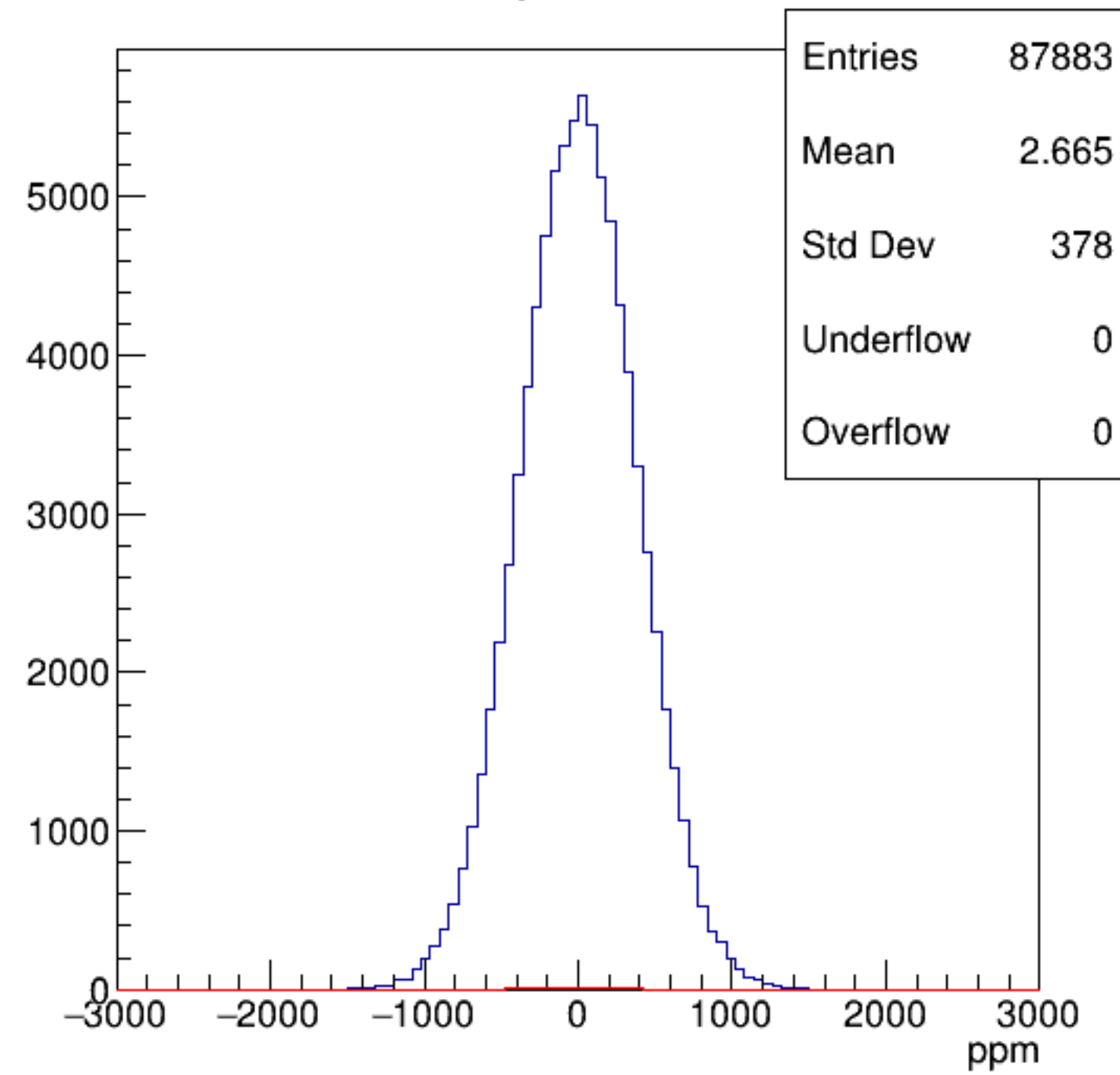
dsr*CurMon {ErrorFlag==0}



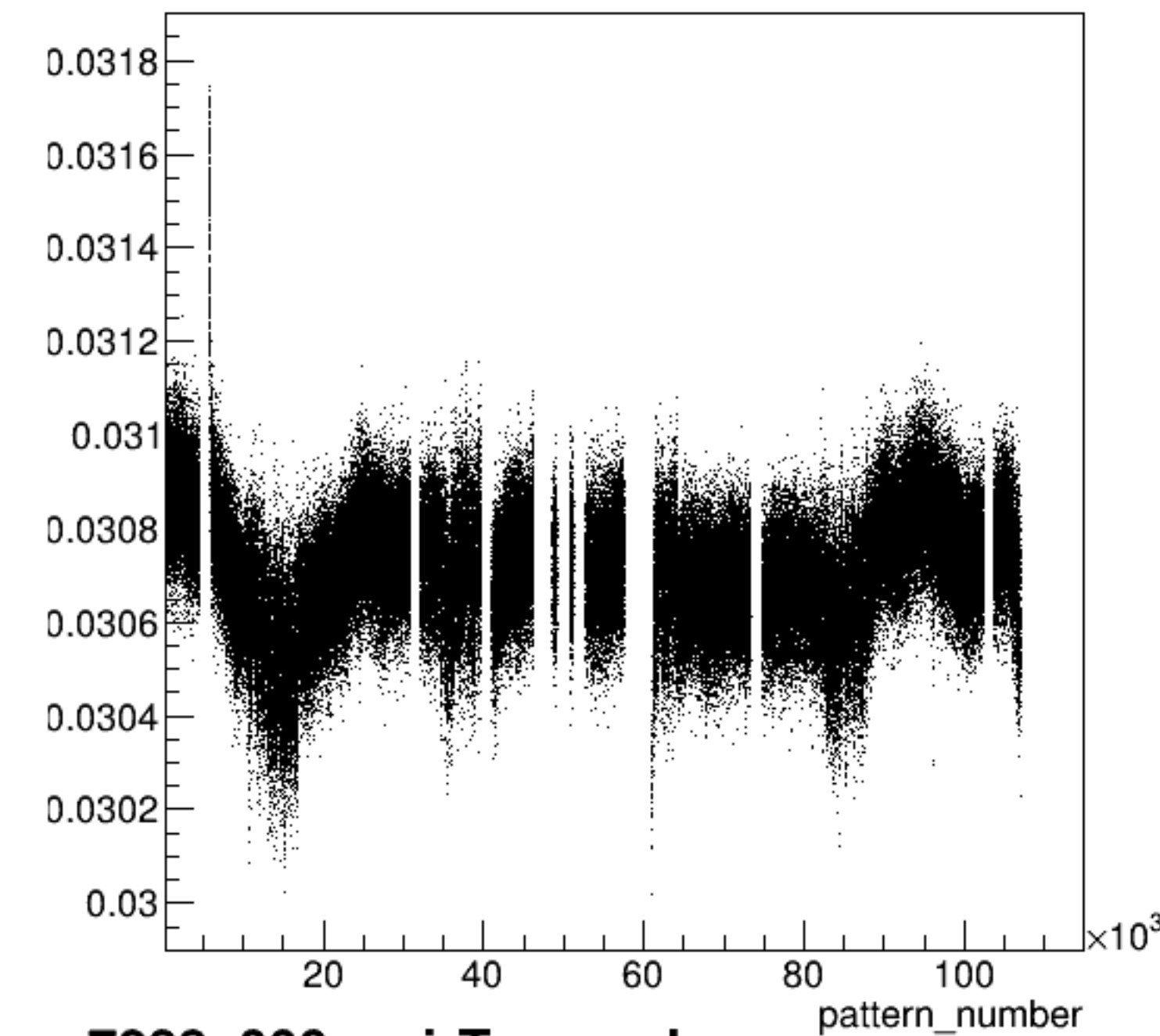
asym_dsr/ppm:pattern_number {ErrorFlag==0}



asym_dsr

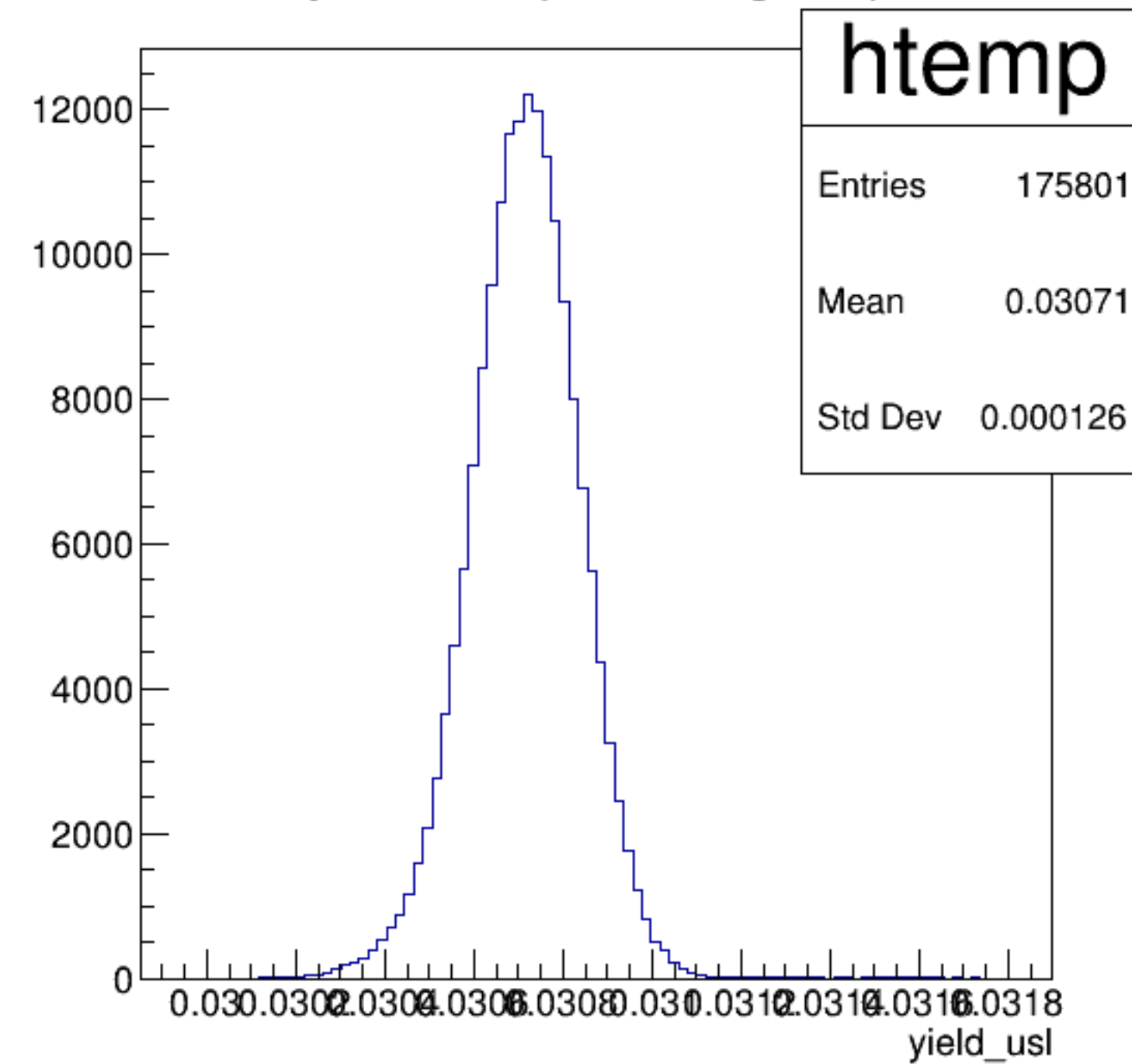


yield_usl:pattern_number {ErrorFlag==0}

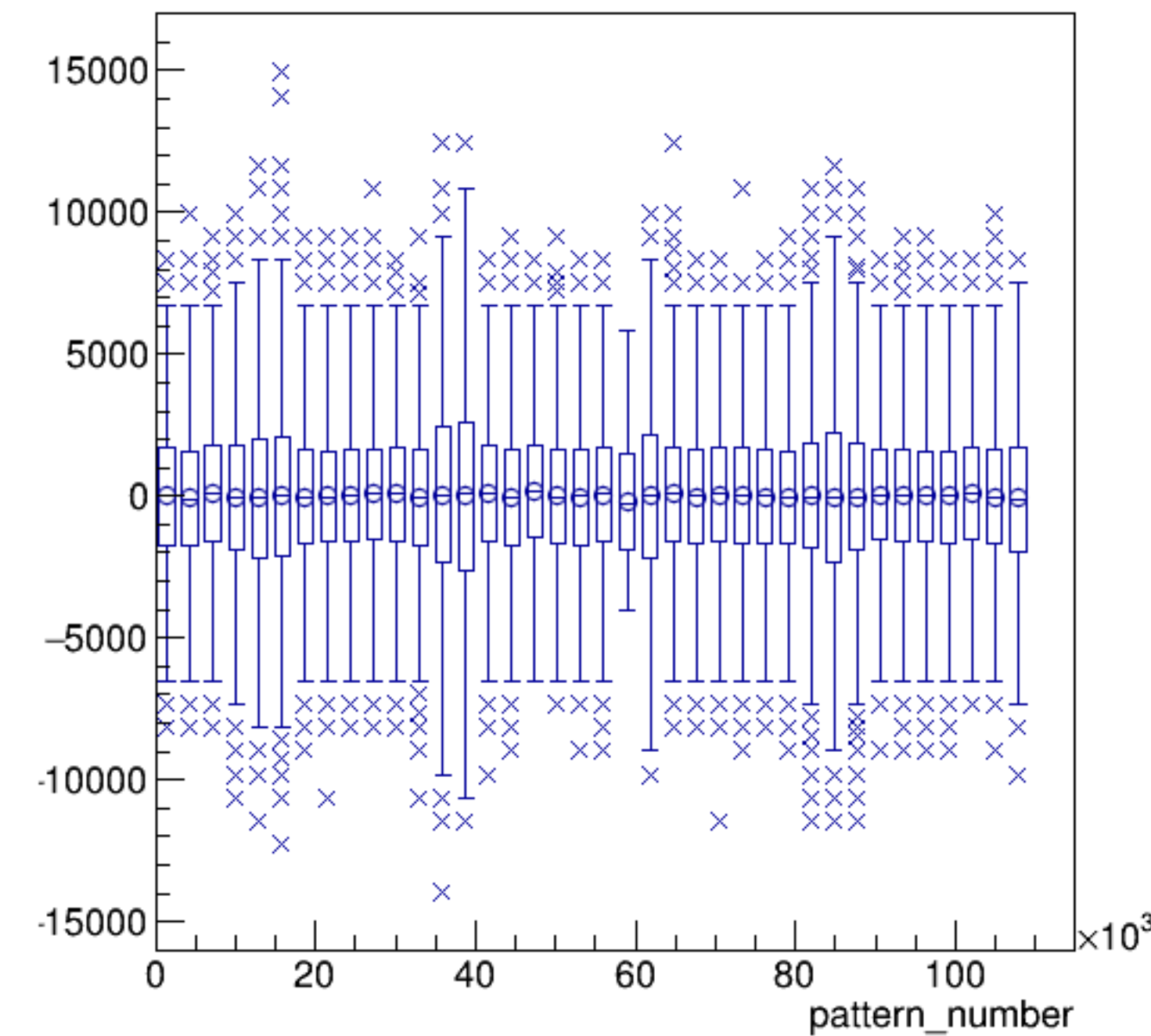


run7288_000_pairTree_usl.png

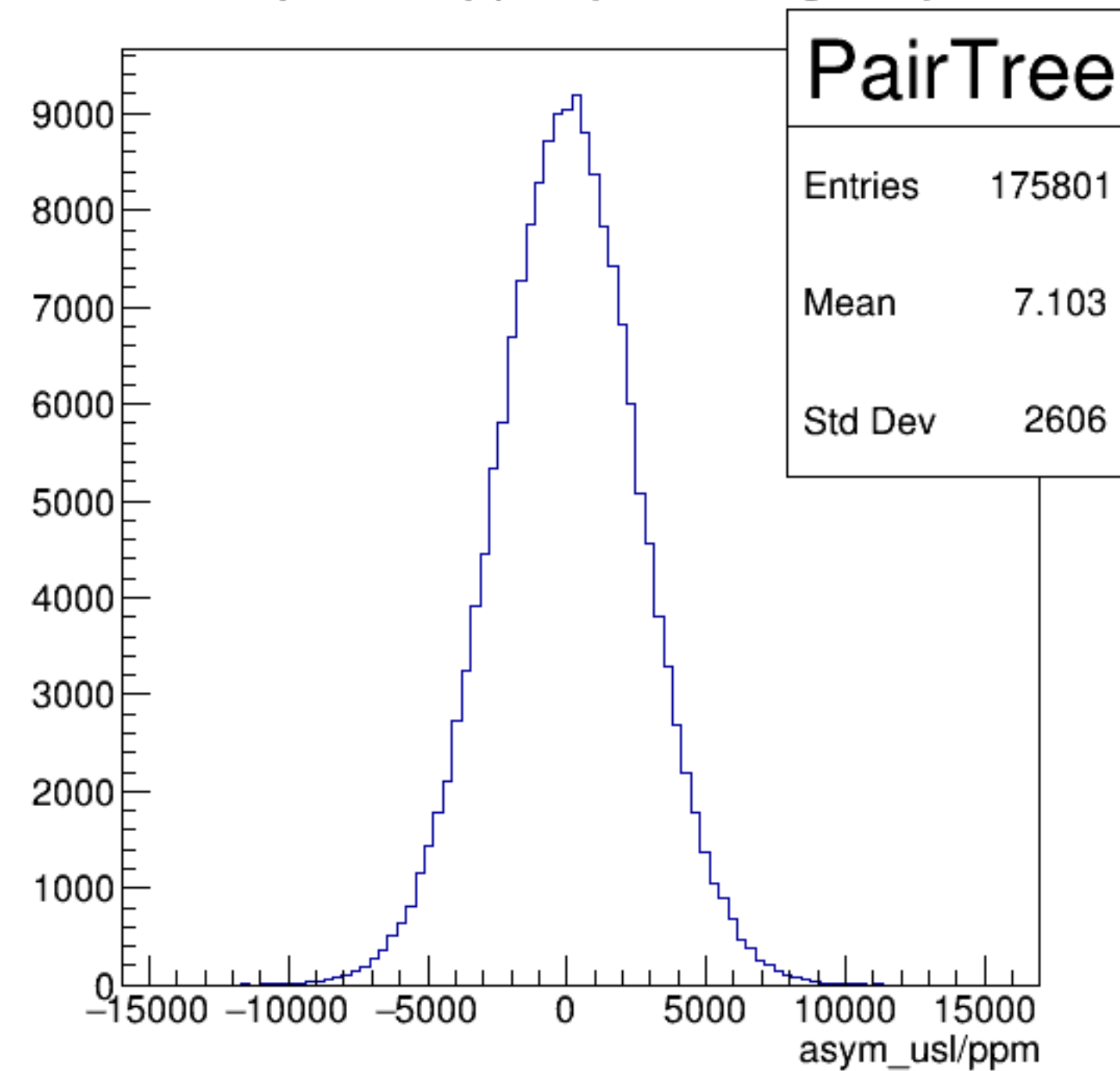
yield_usl {ErrorFlag==0}



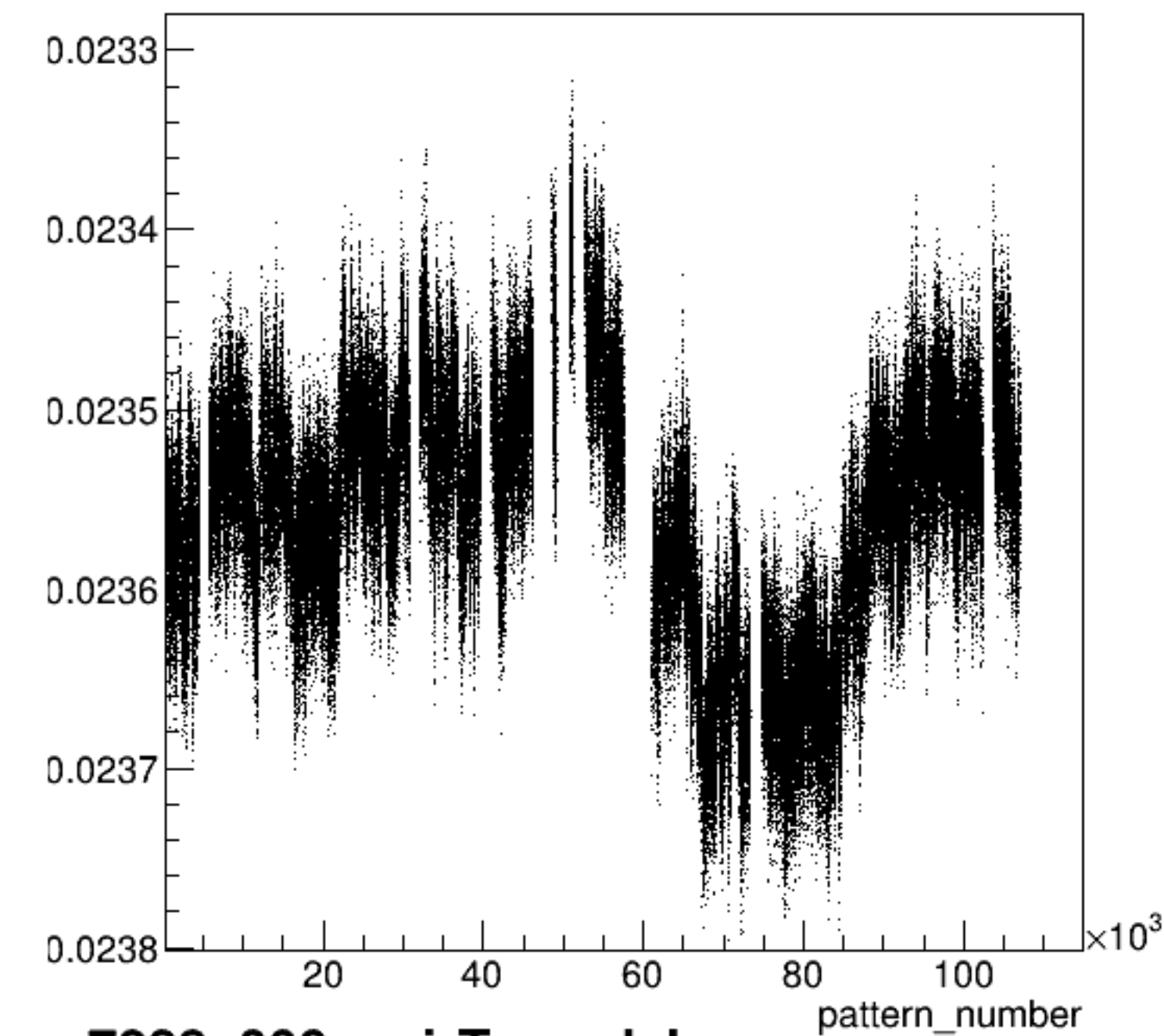
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl/ppm {ErrorFlag==0}

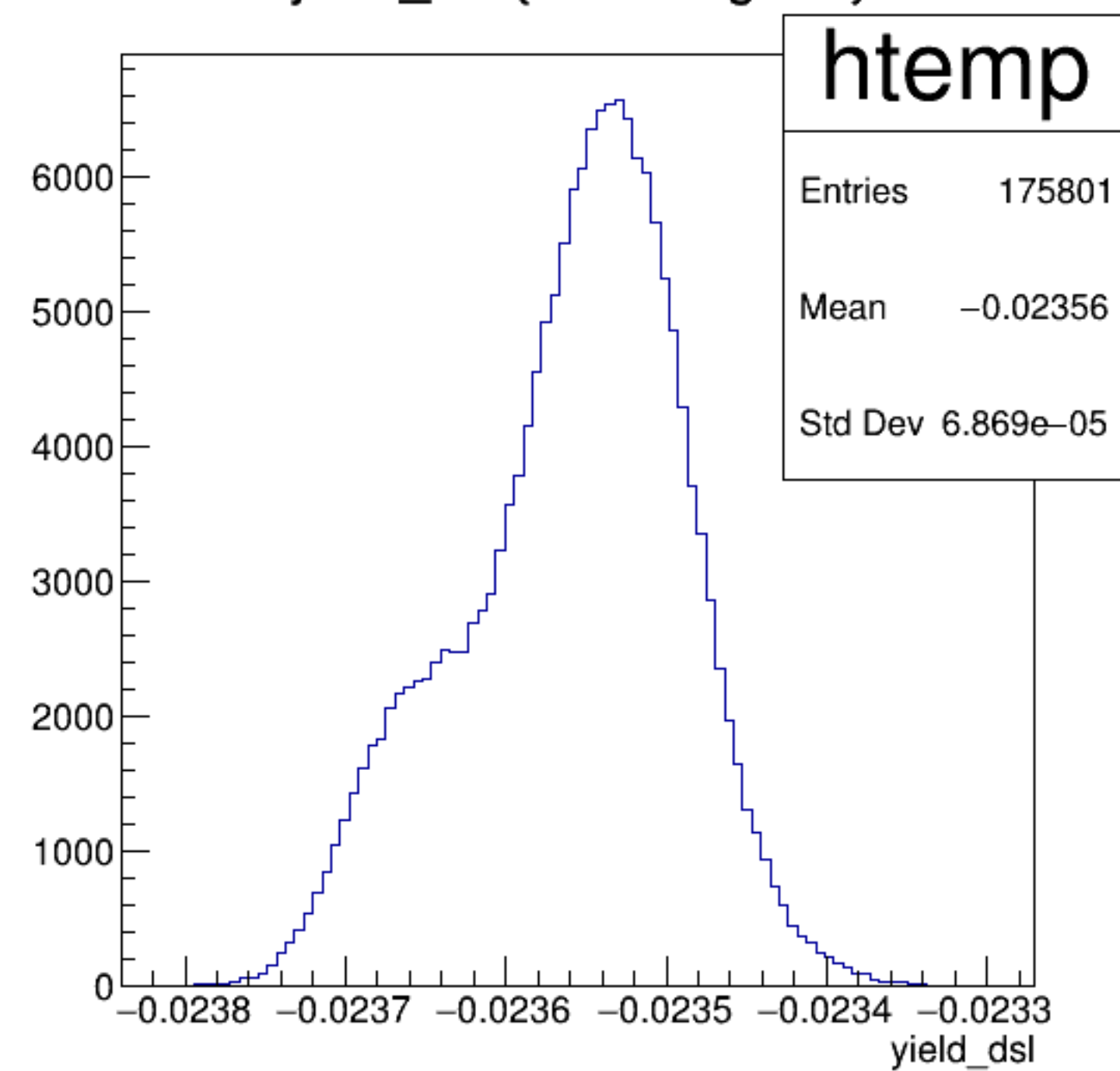


yield_dsl:pattern_number {ErrorFlag==0}

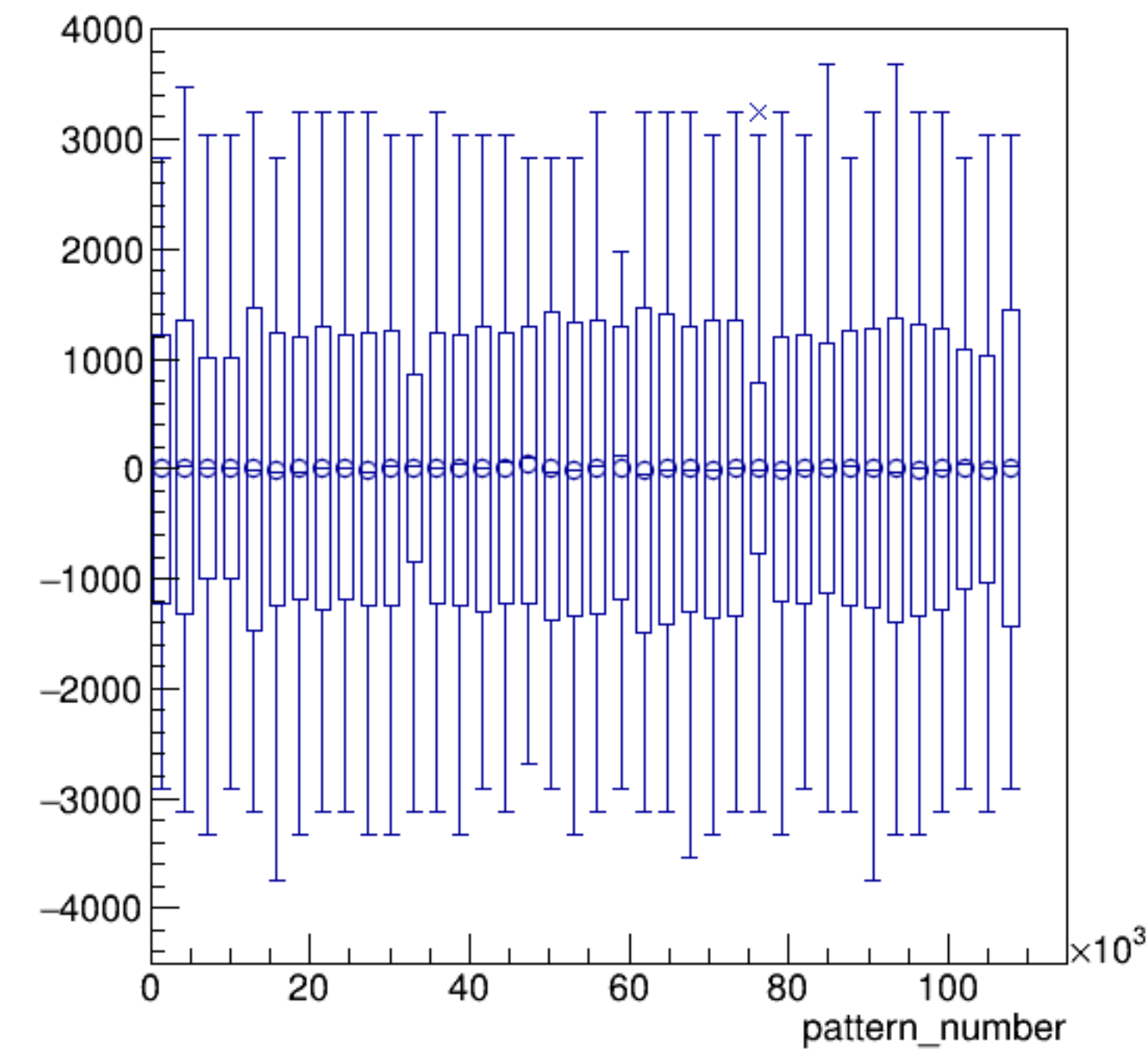


run7288_000_pairTree_dsl.png

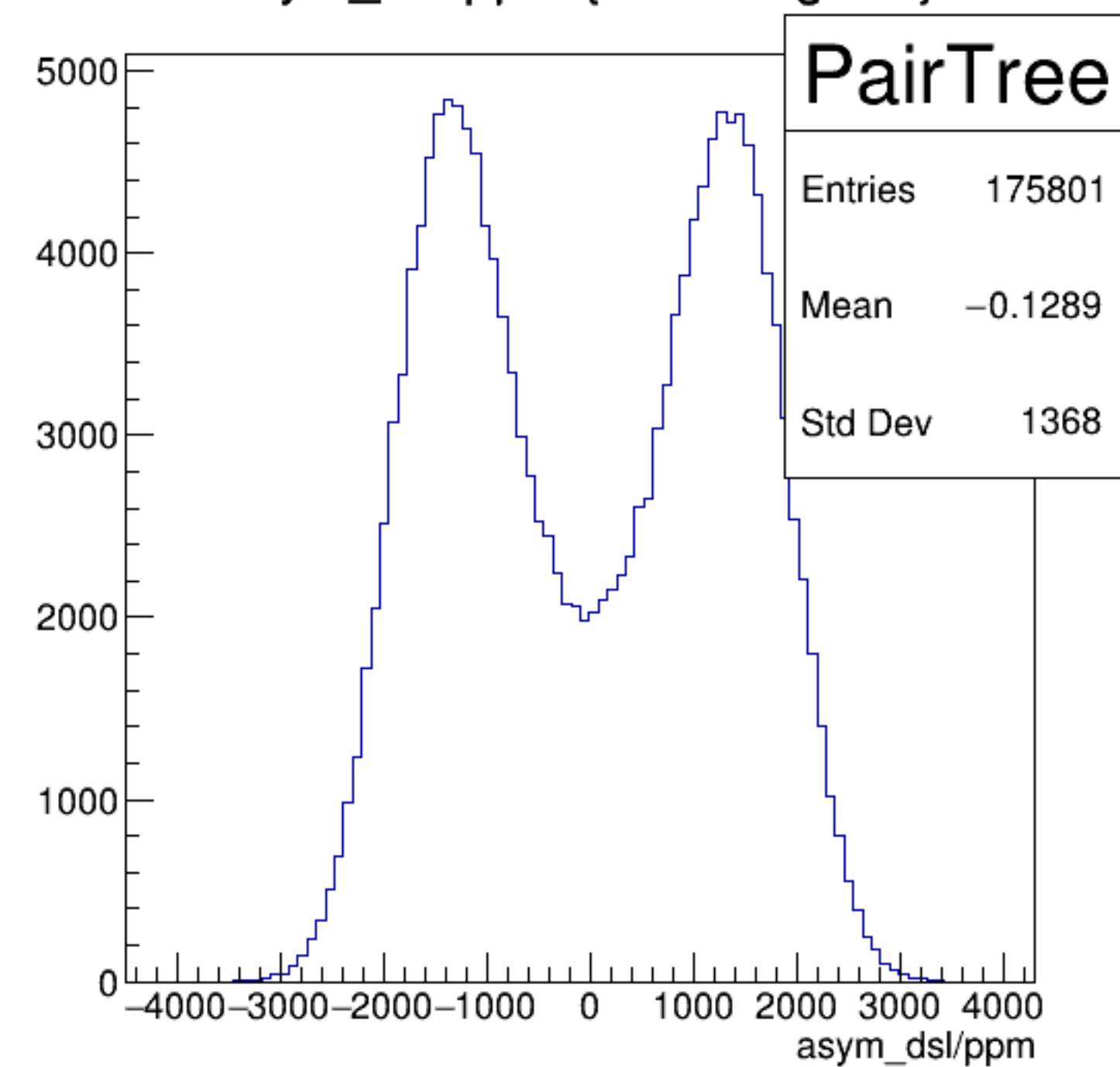
yield_dsl {ErrorFlag==0}

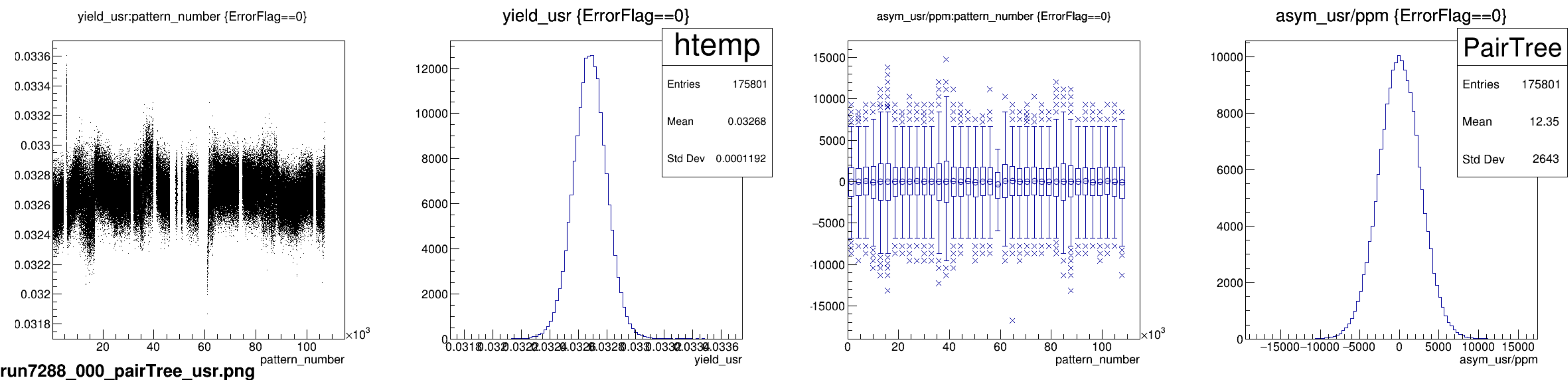


asym_dsl/ppm:pattern_number {ErrorFlag==0}

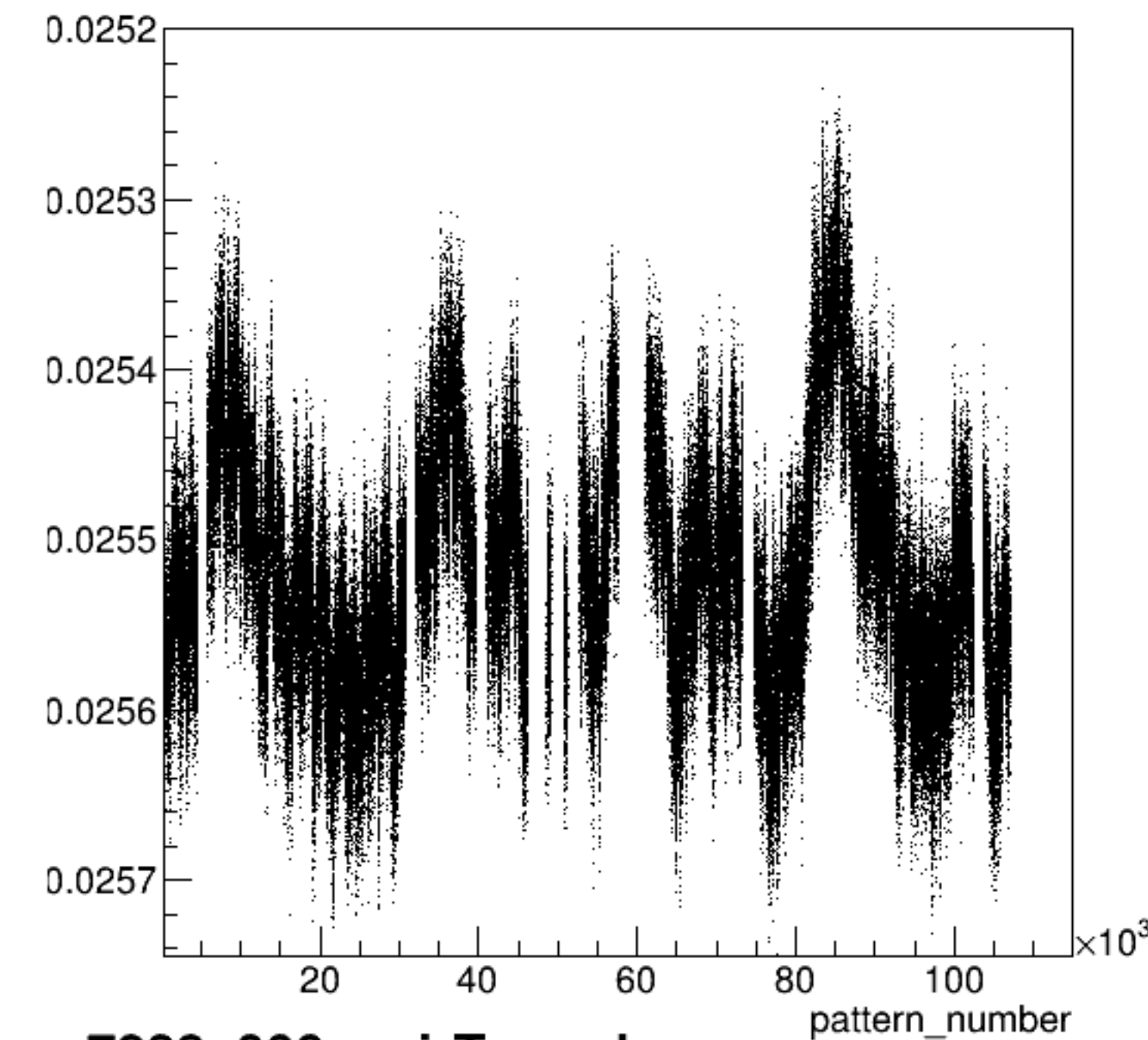


asym_dsl/ppm {ErrorFlag==0}



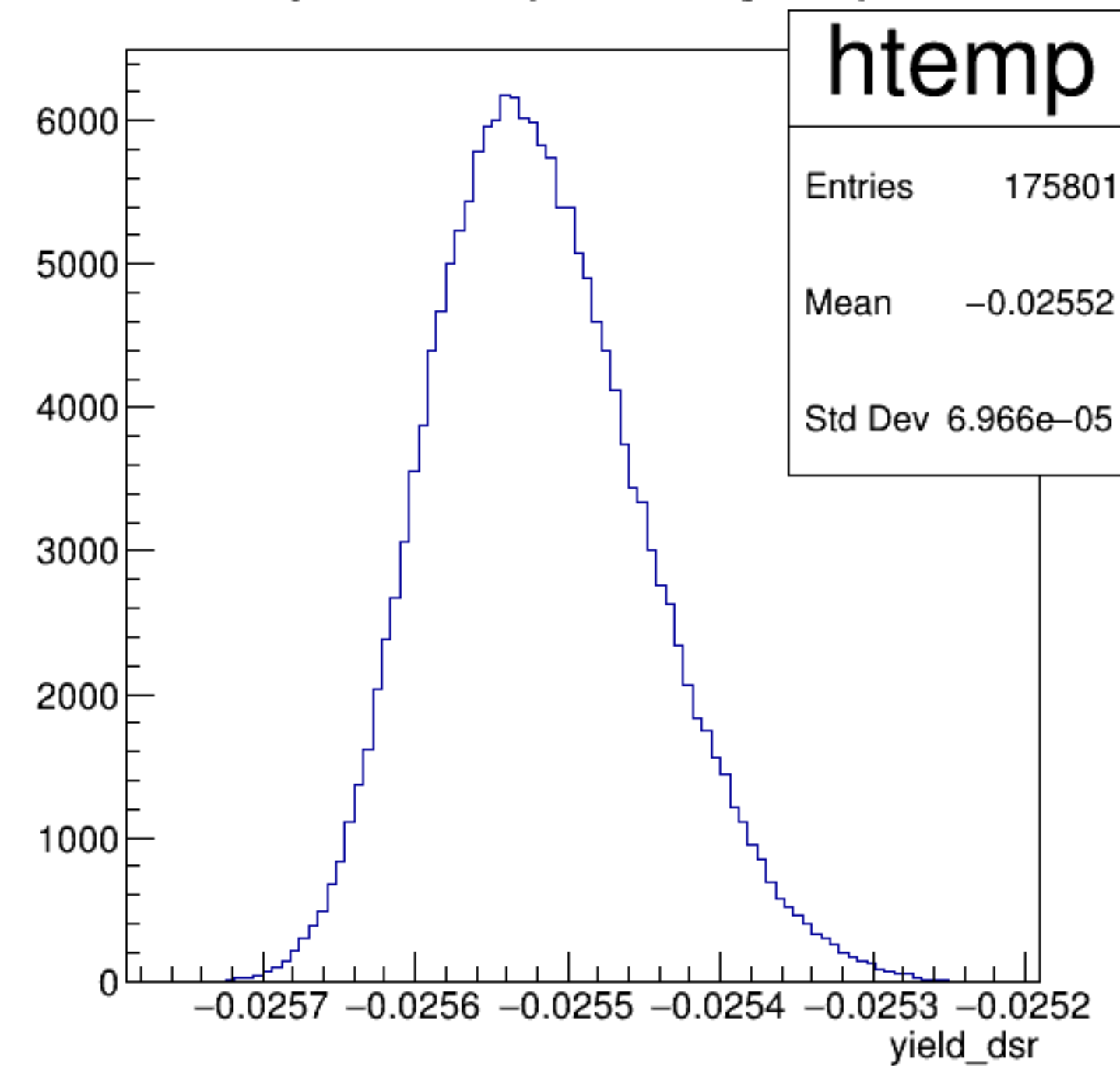


yield_dsr:pattern_number {ErrorFlag==0}

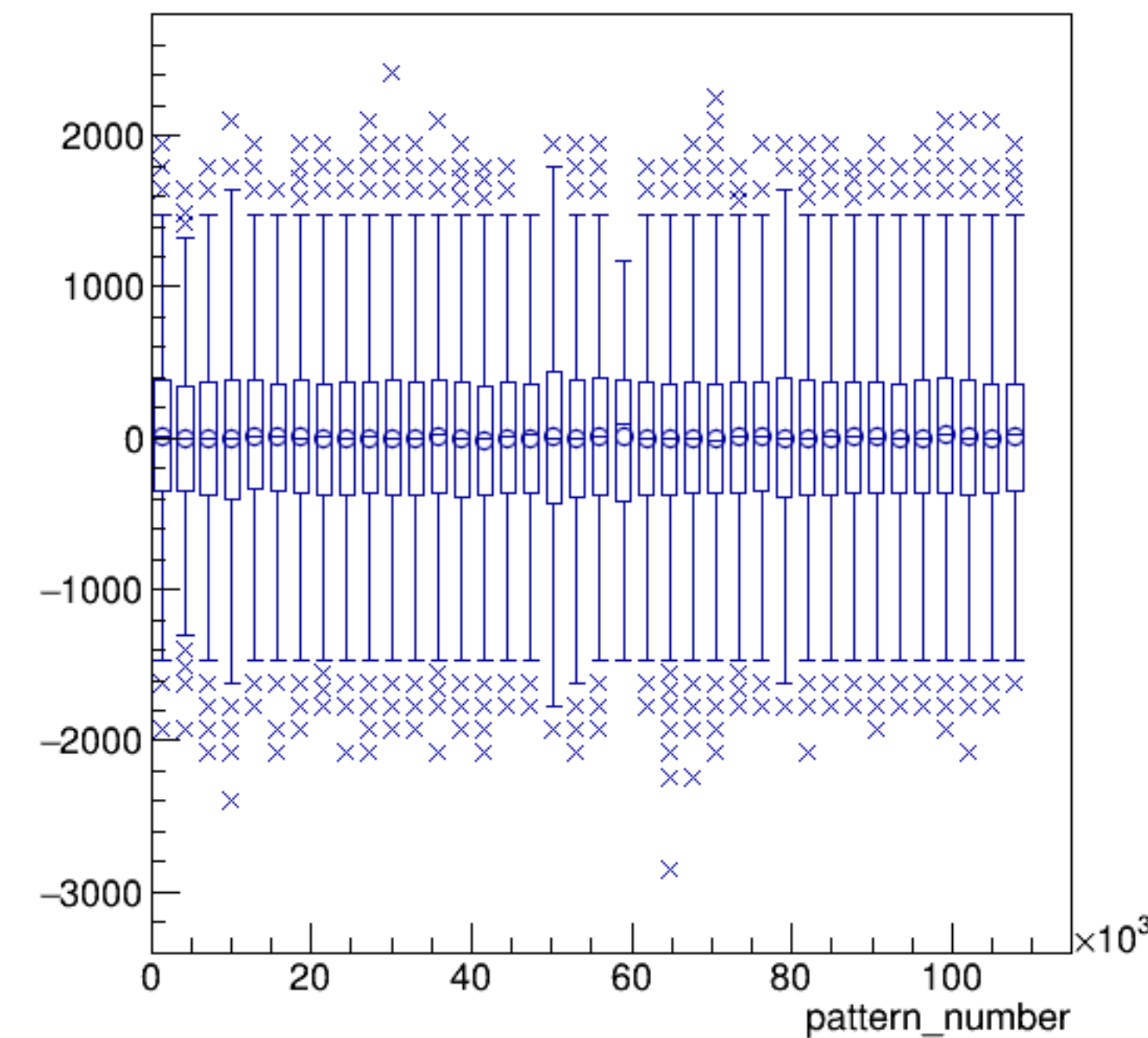


run7288_000_pairTree_dsr.png

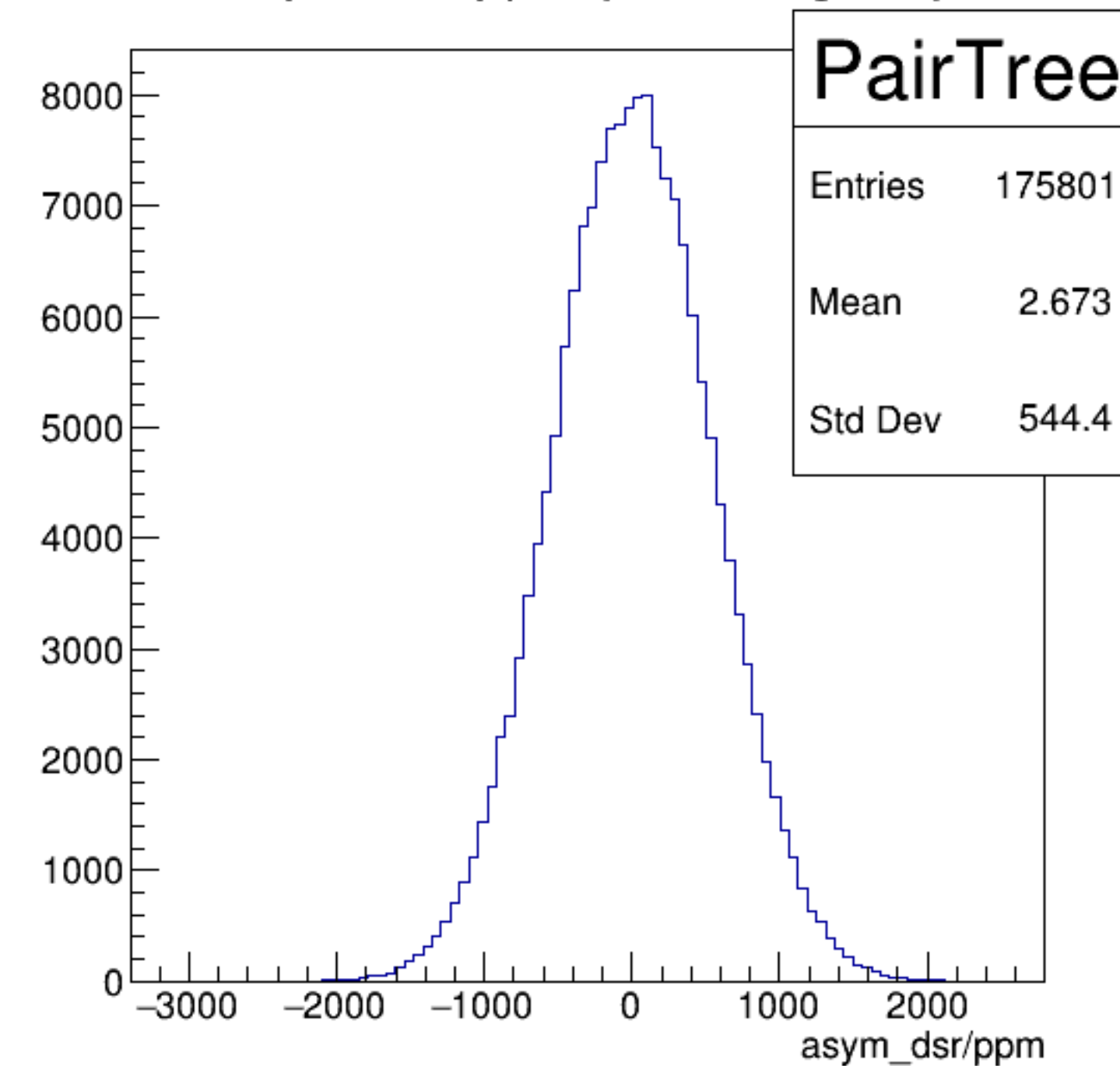
yield_dsr {ErrorFlag==0}



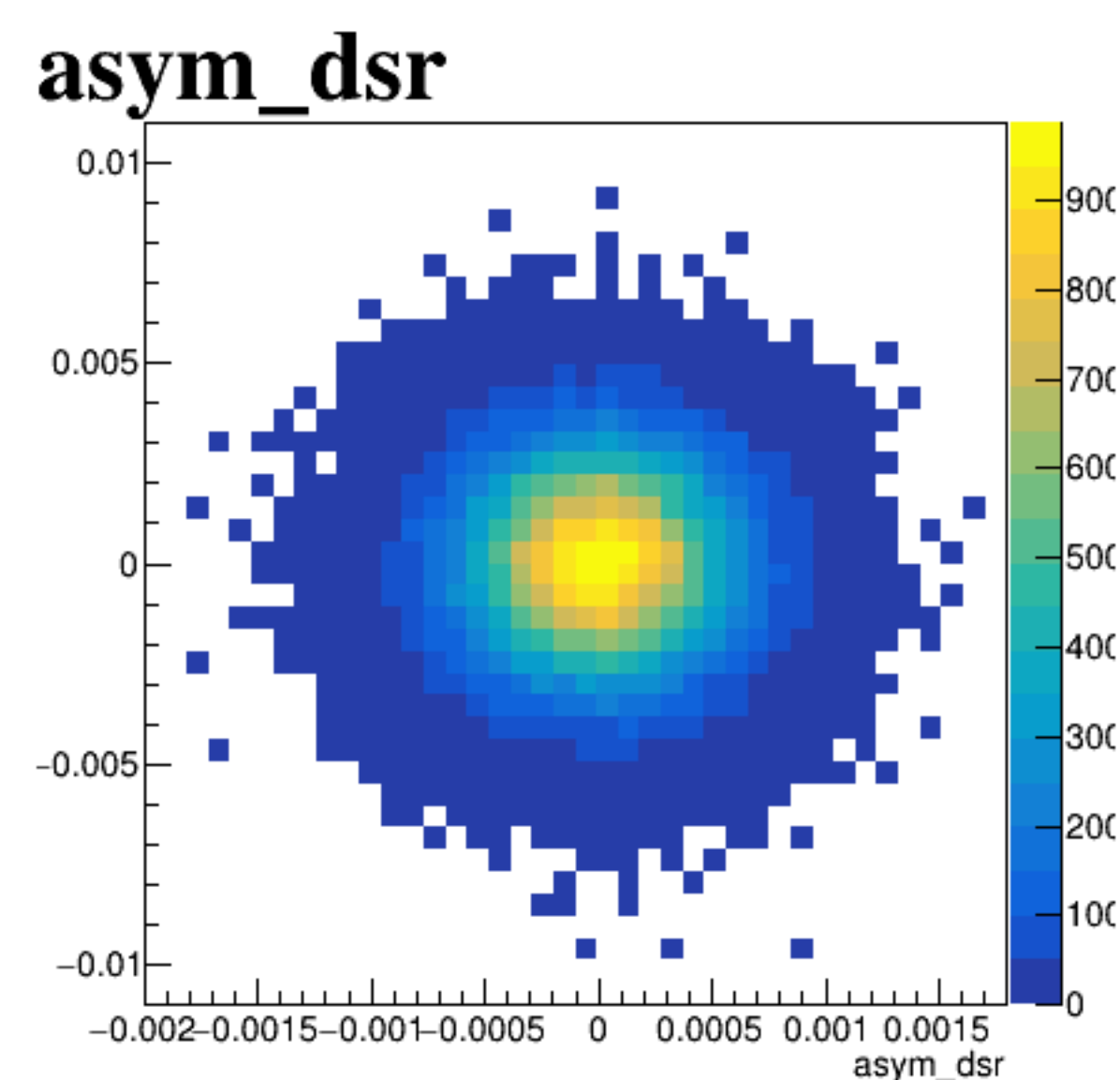
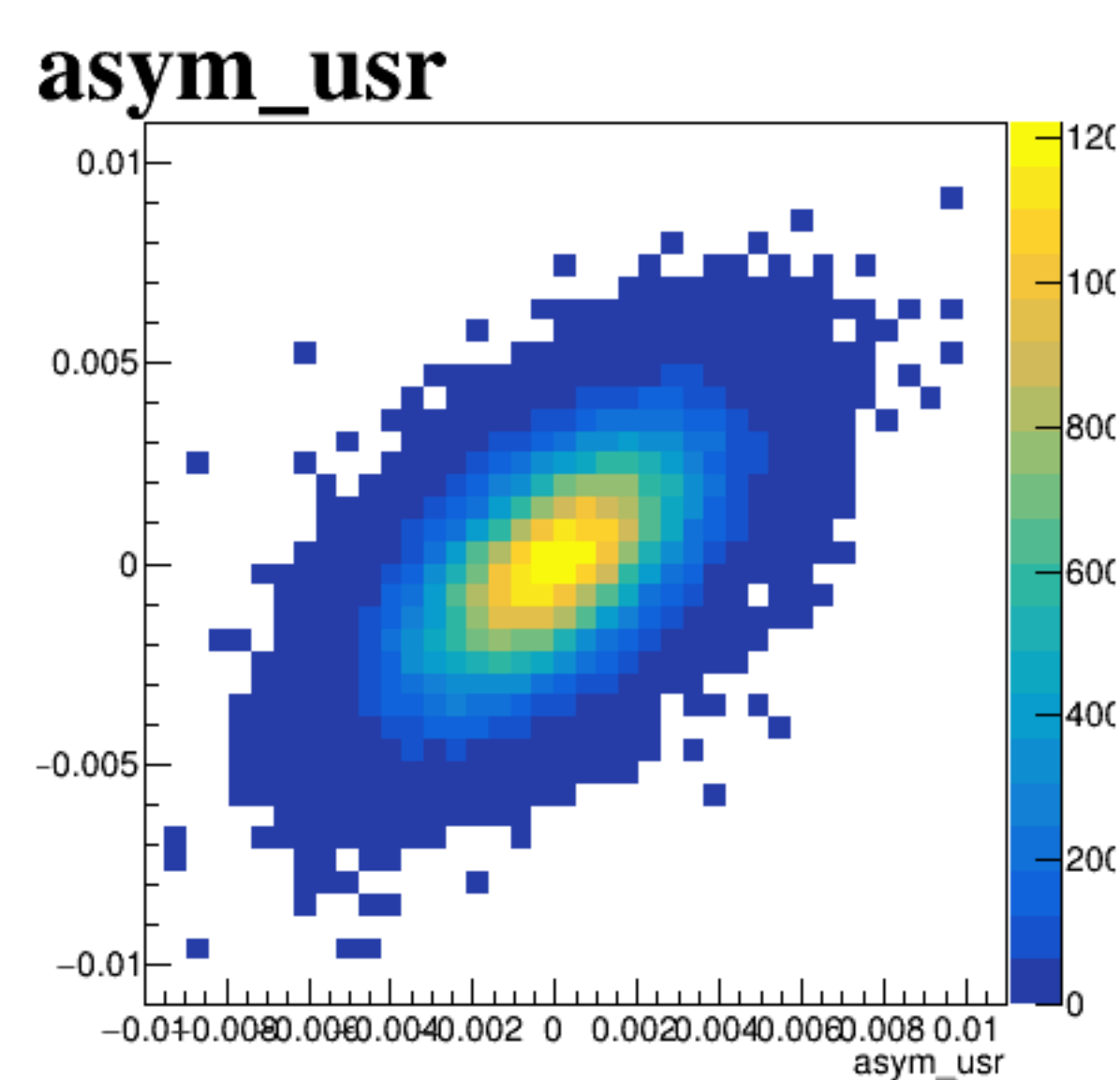
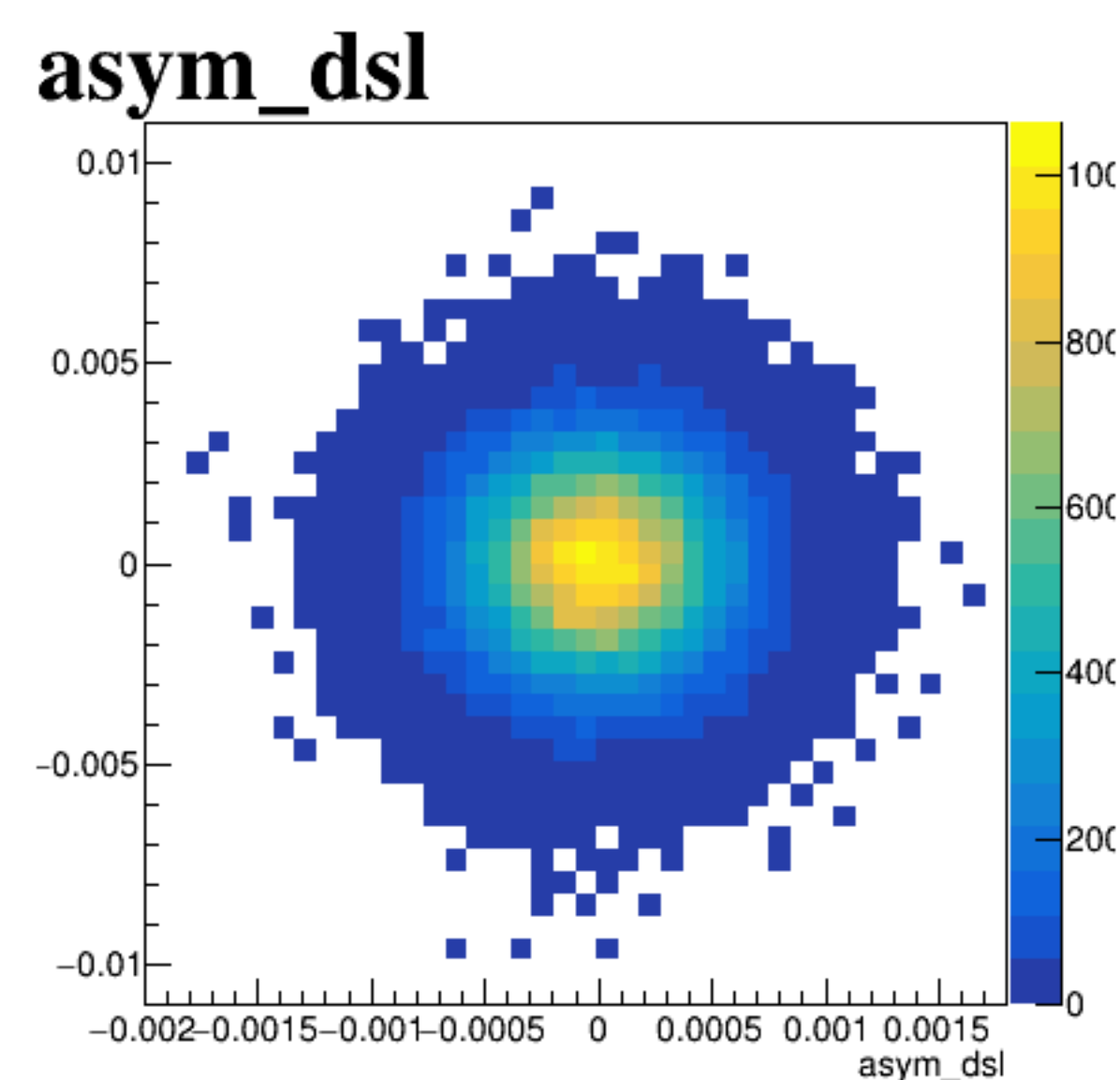
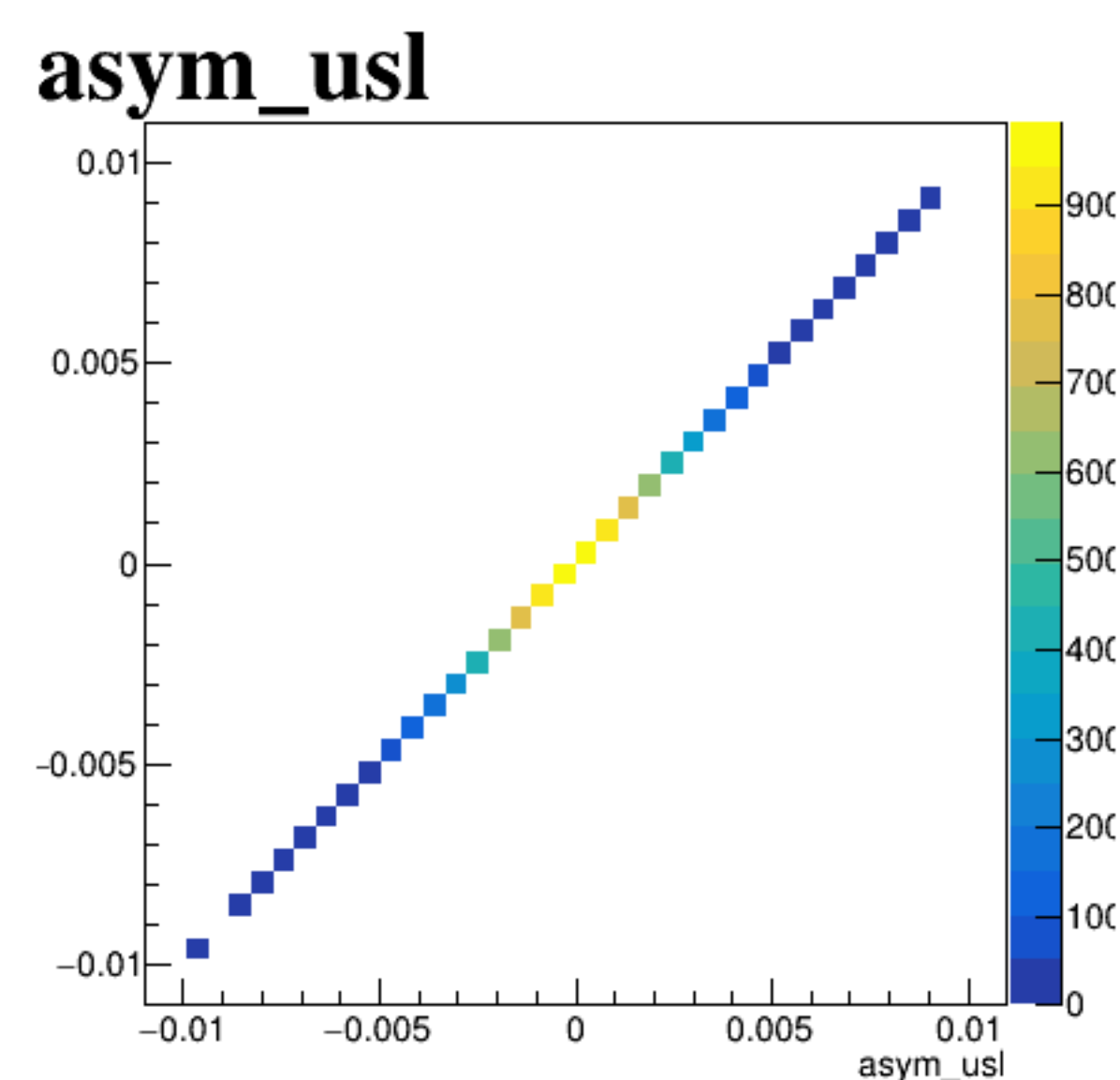
asym_dsr/ppm:pattern_number {ErrorFlag==0}



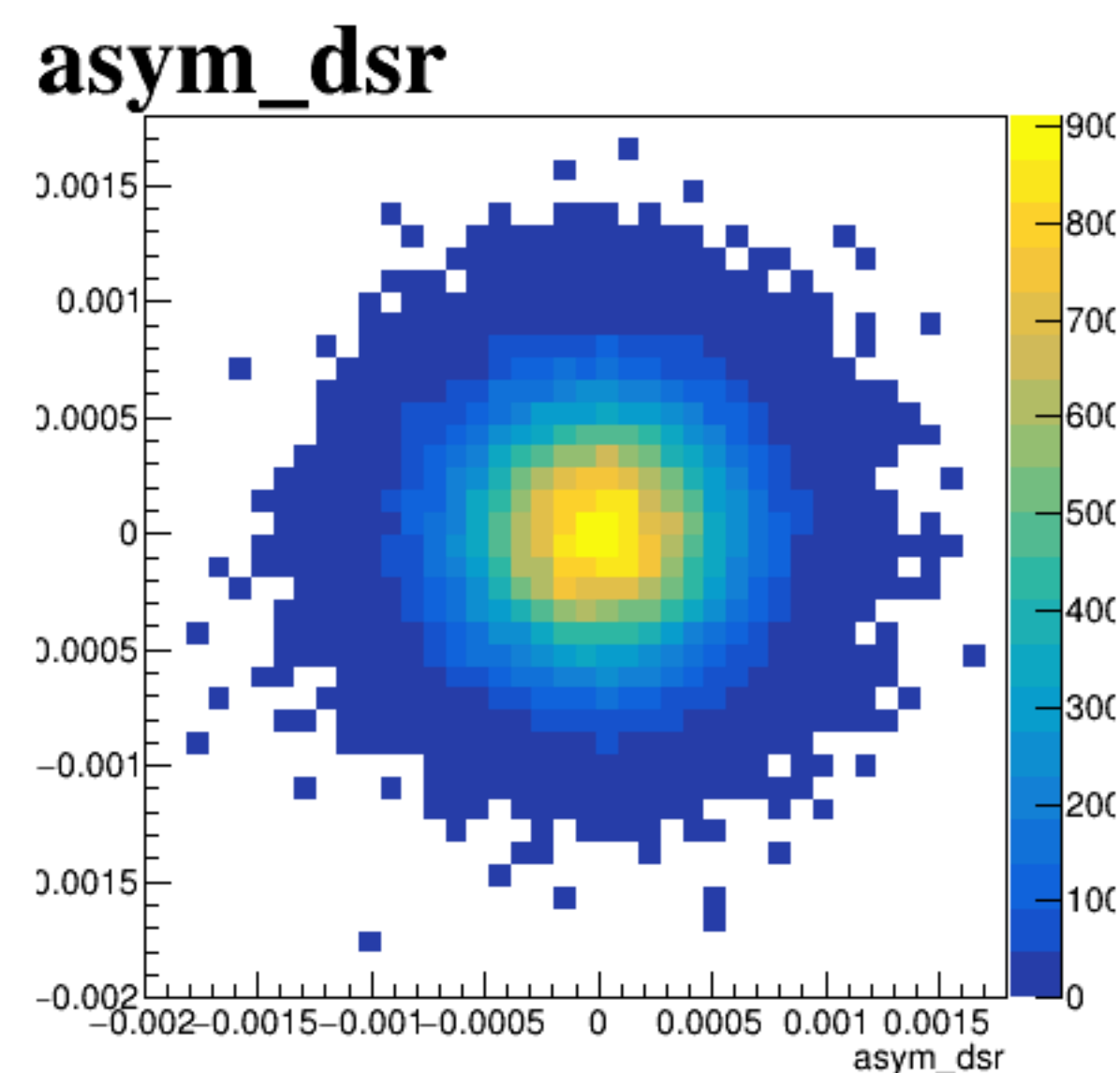
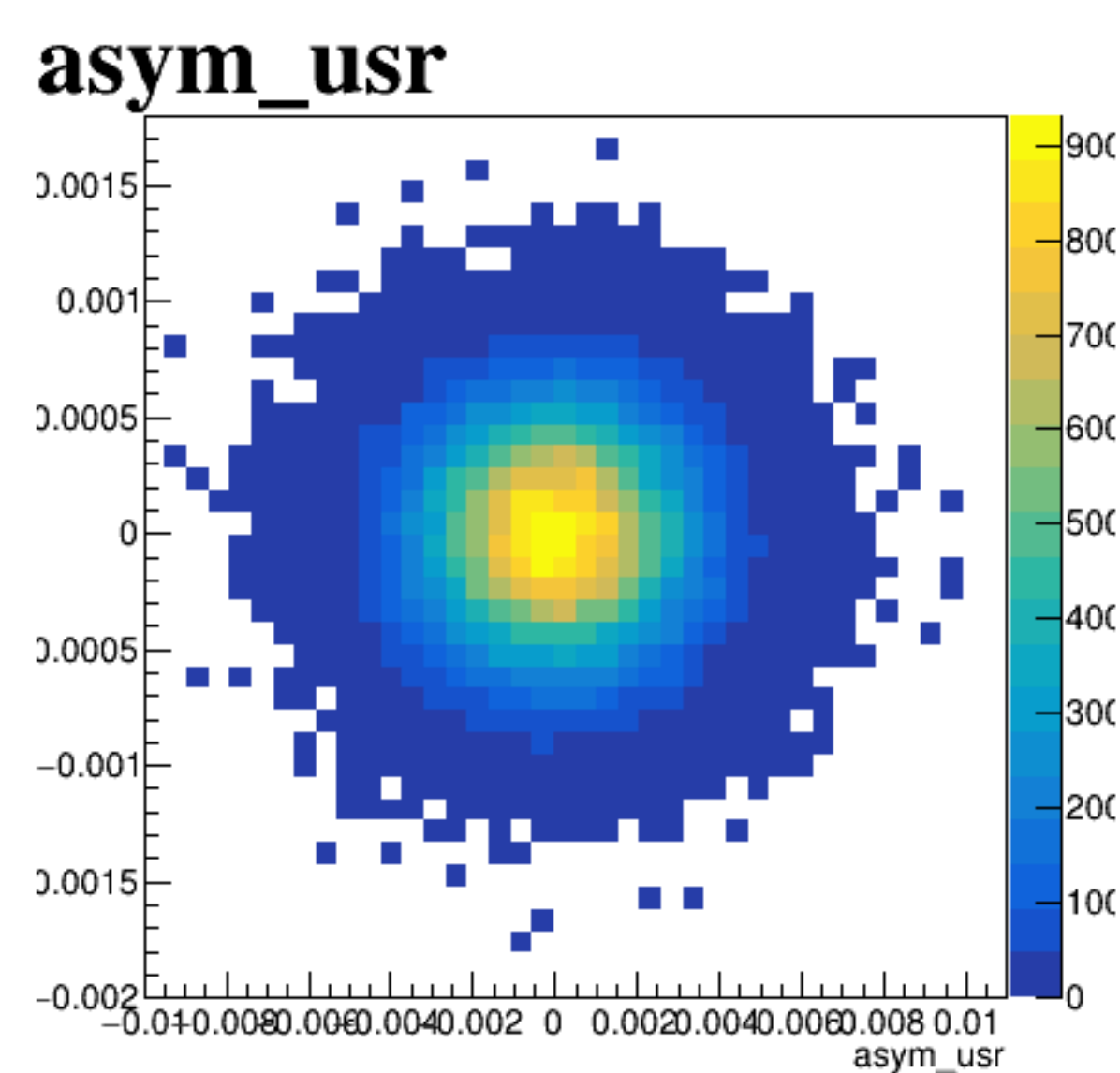
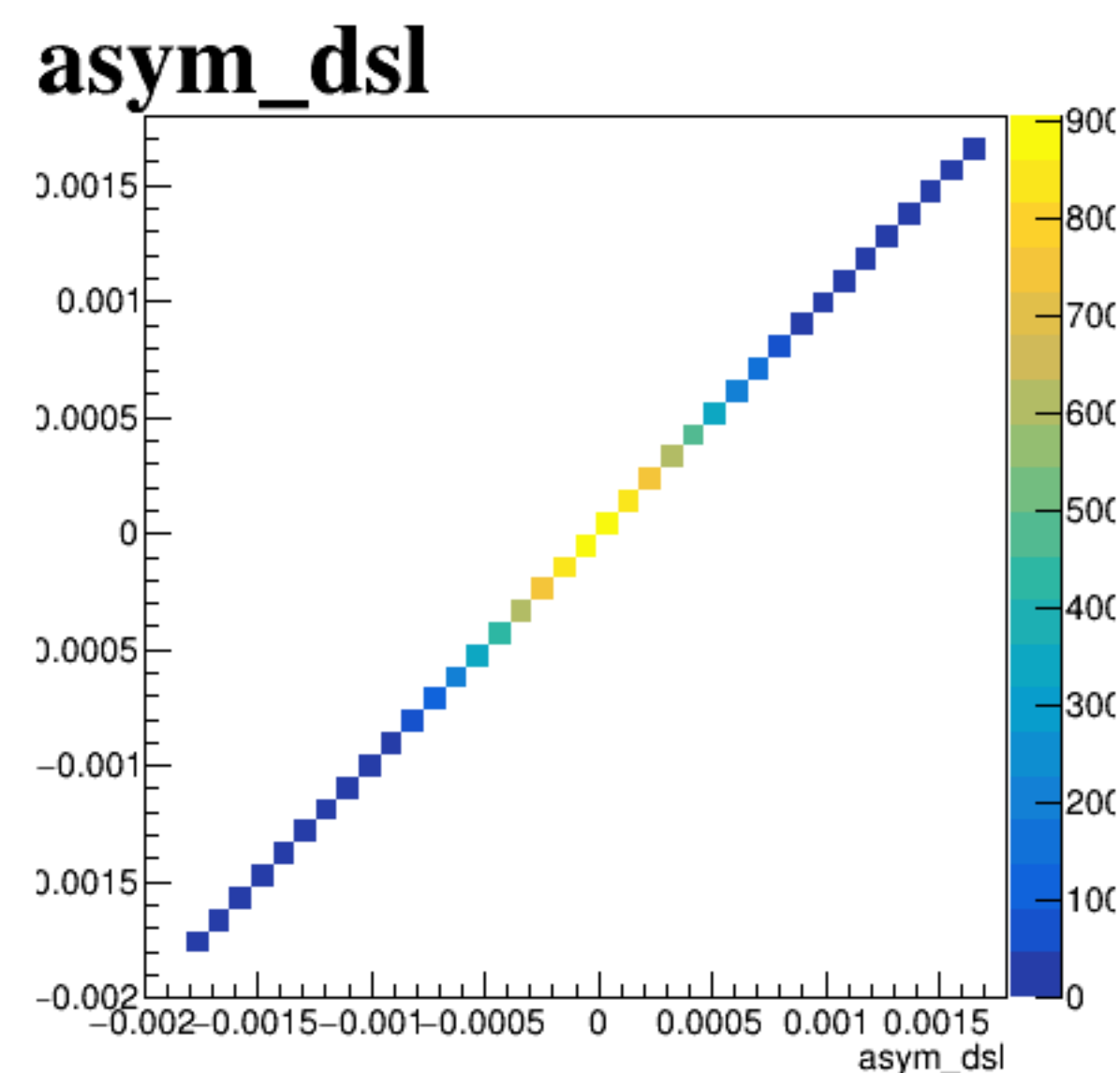
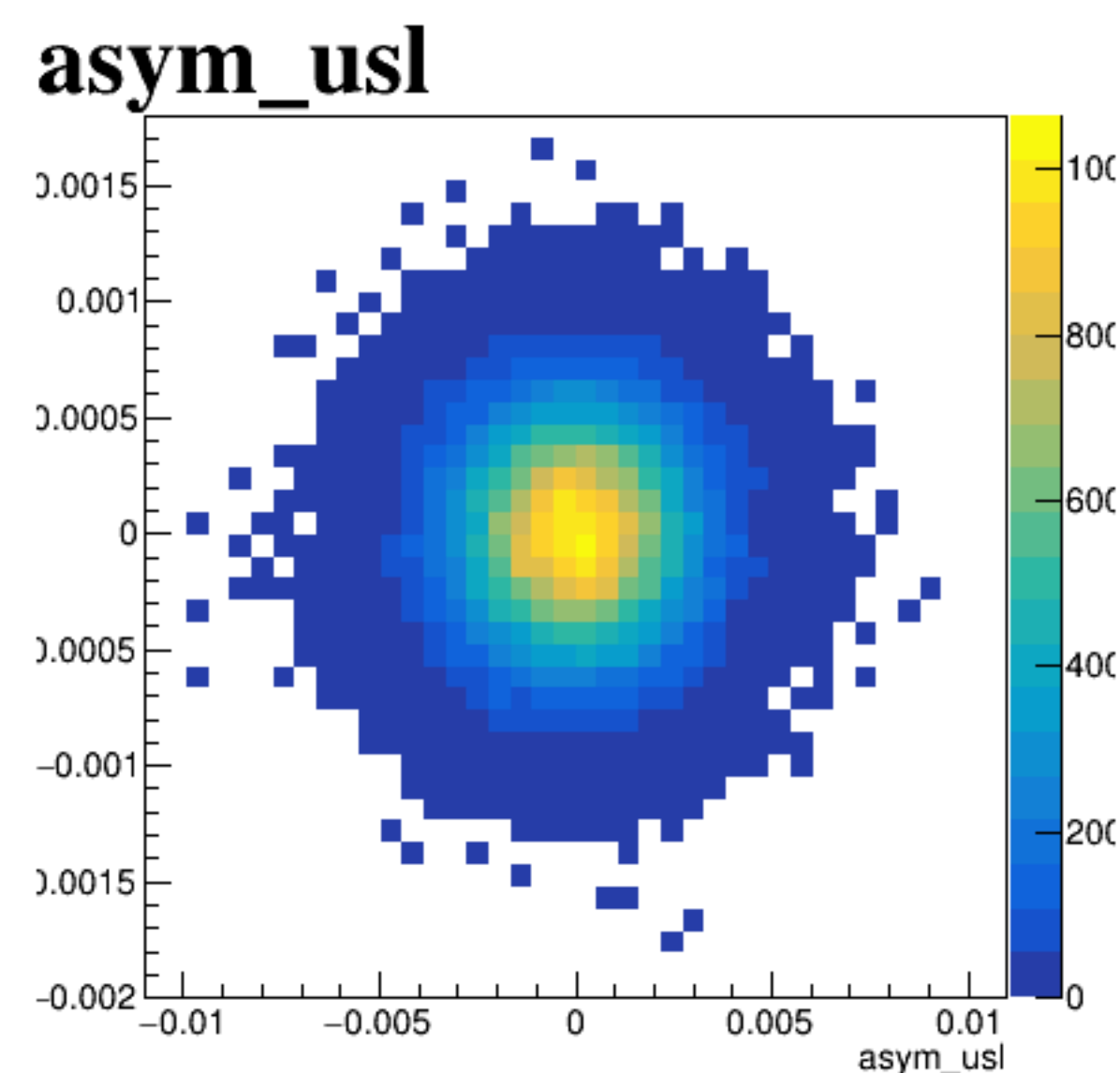
asym_dsr/ppm {ErrorFlag==0}



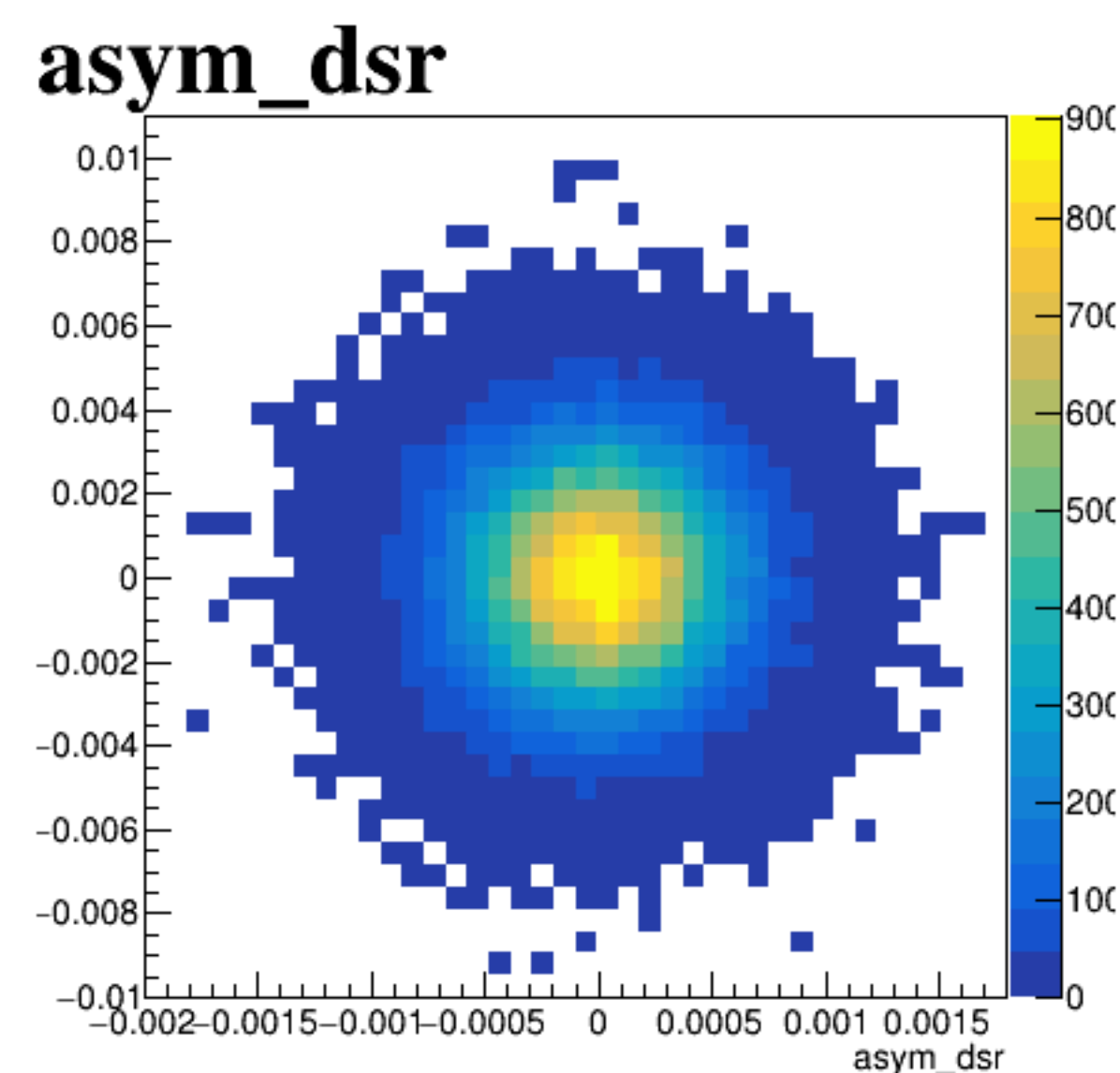
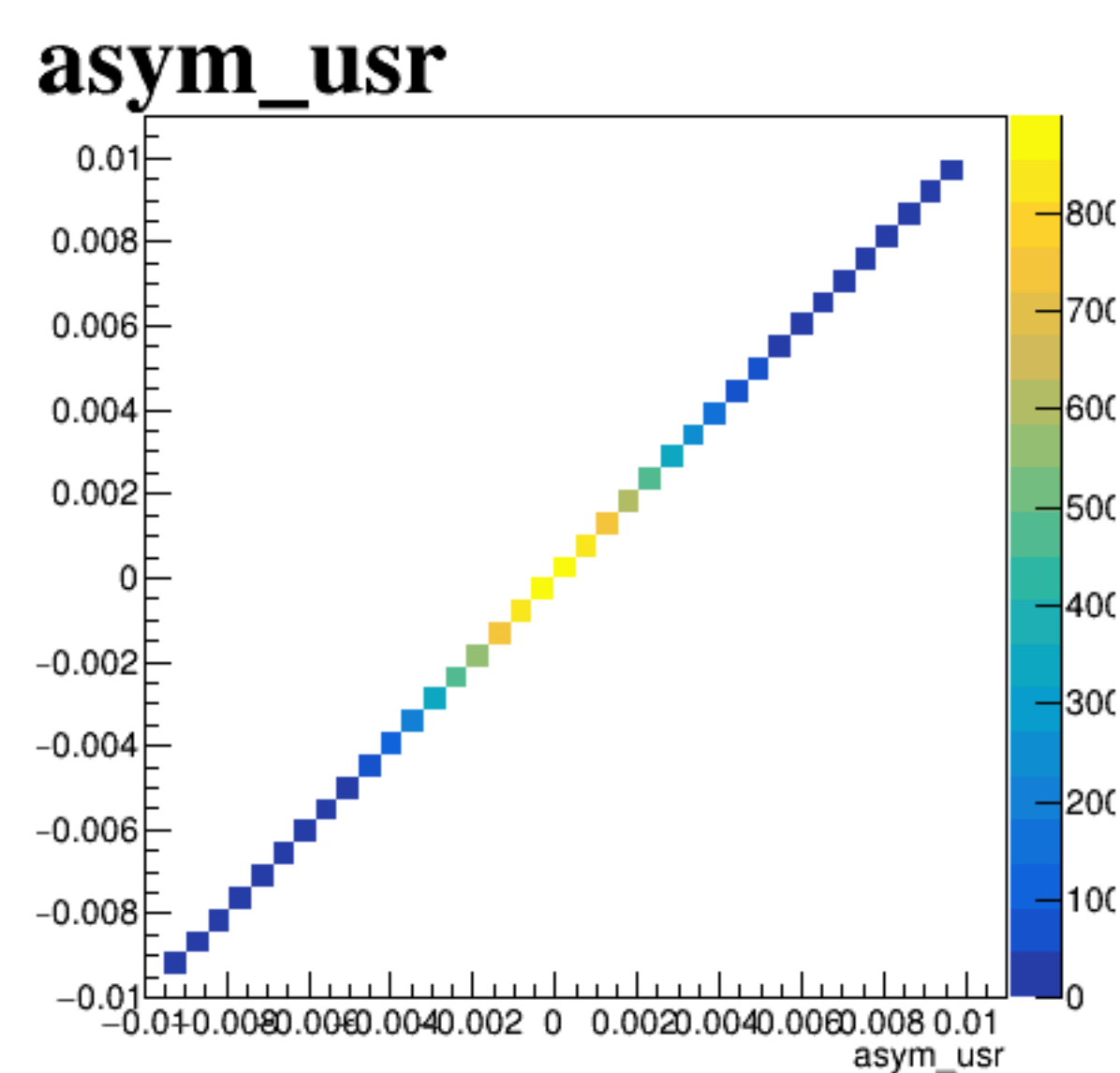
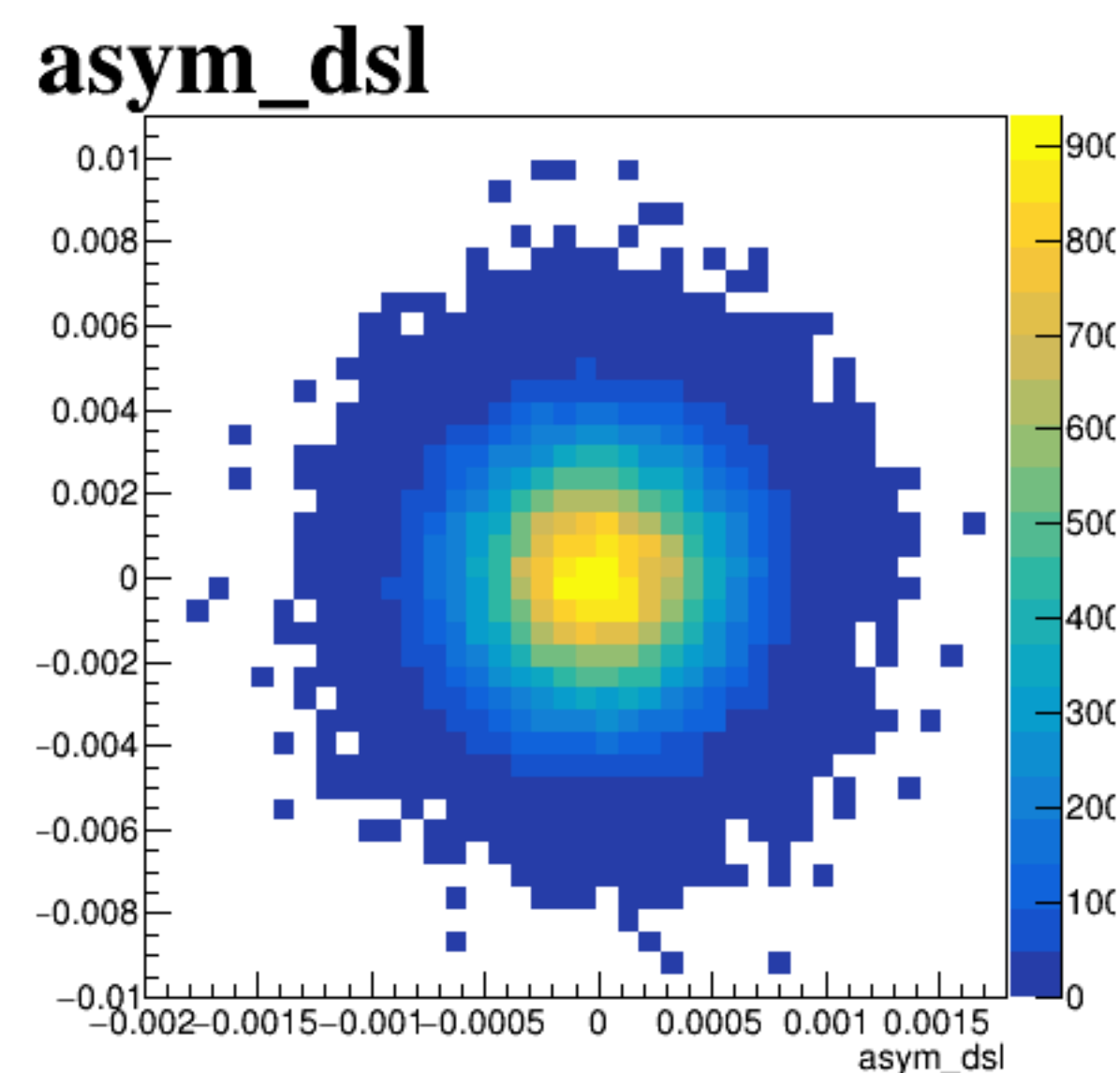
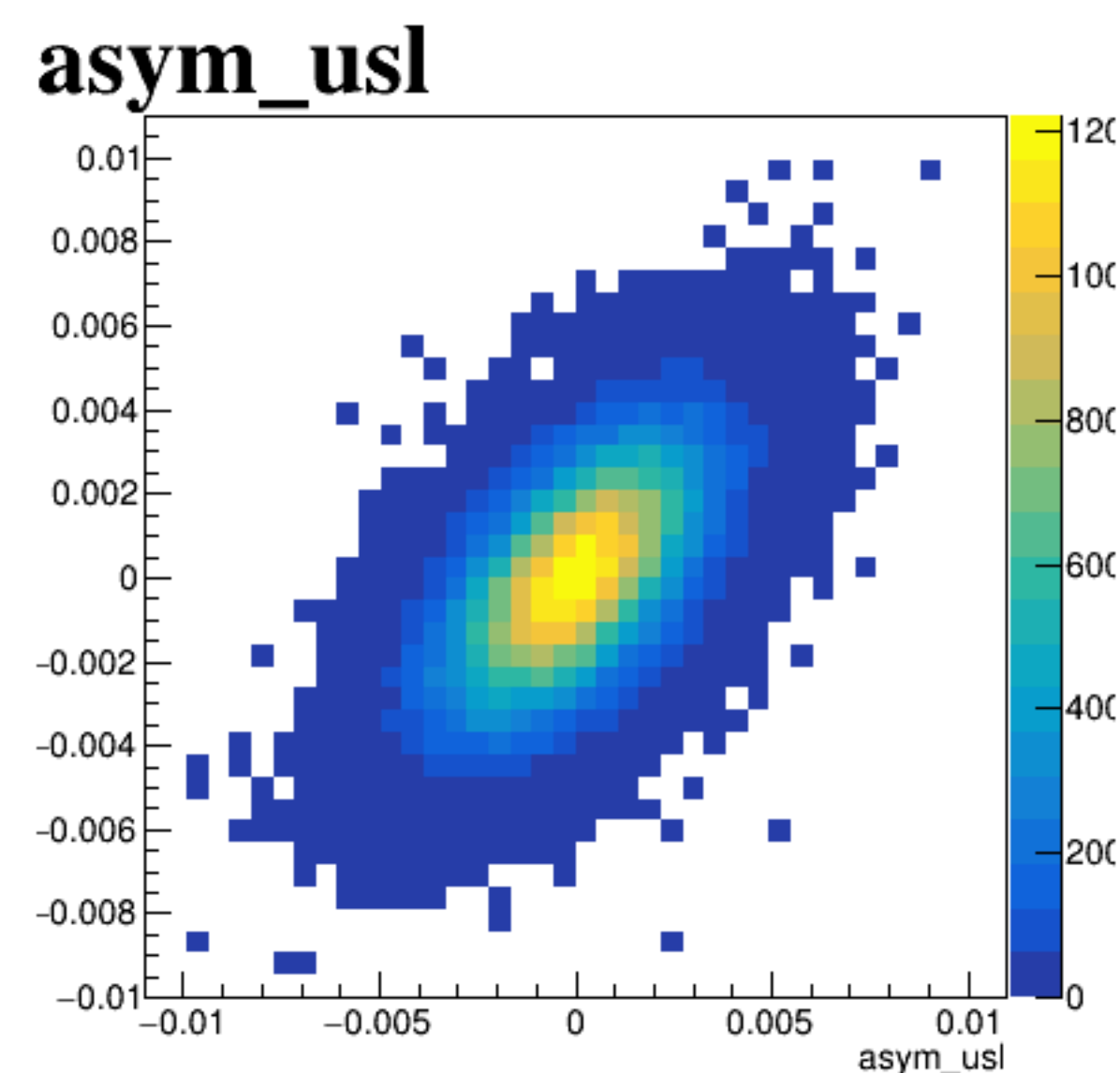
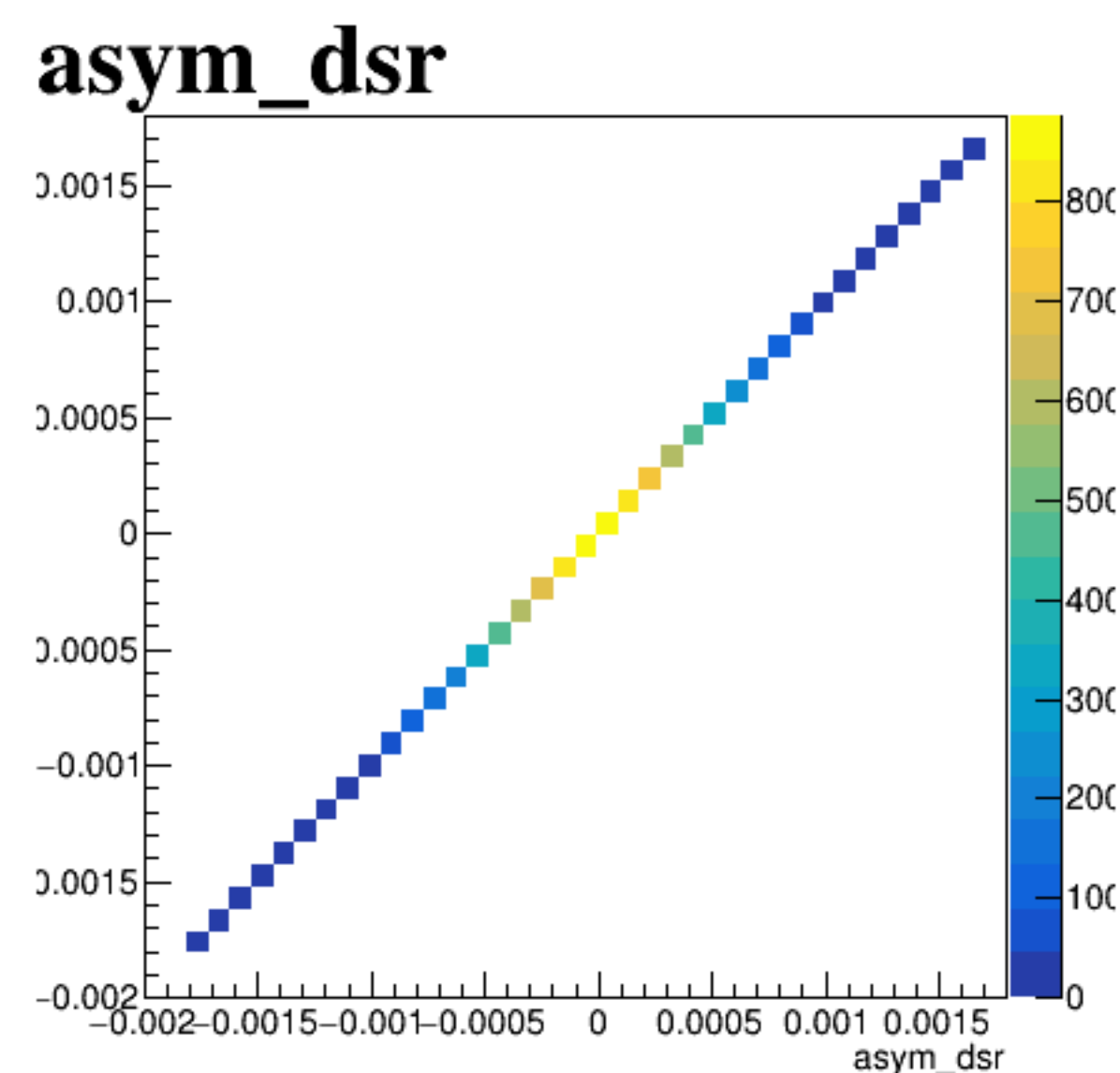
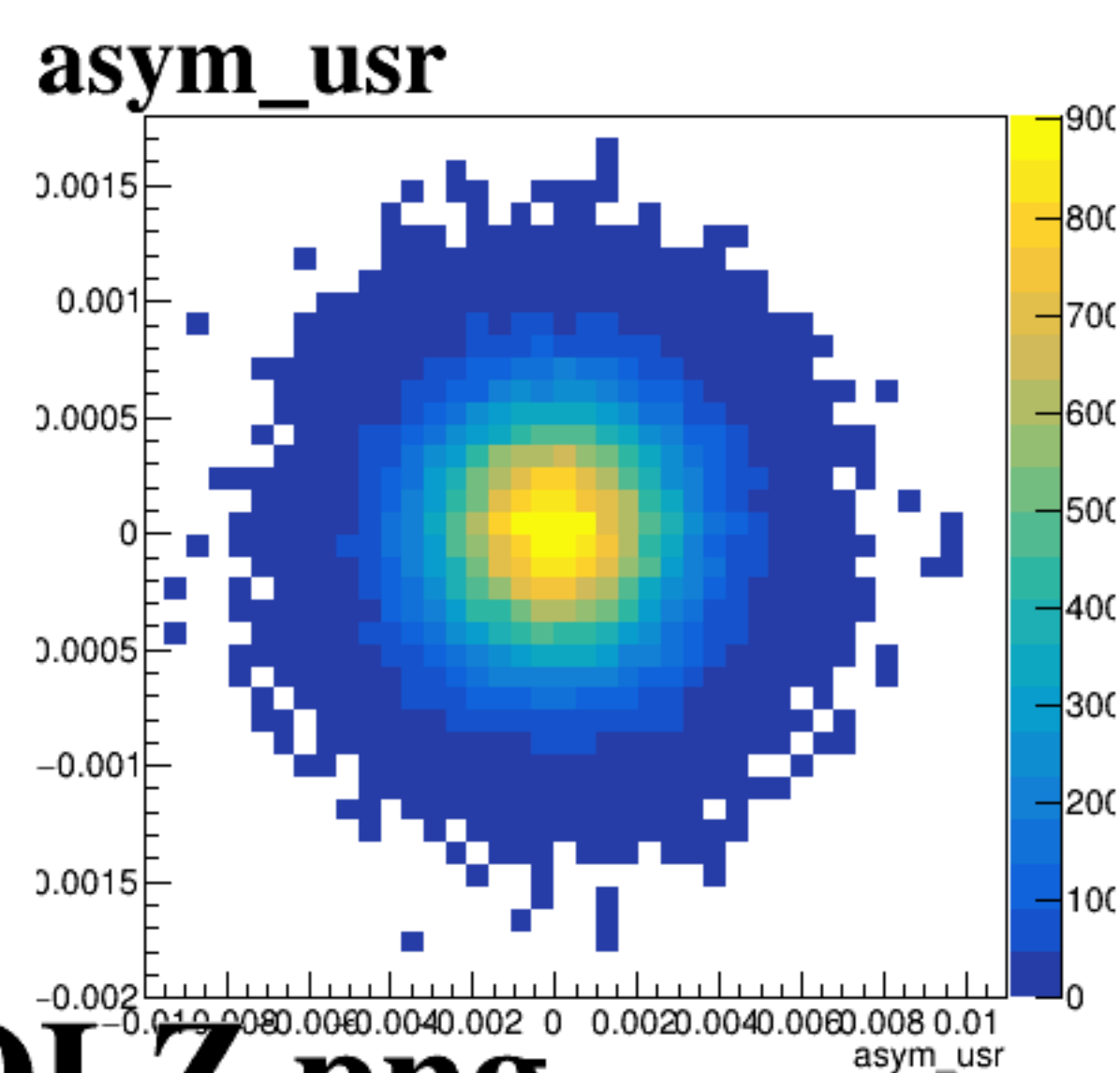
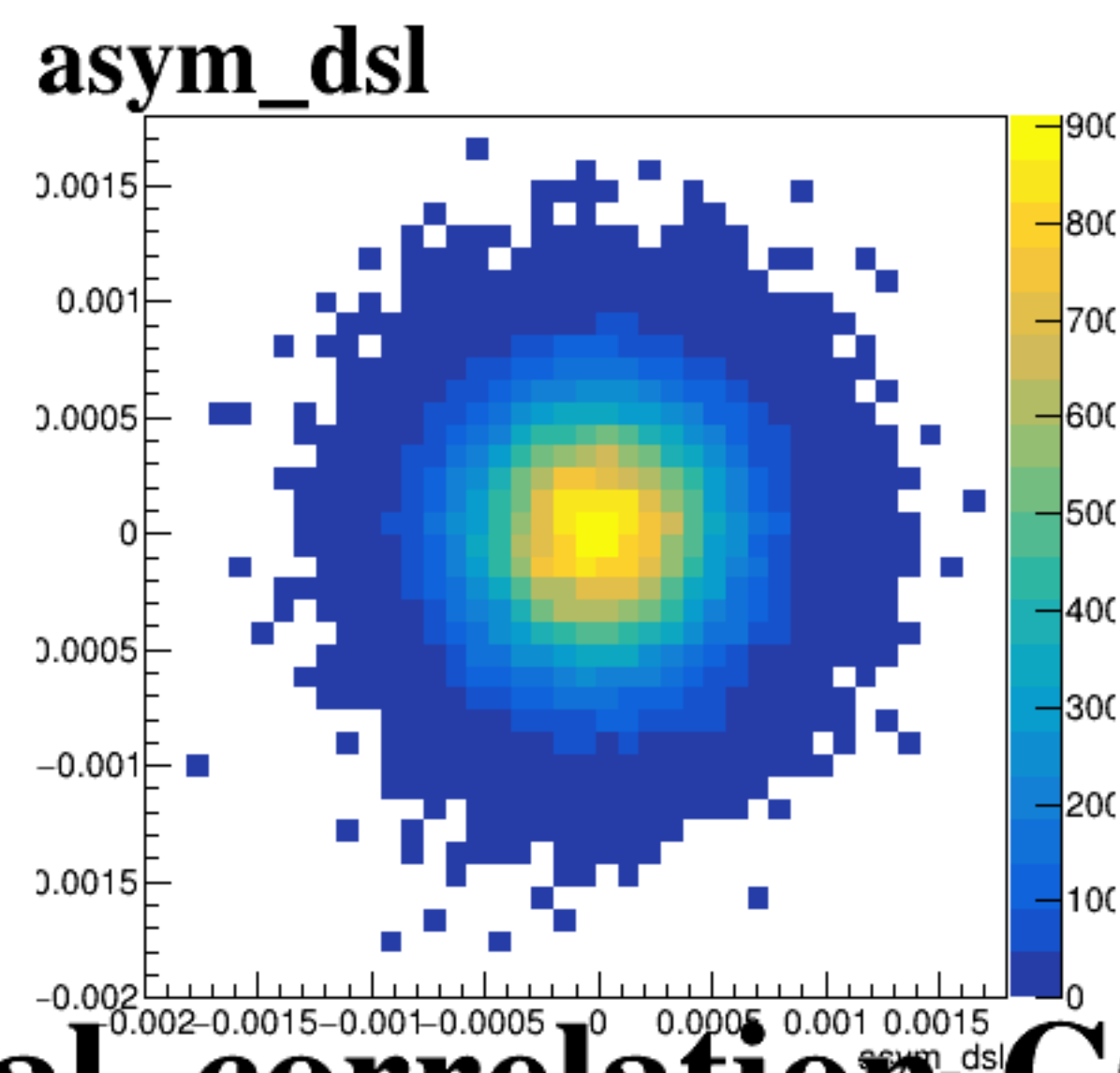
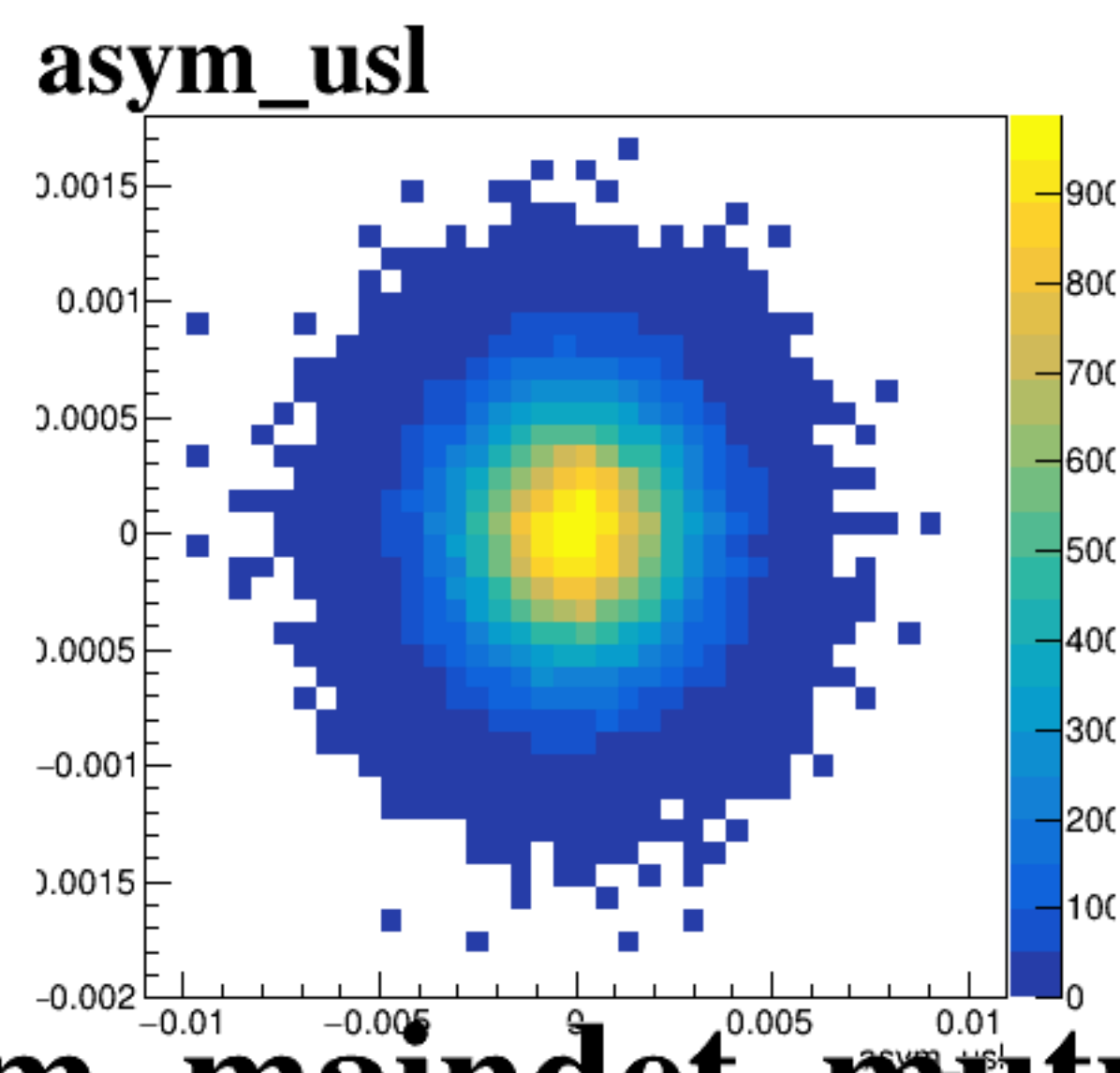
asym_usl



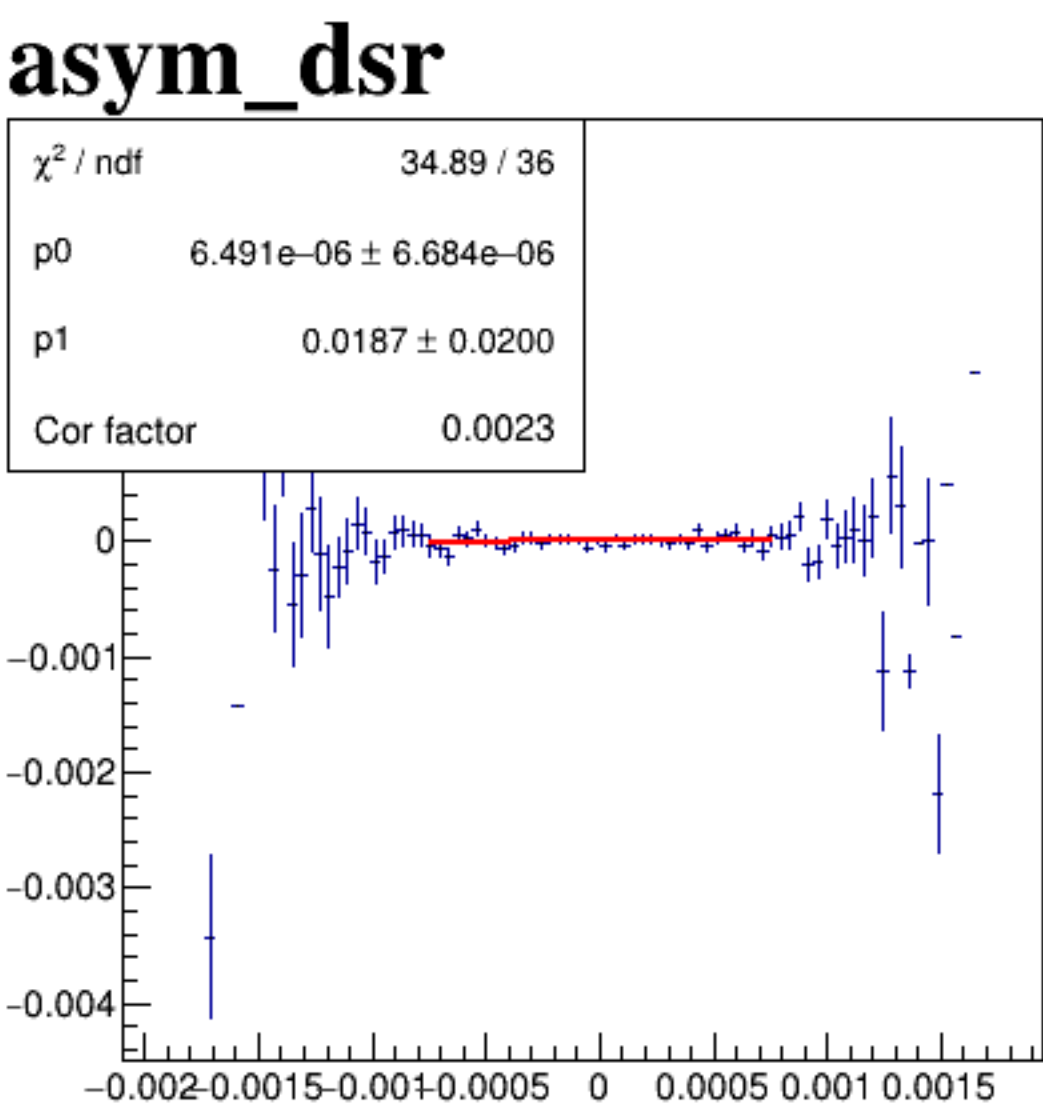
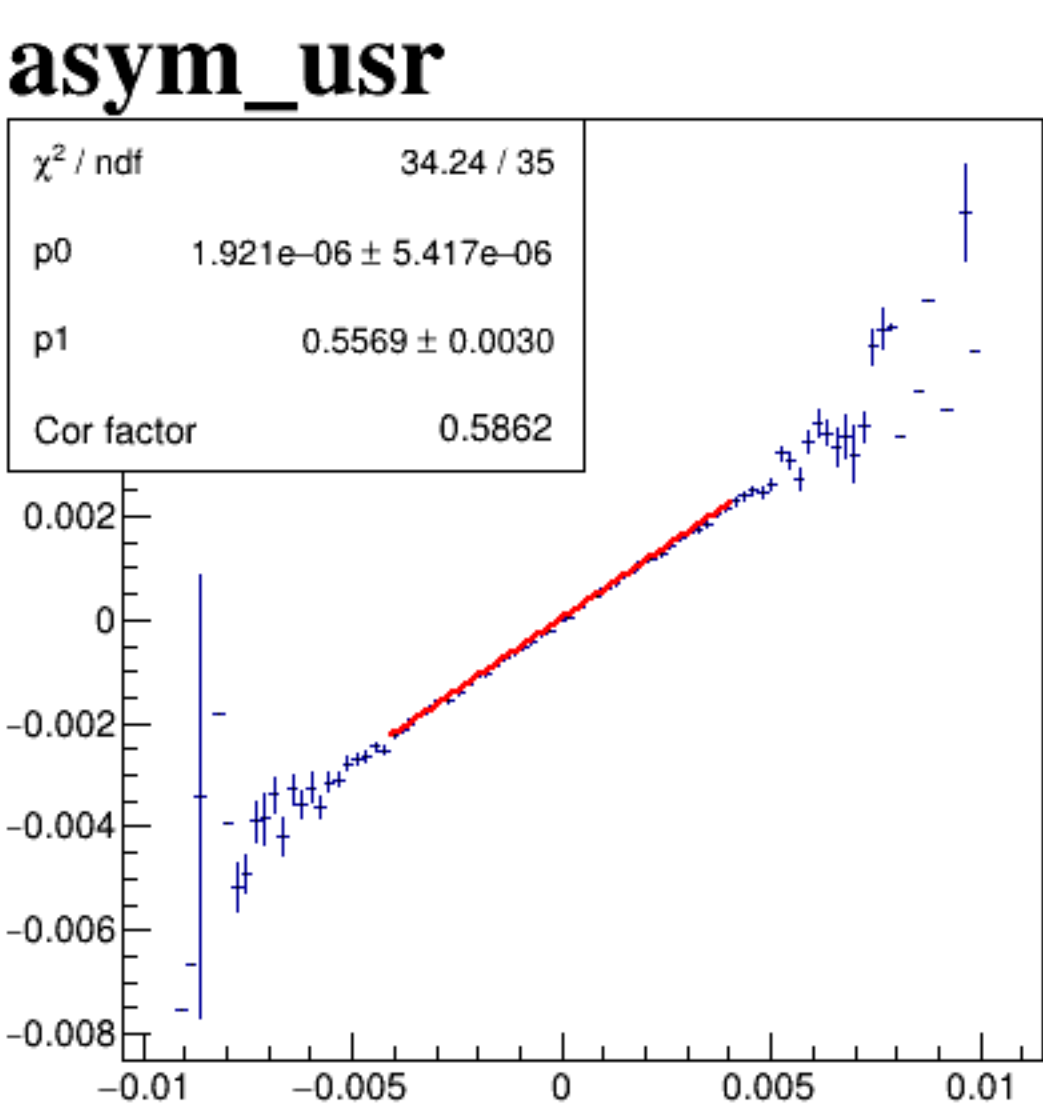
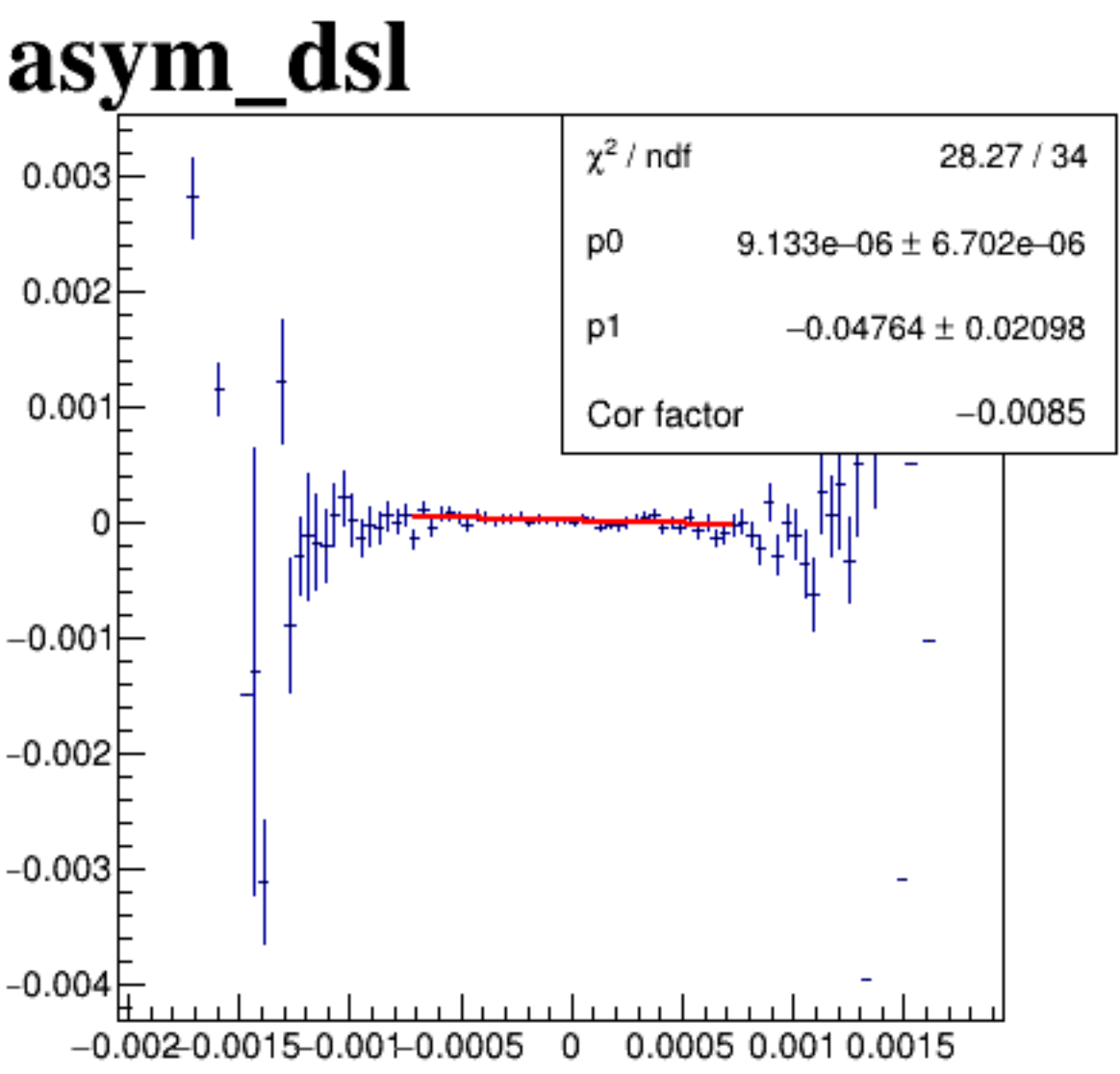
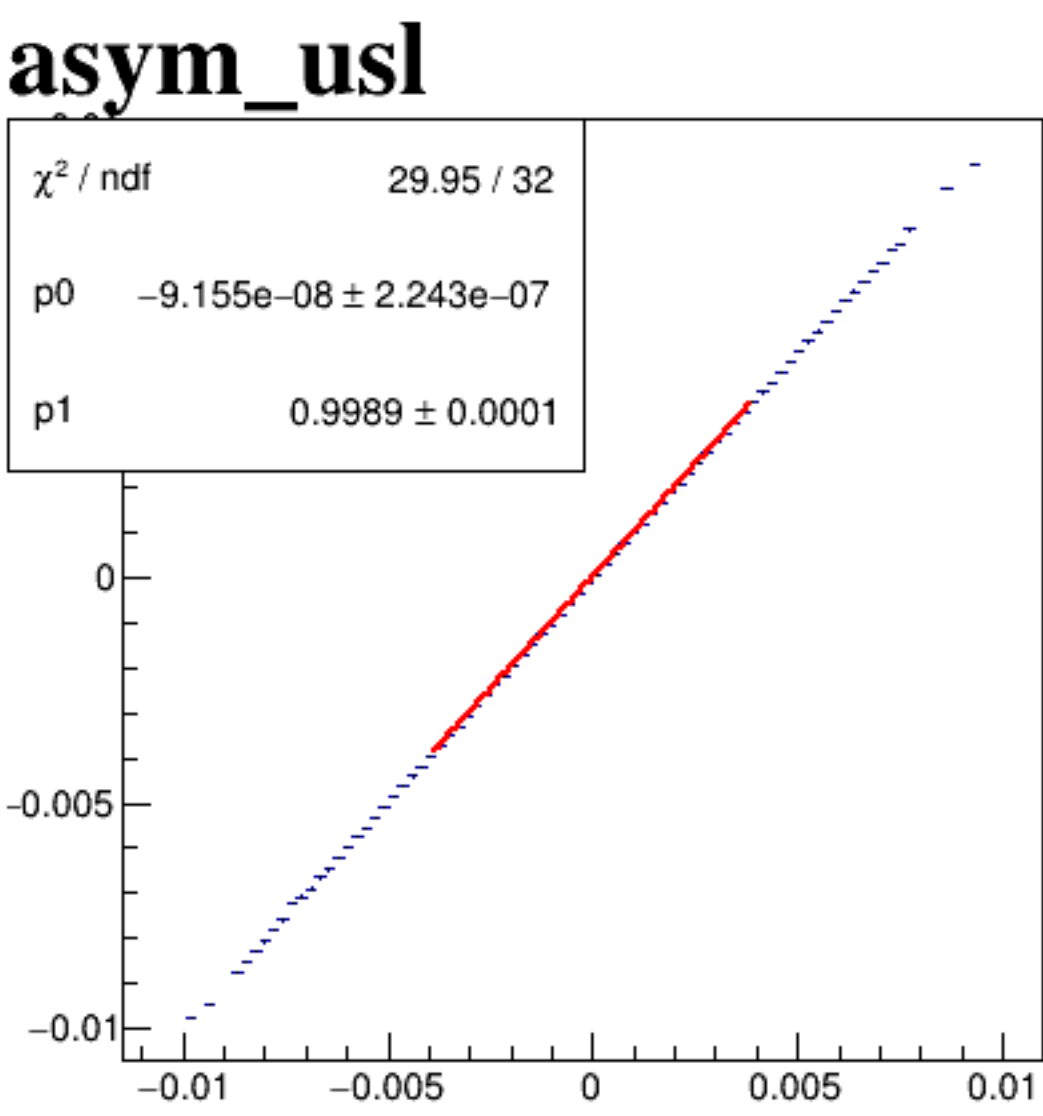
asym_dsl



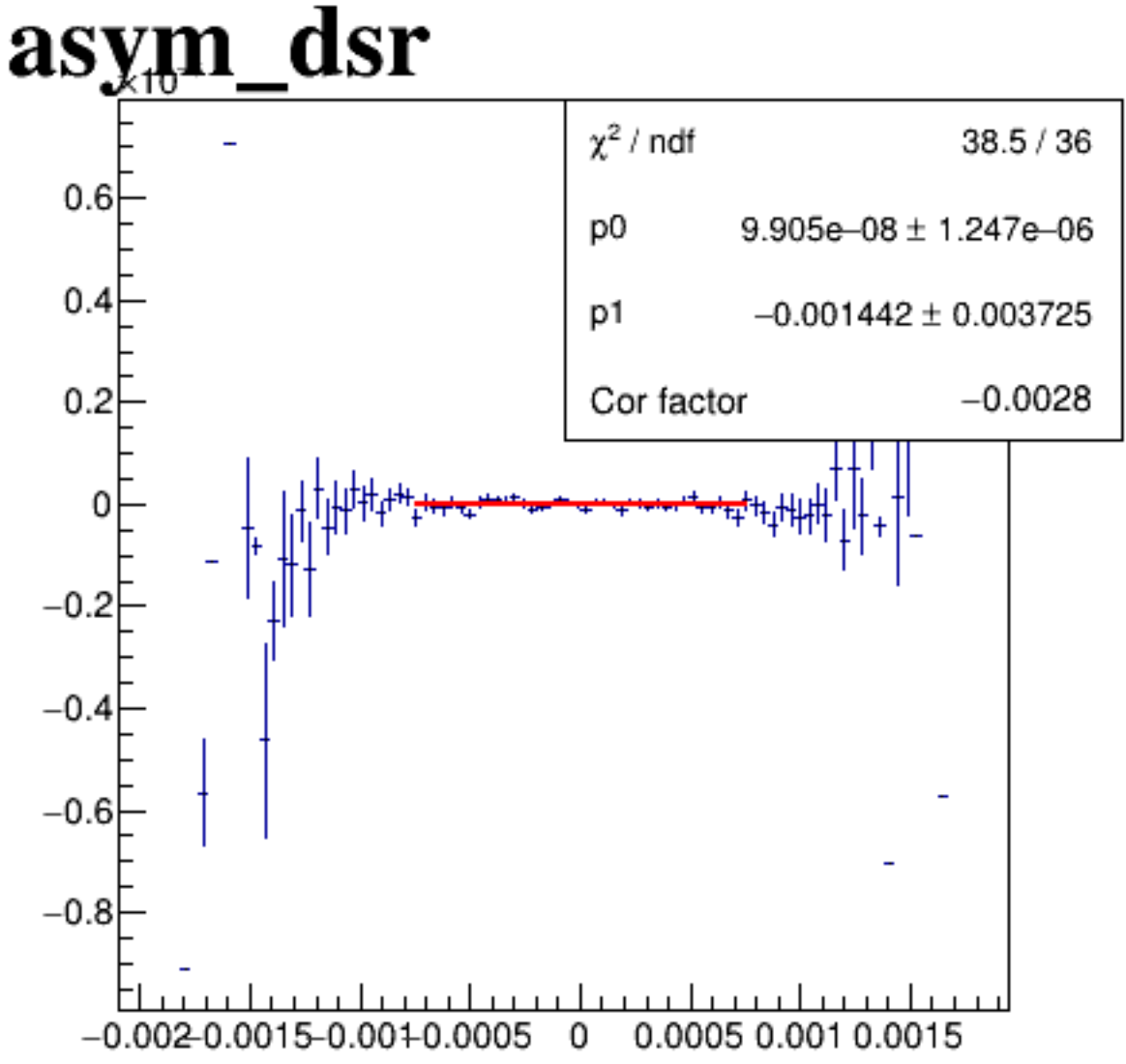
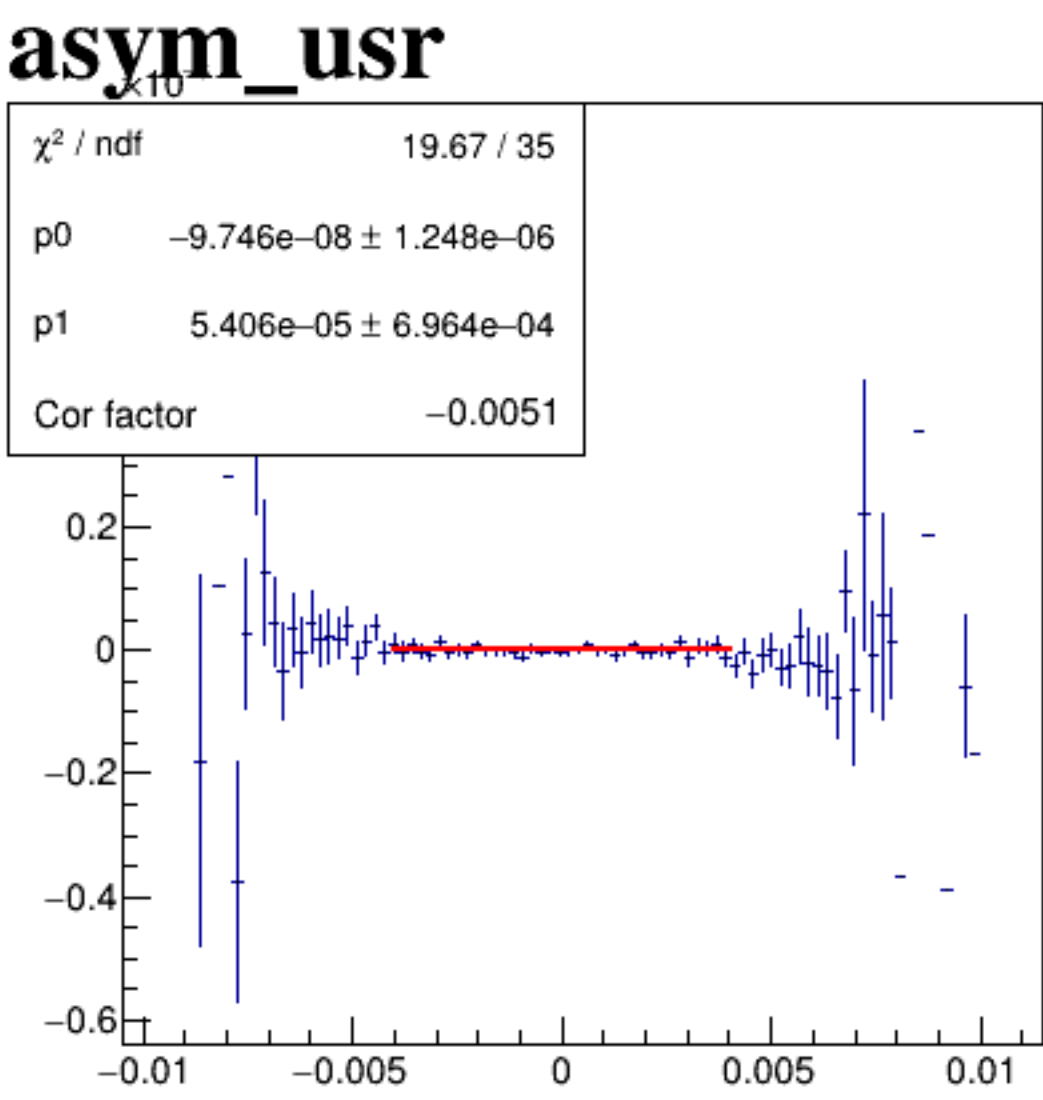
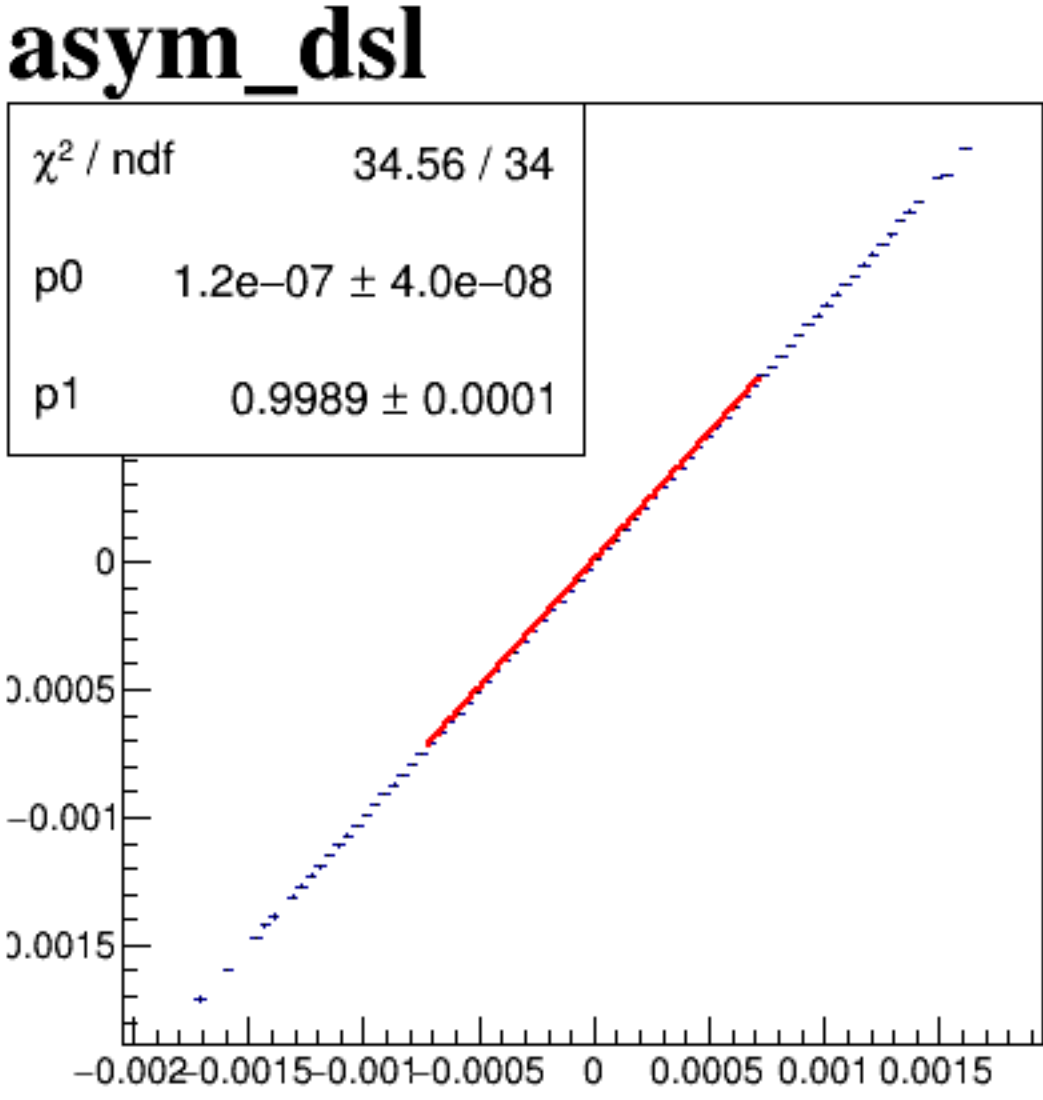
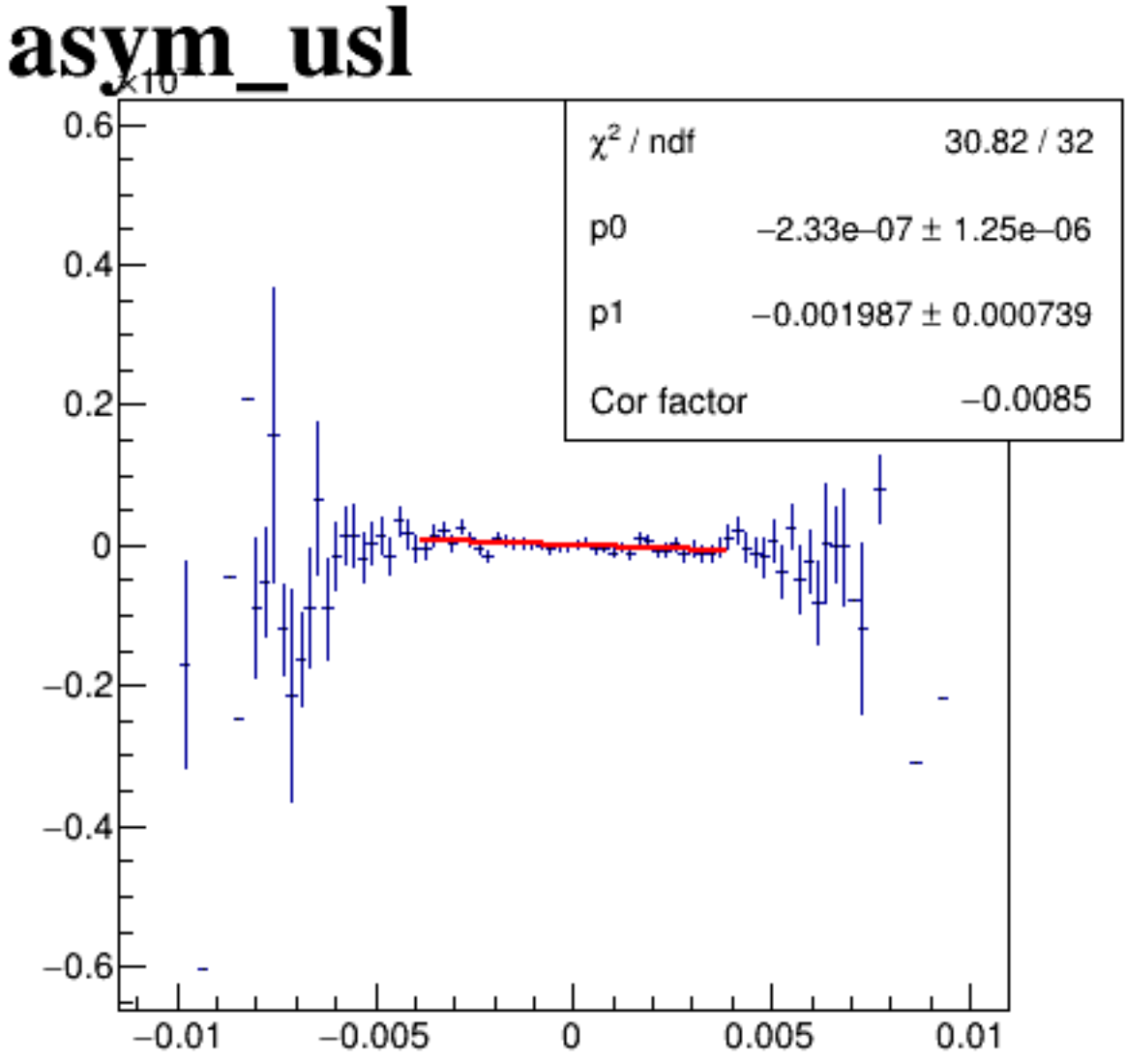
asym_usr

**asym_dsr**

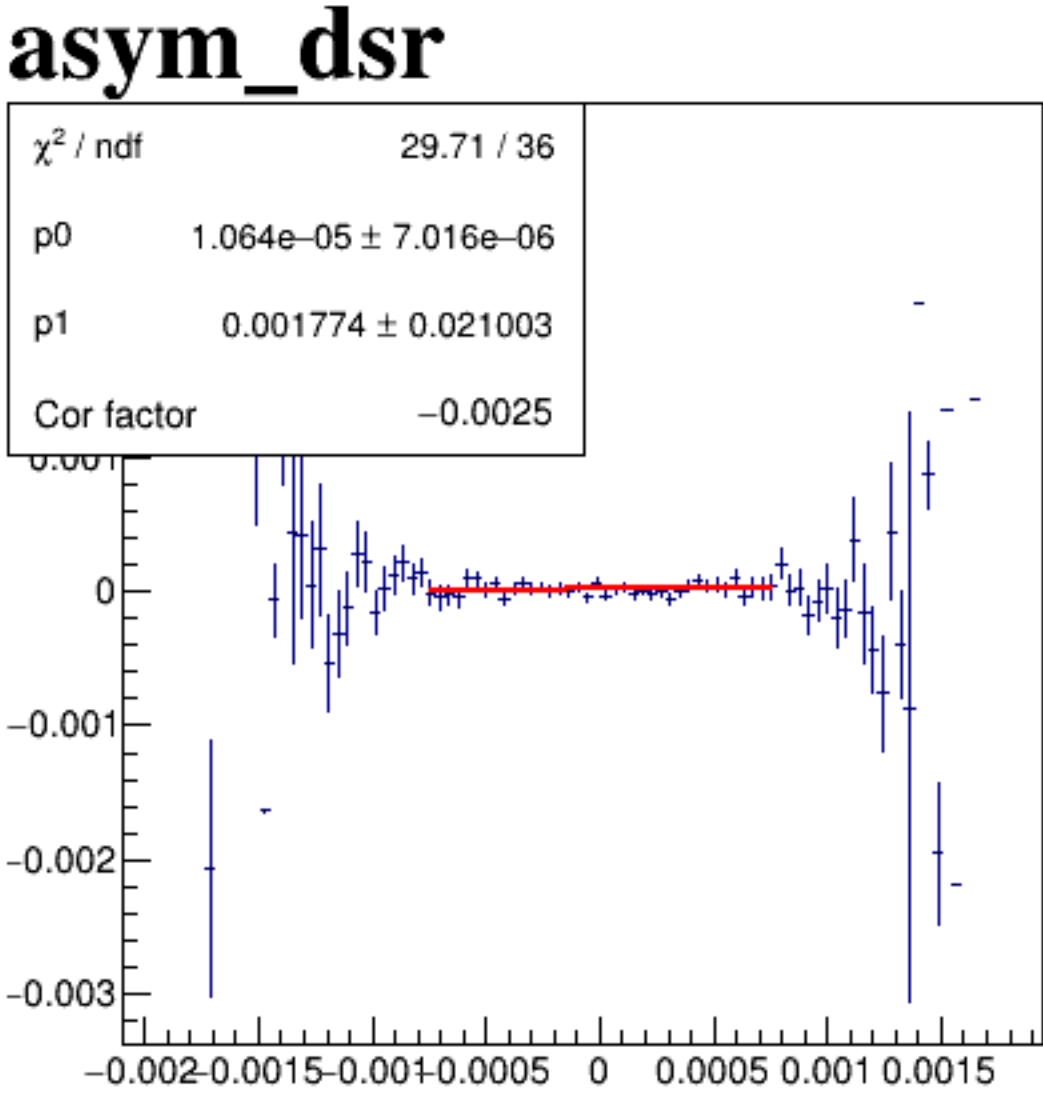
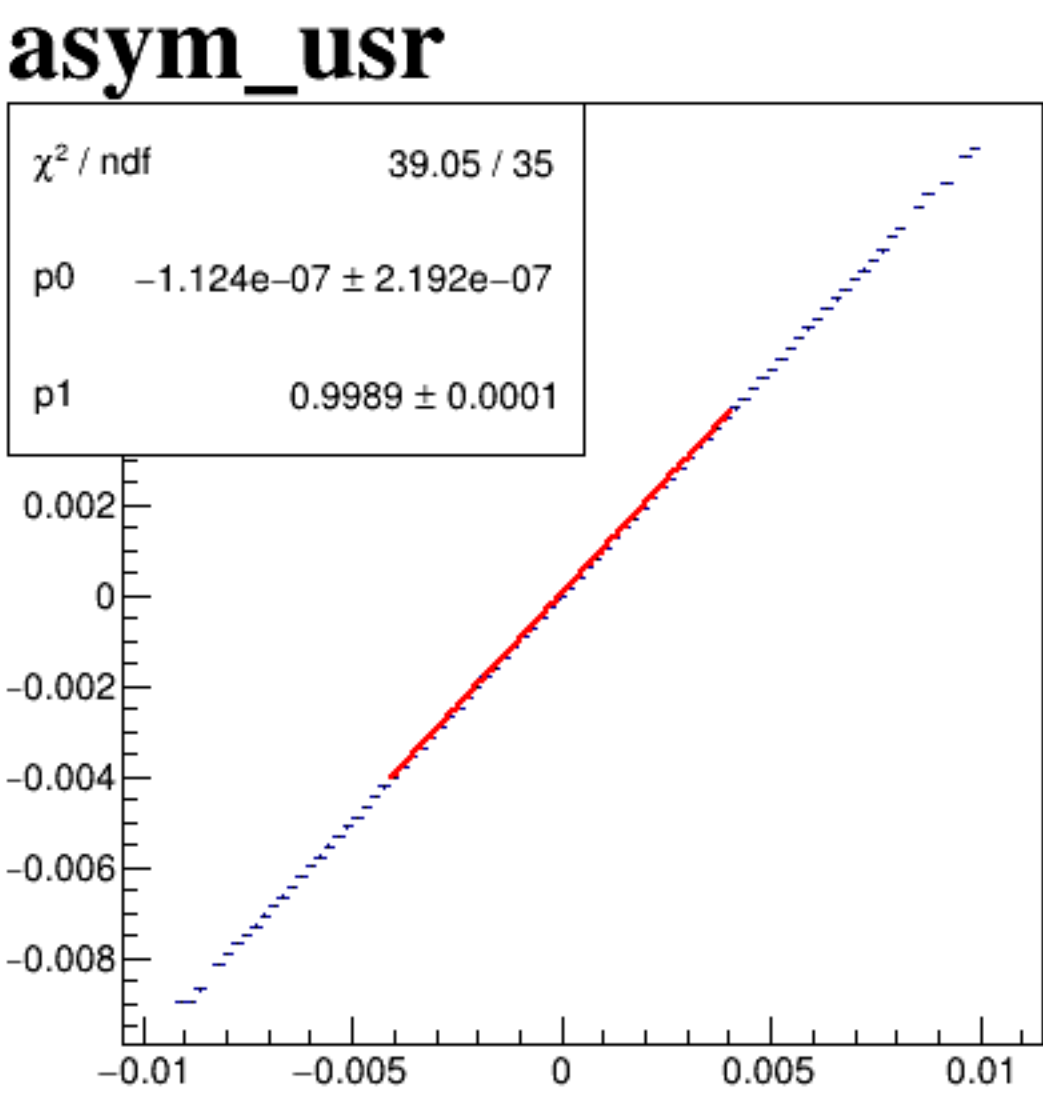
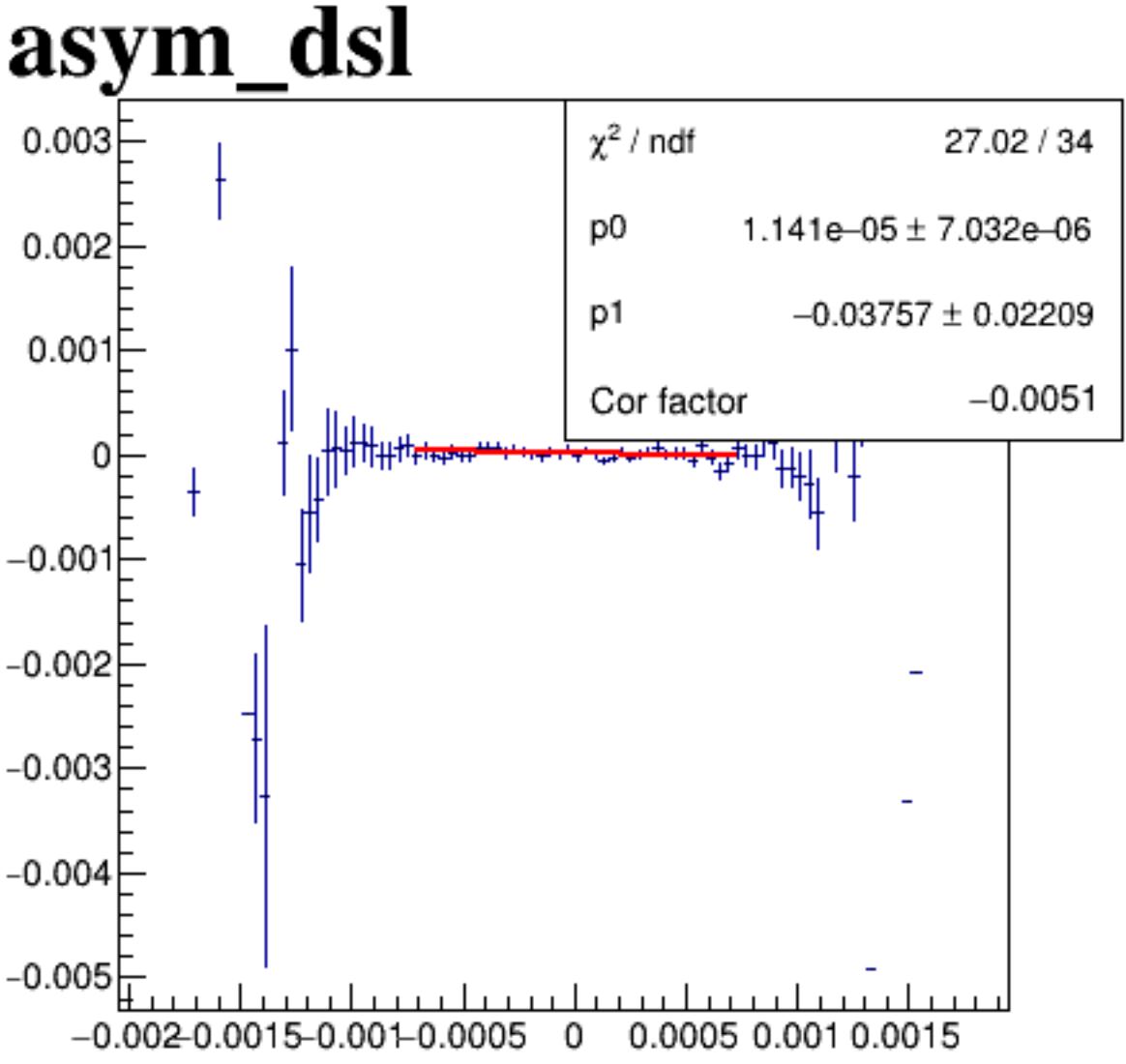
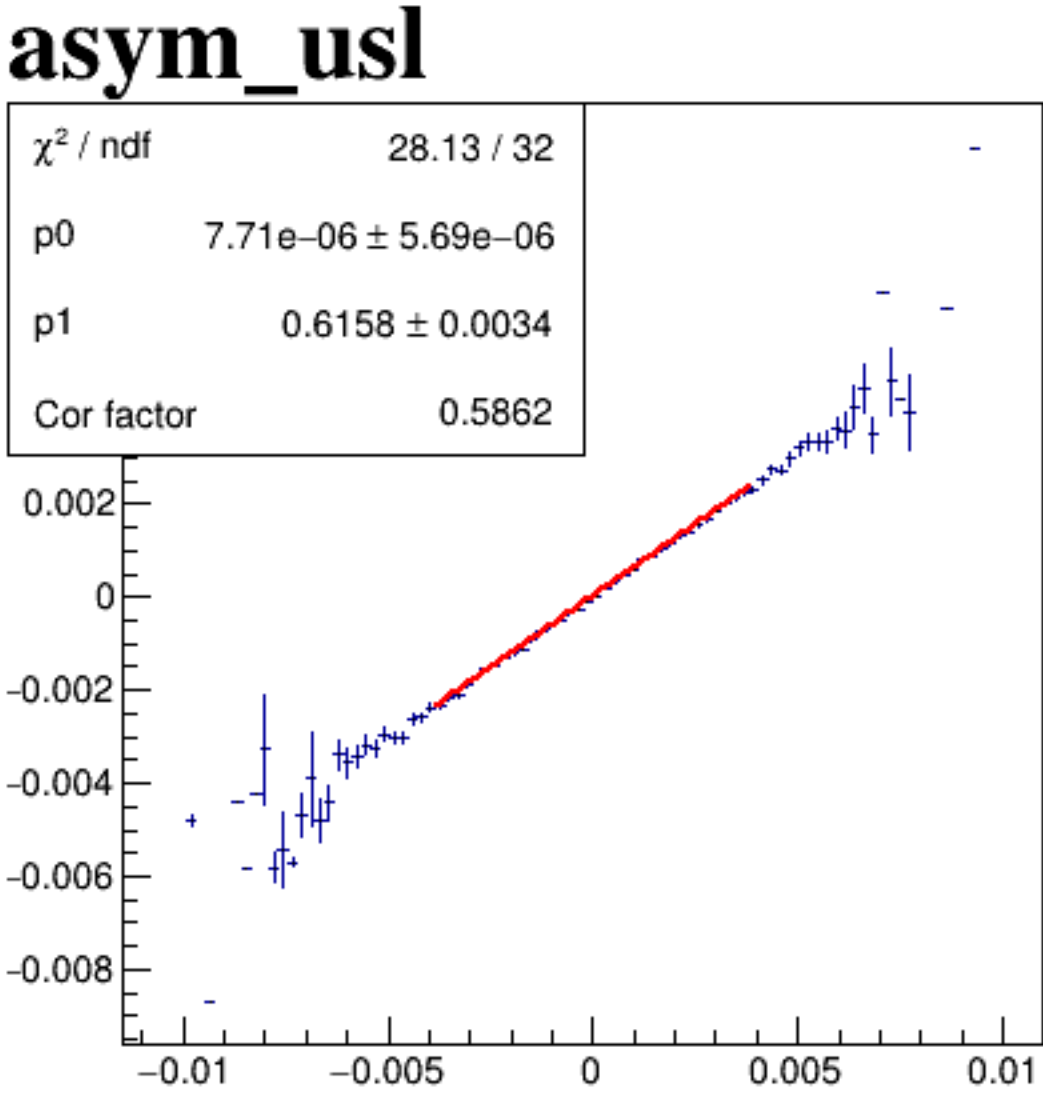
asym_usl



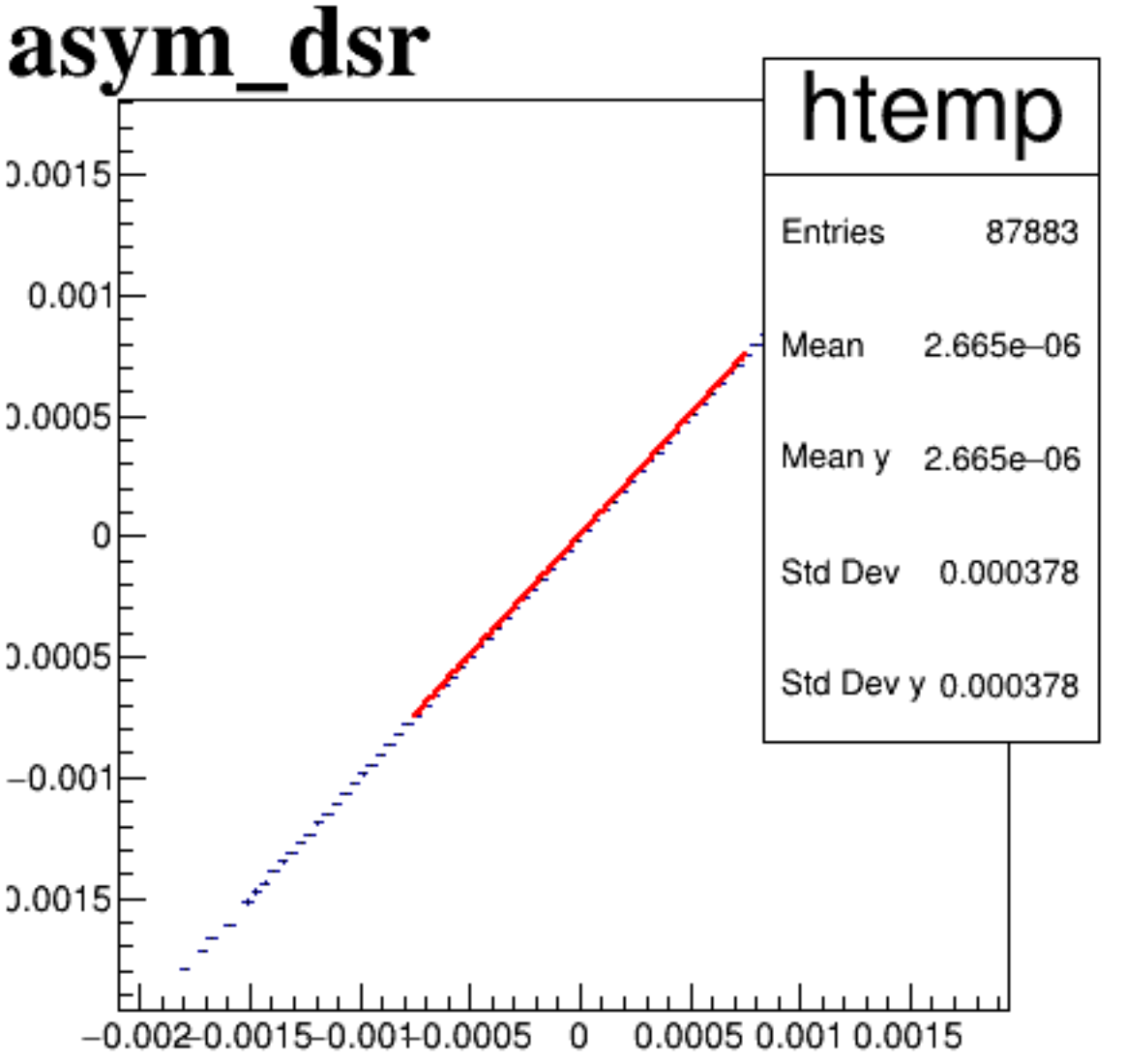
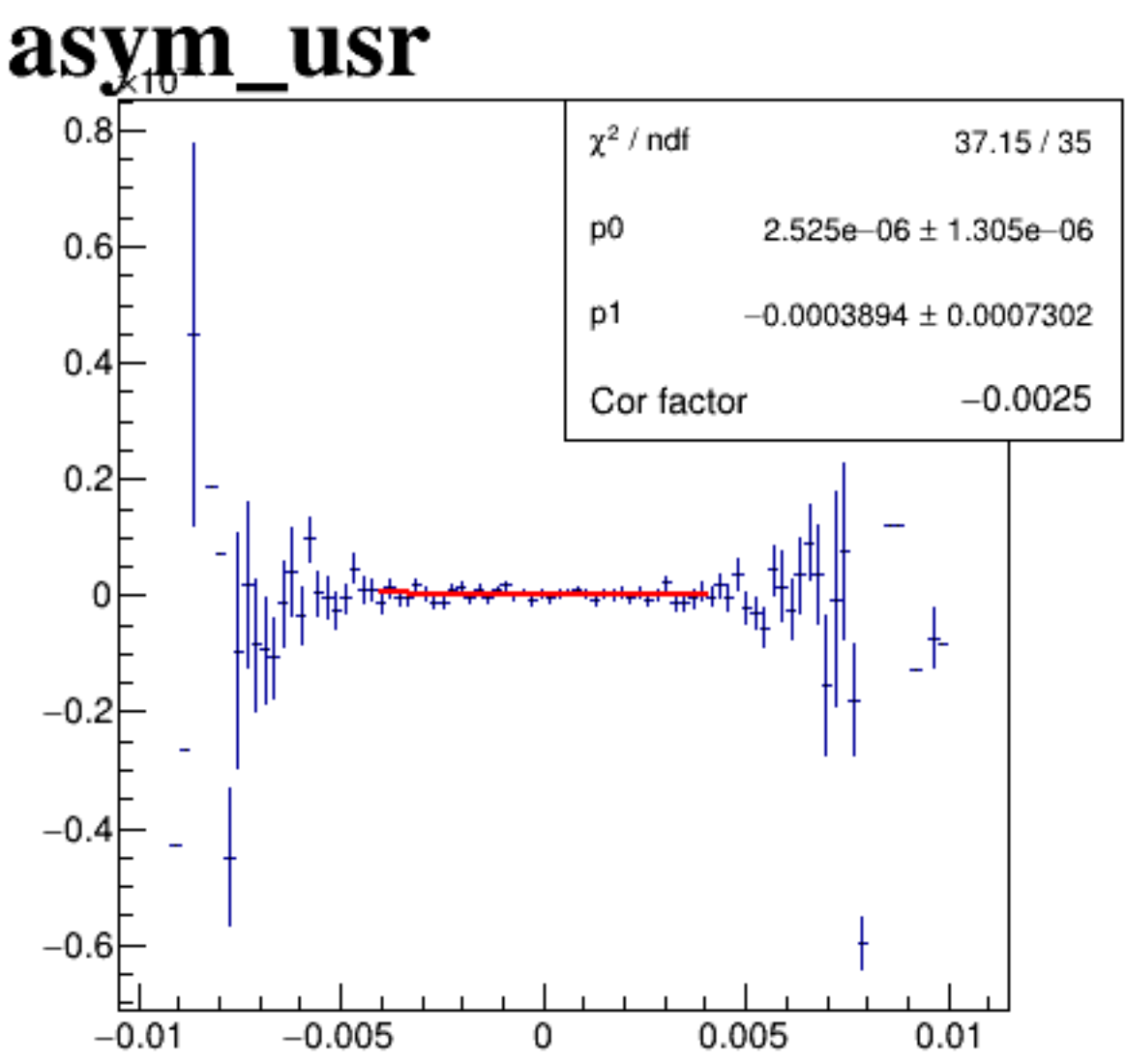
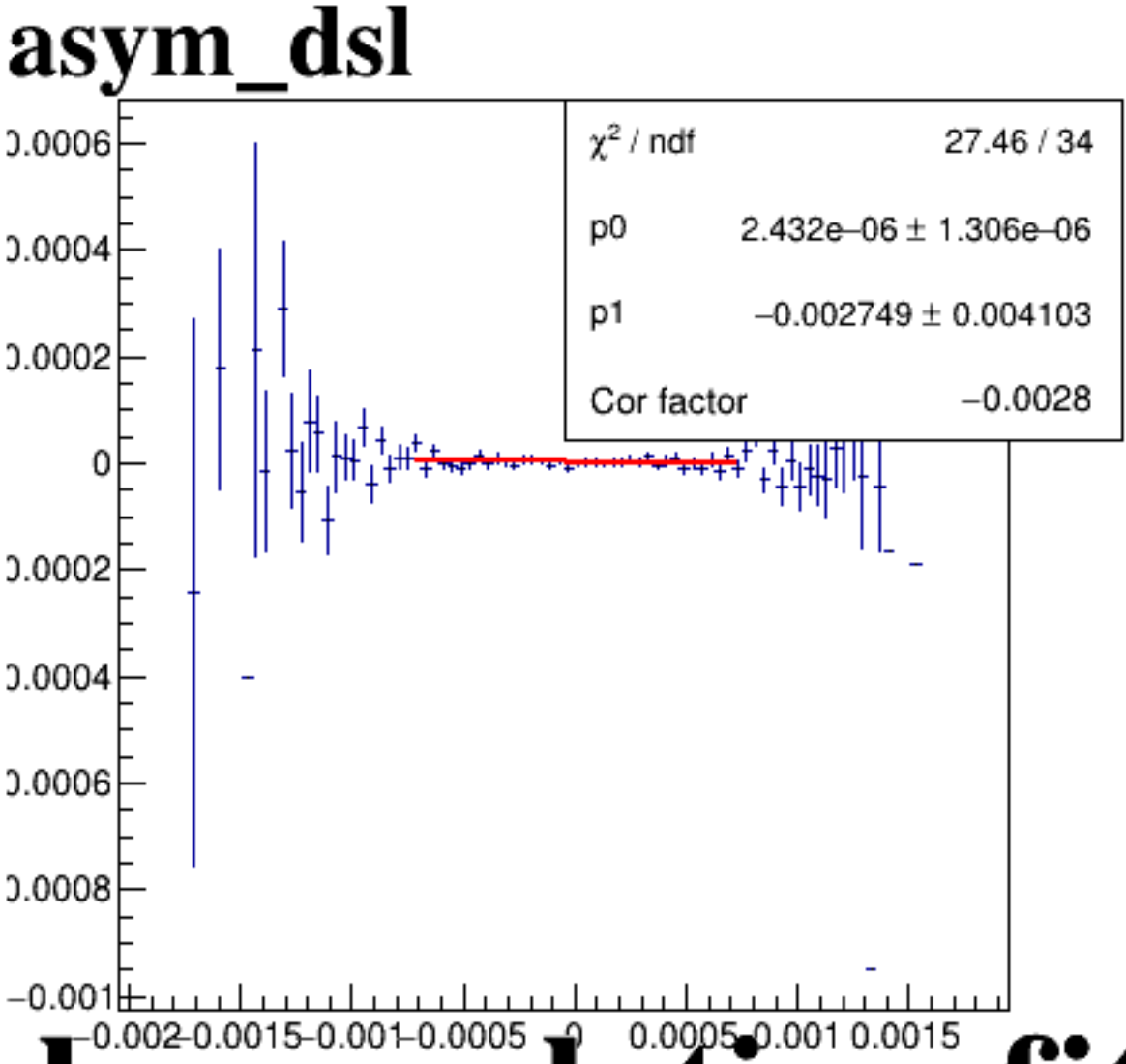
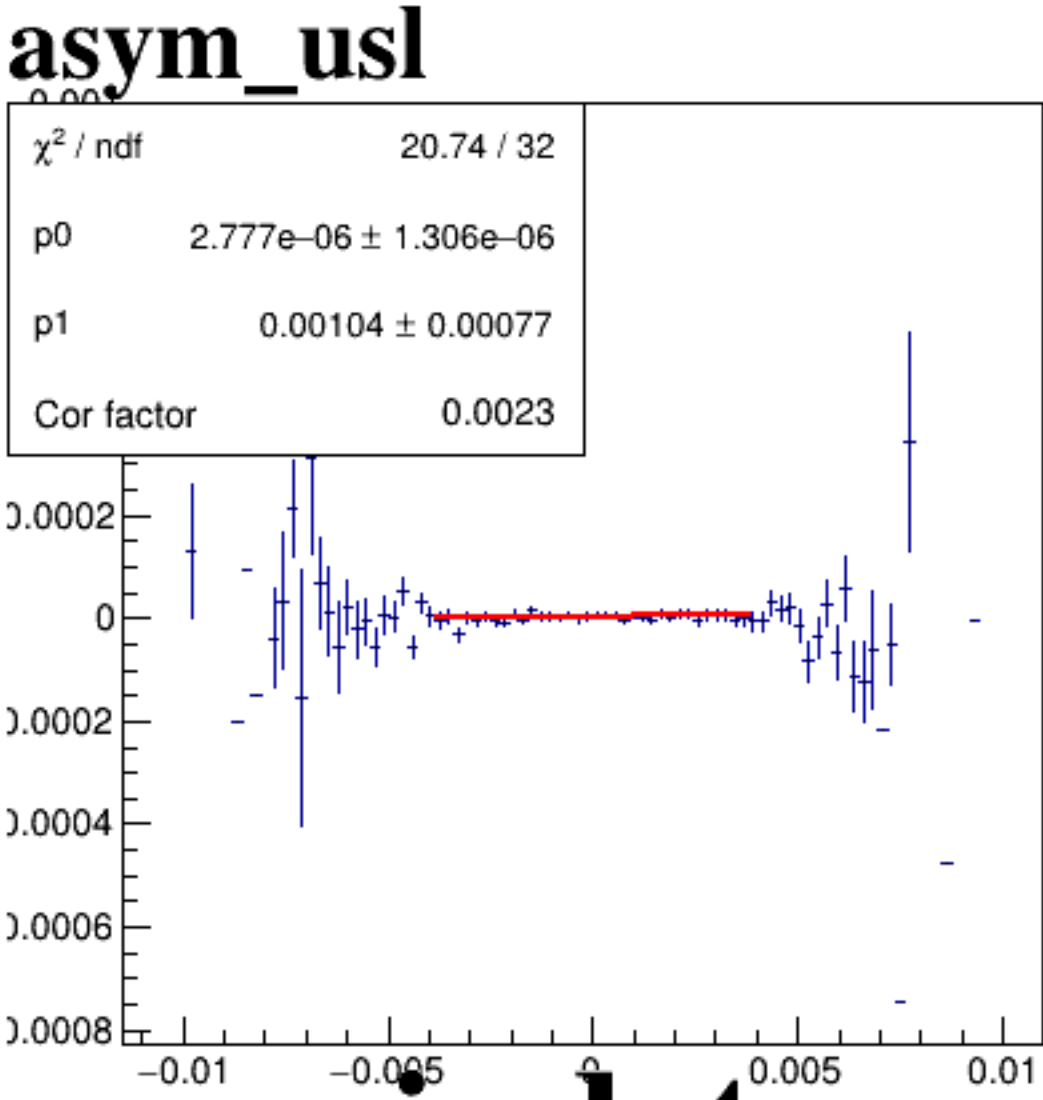
asym_dsl



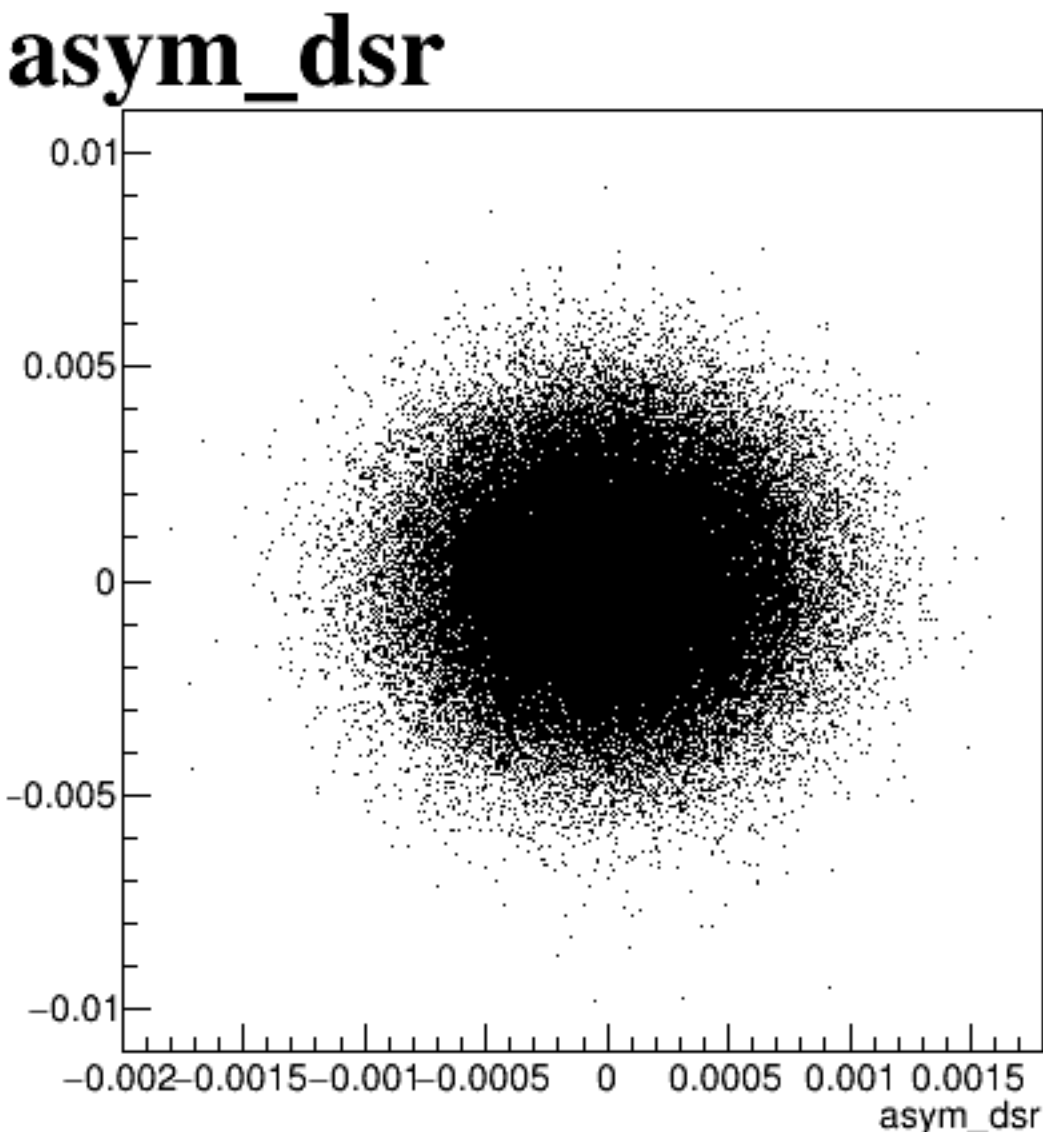
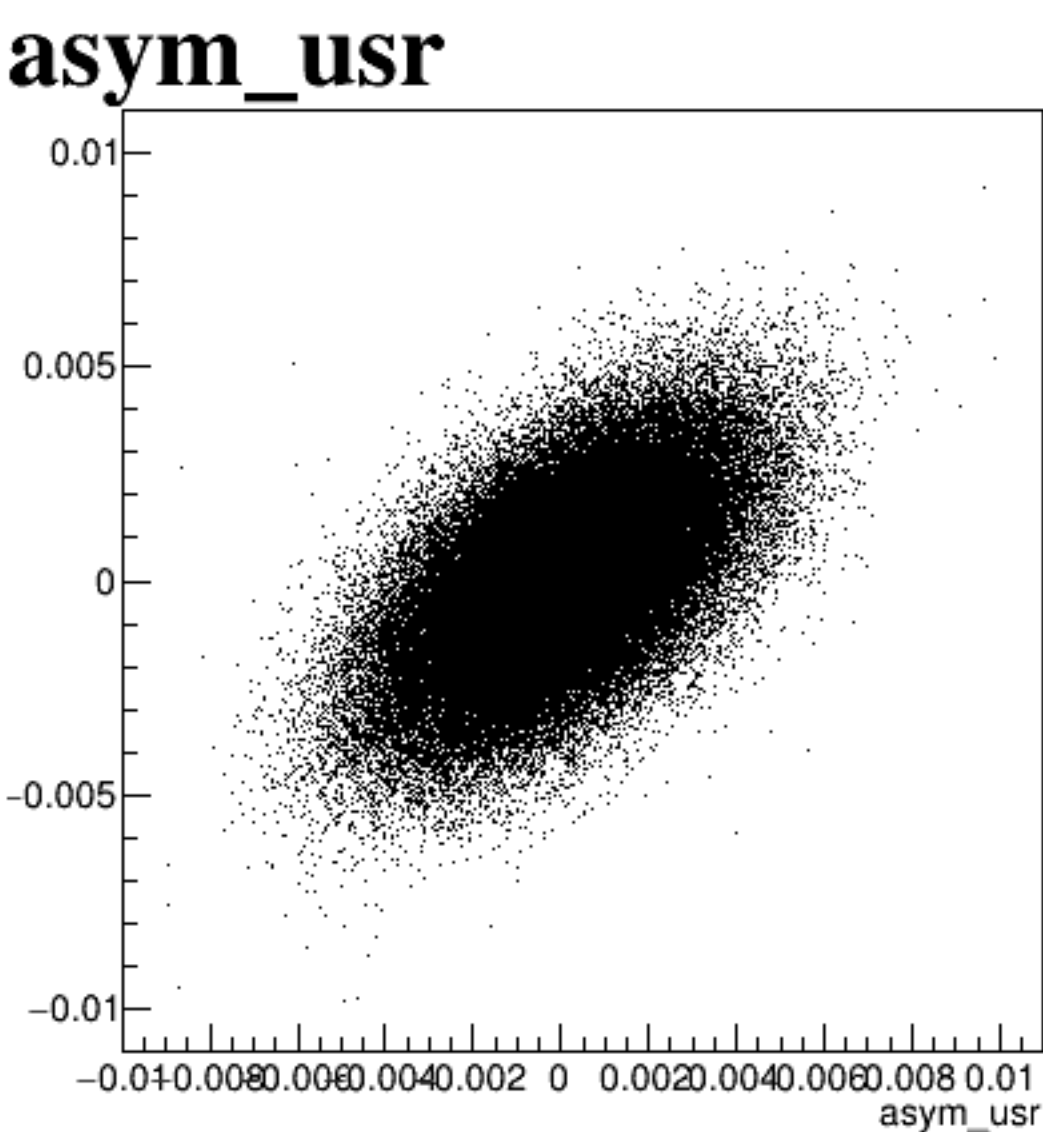
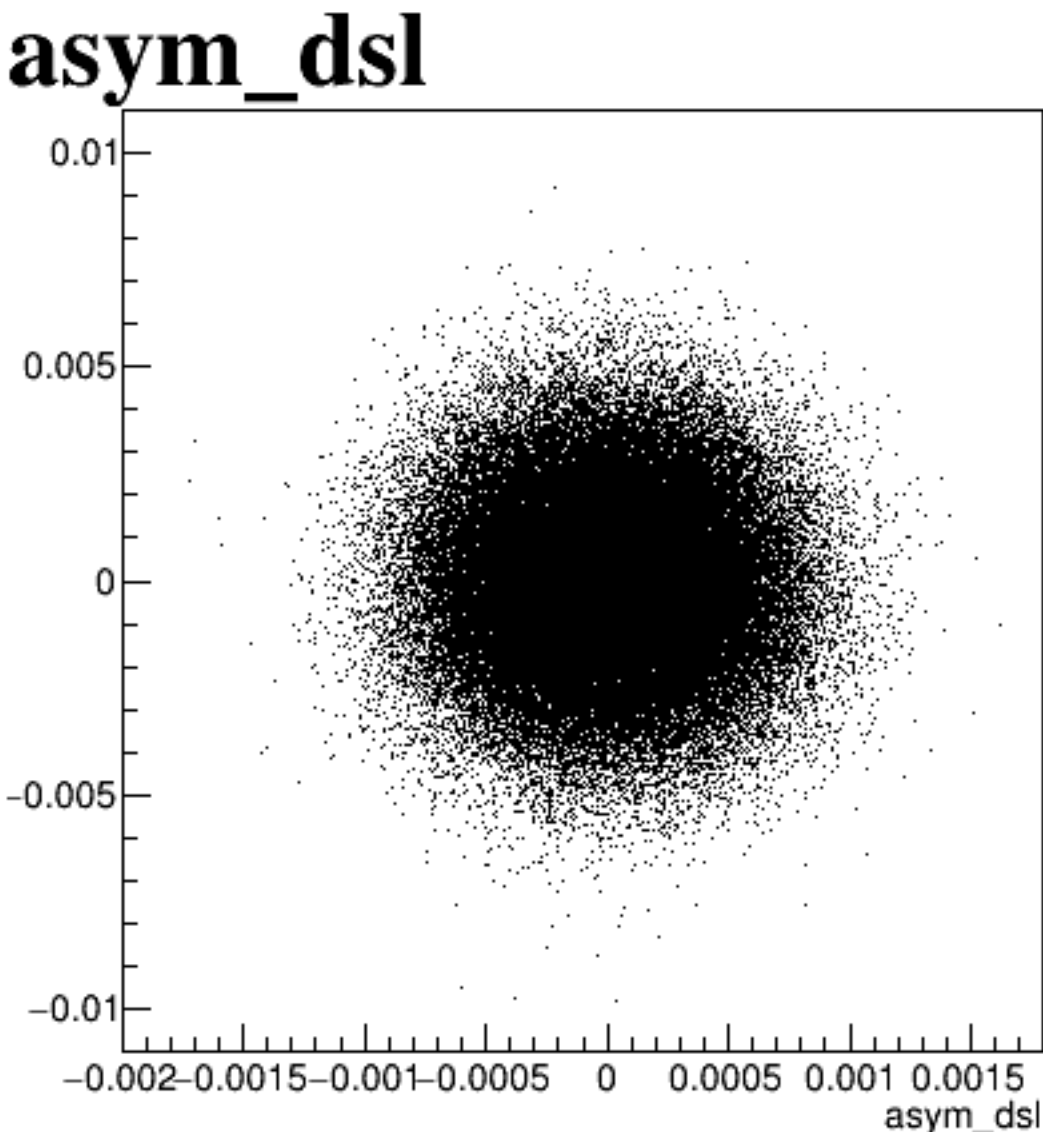
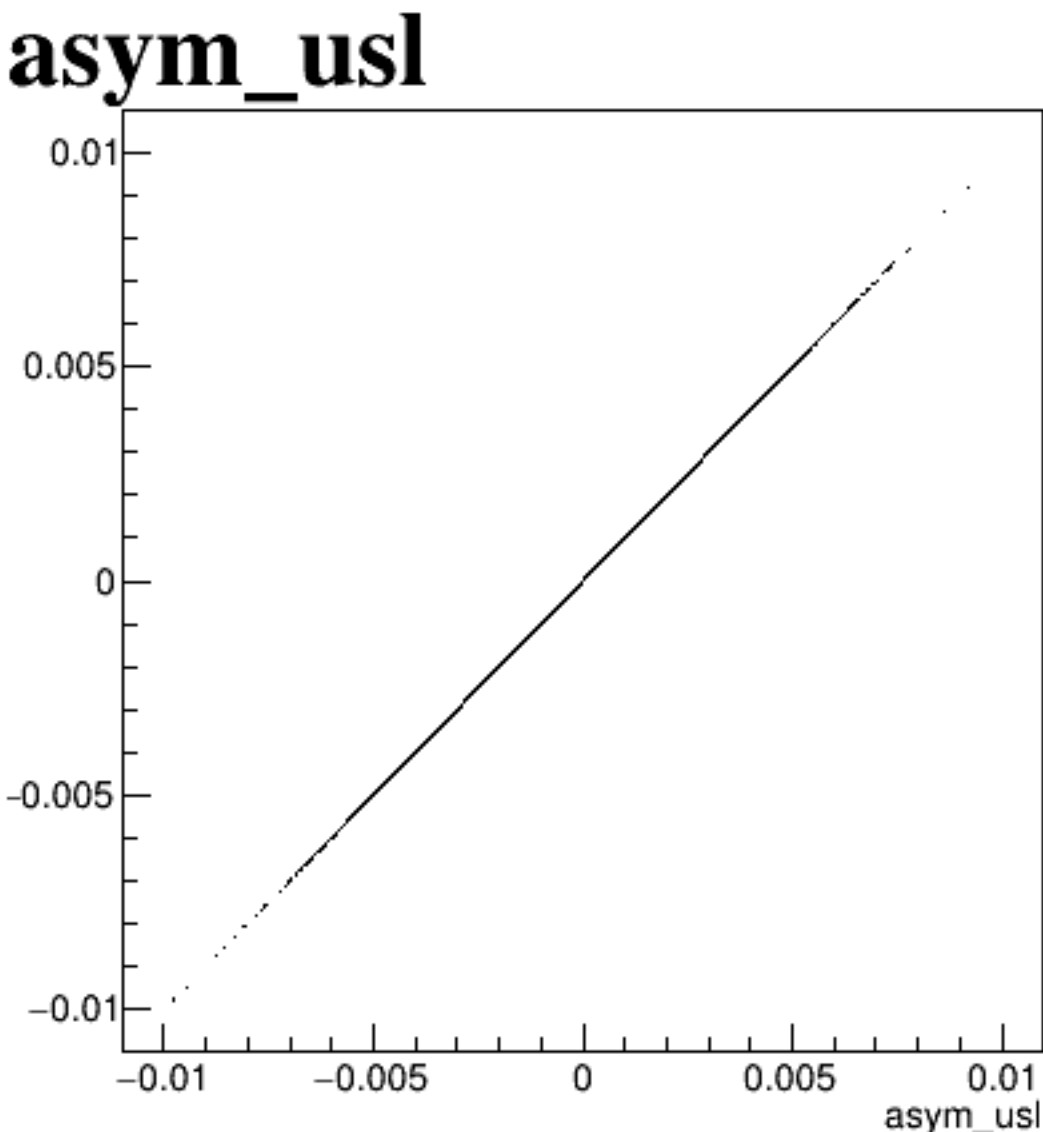
asym_usr



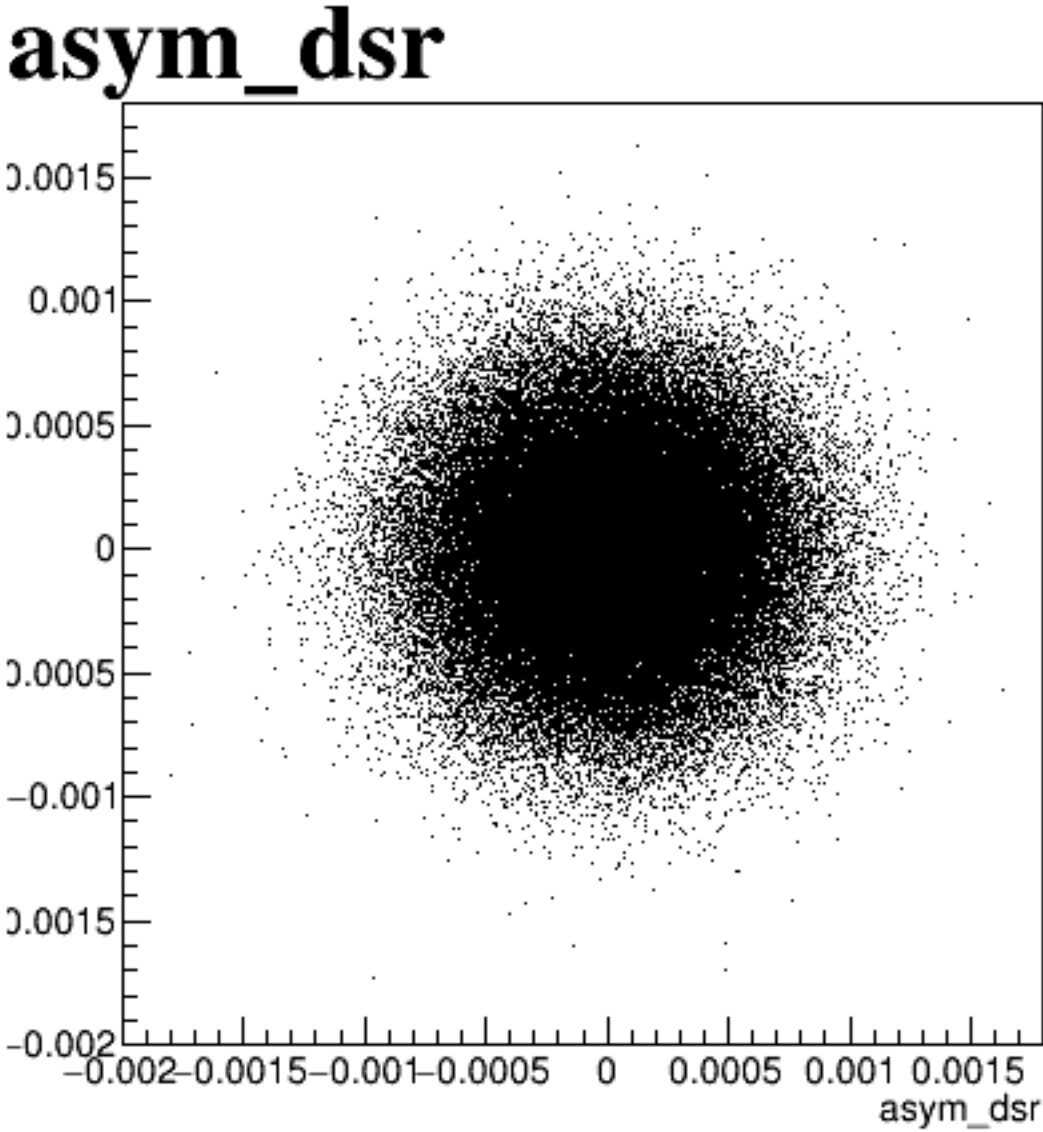
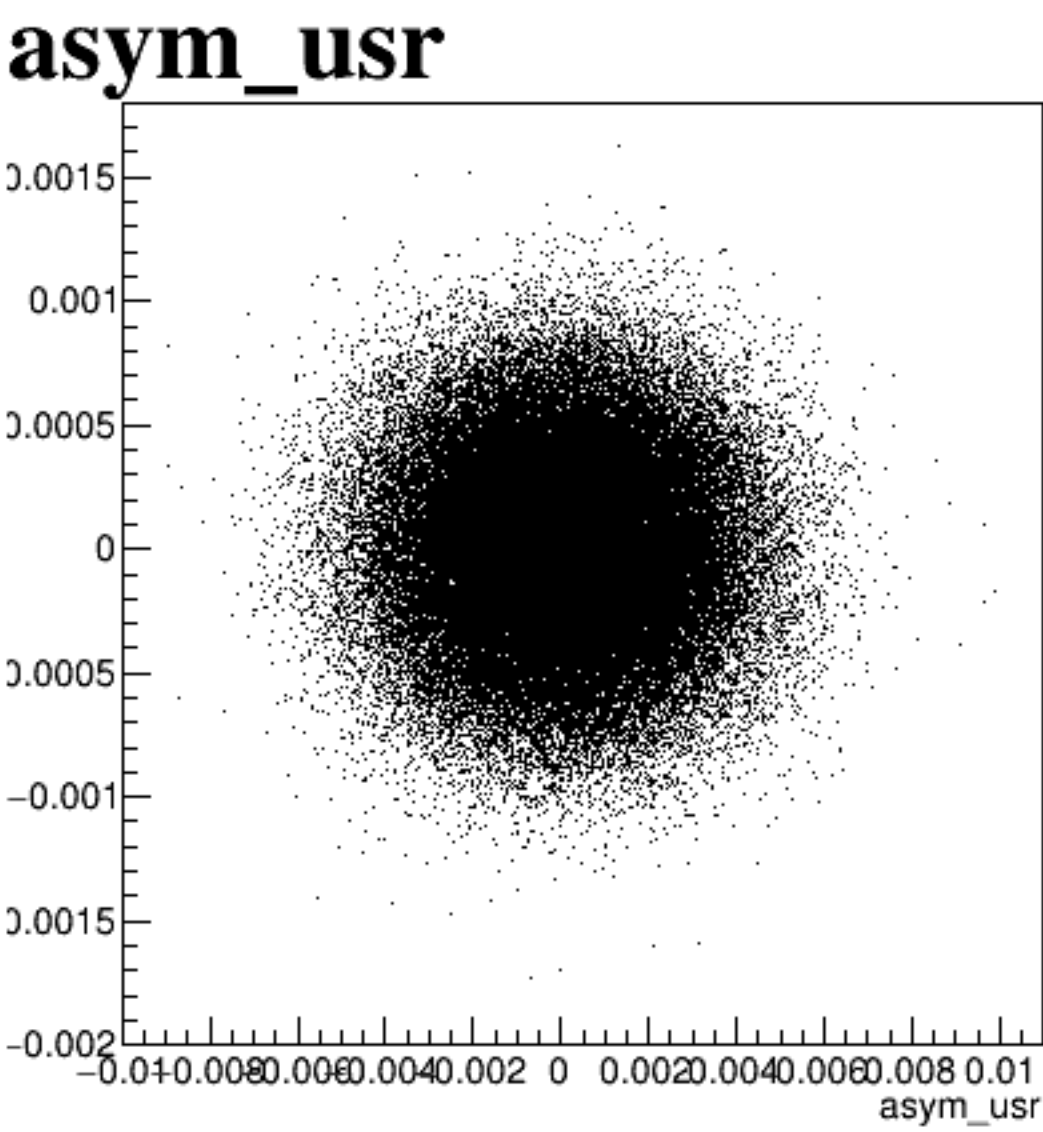
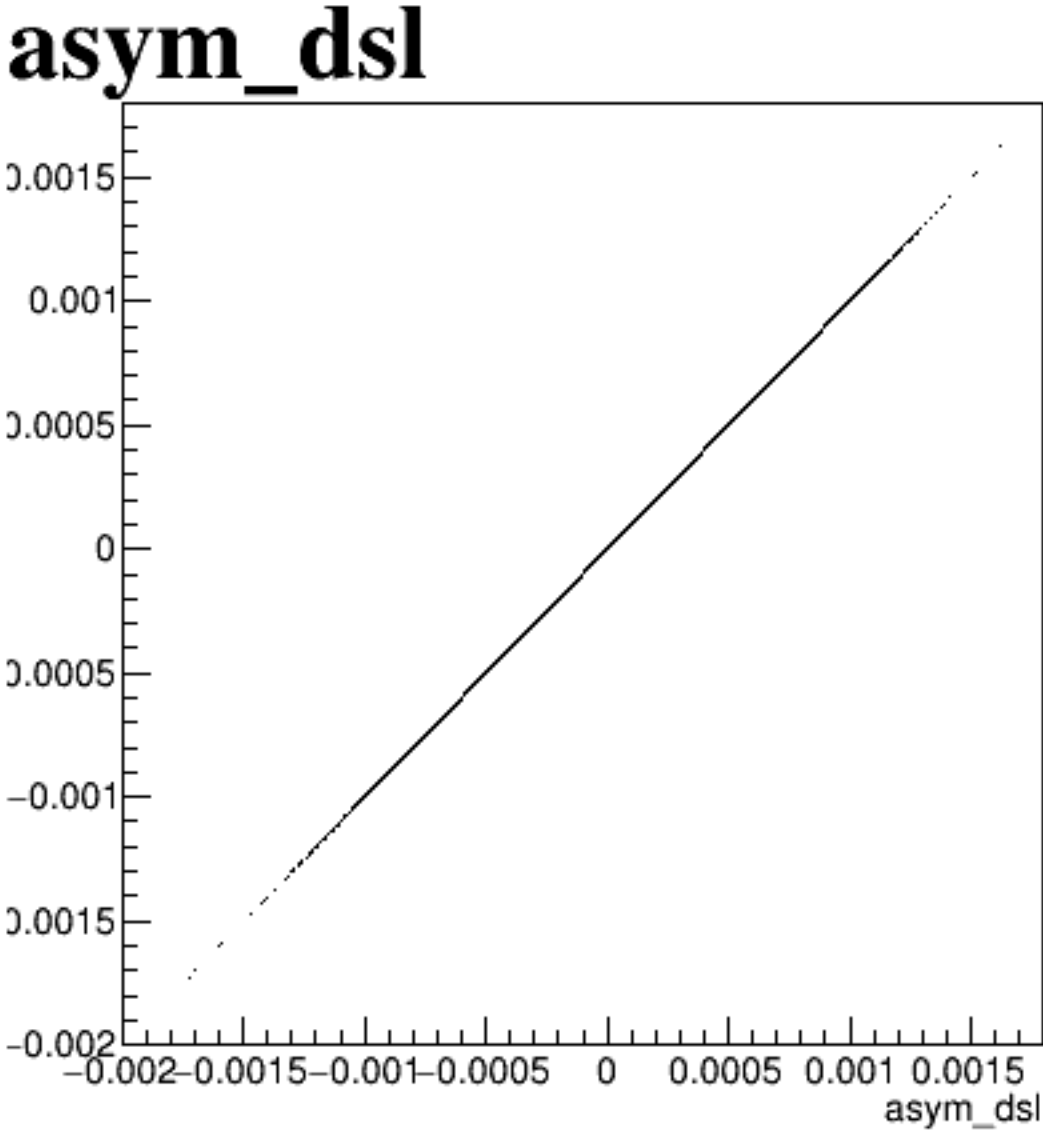
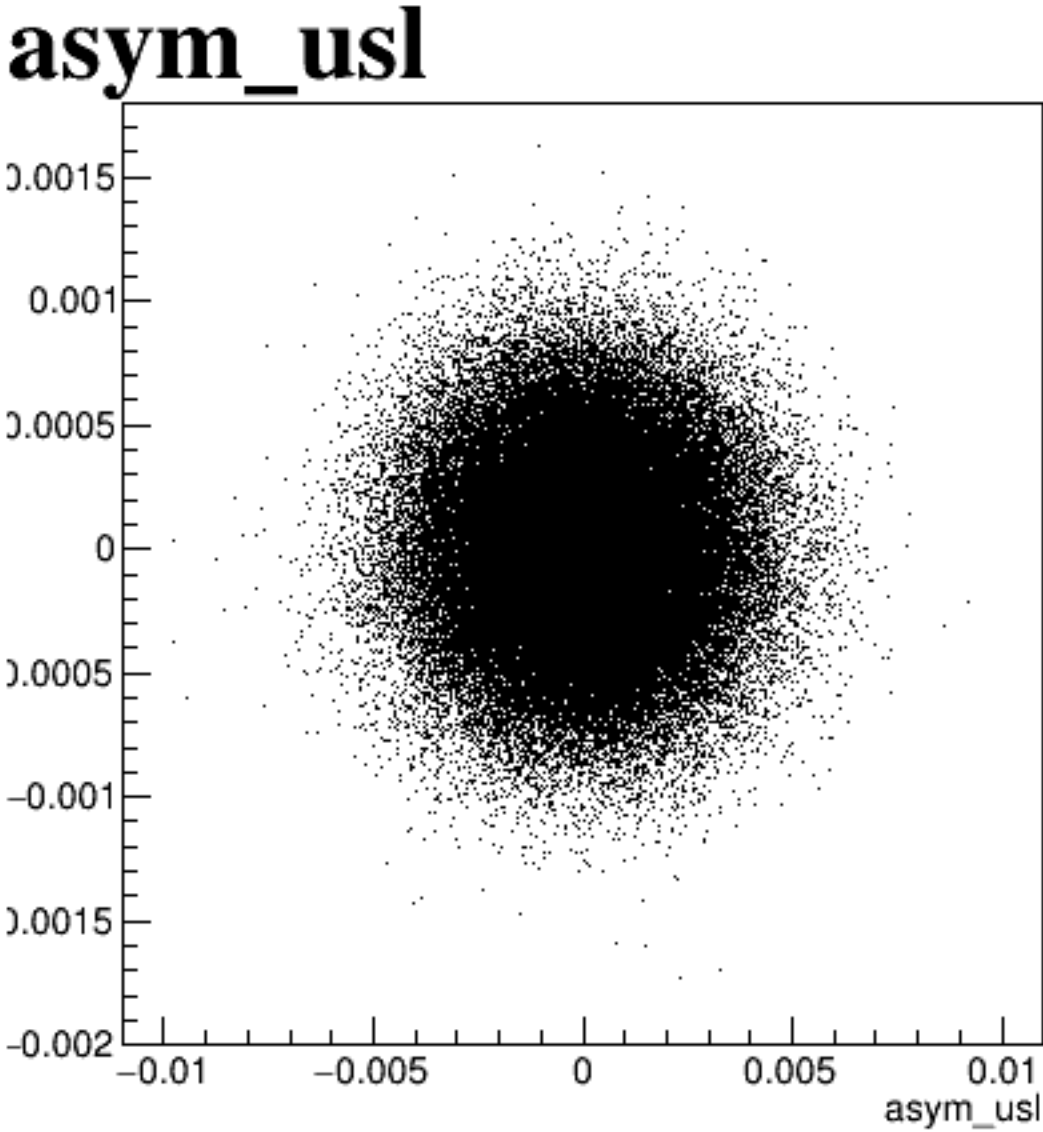
asym_dsr



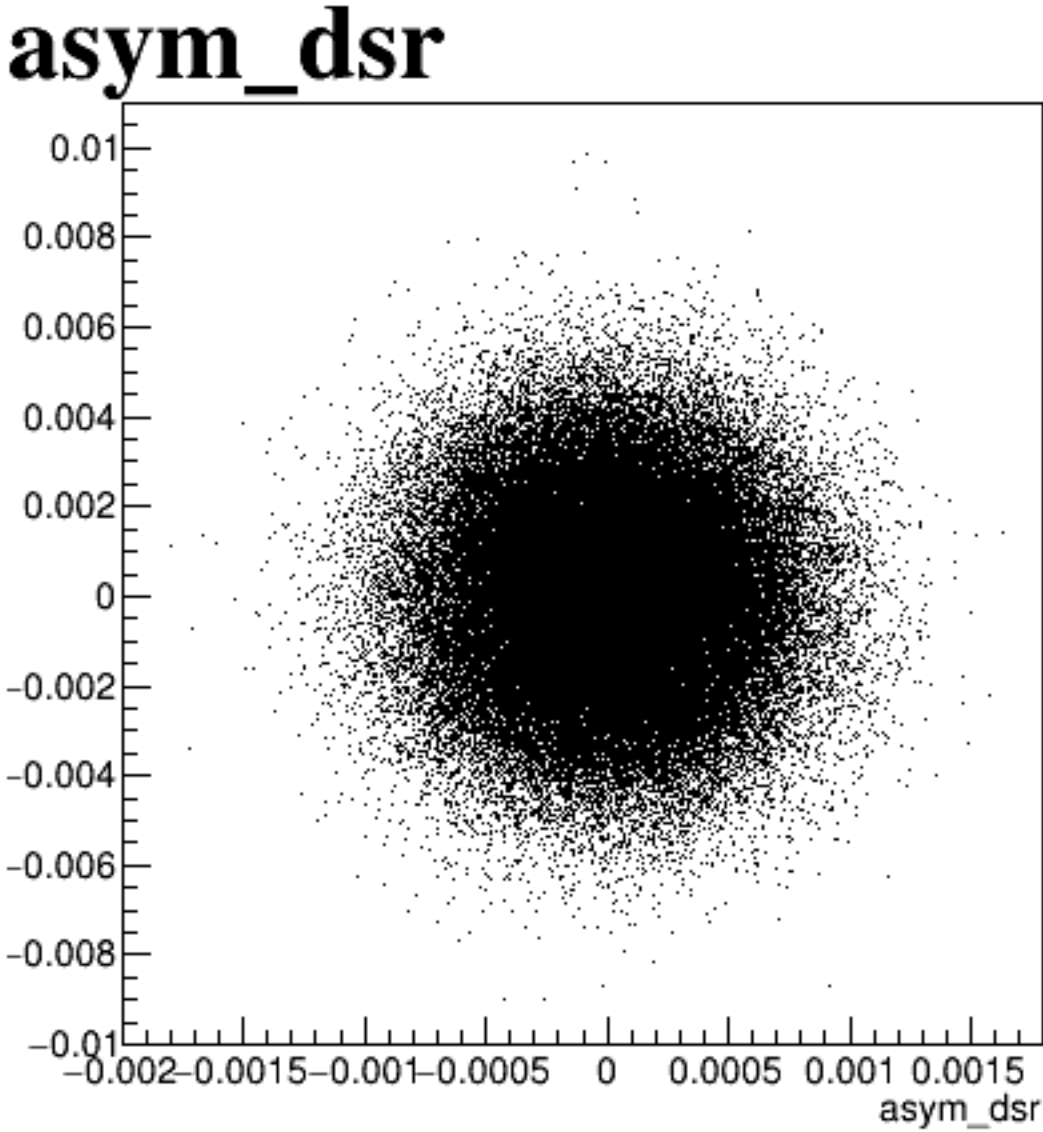
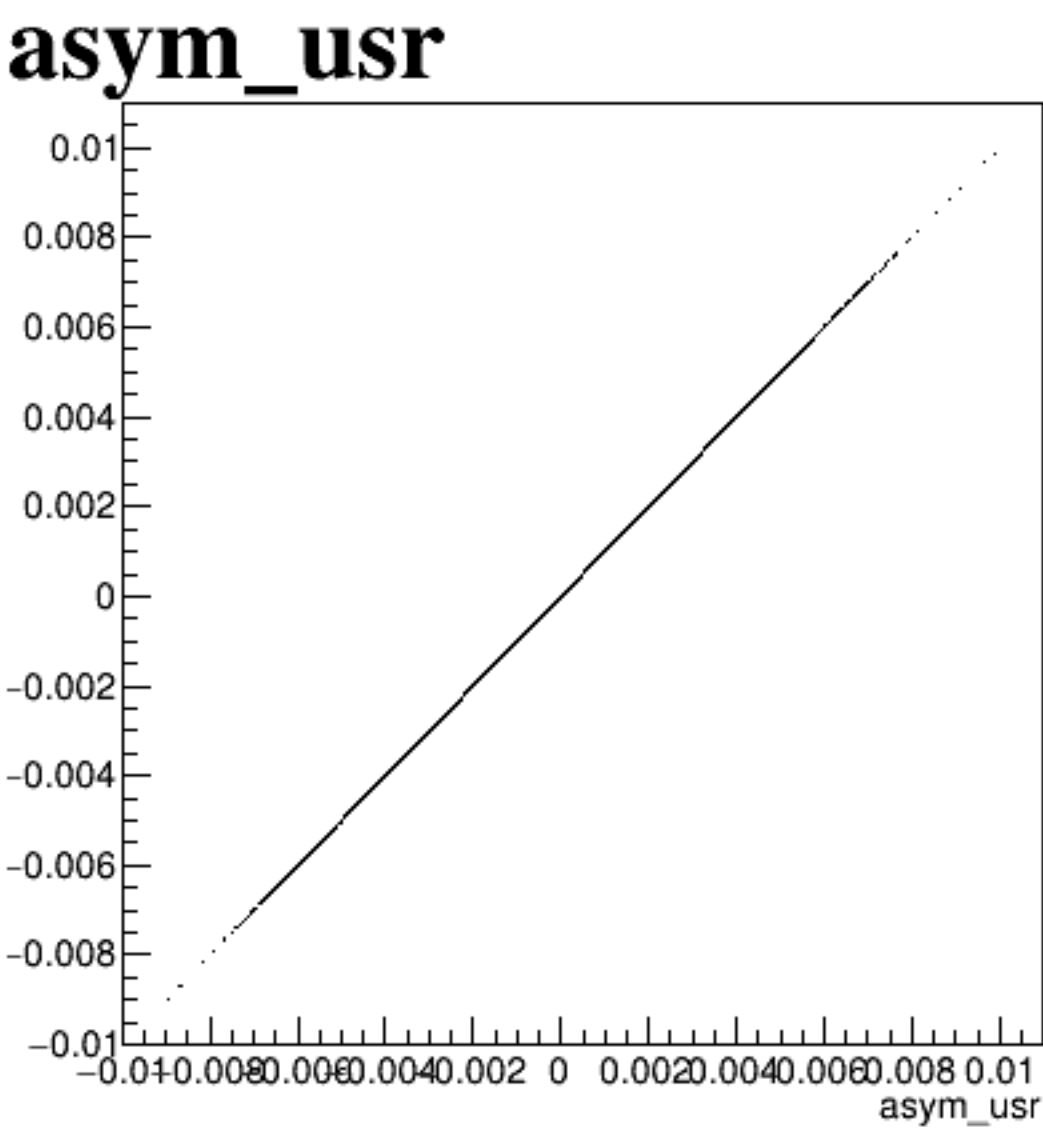
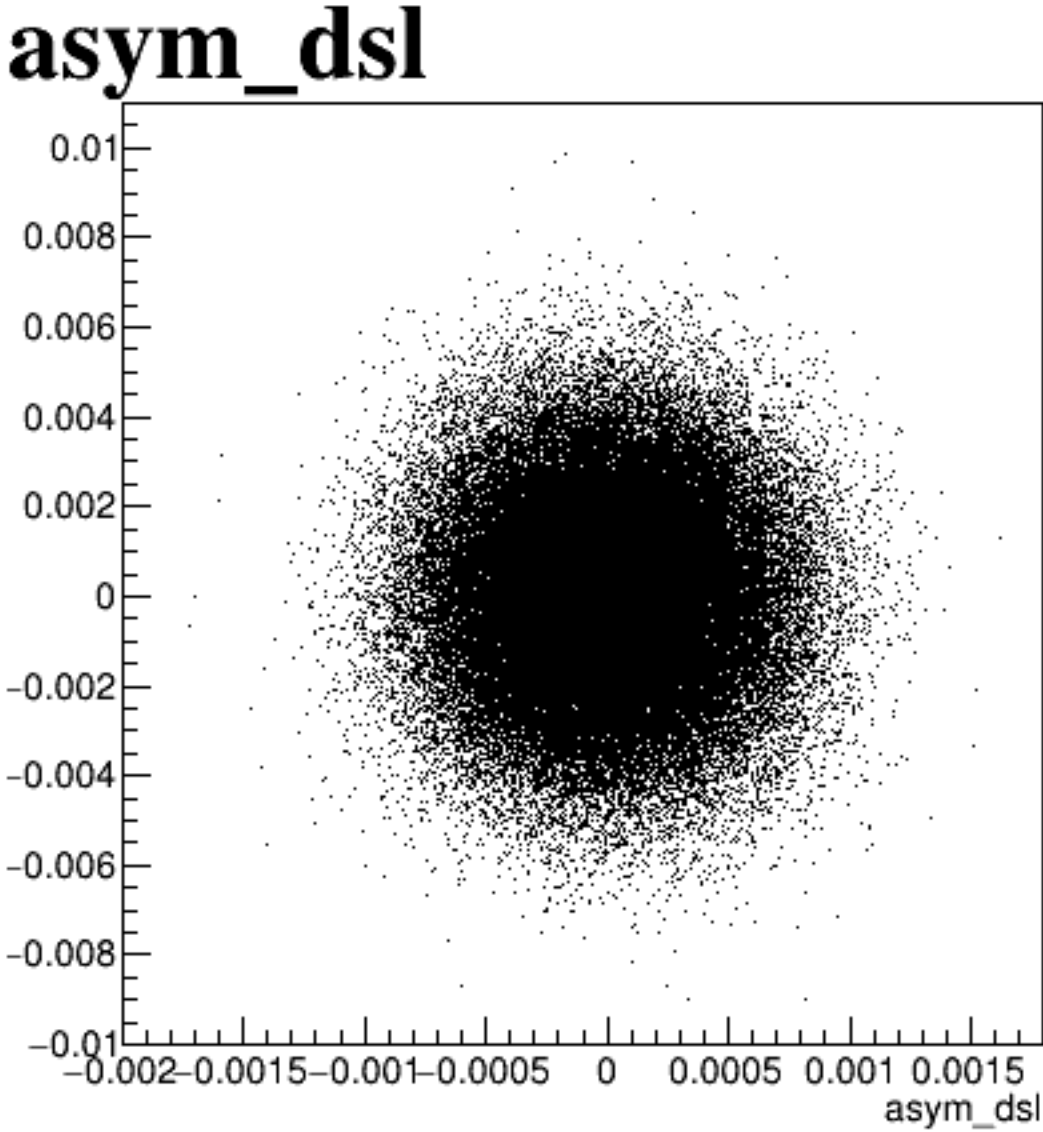
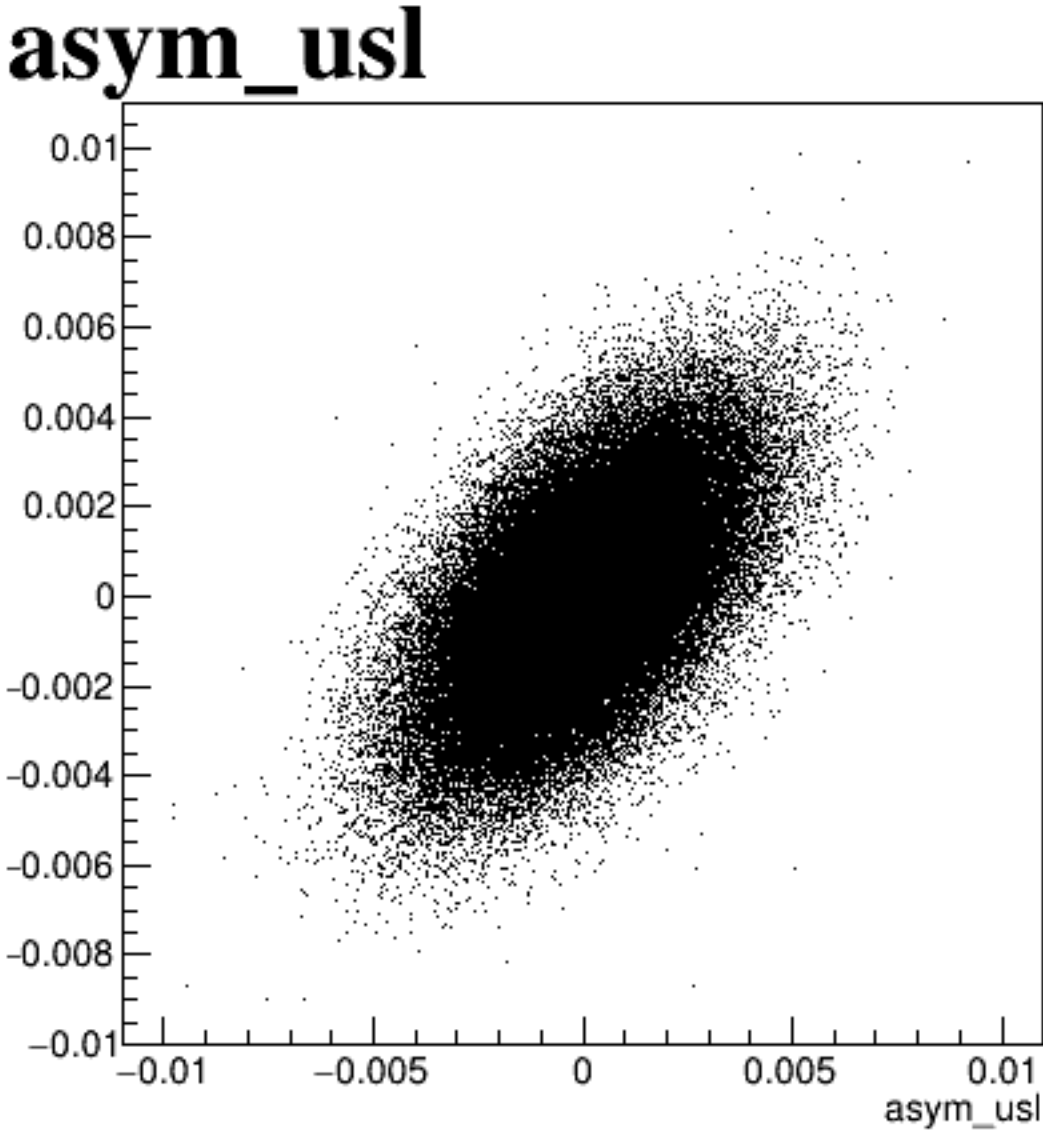
asym_usl



asym_dsl



asym_usr



asym_dsr

