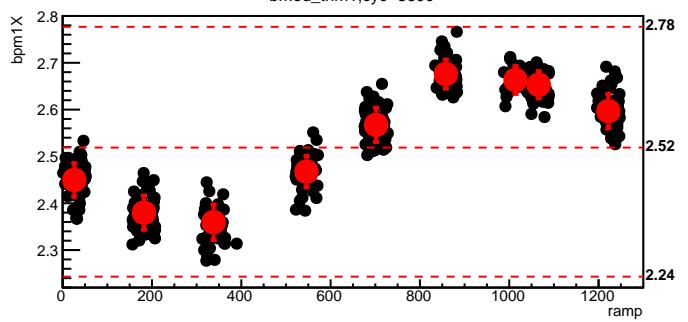
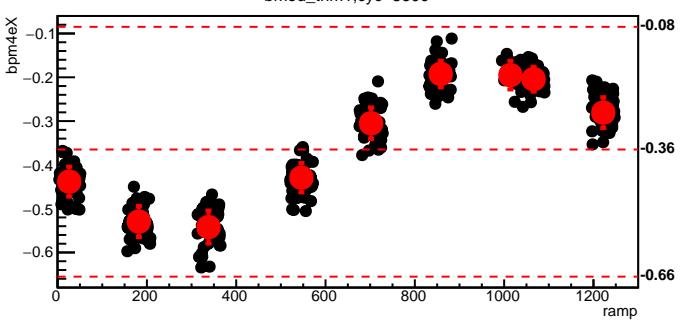


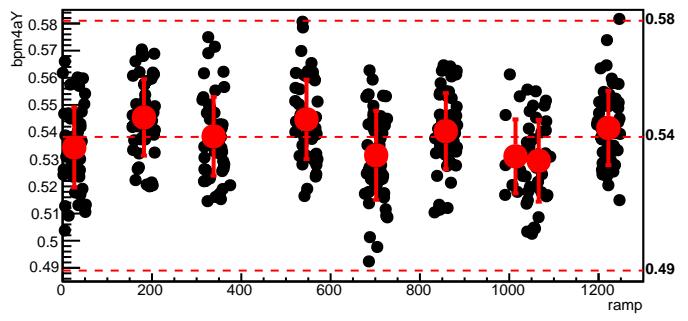
bmod Trim1,cyc=8600



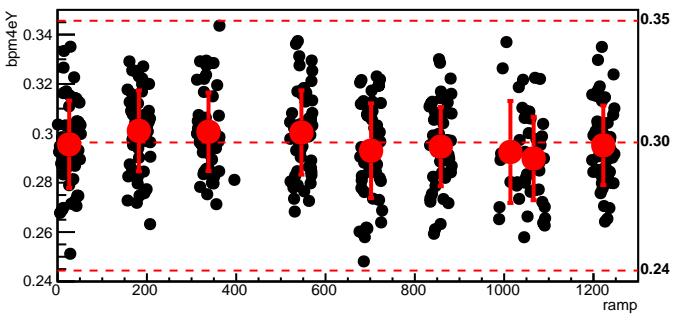
bmod Trim1,cyc=8600



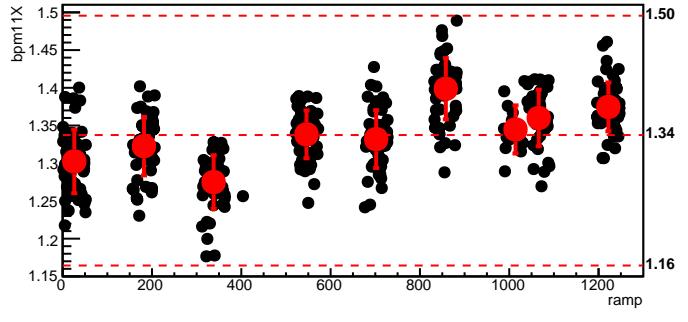
bmod Trim1,cyc=8600



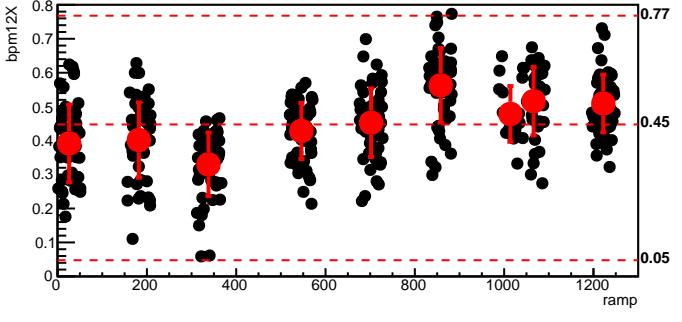
bmod Trim1,cyc=8600



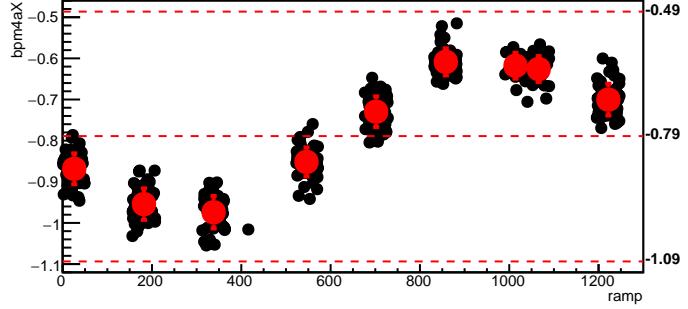
bmod Trim1,cyc=8600



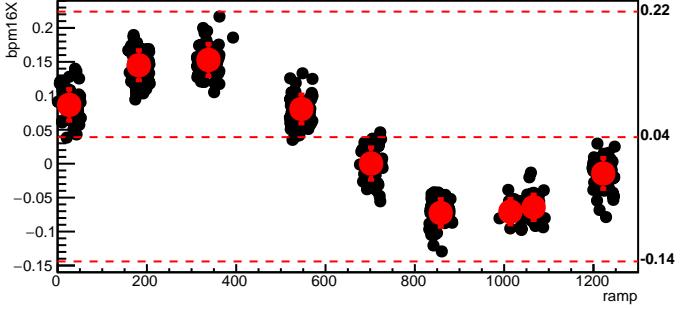
bmod Trim1,cyc=8600



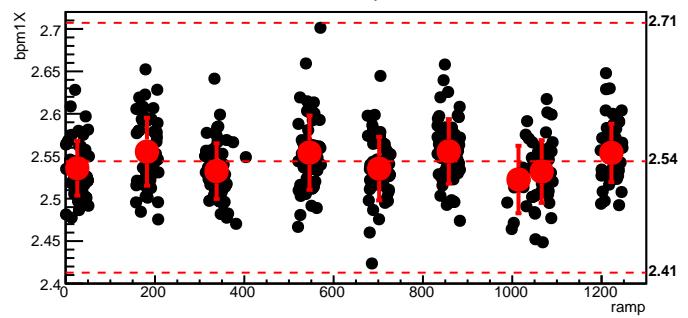
bmod Trim1,cyc=8600



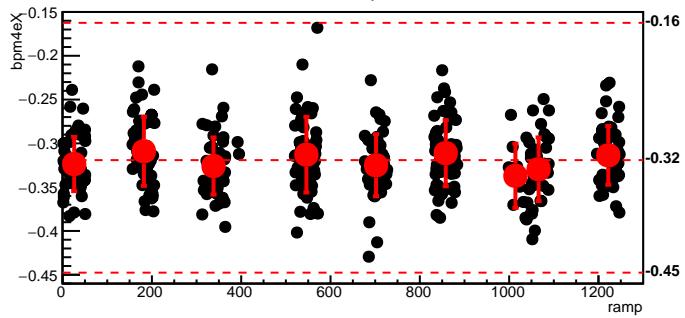
bmod Trim1,cyc=8600



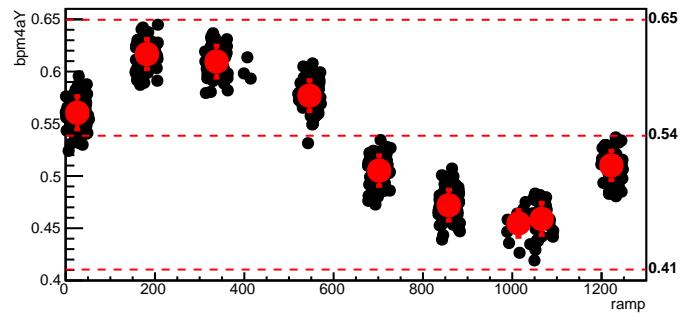
bmod Trim2,cyc=8600



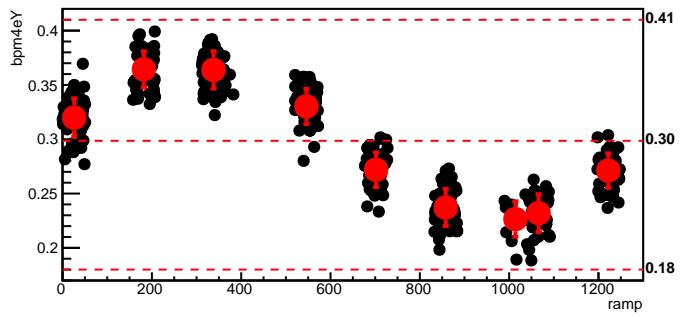
bmod Trim2,cyc=8600



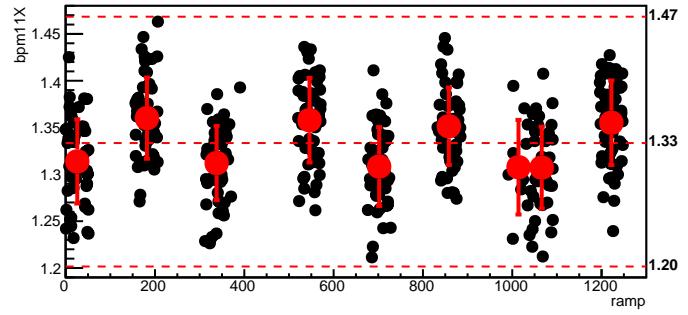
bmod Trim2,cyc=8600



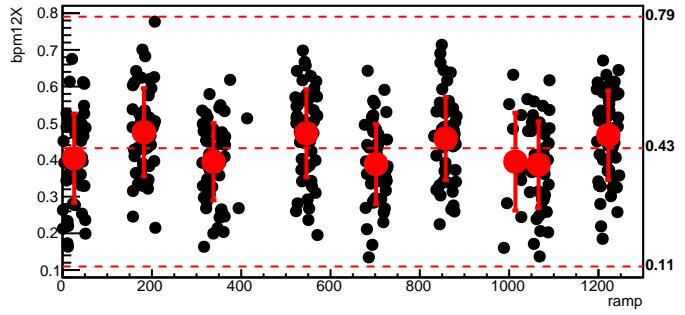
bmod Trim2,cyc=8600



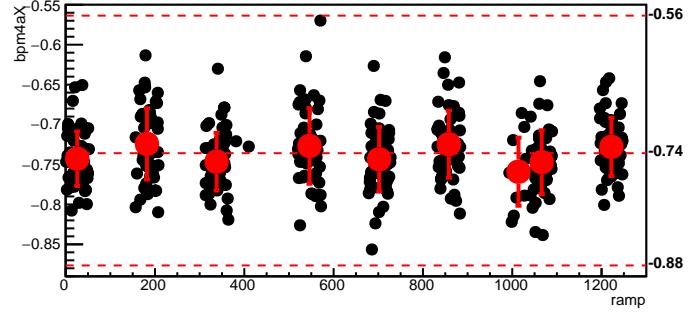
bmod Trim2,cyc=8600



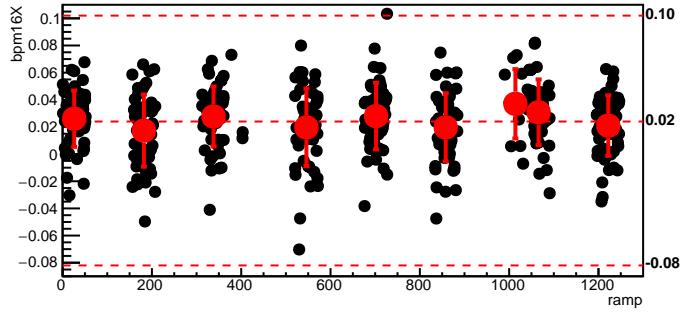
bmod Trim2,cyc=8600



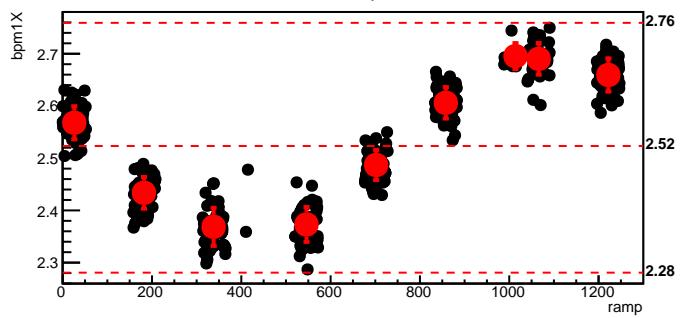
bmod Trim2,cyc=8600



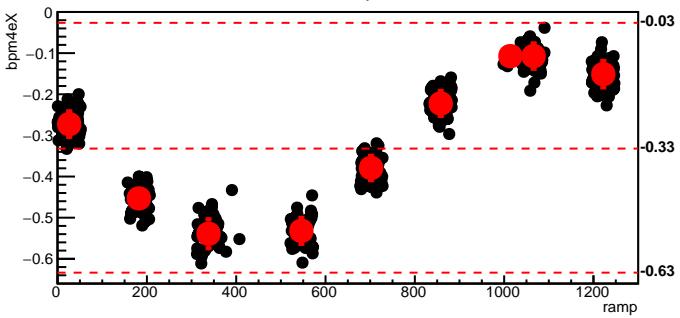
bmod Trim2,cyc=8600



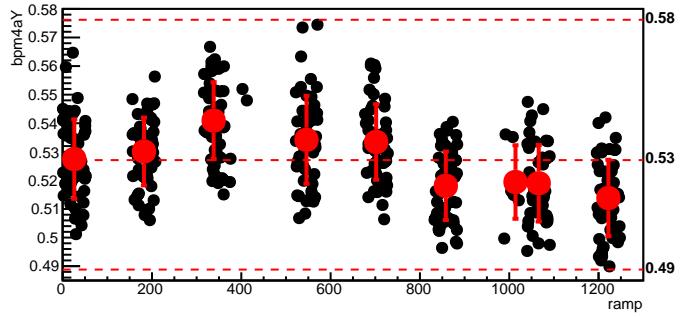
bmod Trim3,cyc=8600



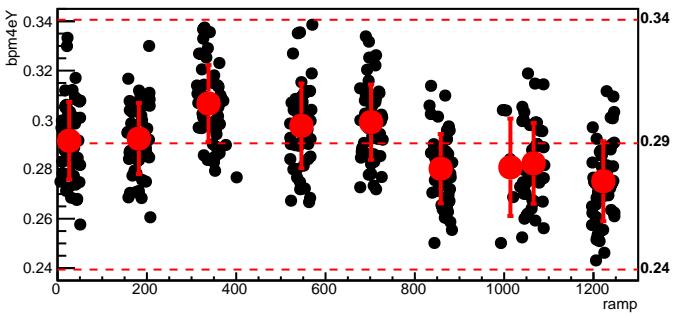
bmod Trim3,cyc=8600



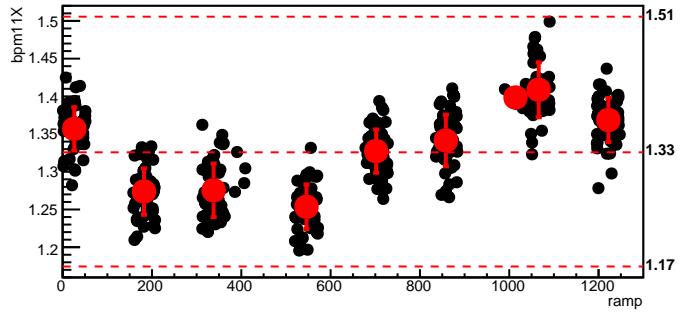
bmod Trim3,cyc=8600



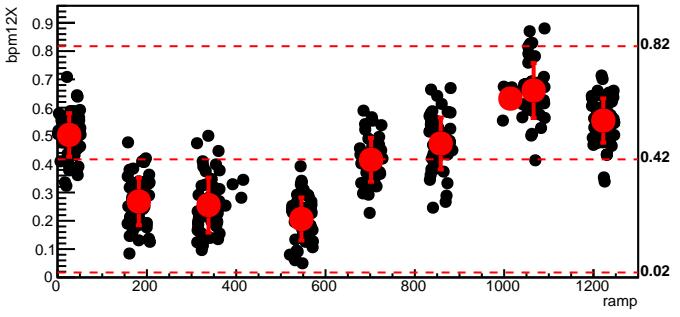
bmod Trim3,cyc=8600



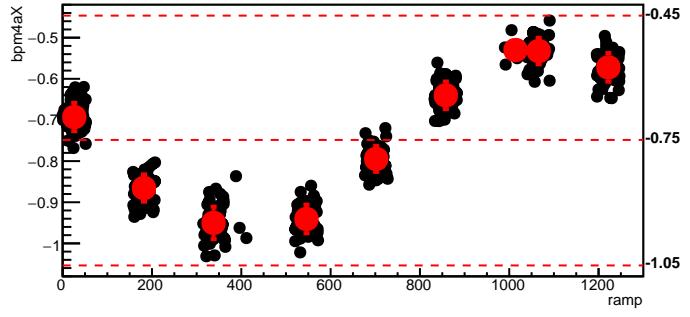
bmod Trim3,cyc=8600



bmod Trim3,cyc=8600



bmod Trim3,cyc=8600



bmod Trim3,cyc=8600

