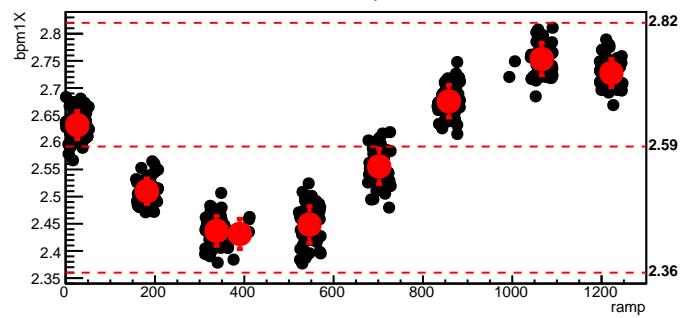


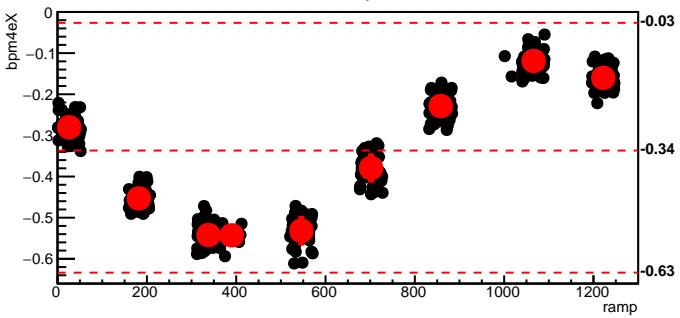




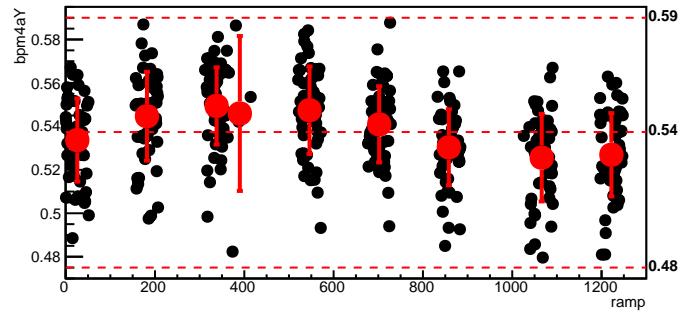
bmod Trim3,cyc=8712



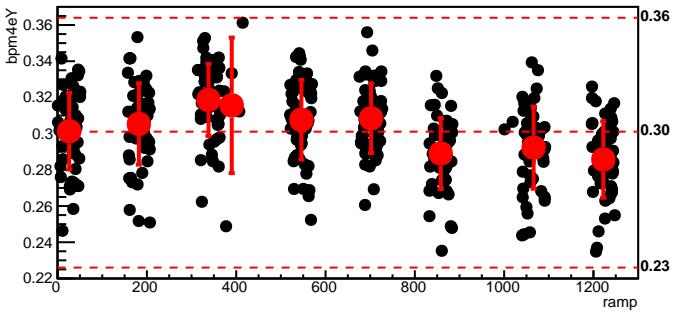
bmod Trim3,cyc=8712



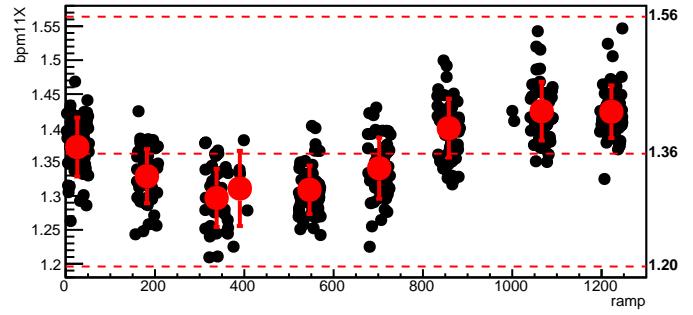
bmod Trim3,cyc=8712



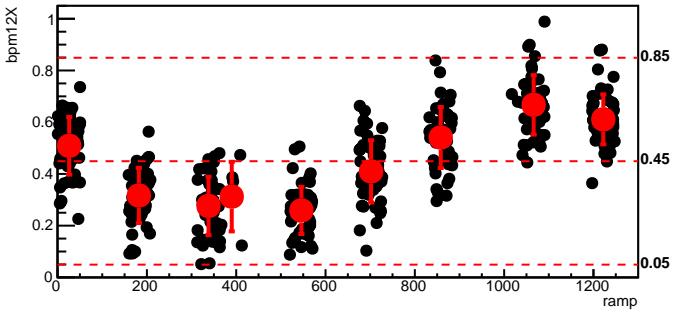
bmod Trim3,cyc=8712



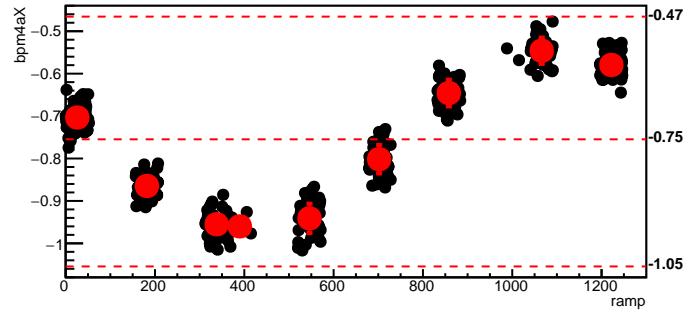
bmod Trim3,cyc=8712



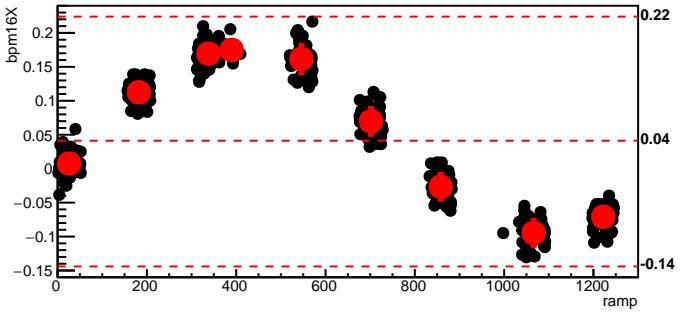
bmod Trim3,cyc=8712



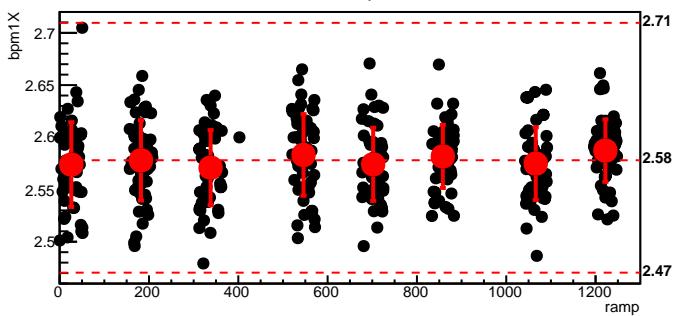
bmod Trim3,cyc=8712



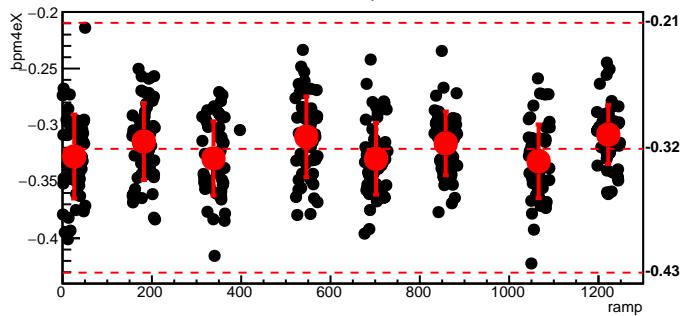
bmod Trim3,cyc=8712



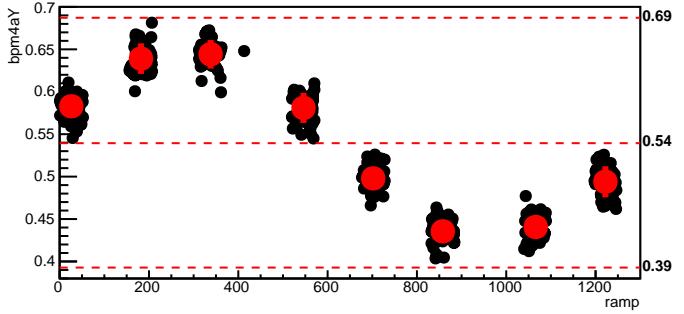
bmod Trim4,cyc=8712



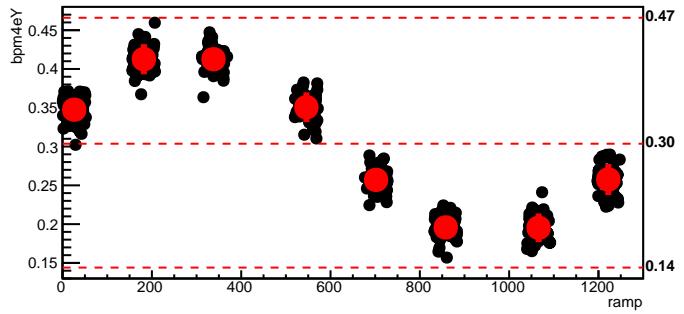
bmod Trim4,cyc=8712



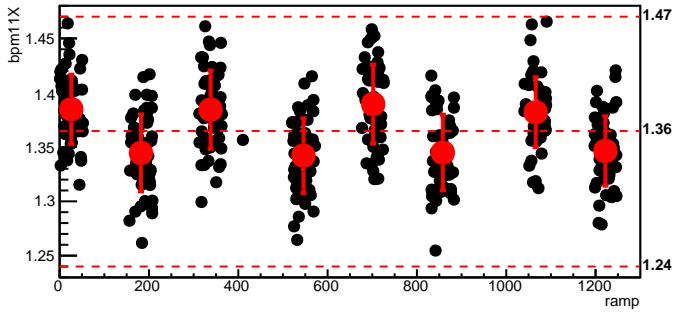
bmod Trim4,cyc=8712



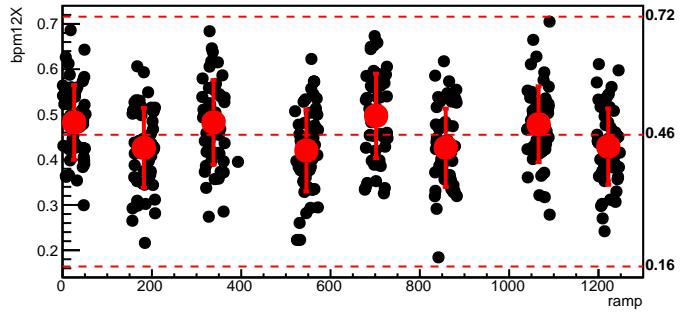
bmod Trim4,cyc=8712



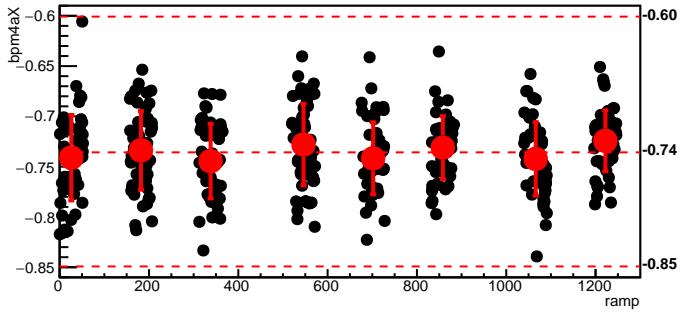
bmod Trim4,cyc=8712



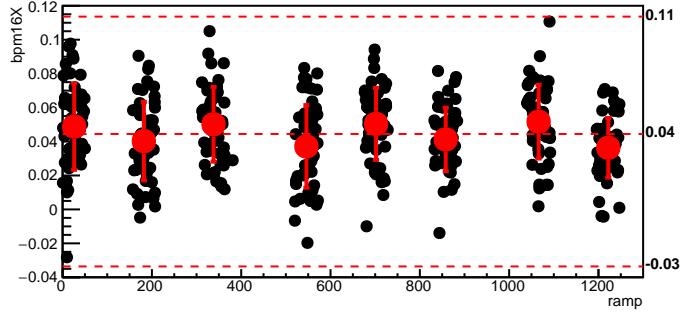
bmod Trim4,cyc=8712



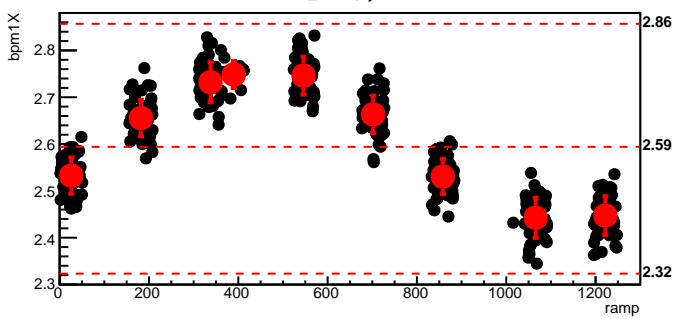
bmod Trim4,cyc=8712



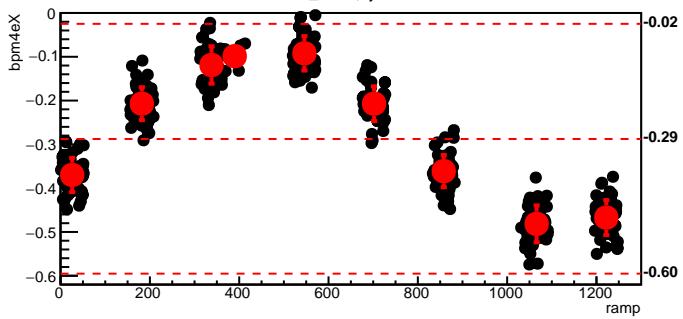
bmod Trim4,cyc=8712



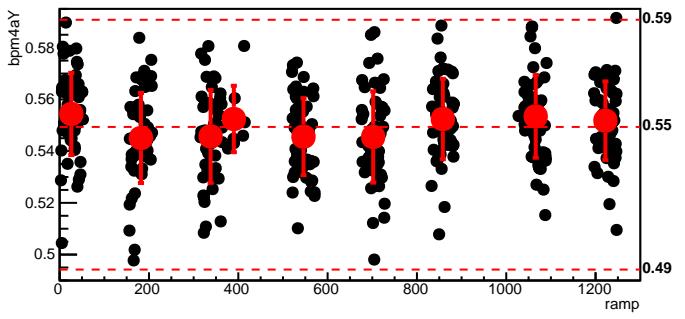
bmod Trim5,cyc=8712



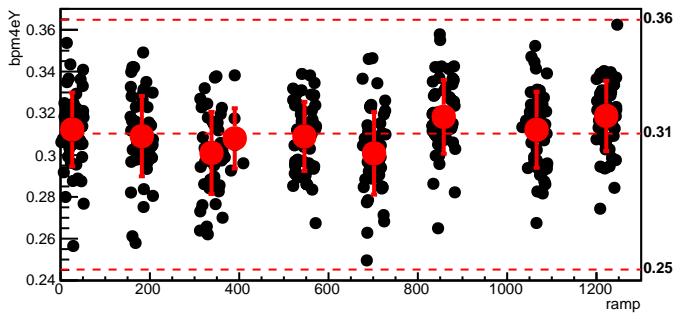
bmod Trim5,cyc=8712



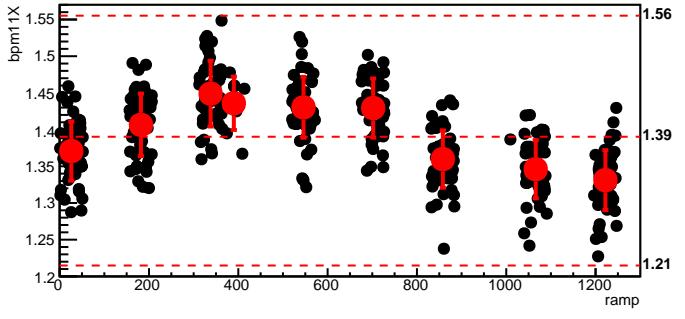
bmod Trim5,cyc=8712



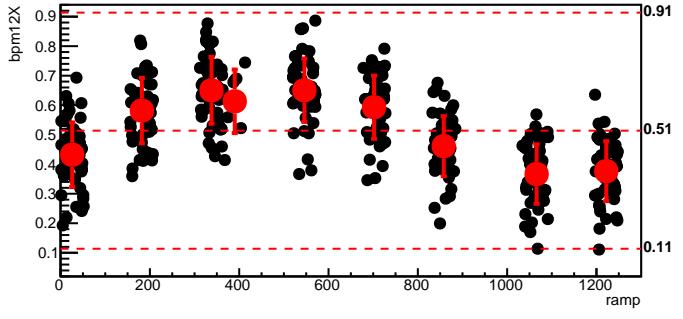
bmod Trim5,cyc=8712



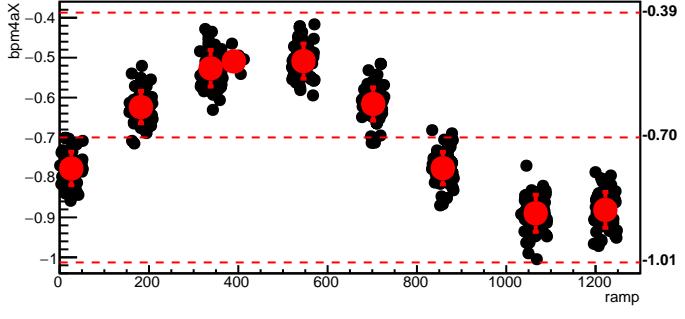
bmod Trim5,cyc=8712



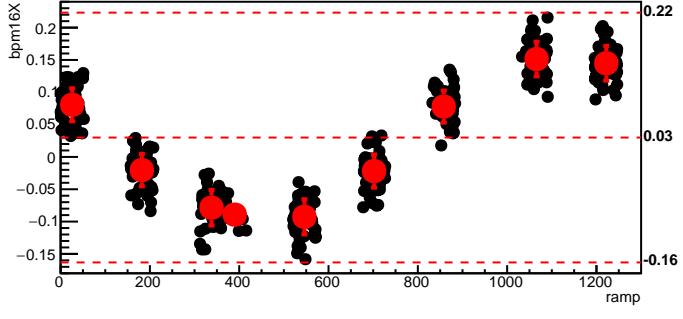
bmod Trim5,cyc=8712



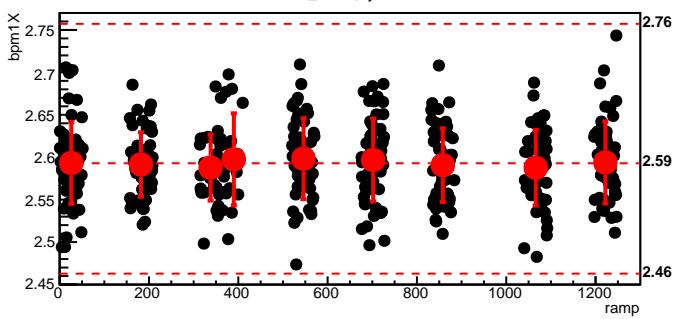
bmod Trim5,cyc=8712



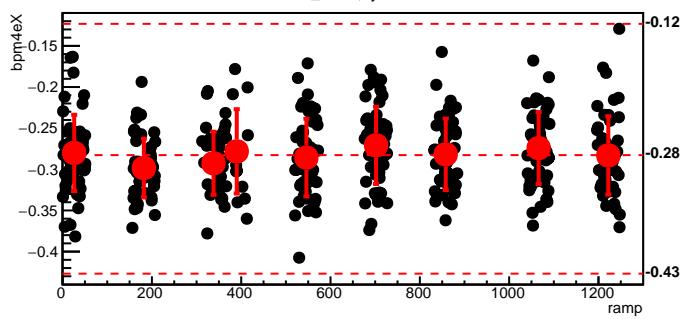
bmod Trim5,cyc=8712



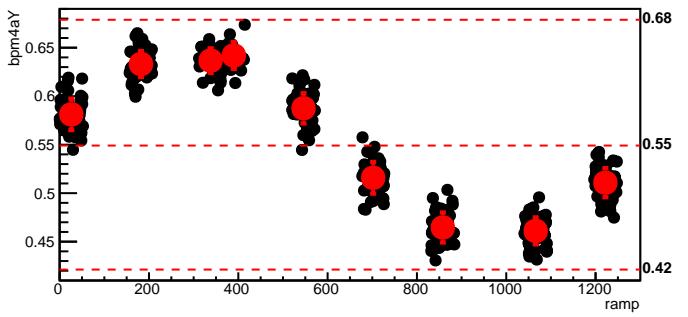
bmod Trim6,cyc=8712



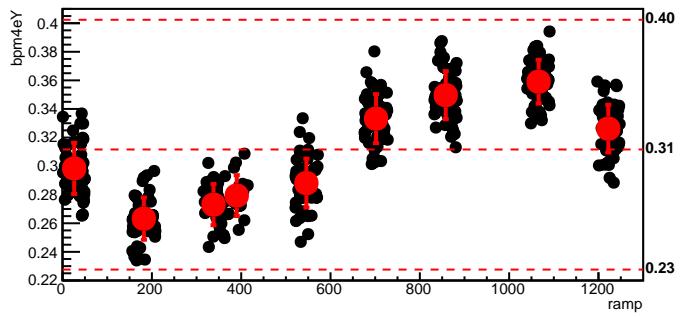
bmod Trim6,cyc=8712



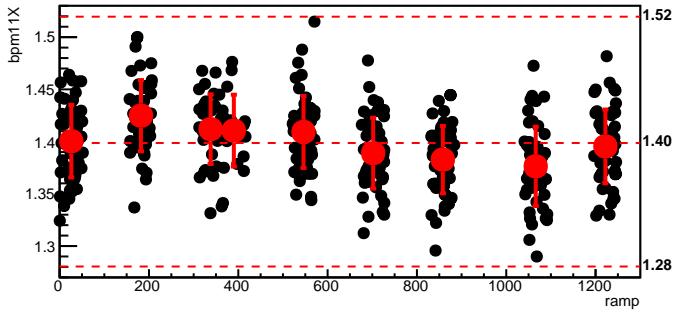
bmod Trim6,cyc=8712



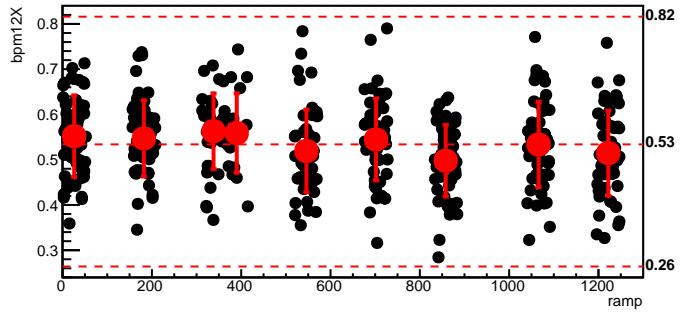
bmod Trim6,cyc=8712



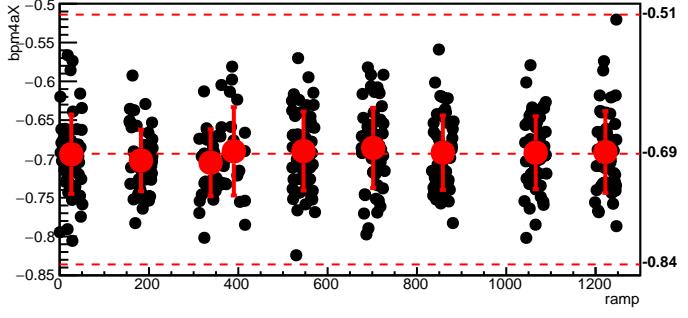
bmod Trim6,cyc=8712



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bmod Trim6,cyc=8712

