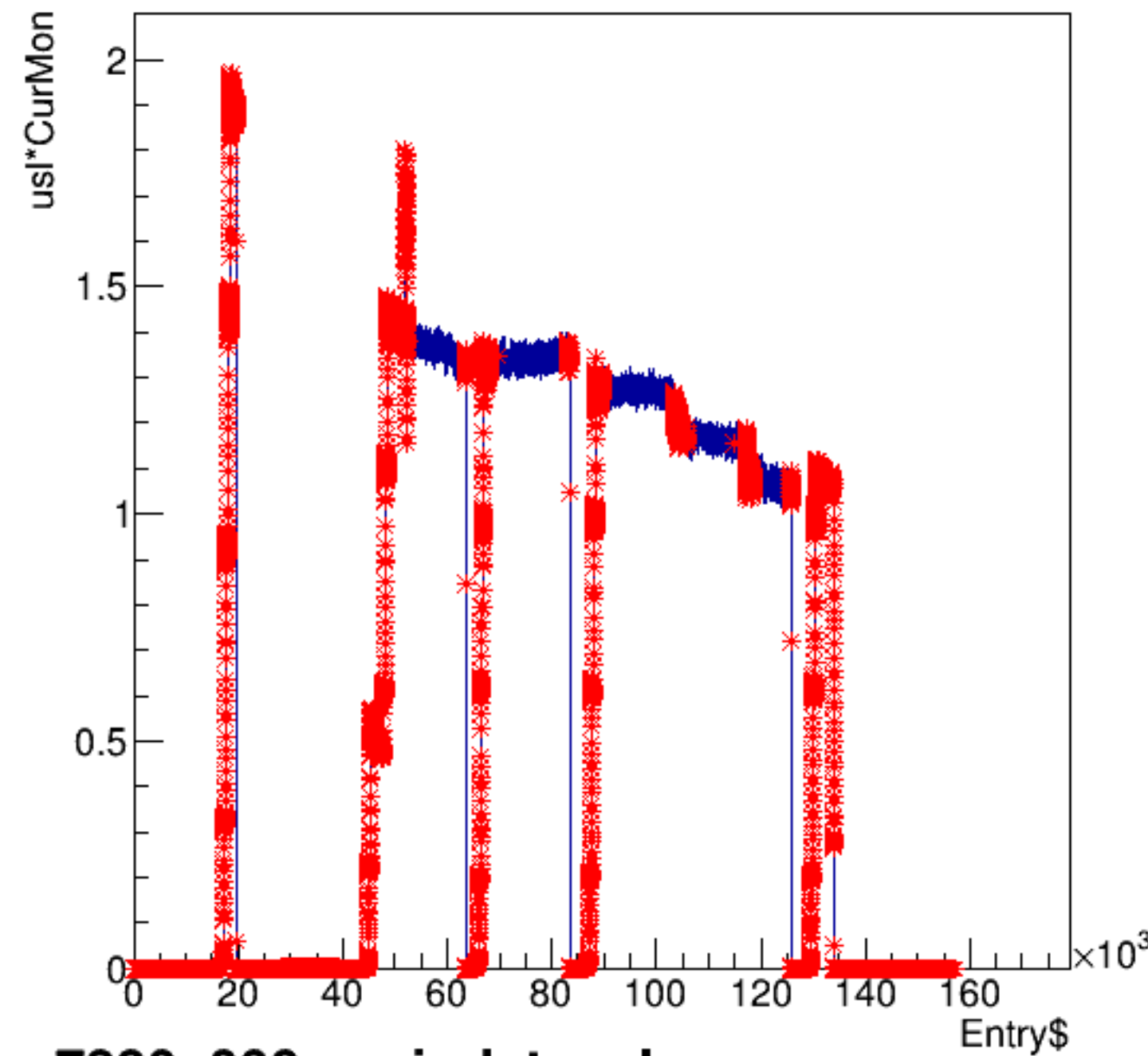
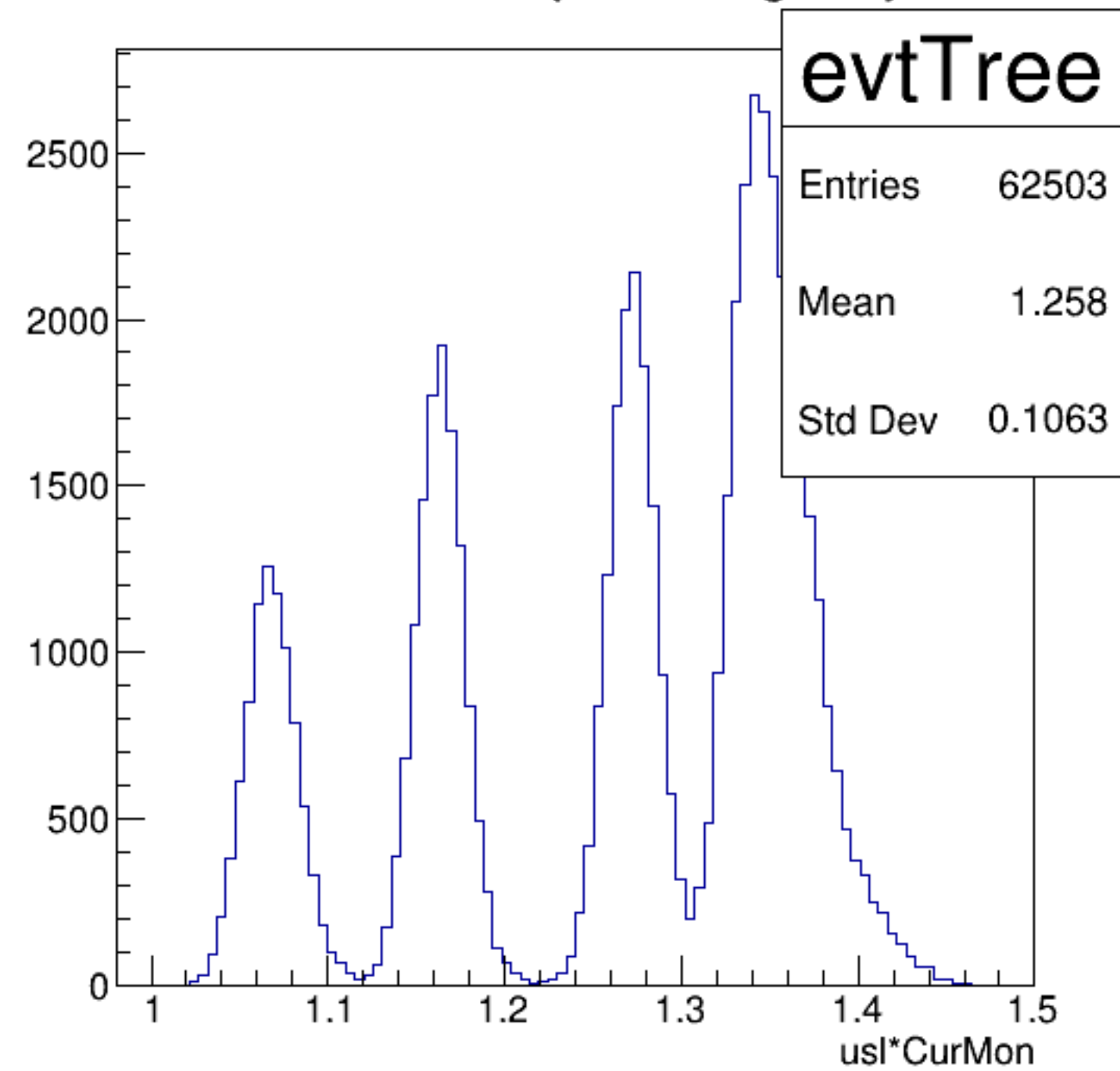


usl\*CurMon:Entry\$

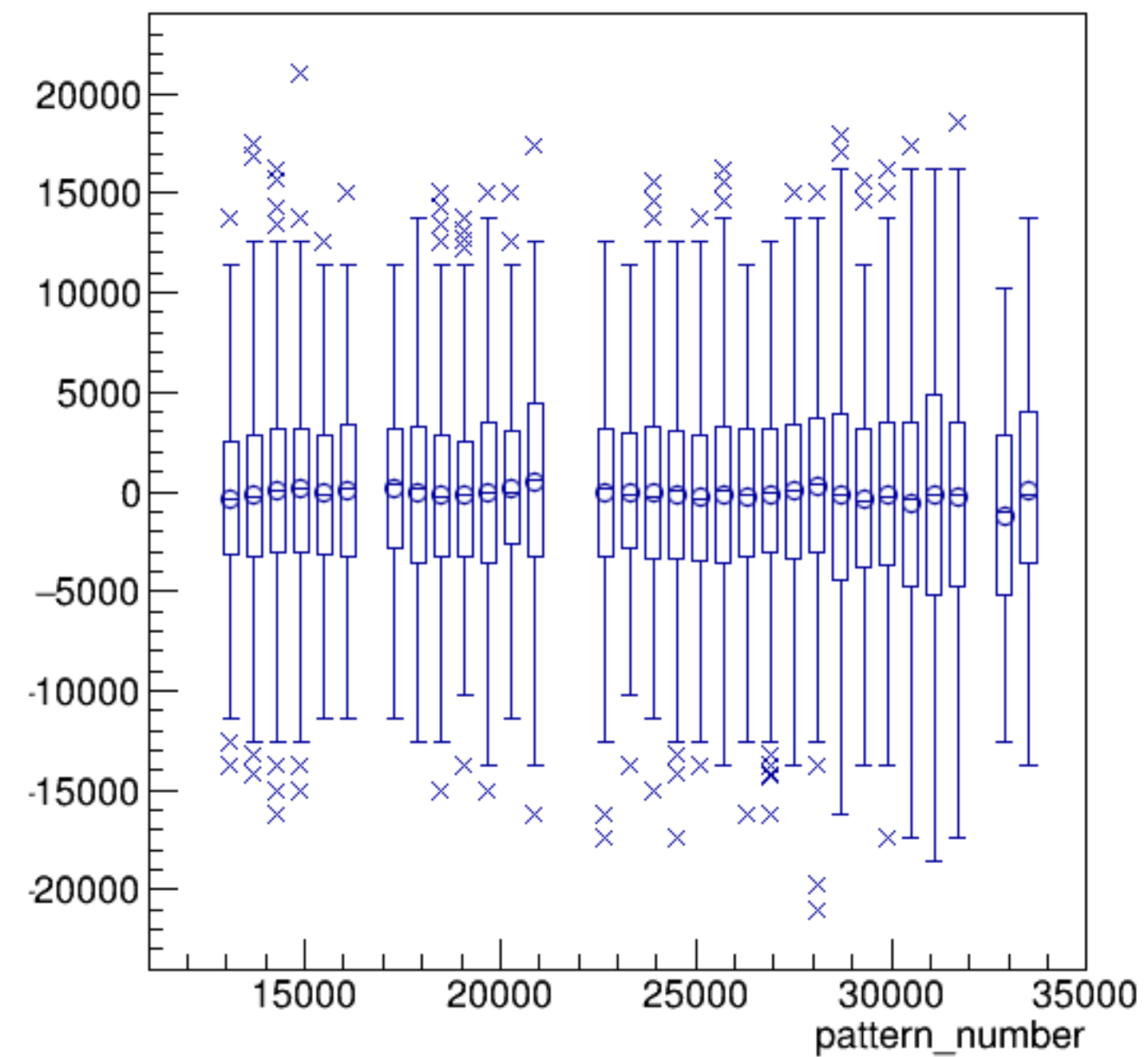


run7330\_000\_maindet\_usl.png

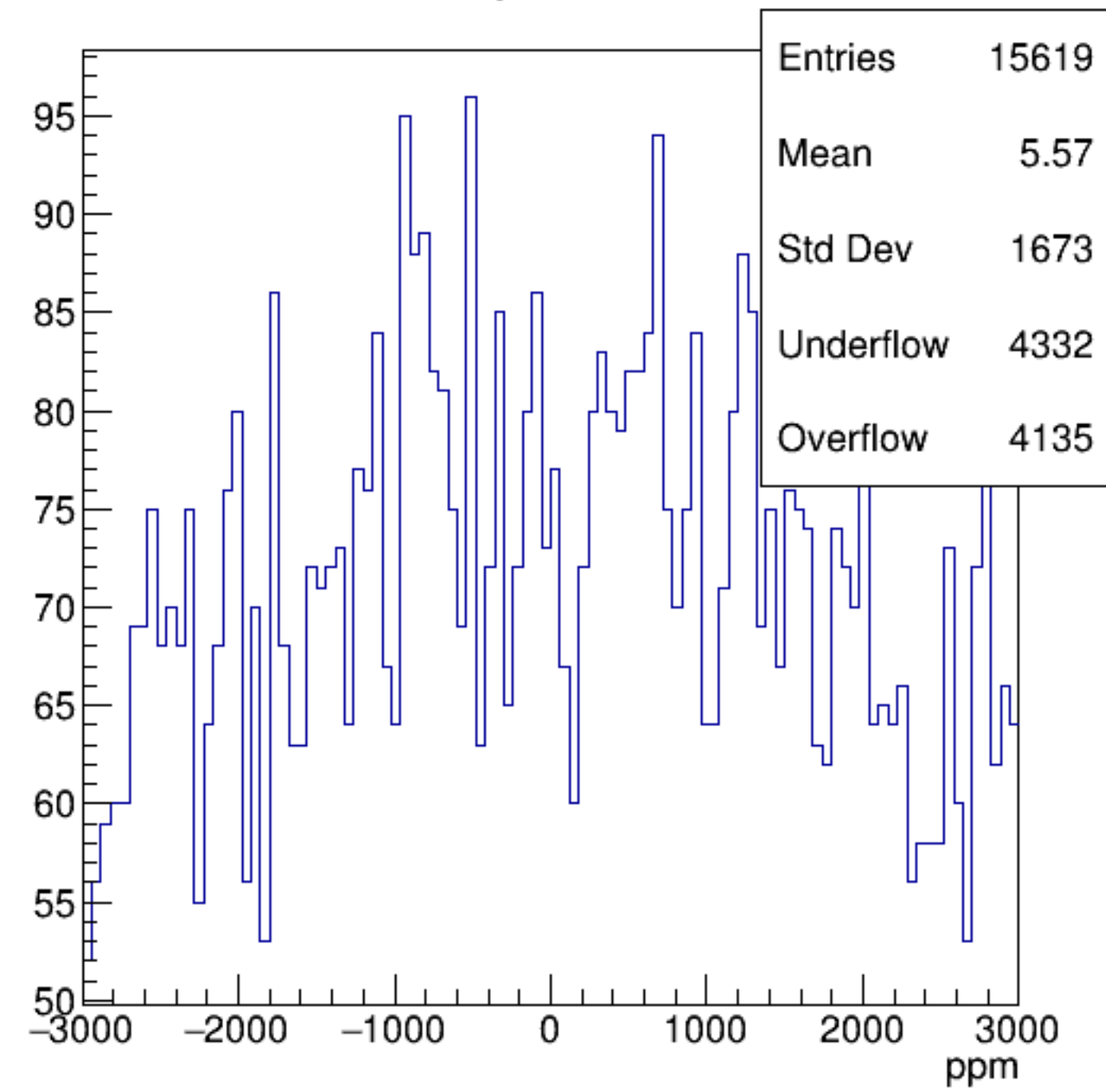
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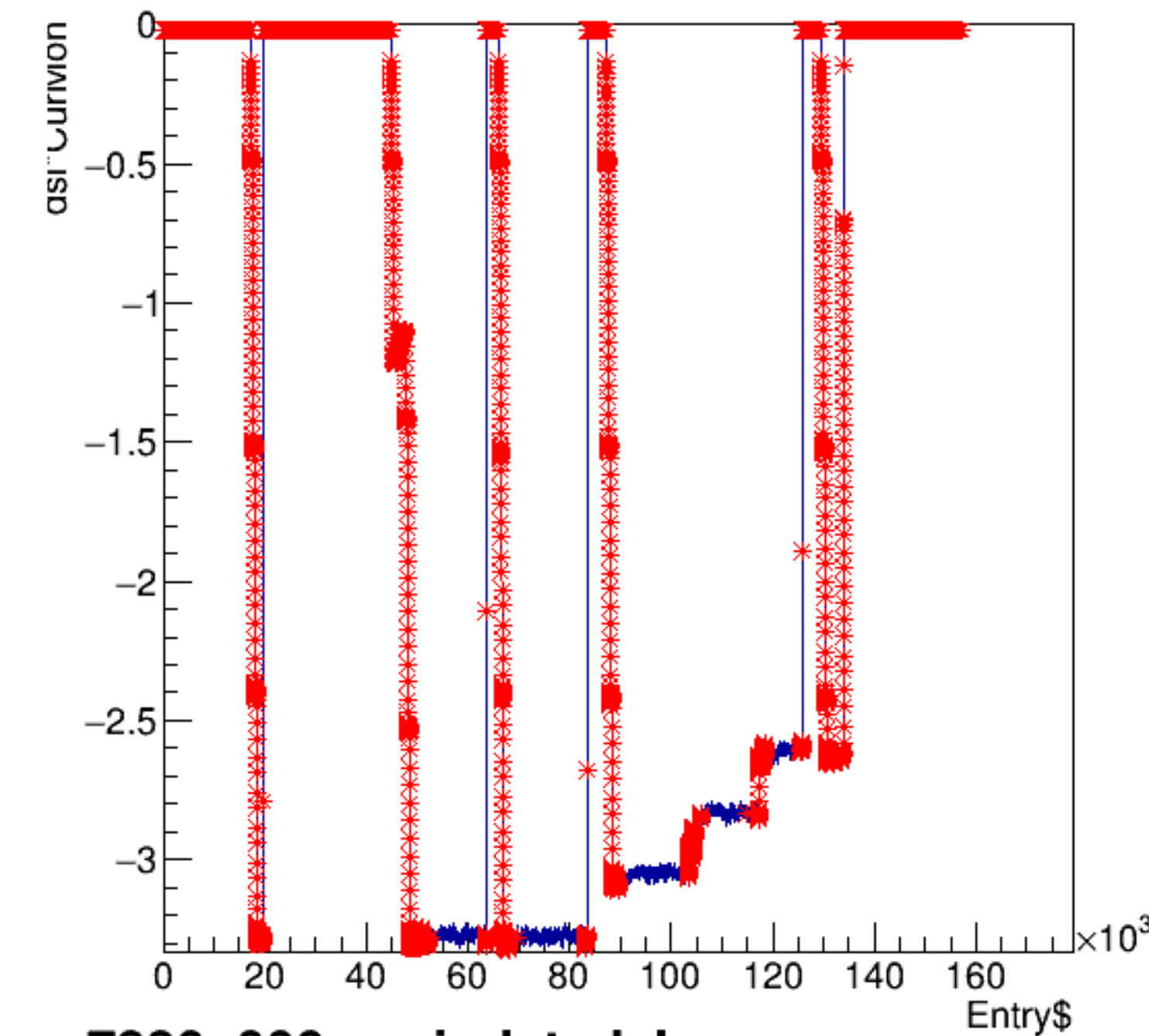
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asym\_usl

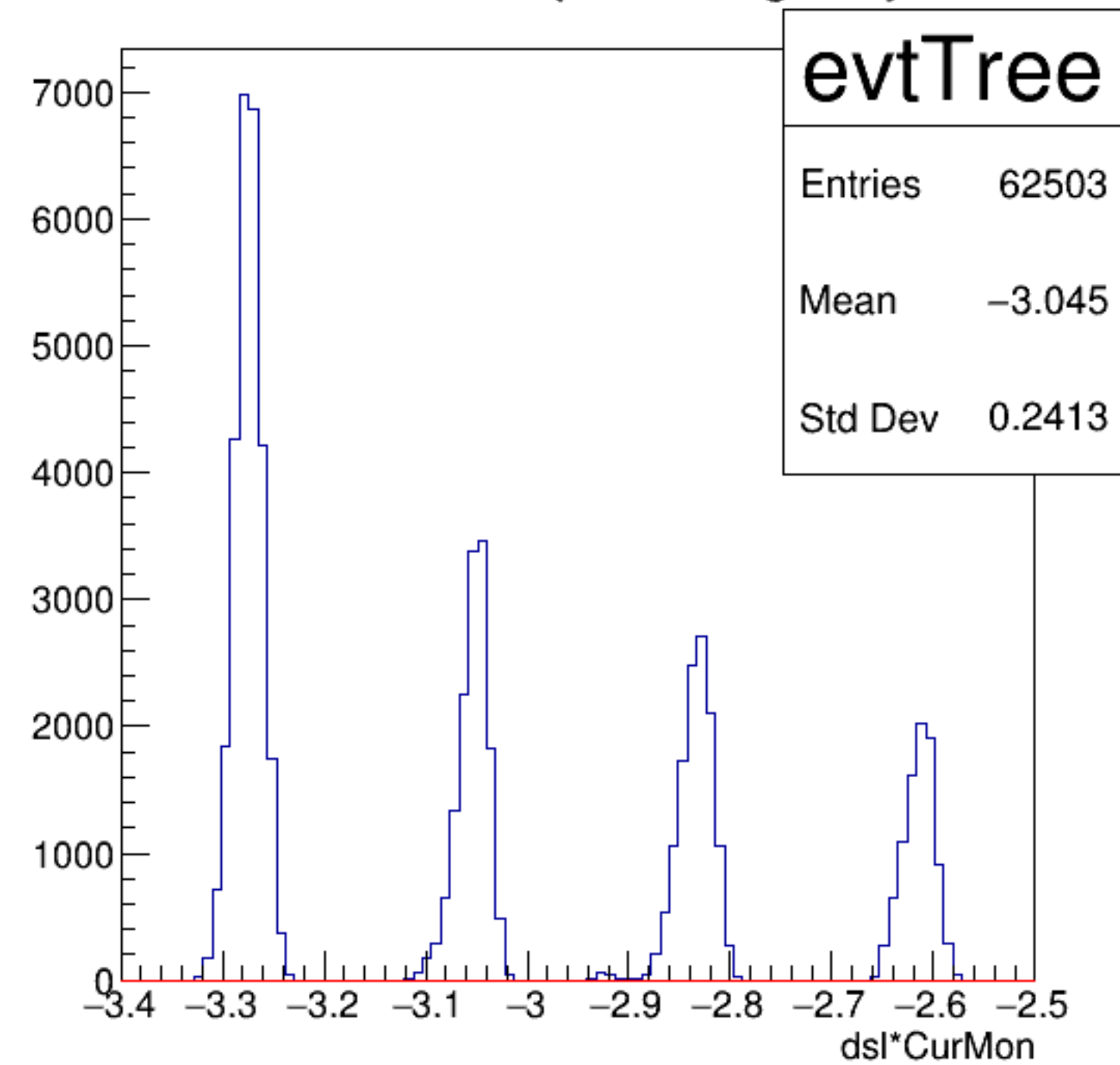


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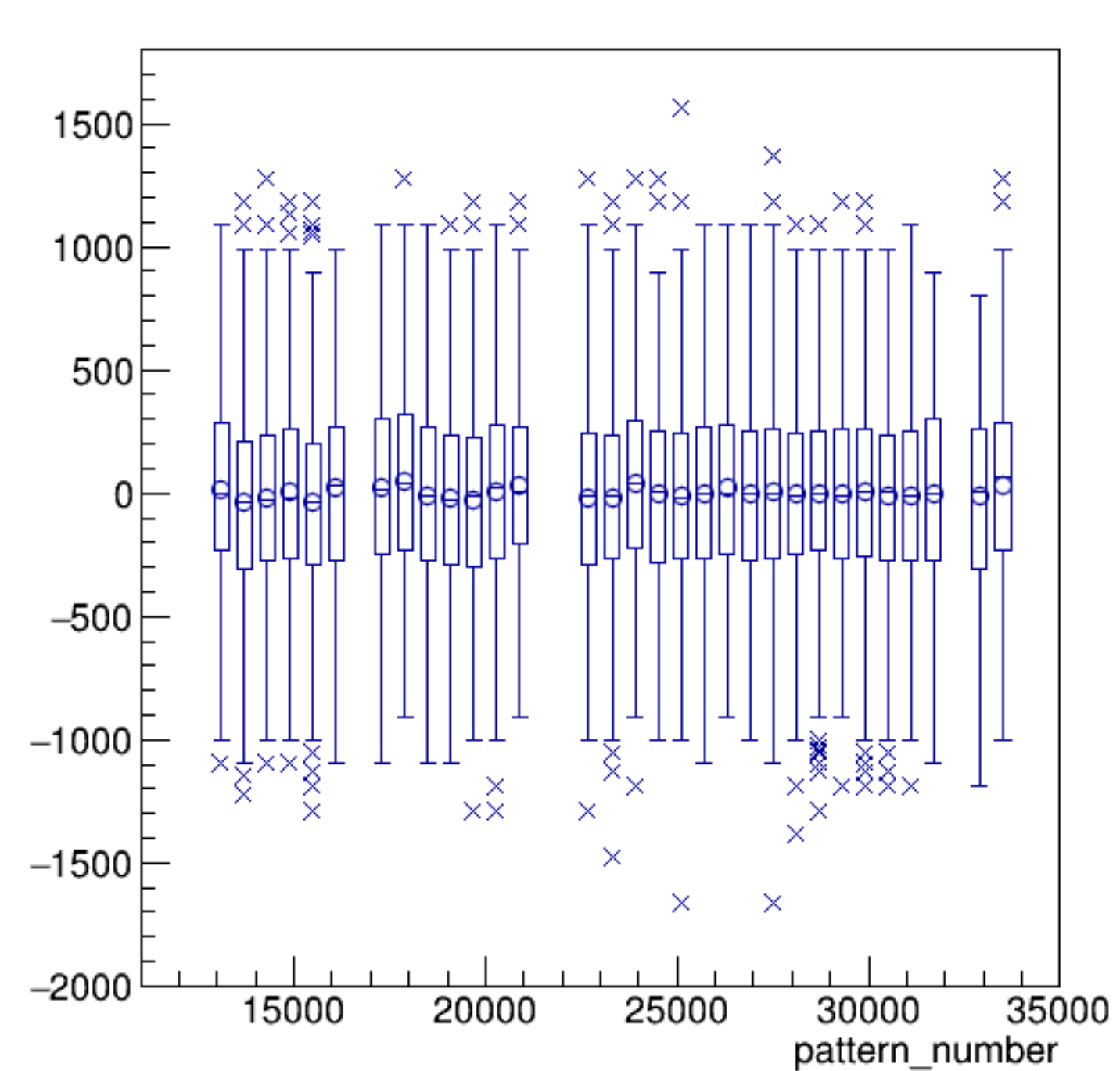


run7330\_000\_maintet\_dsl.png

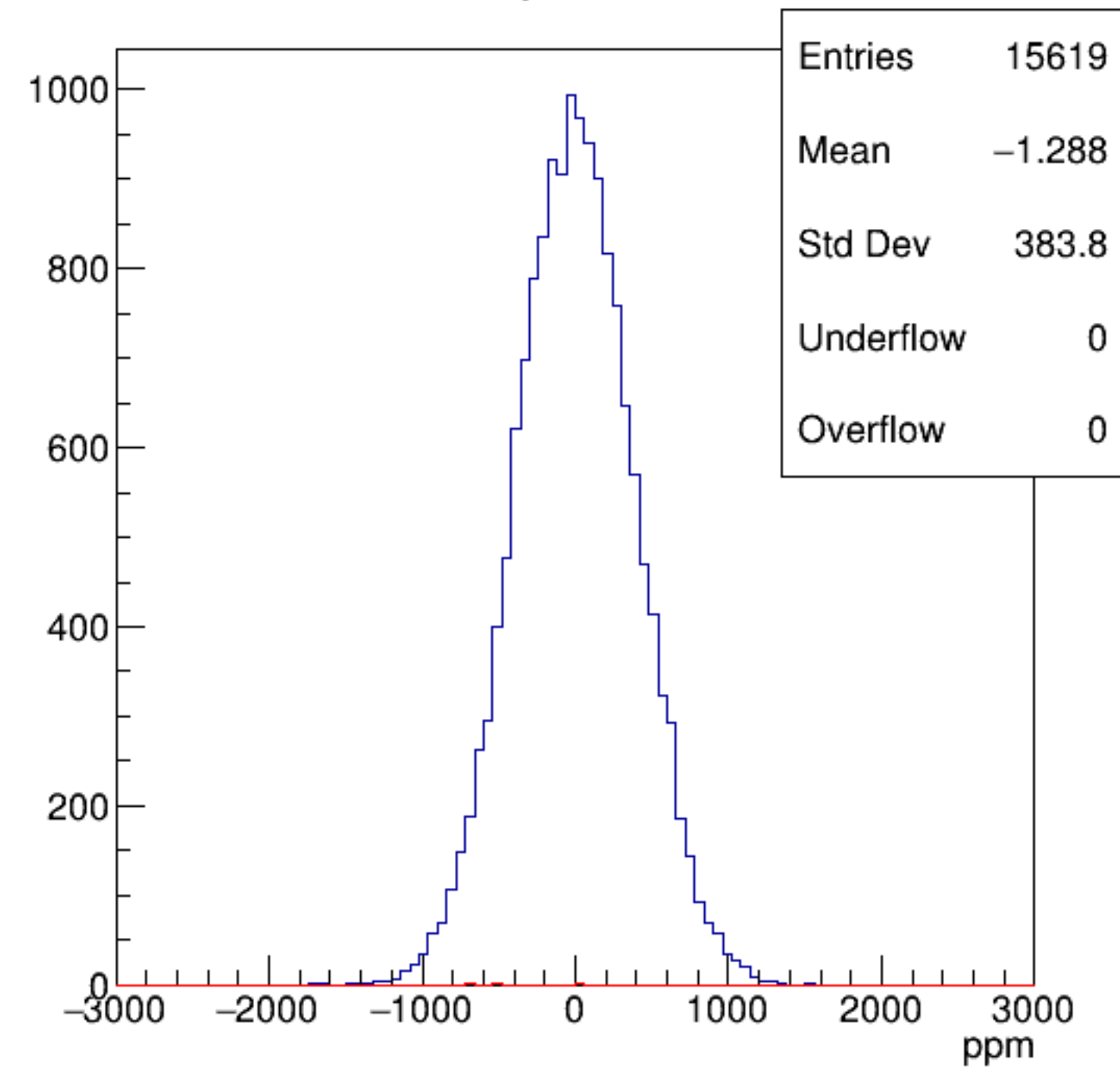
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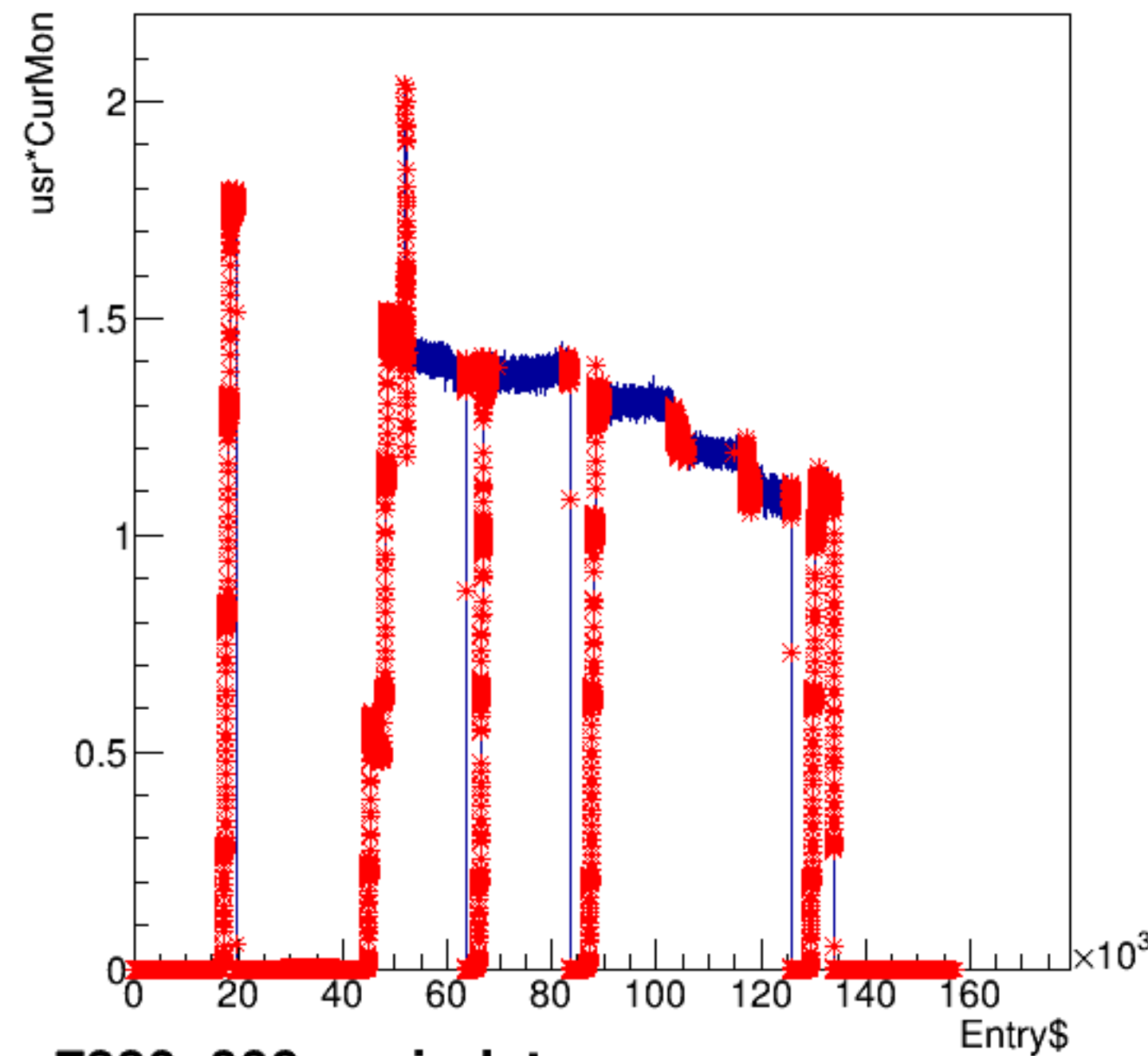
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asym\_dsl

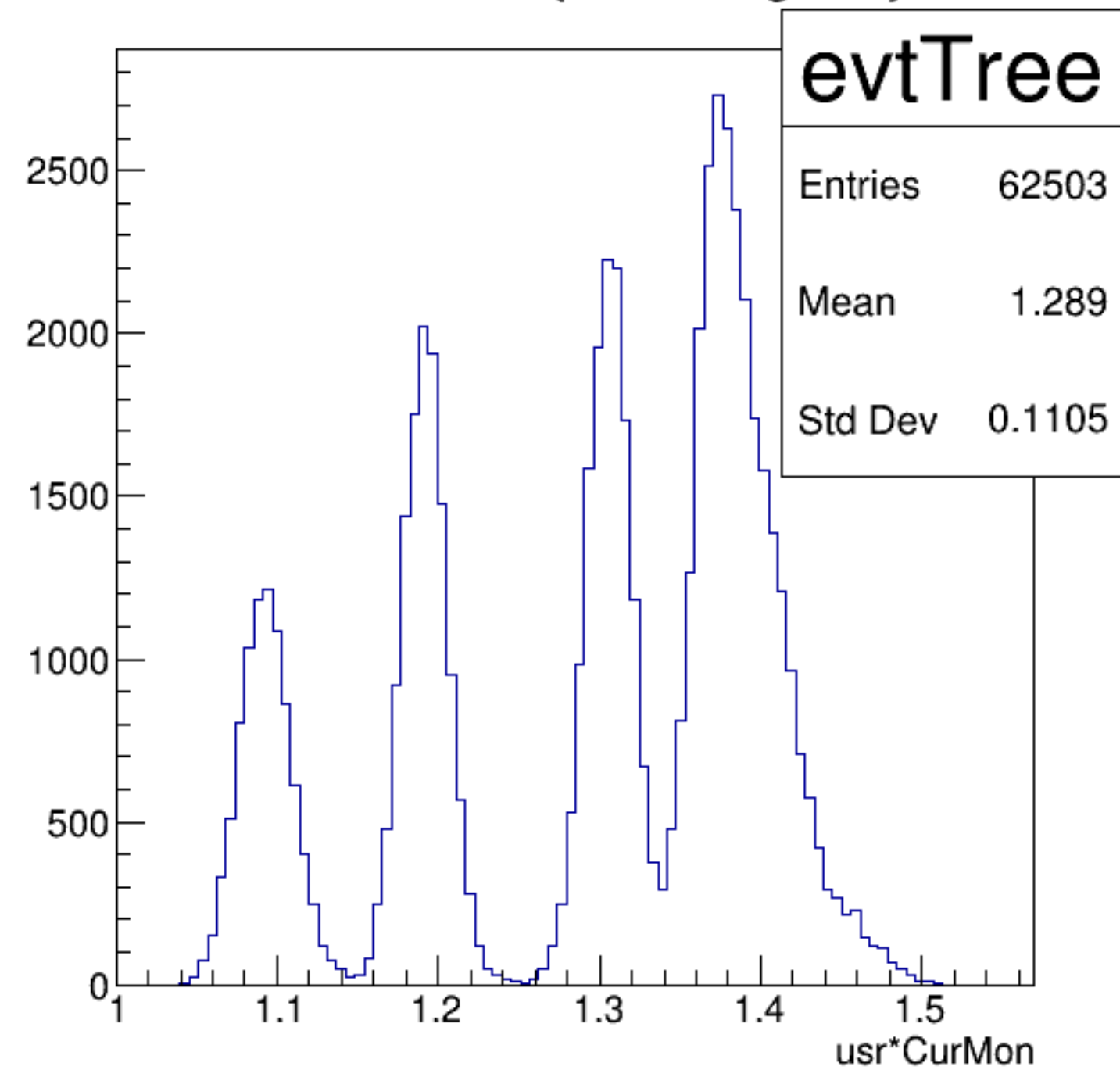


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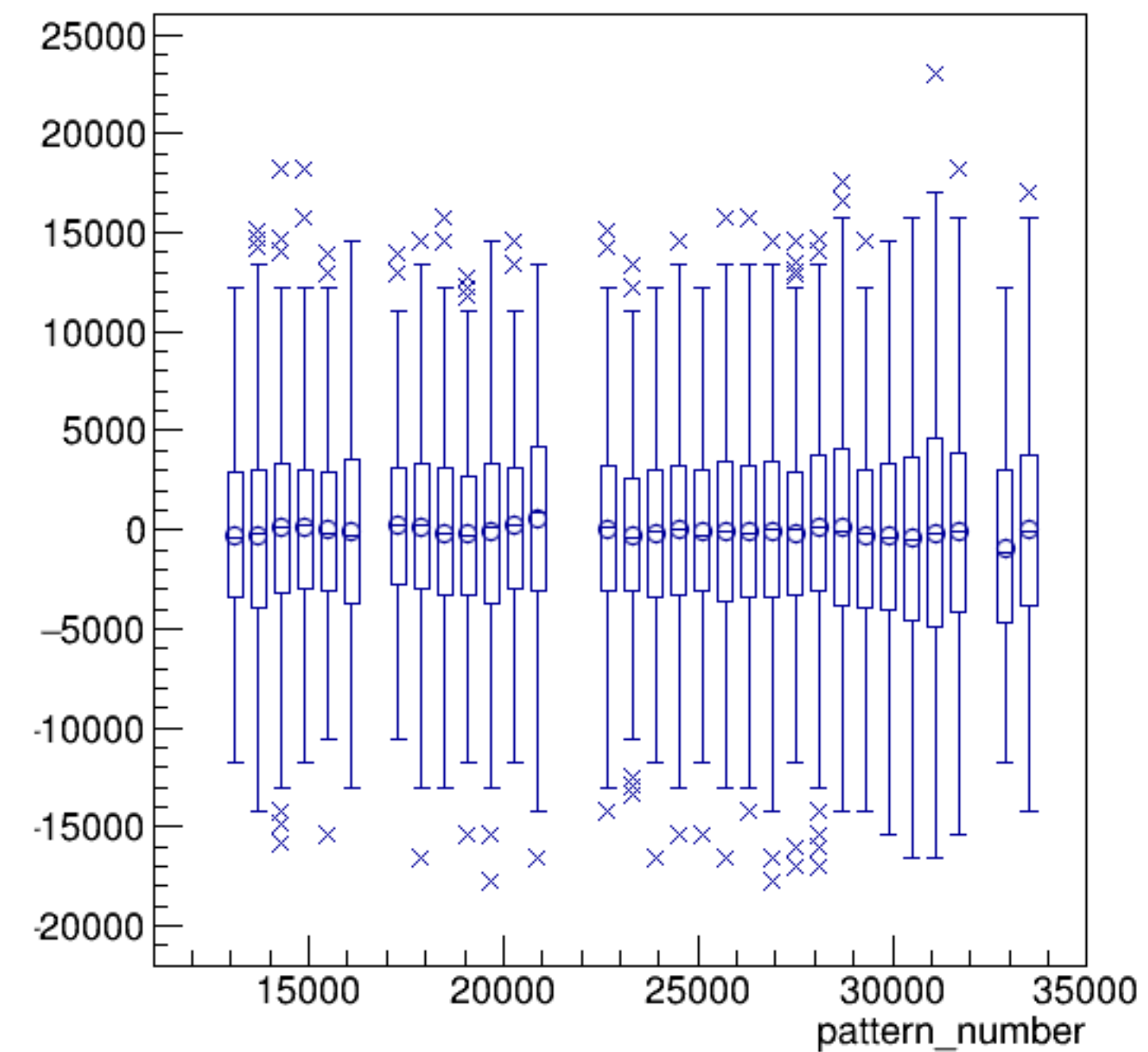


run7330\_000\_maindet\_usr.png

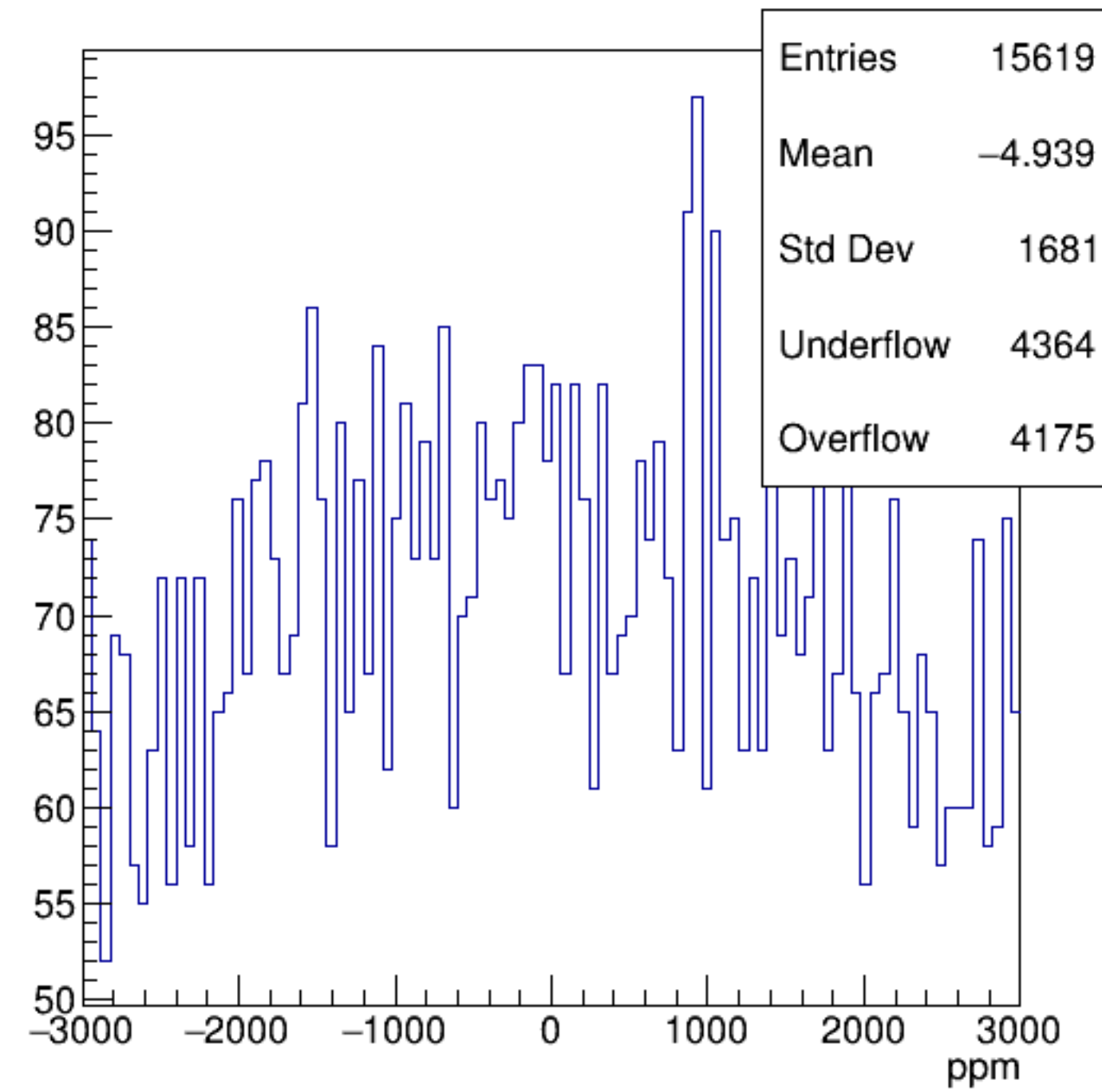
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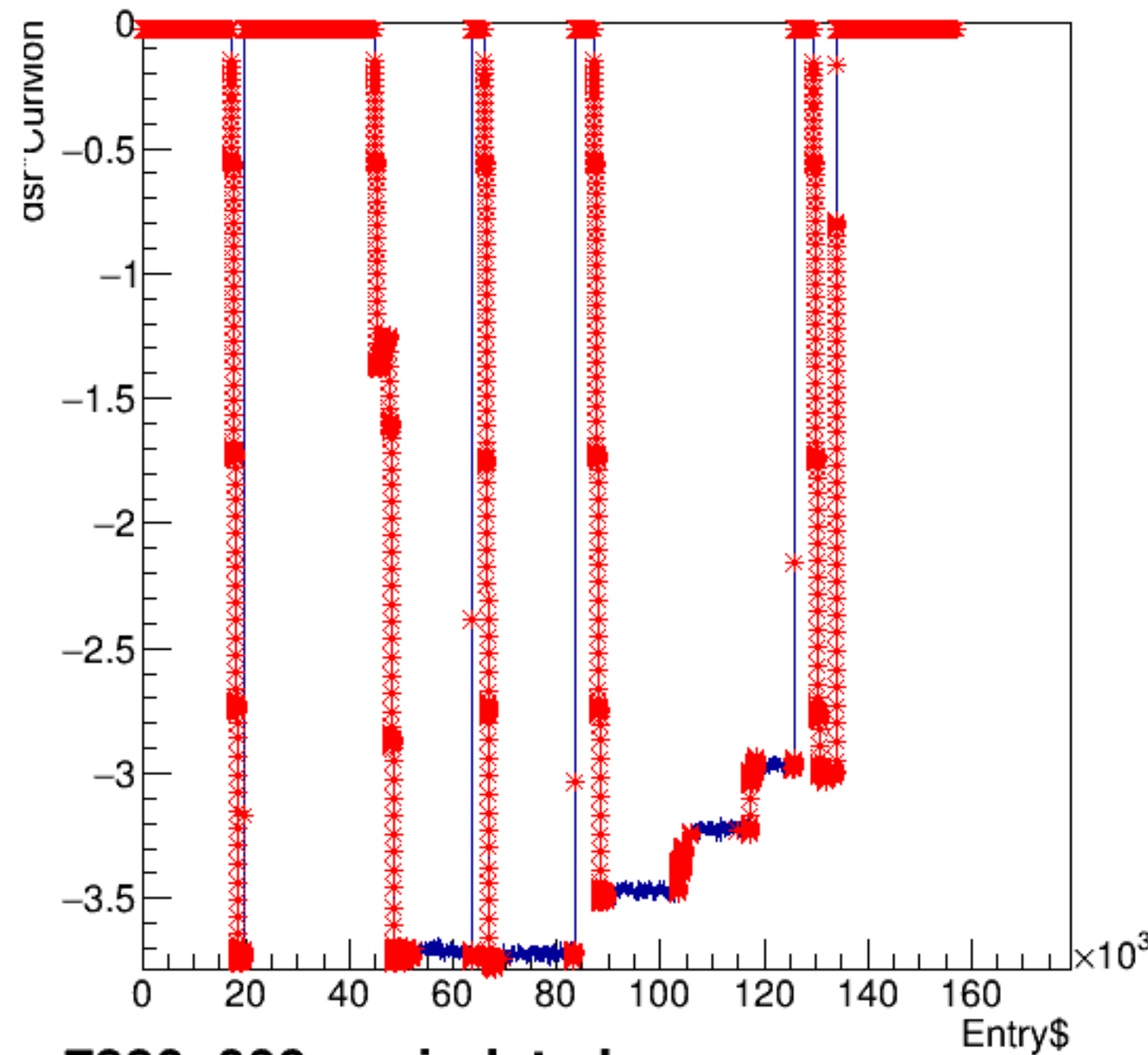
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asym\_usr

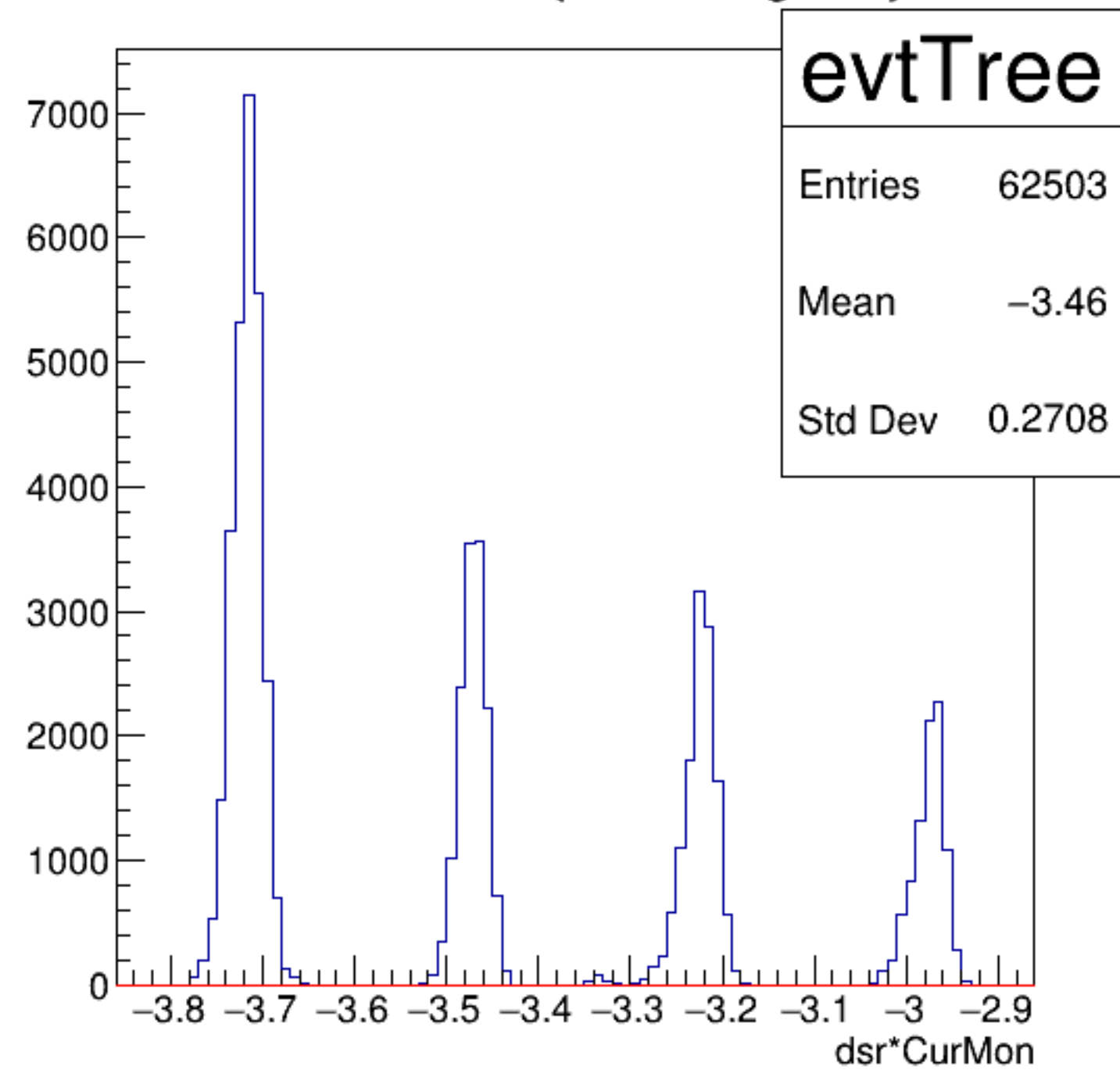


dsr\*CurMon:Entry\$

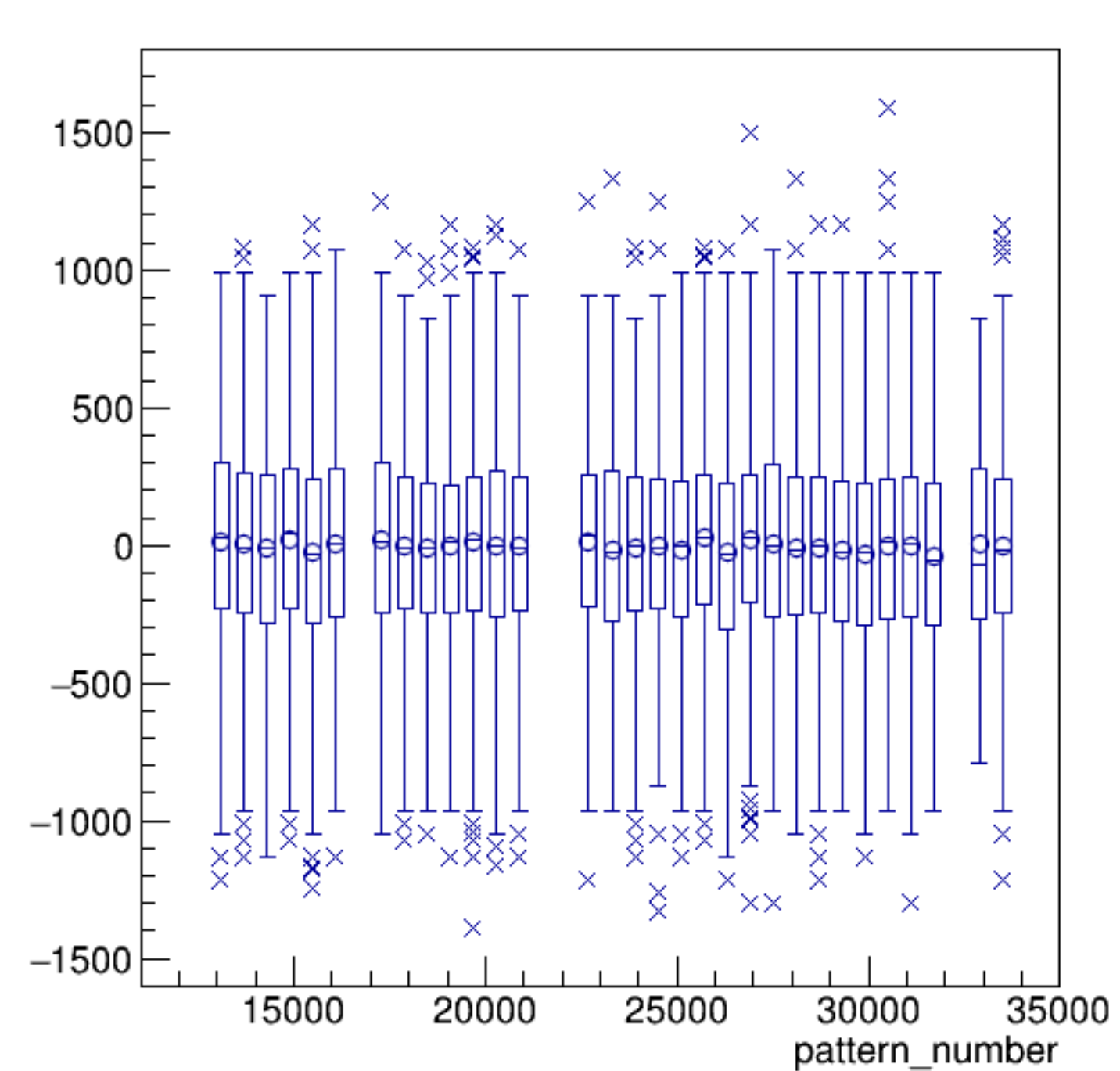


run7330\_000\_maindet\_dsr.png

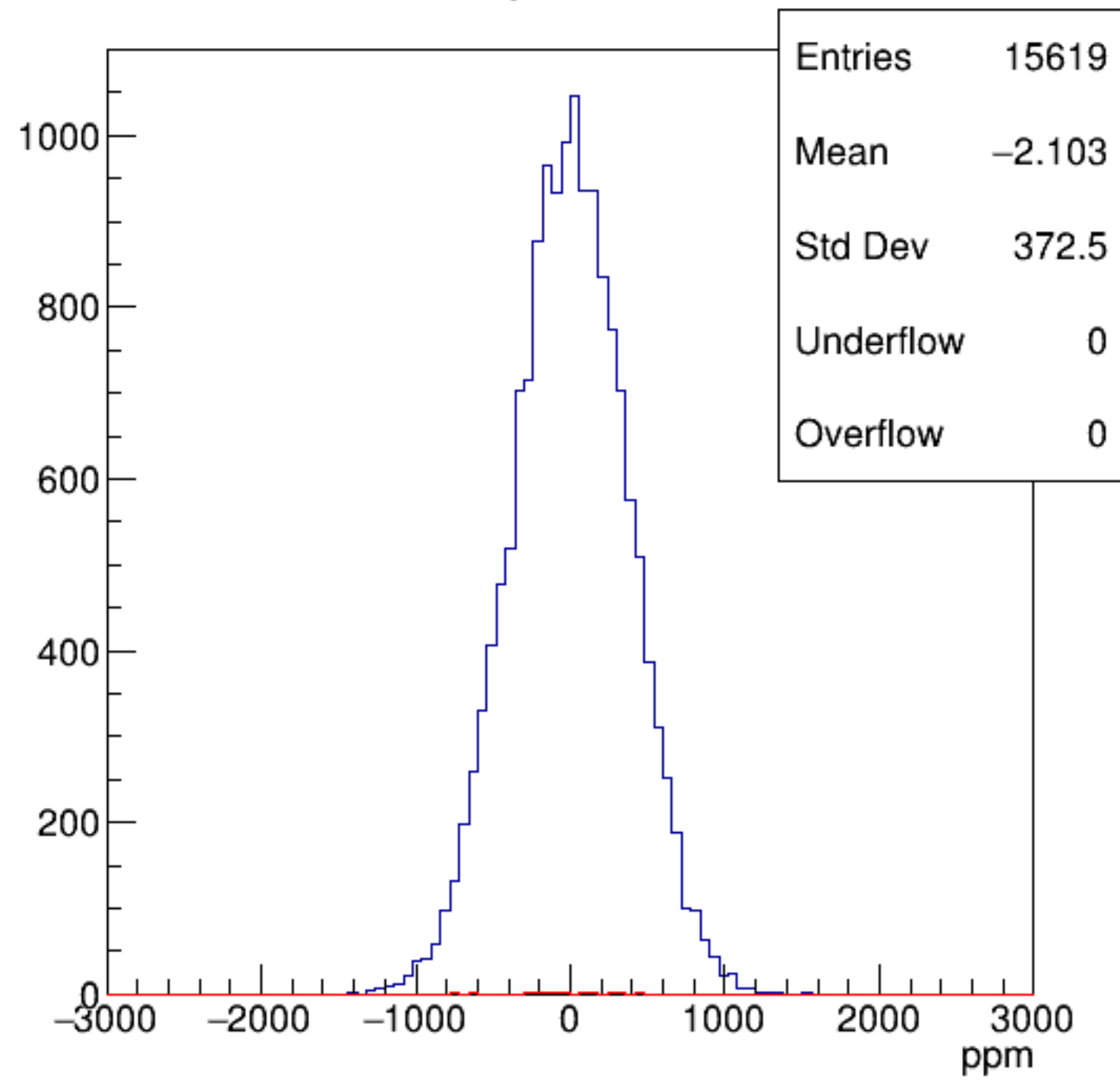
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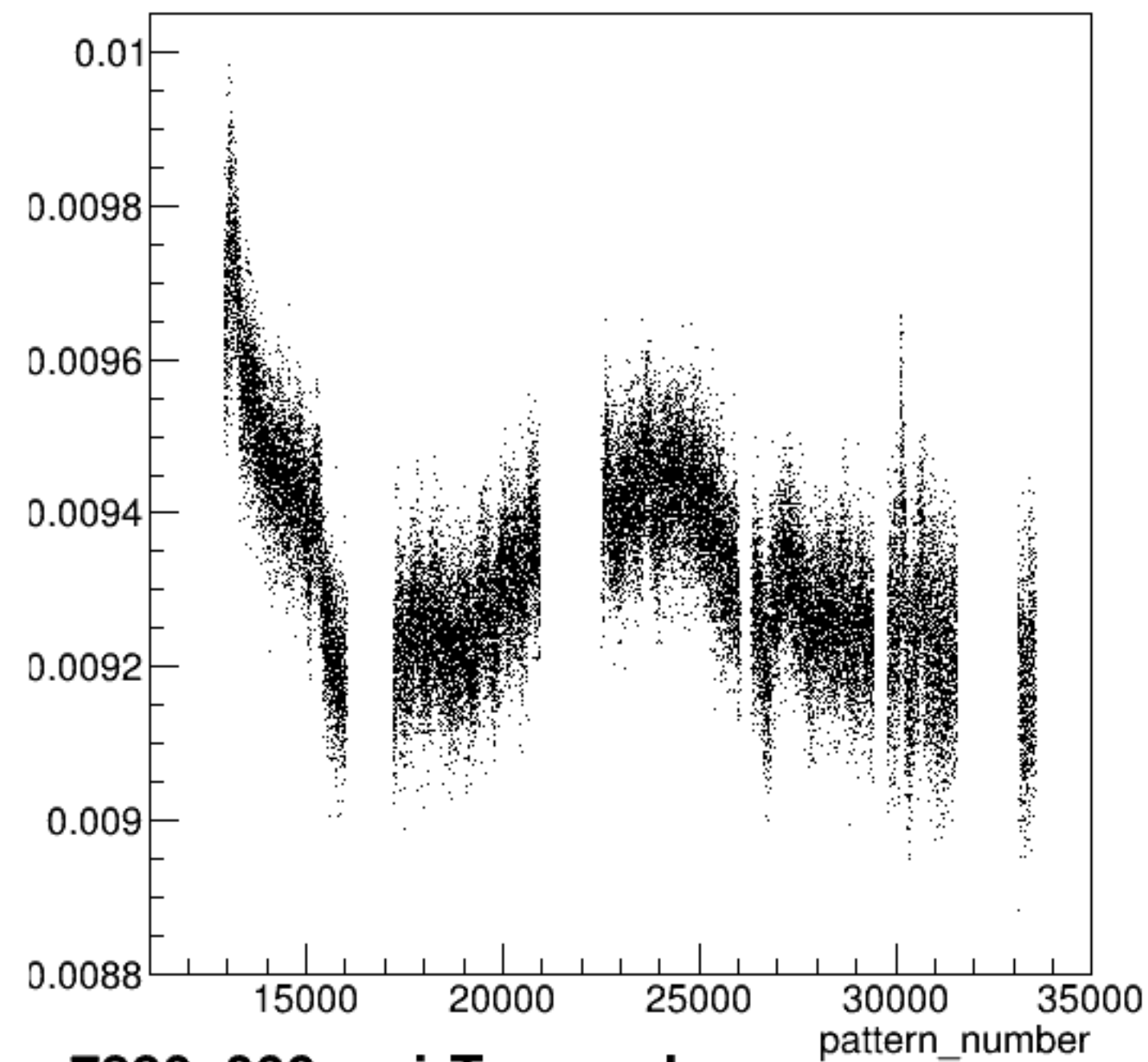
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asym\_dsr

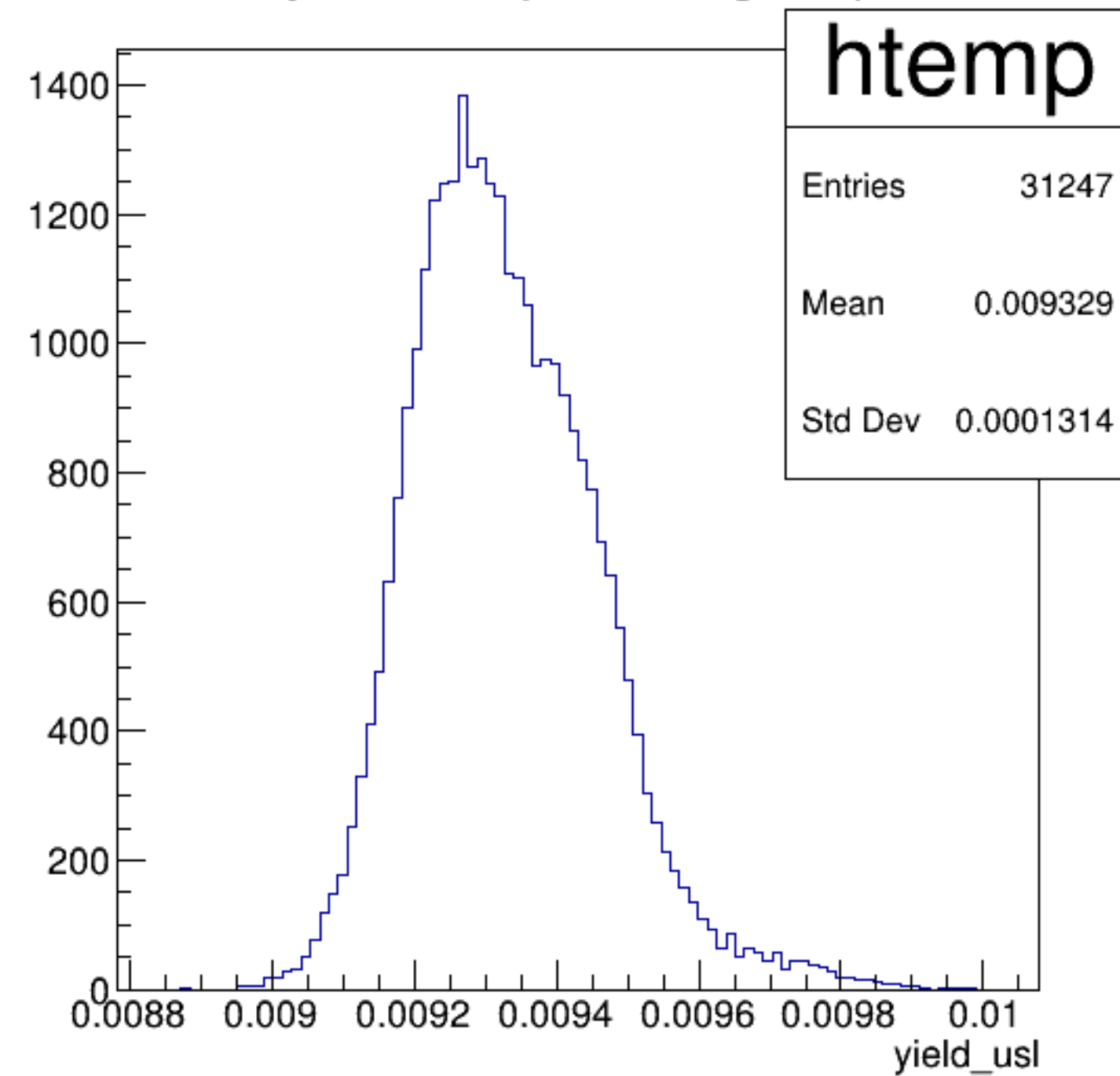


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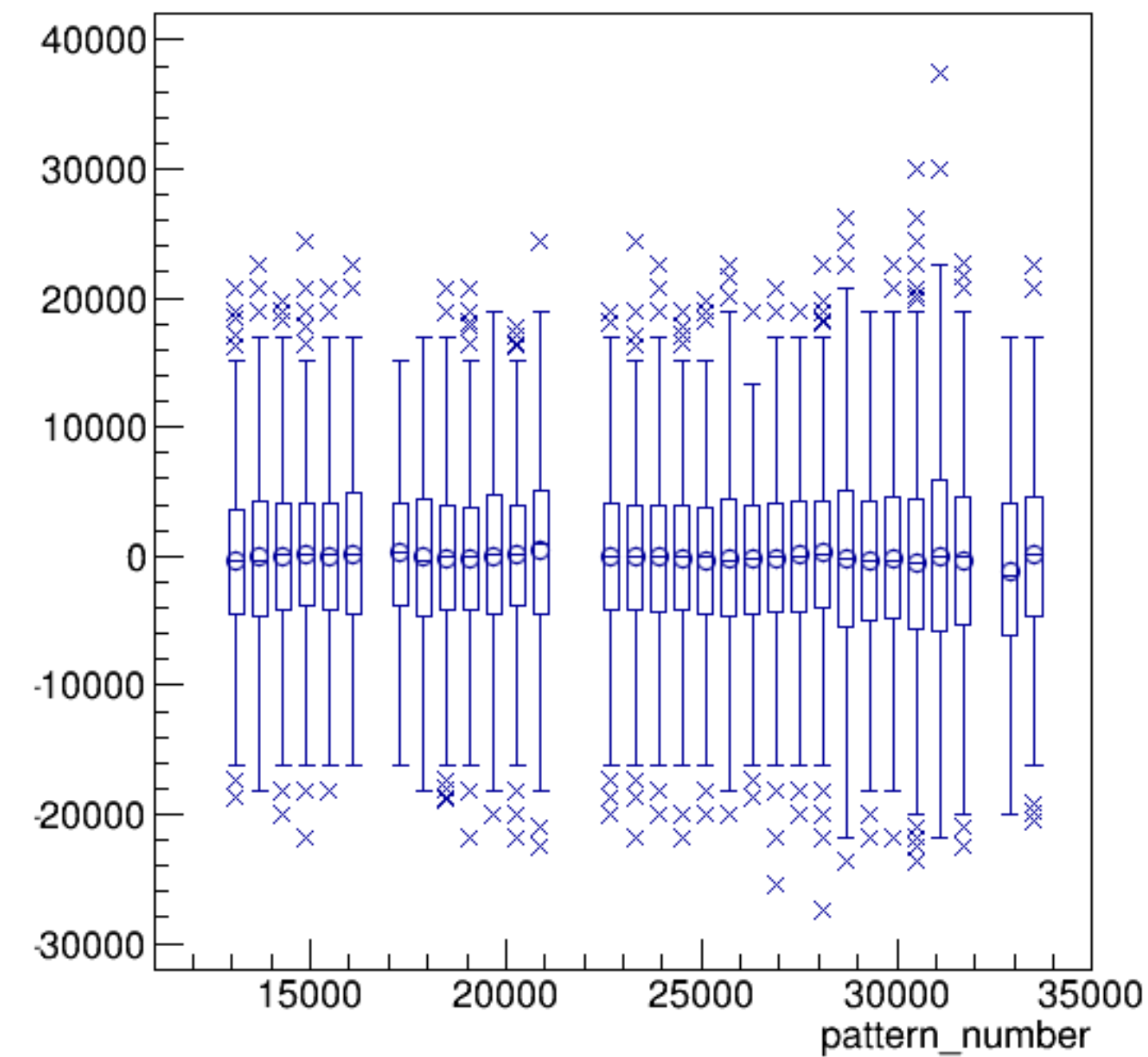


run7330\_000\_pairTree\_usl.png

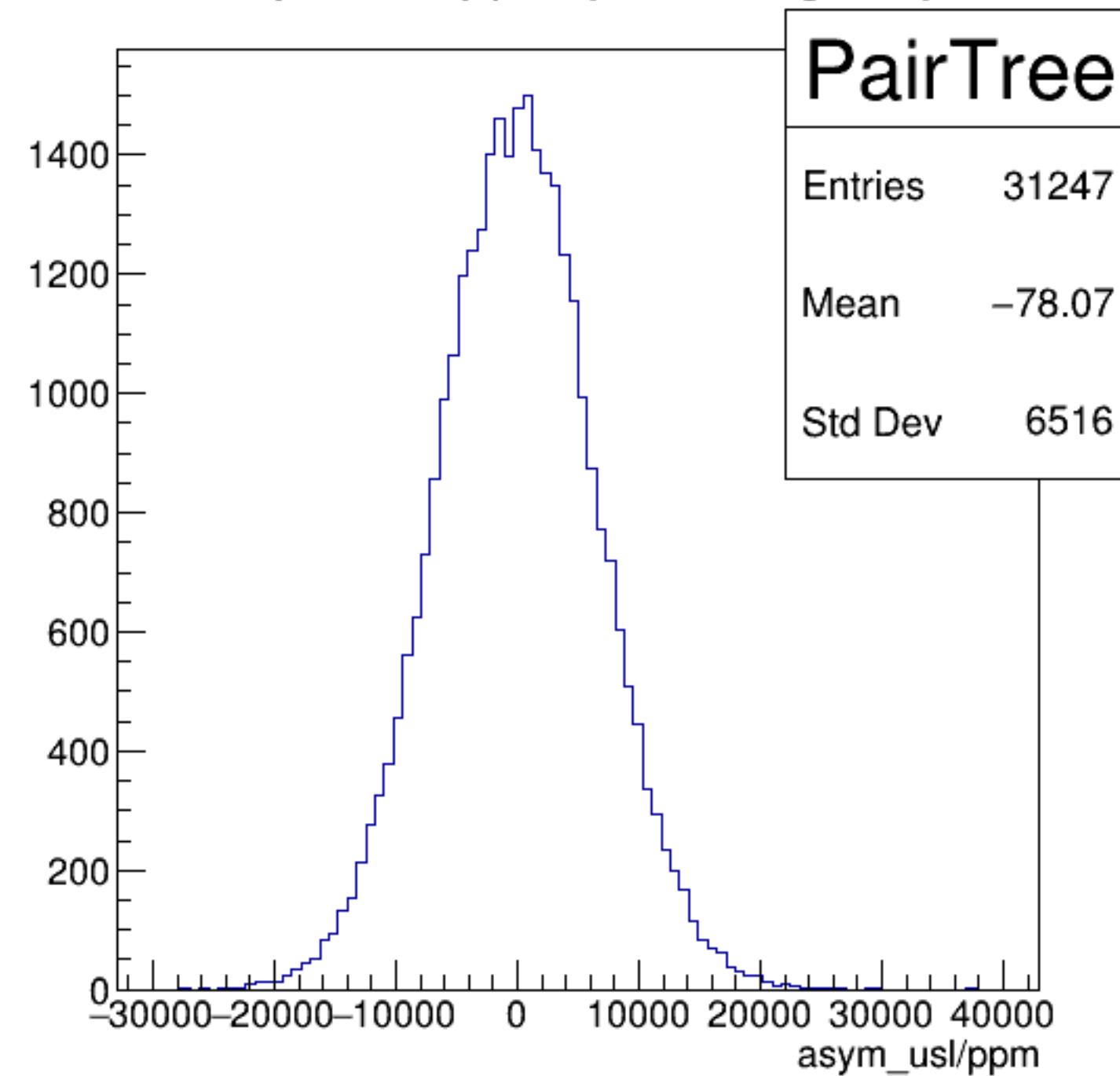
yield\_usl {ErrorFlag==0}



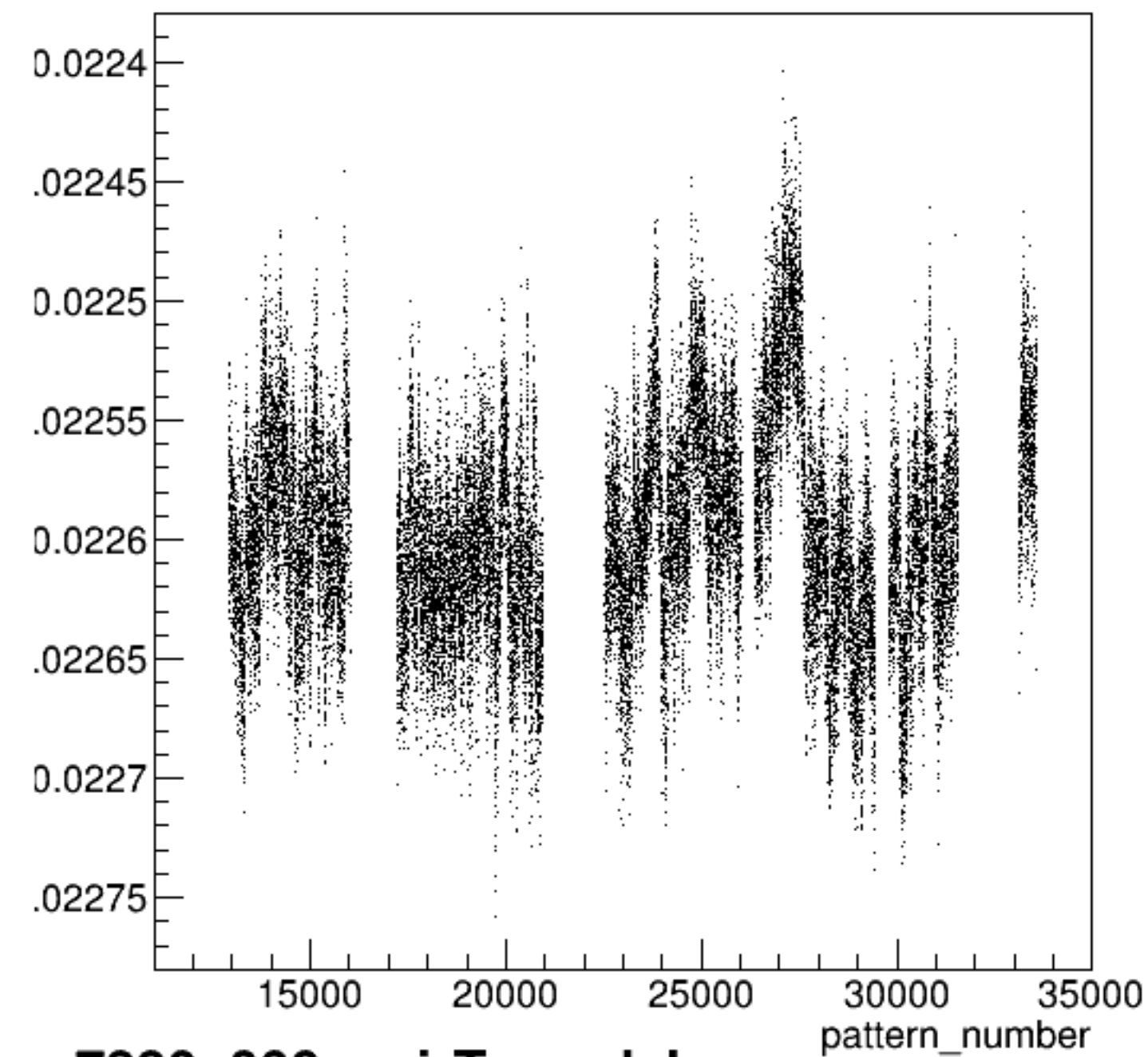
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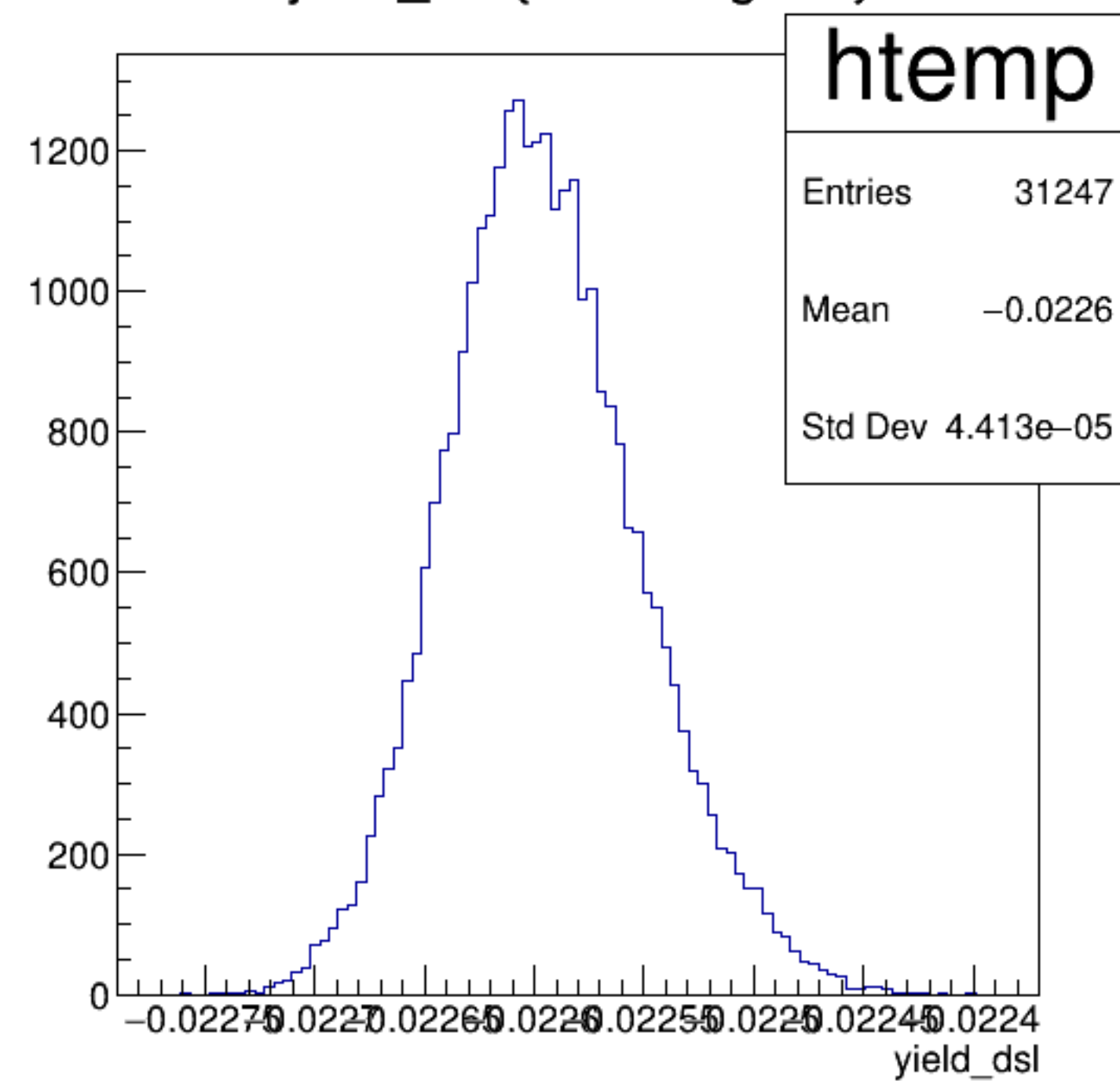
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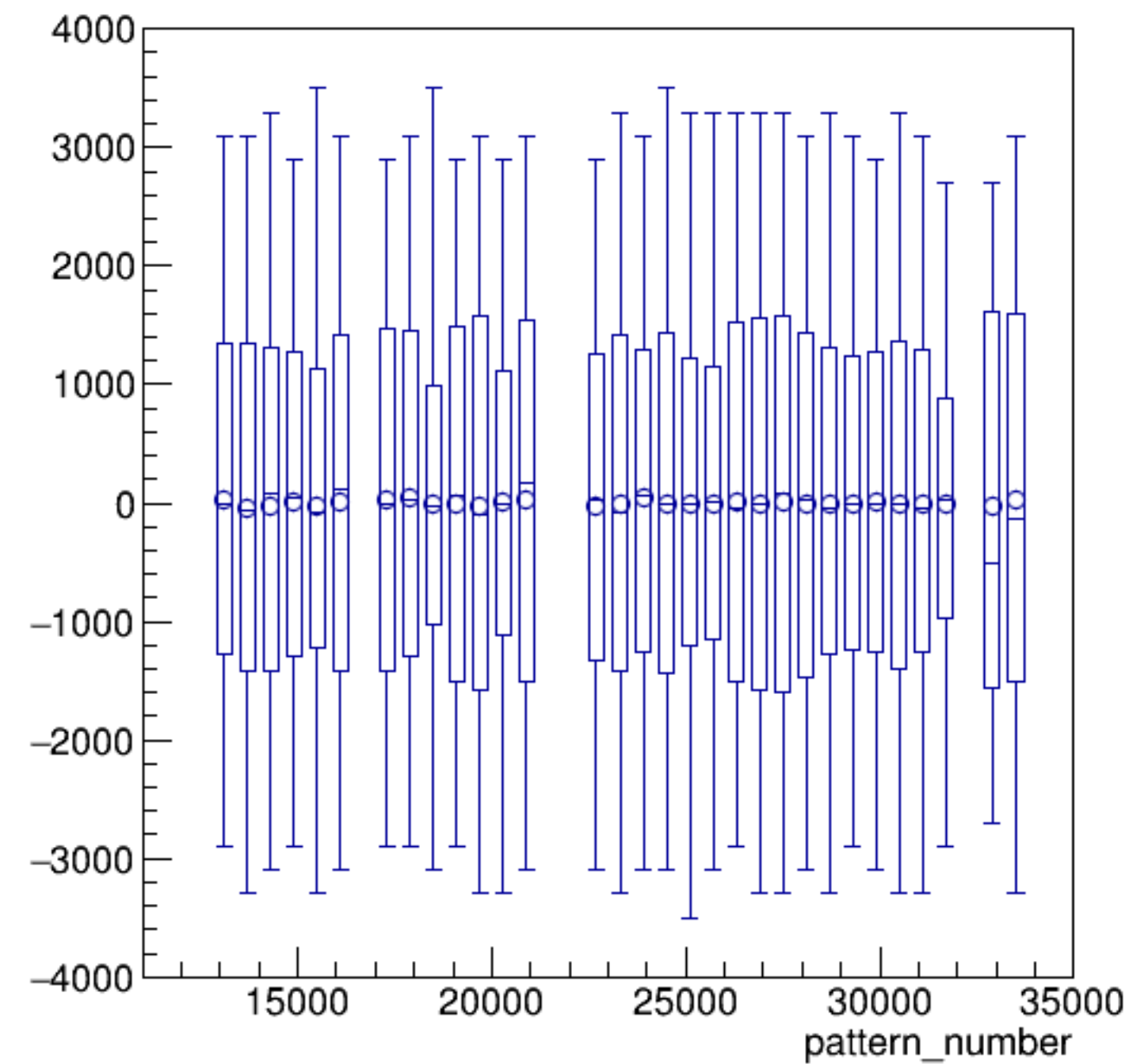
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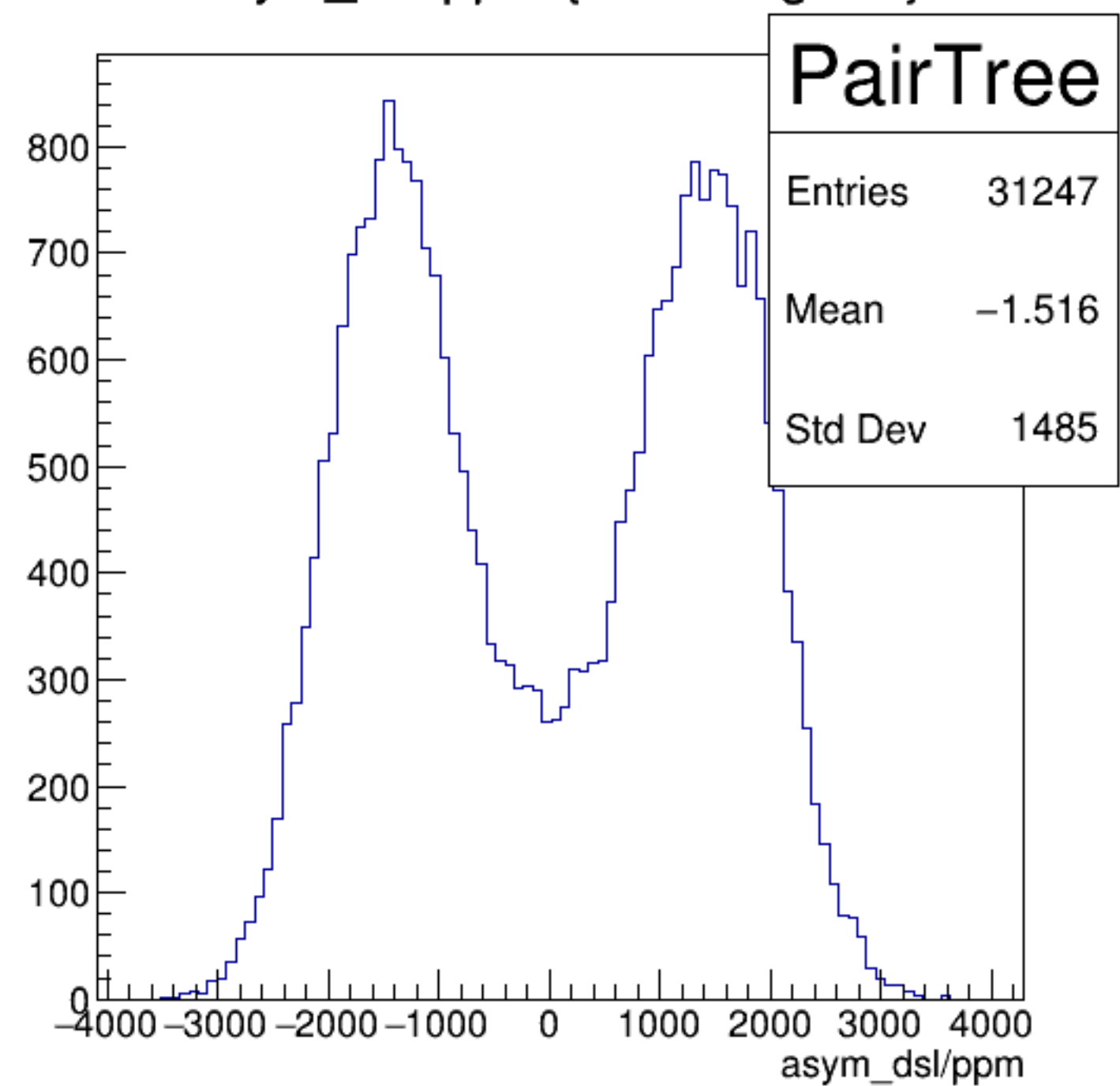
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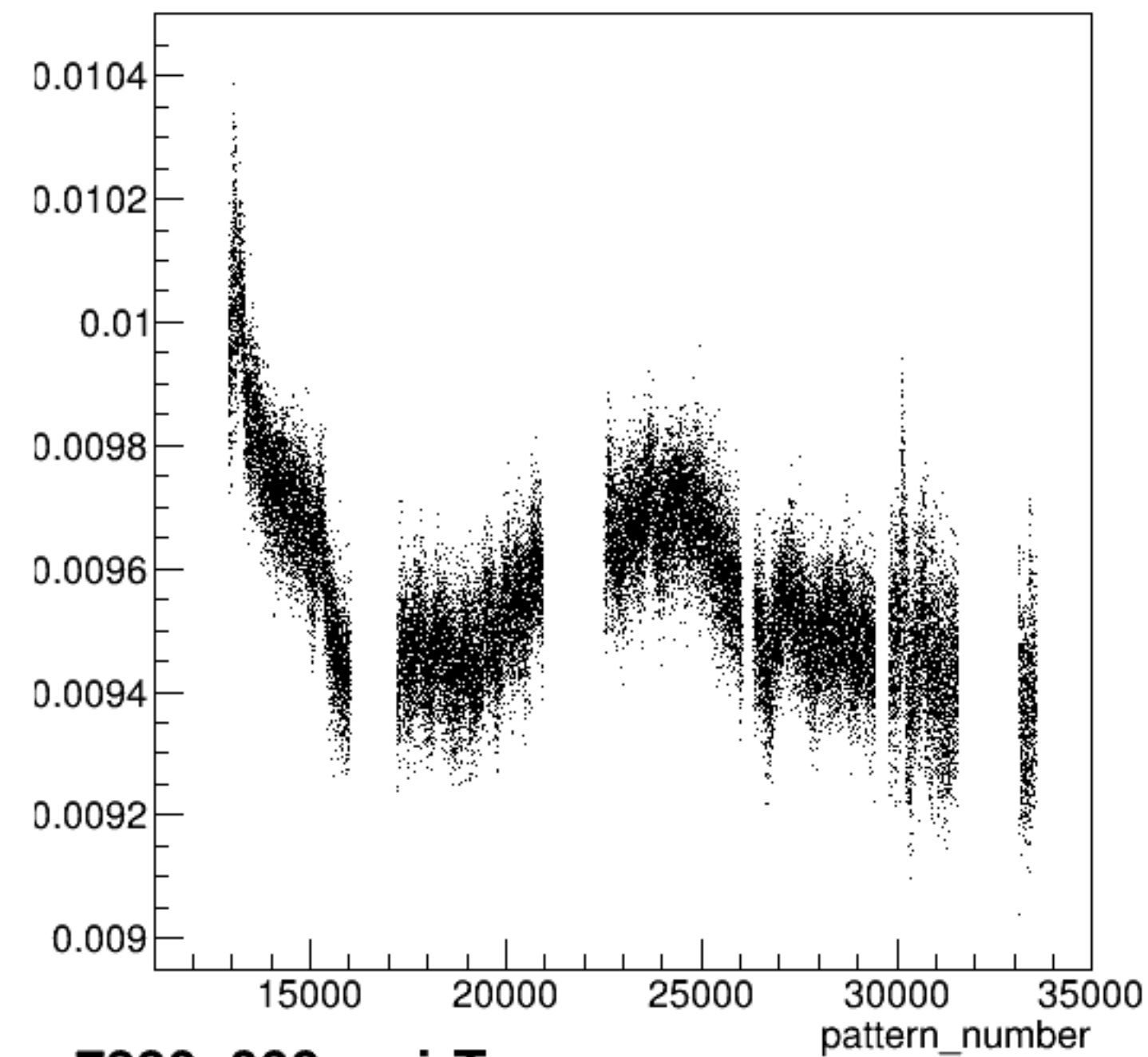
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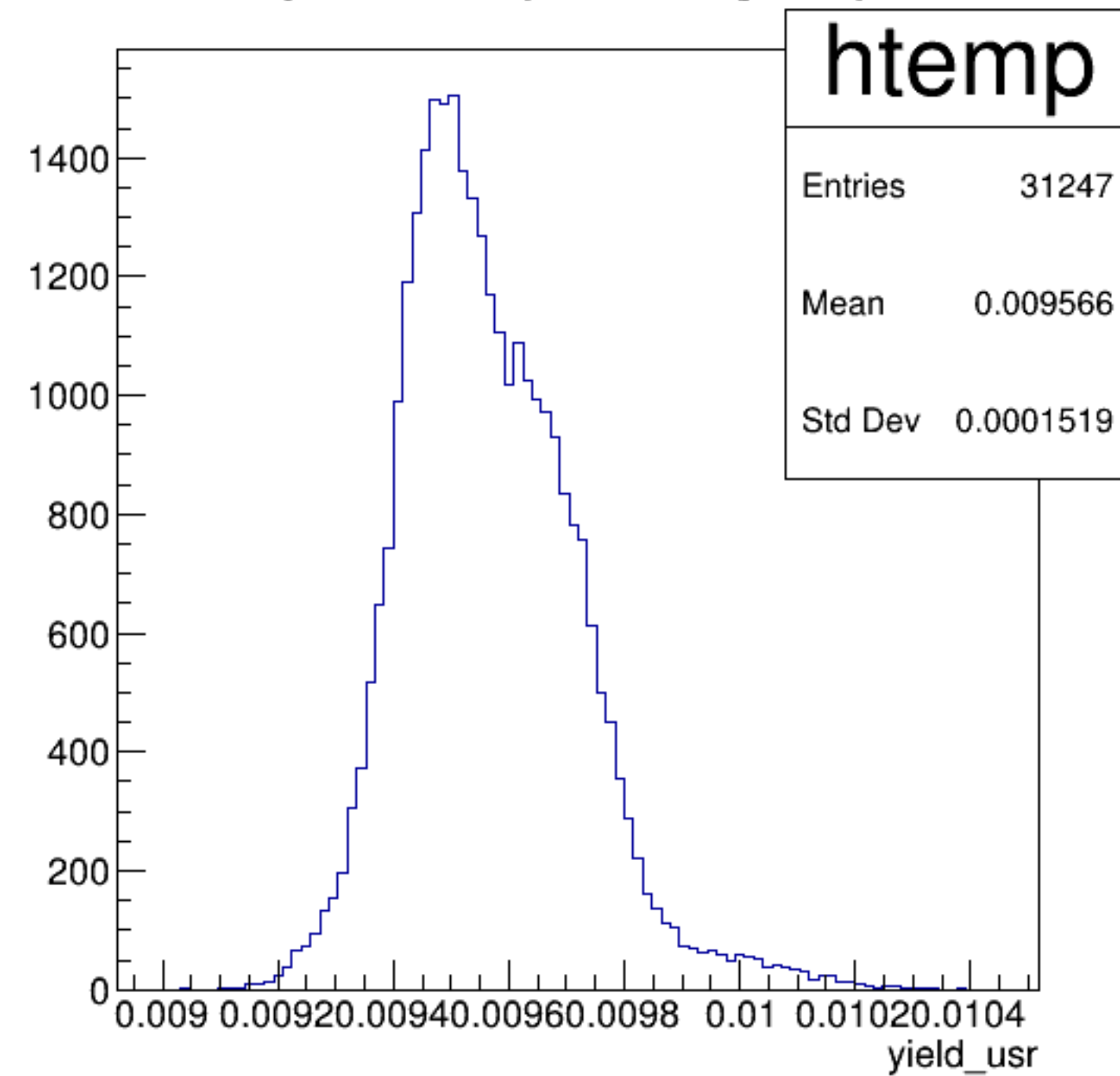


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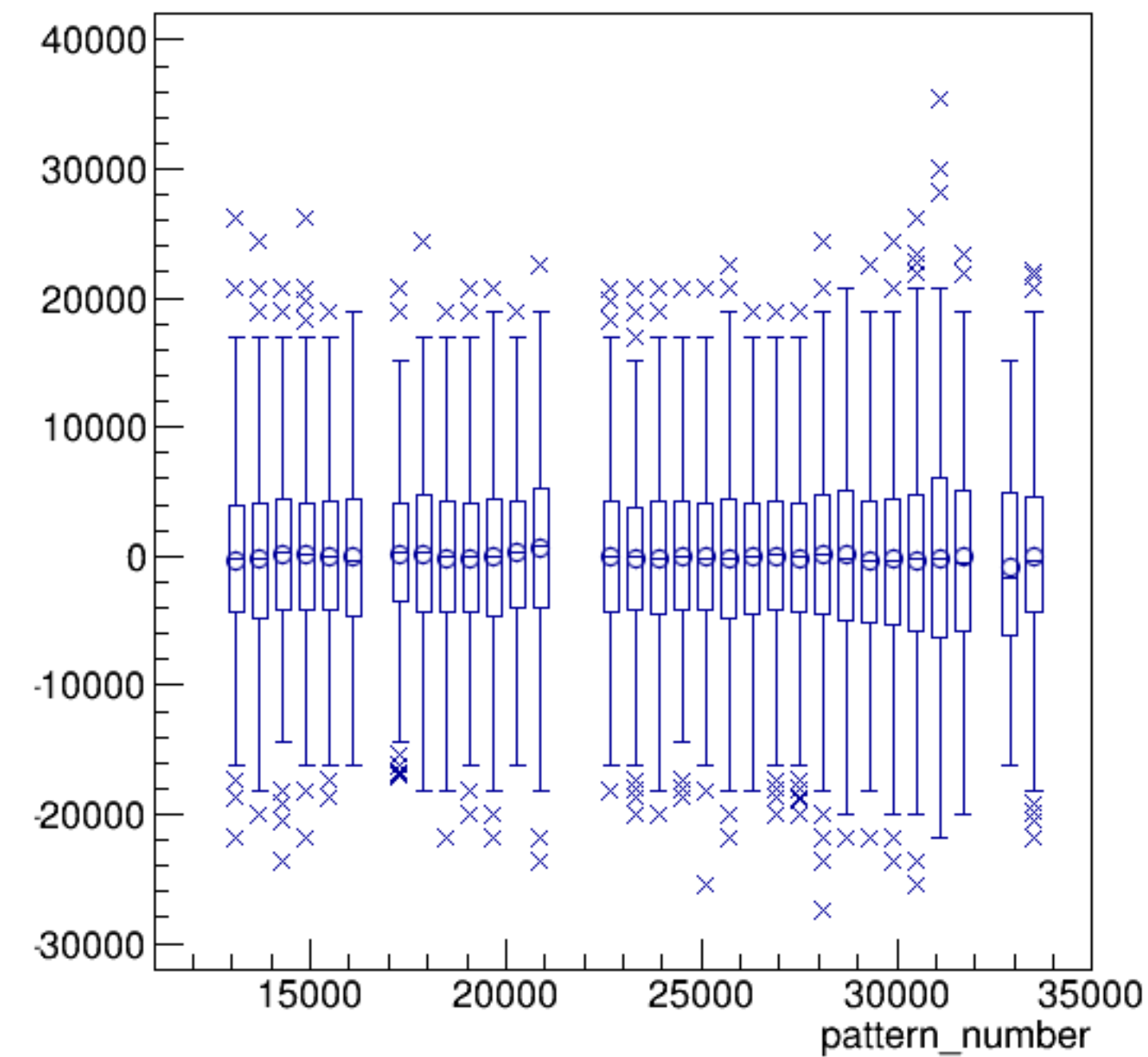


run7330\_000\_pairTree\_usr.png

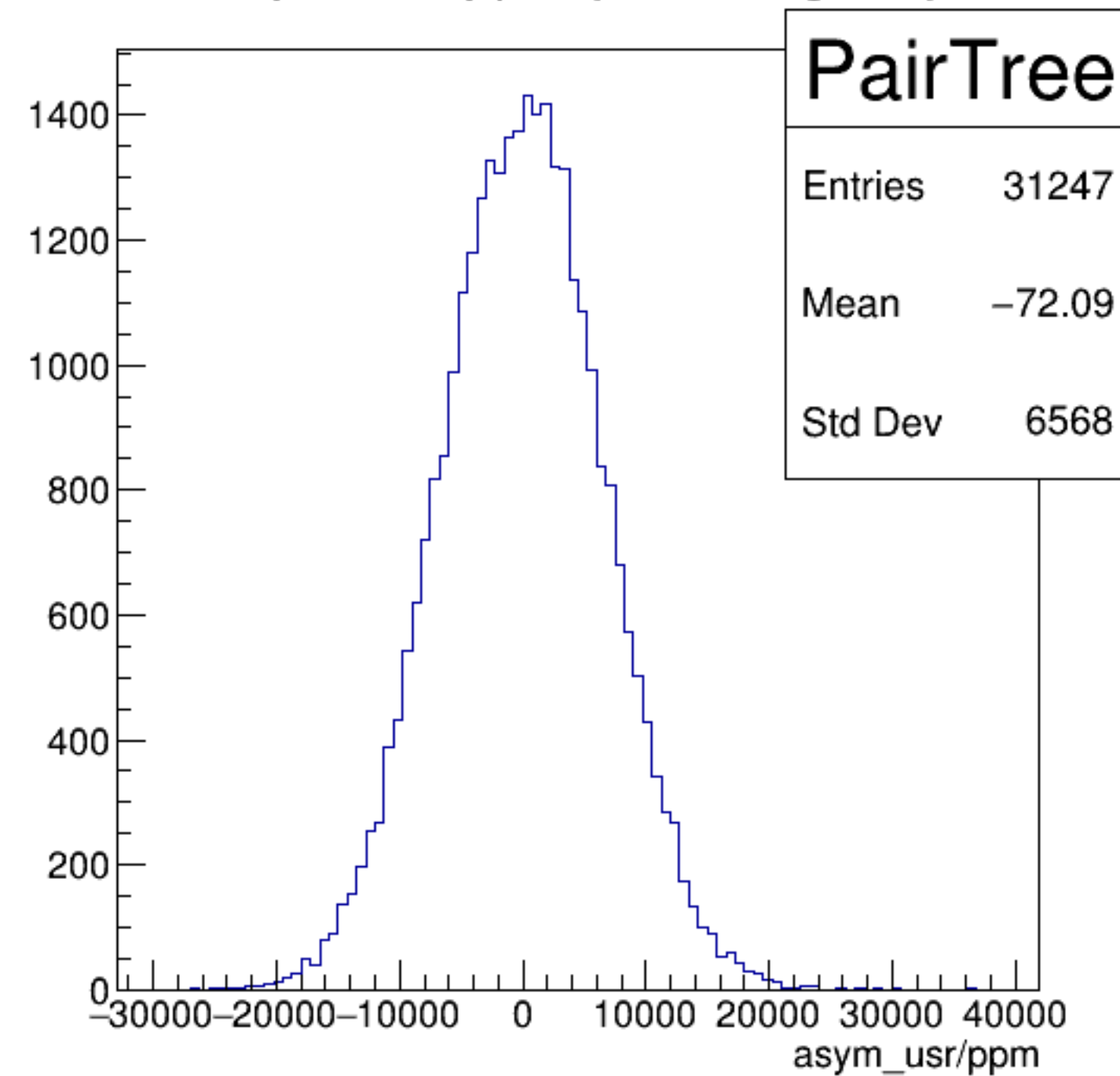
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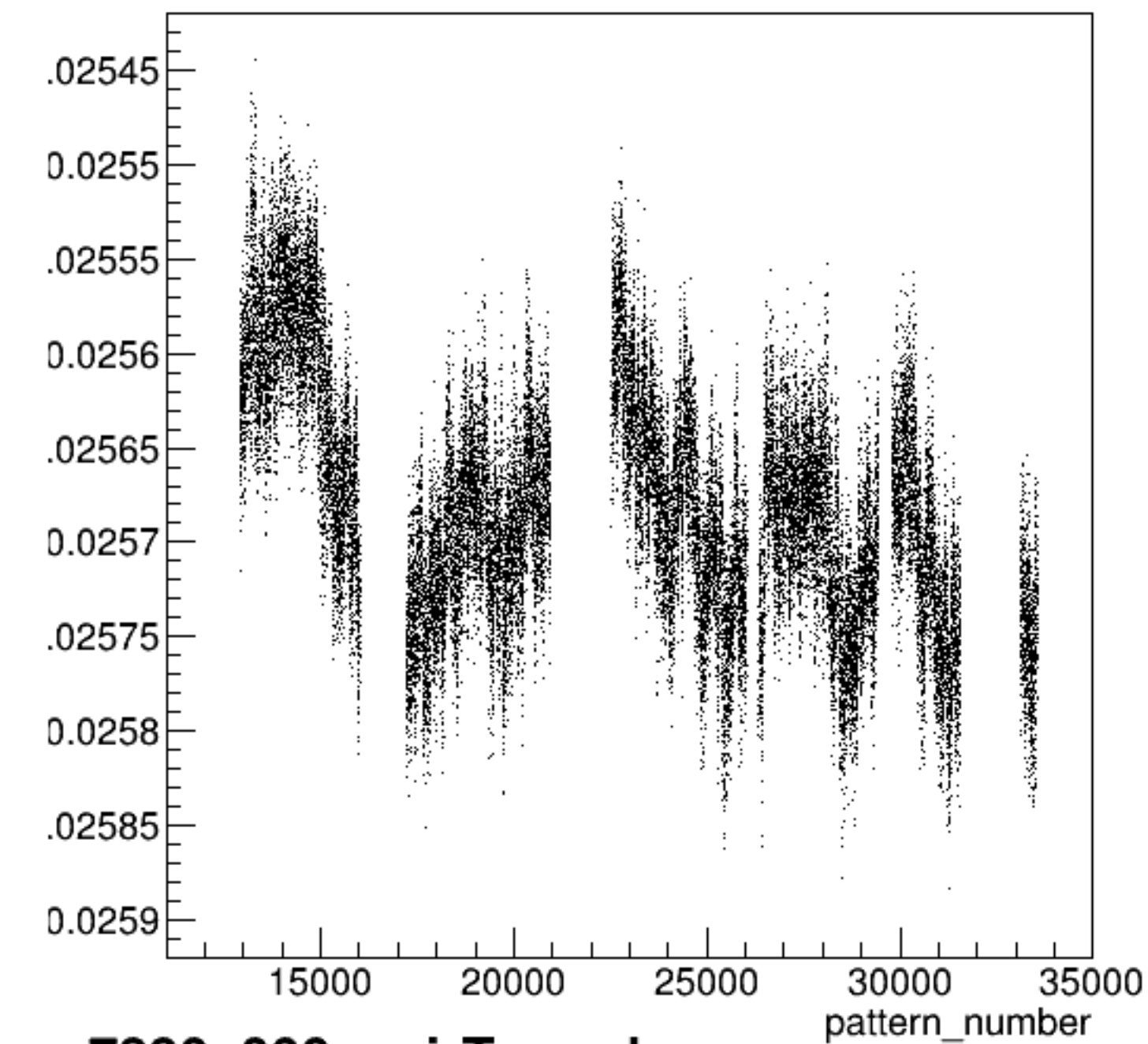
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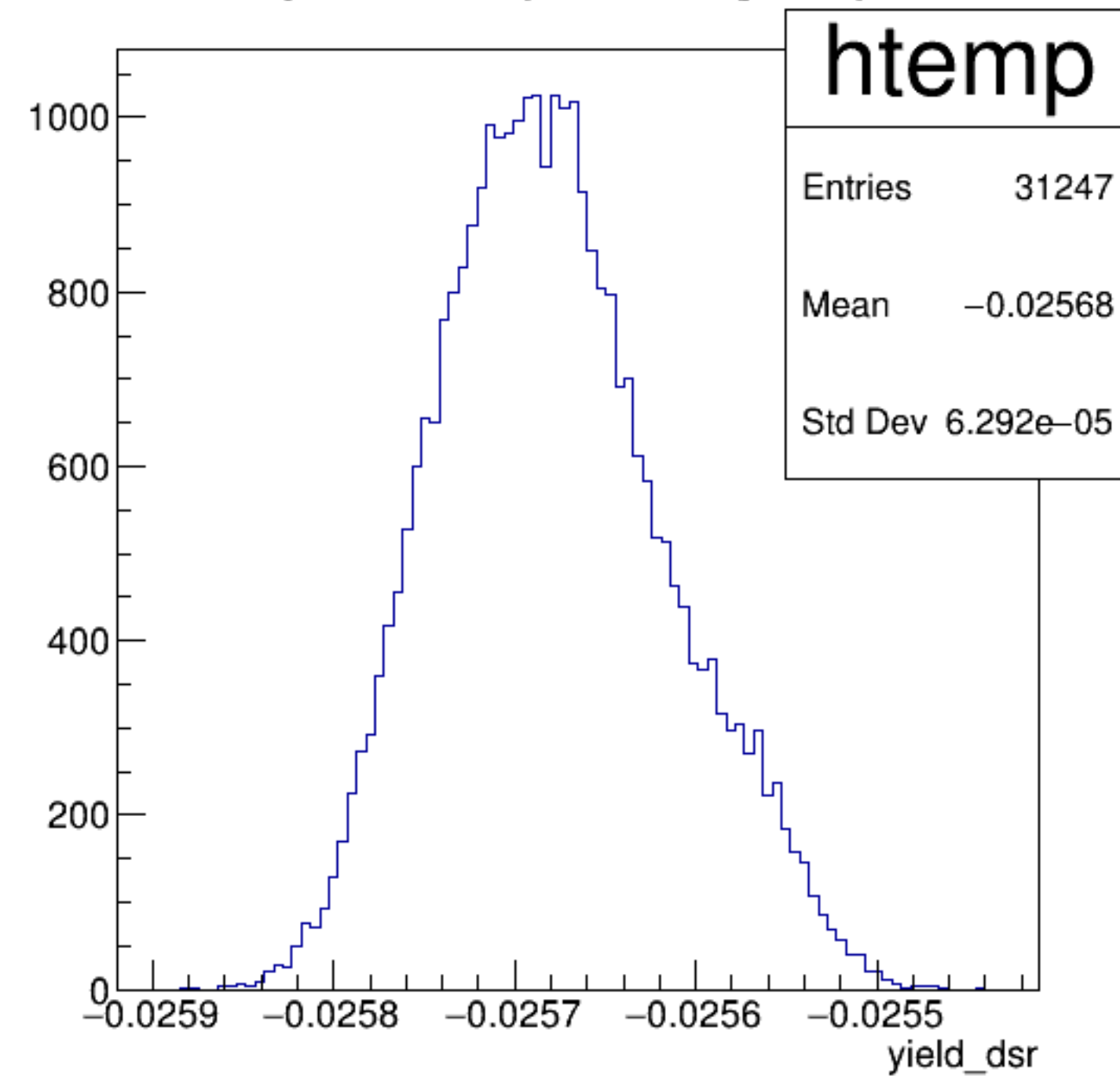
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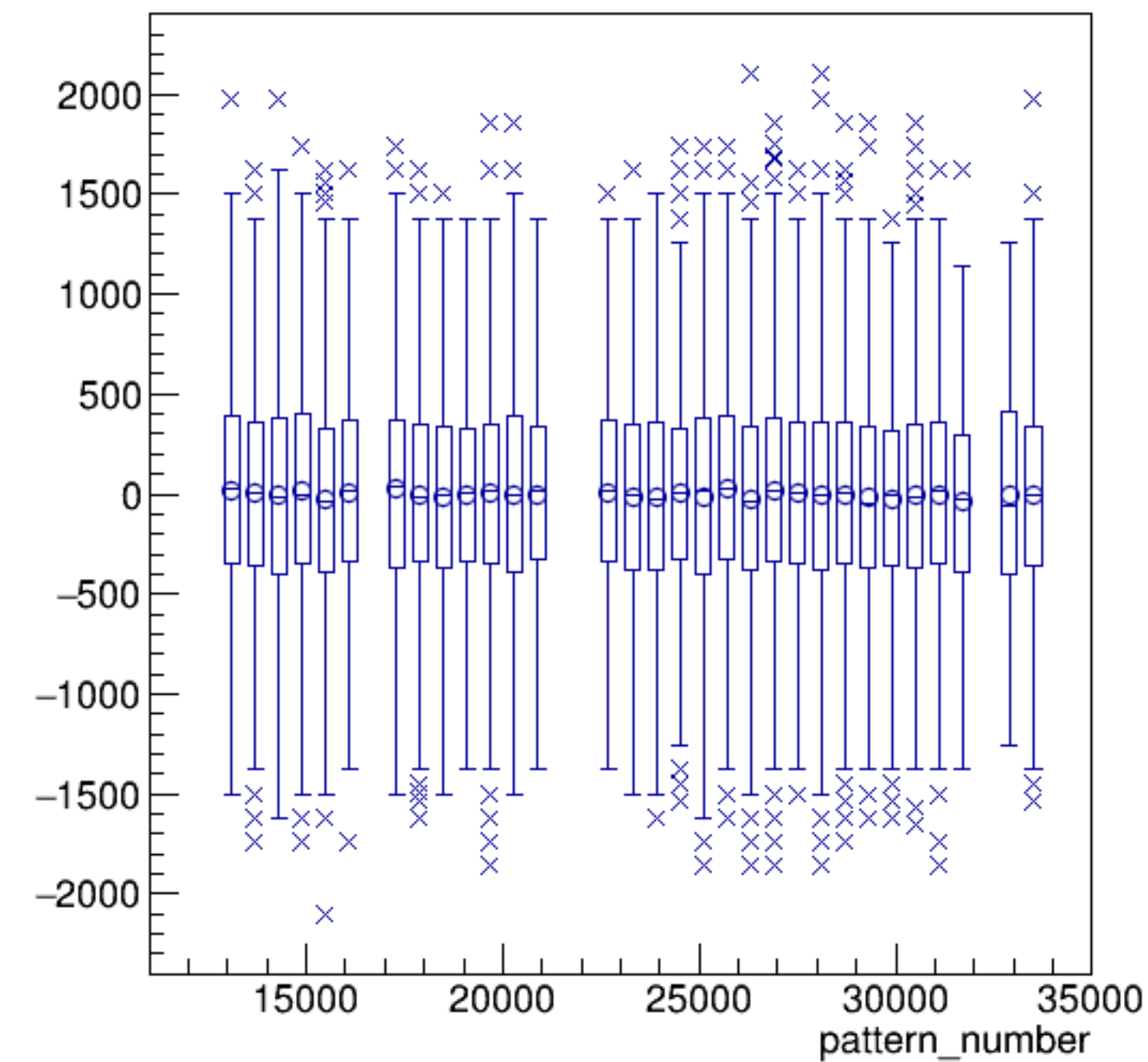
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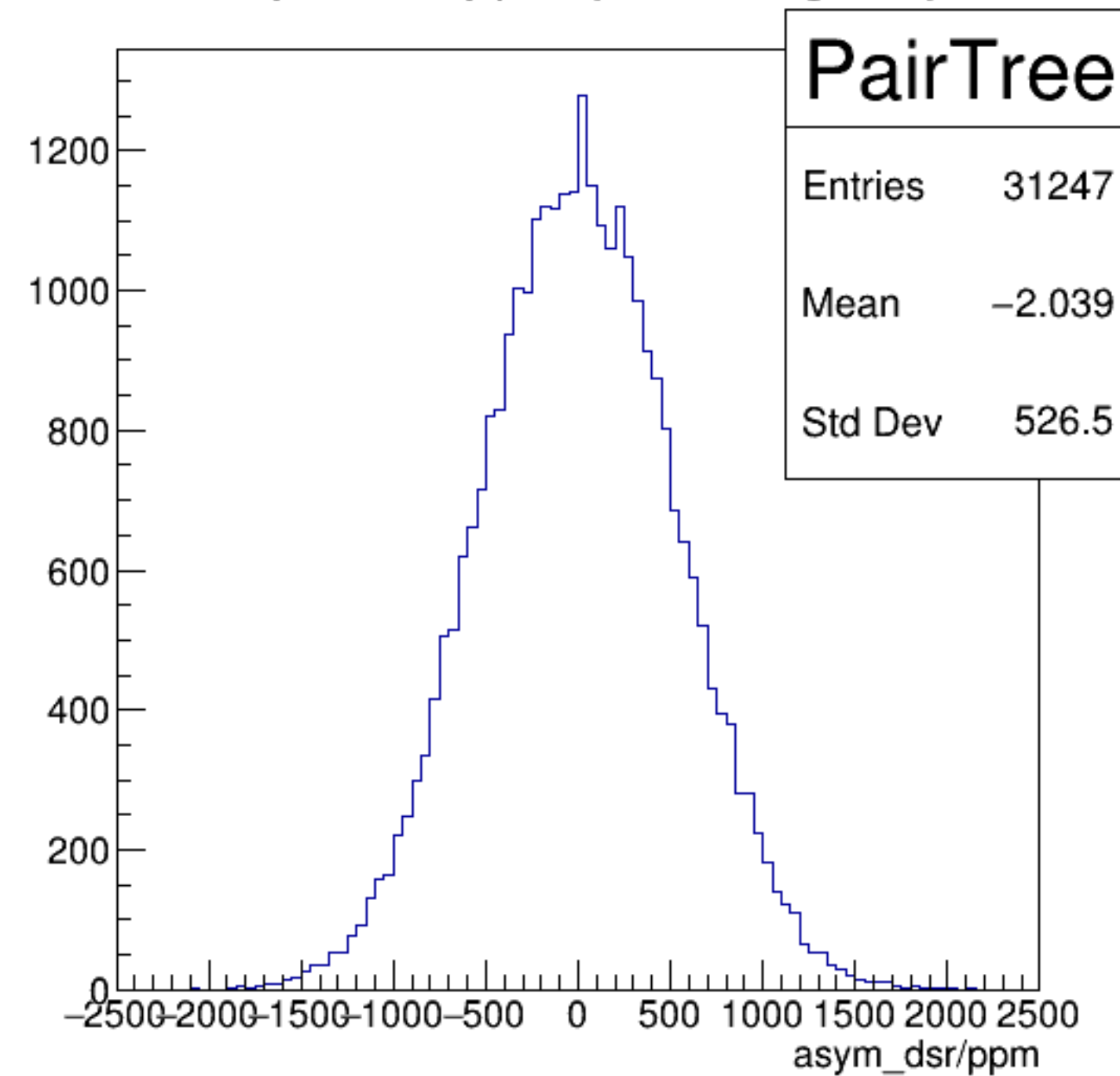
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asym\_dsr/ppm:pattern\_number {ErrorFlag==0}

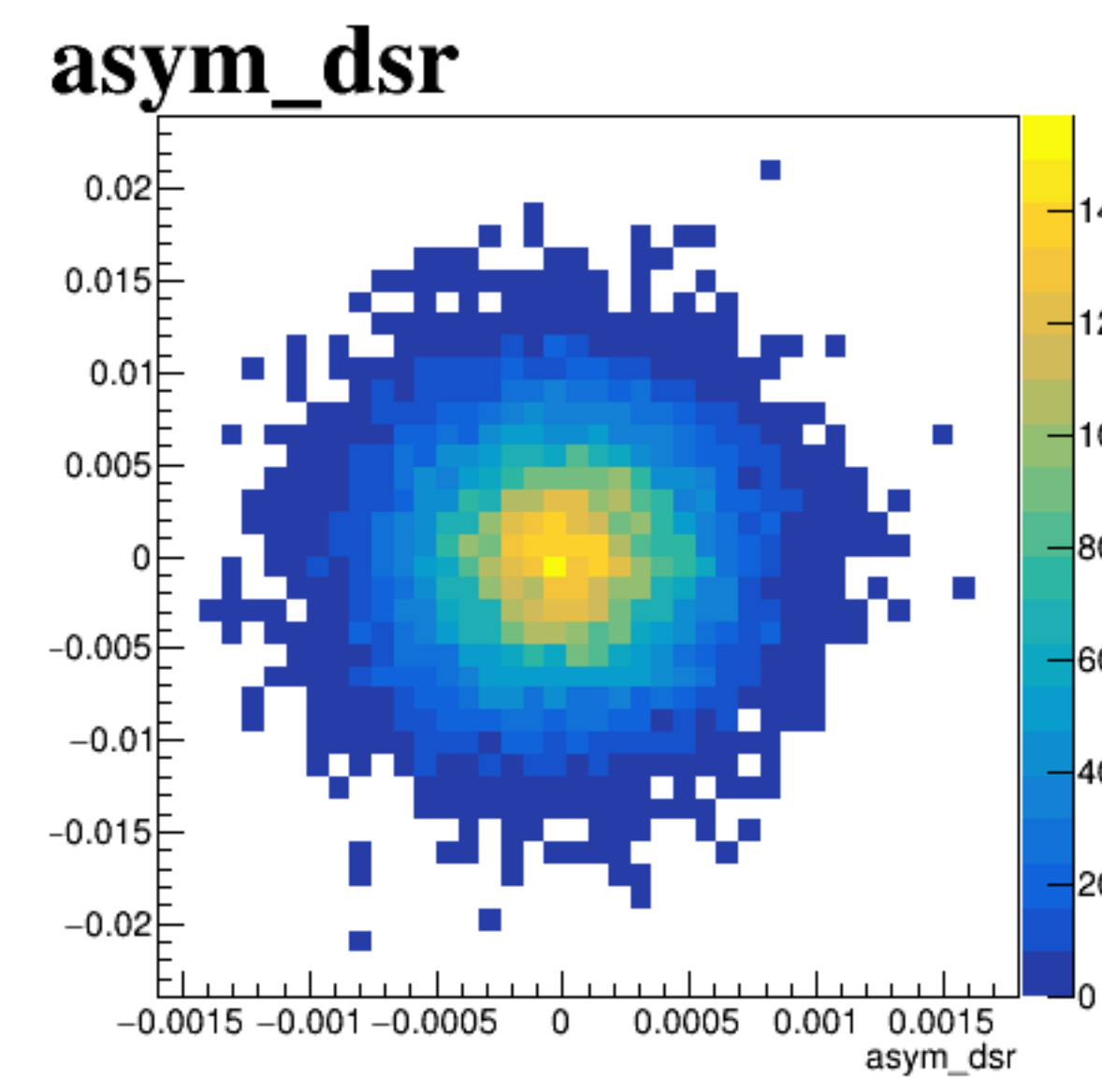
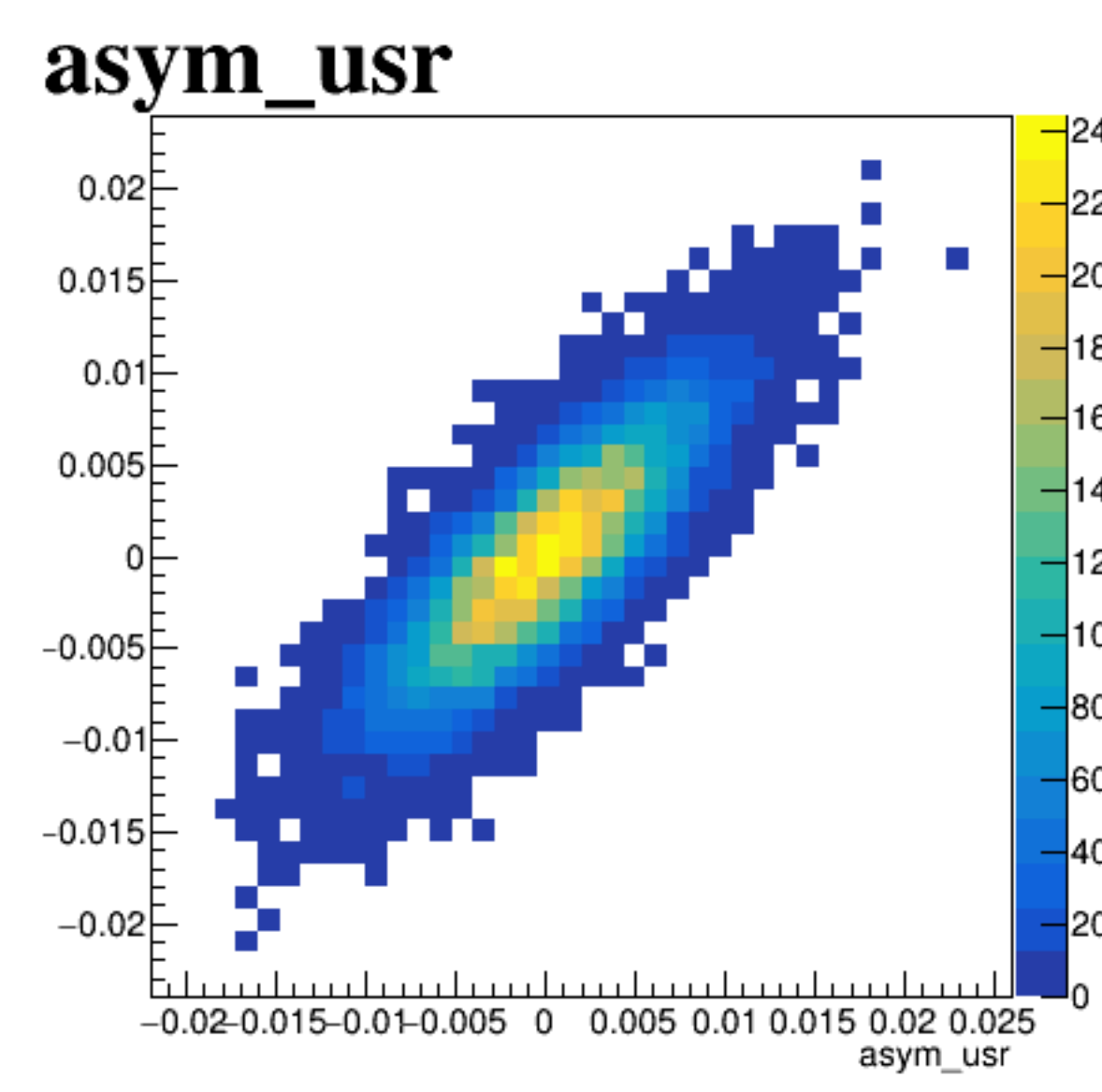
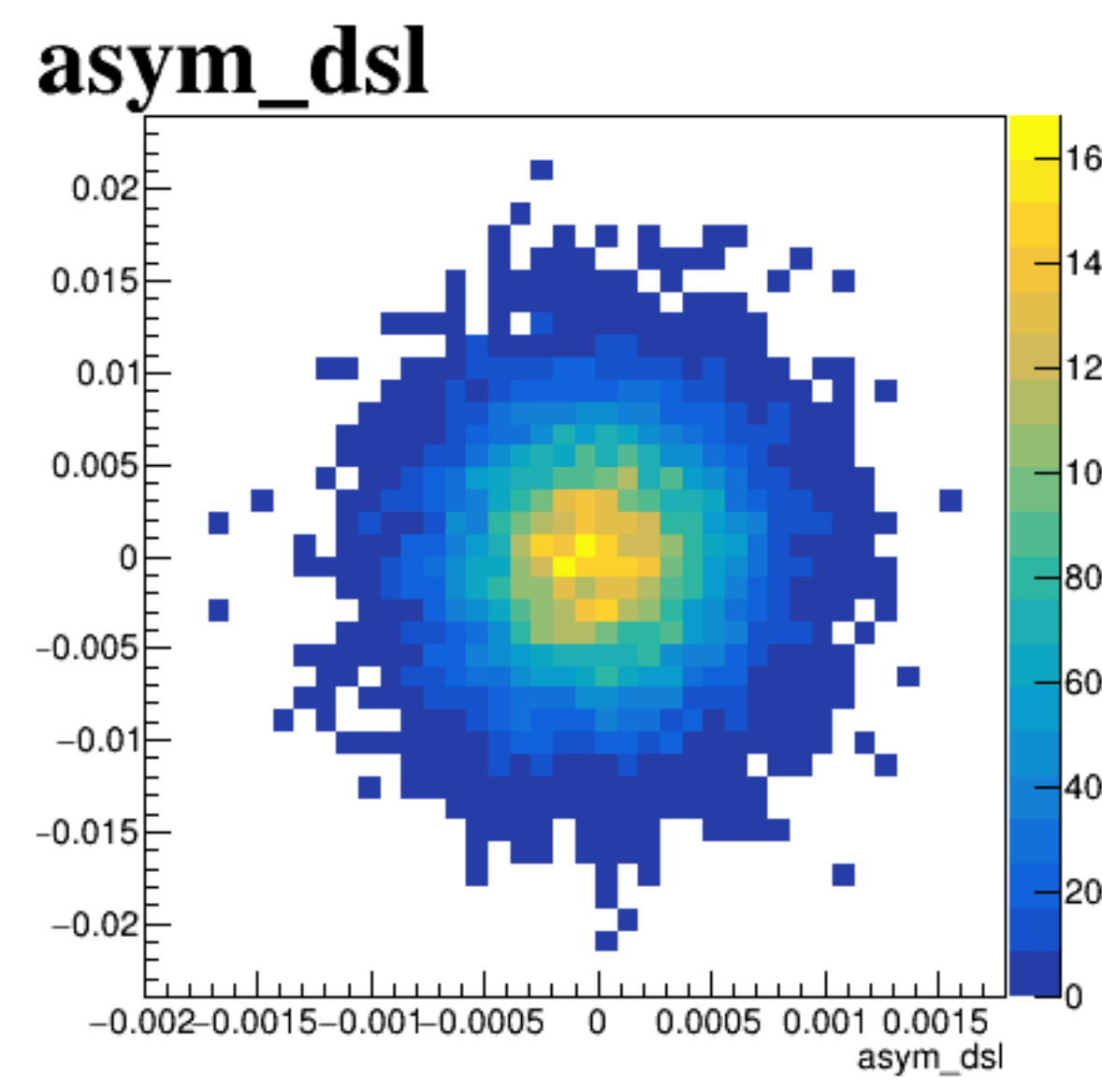
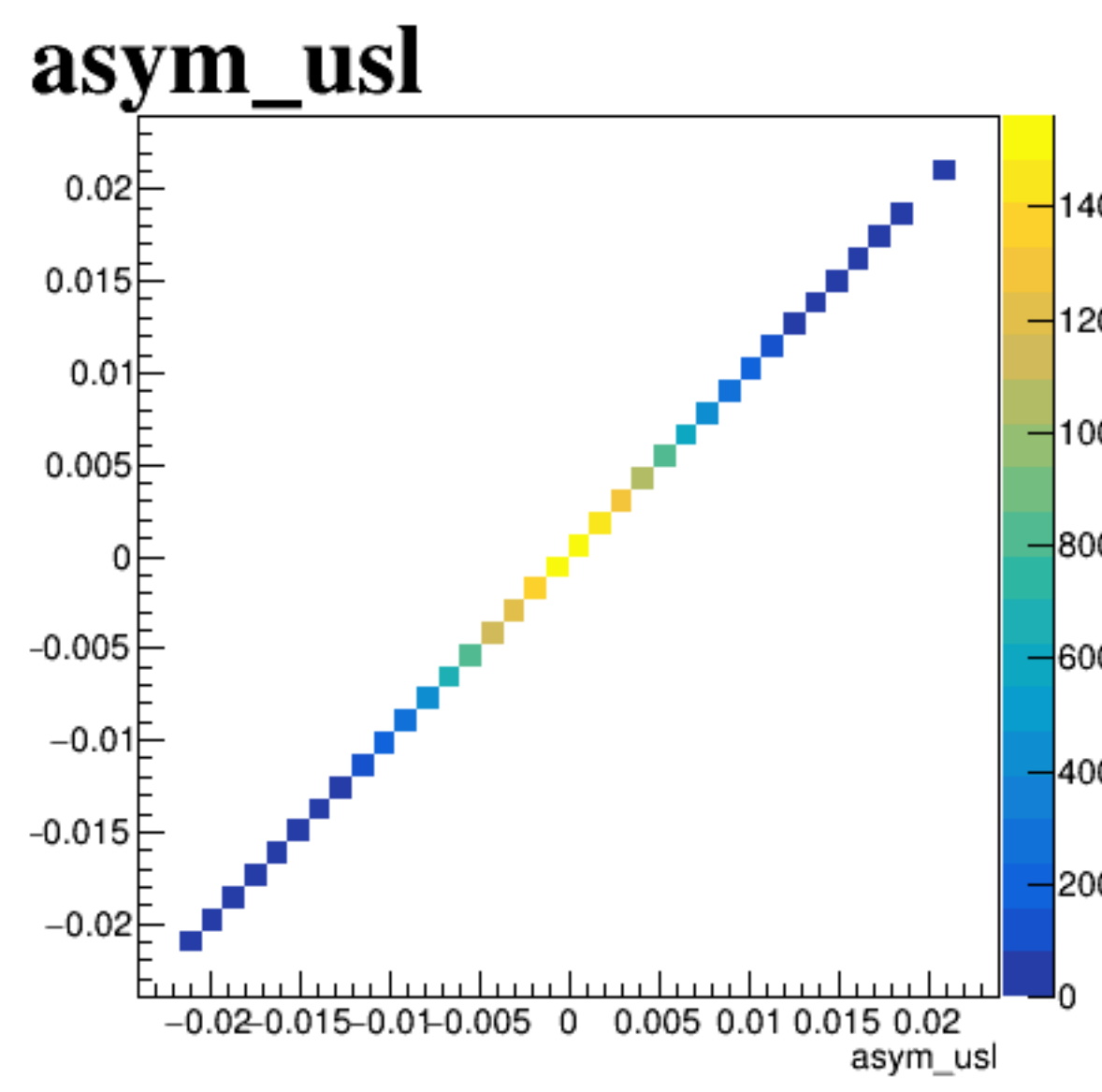


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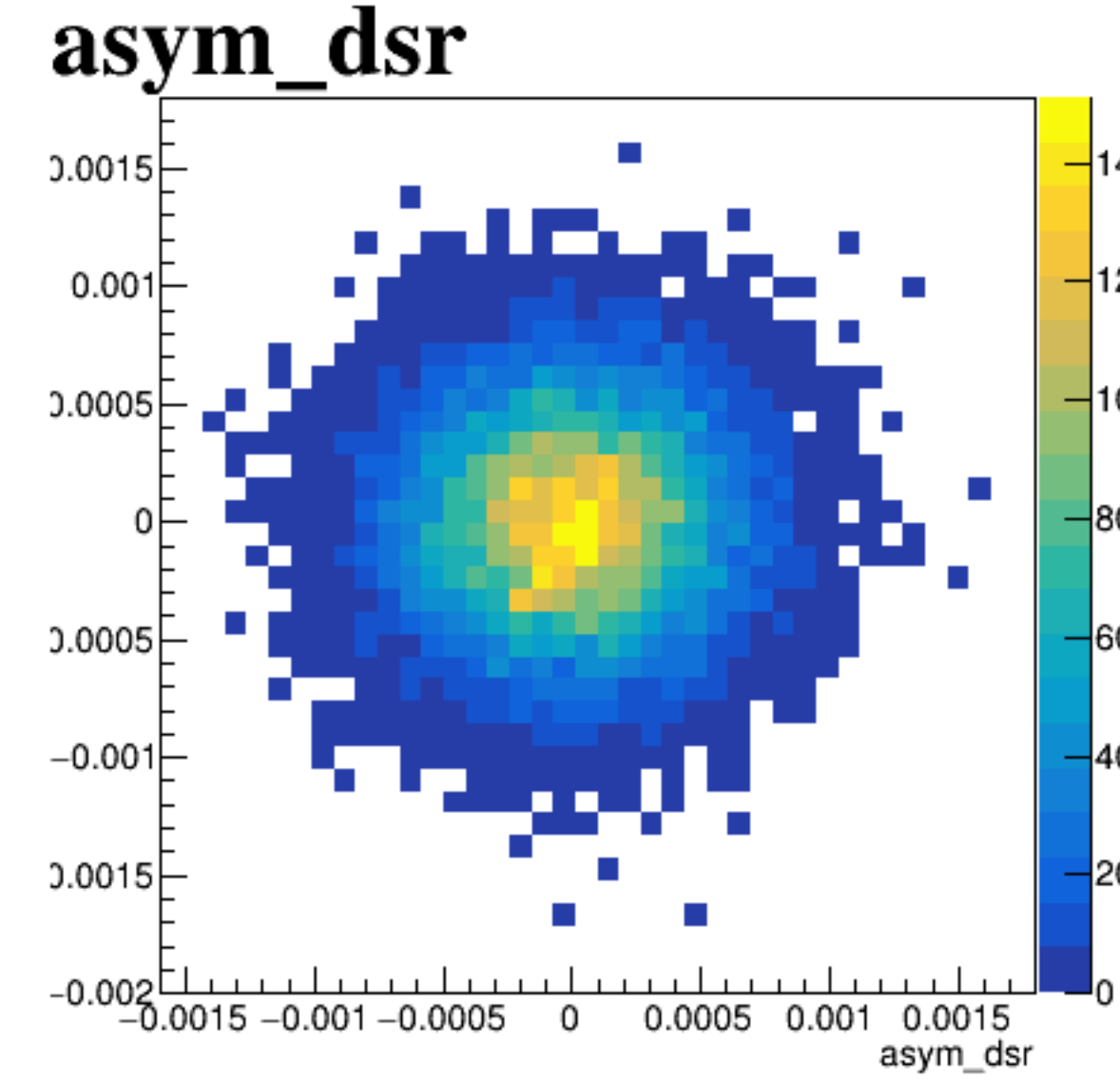
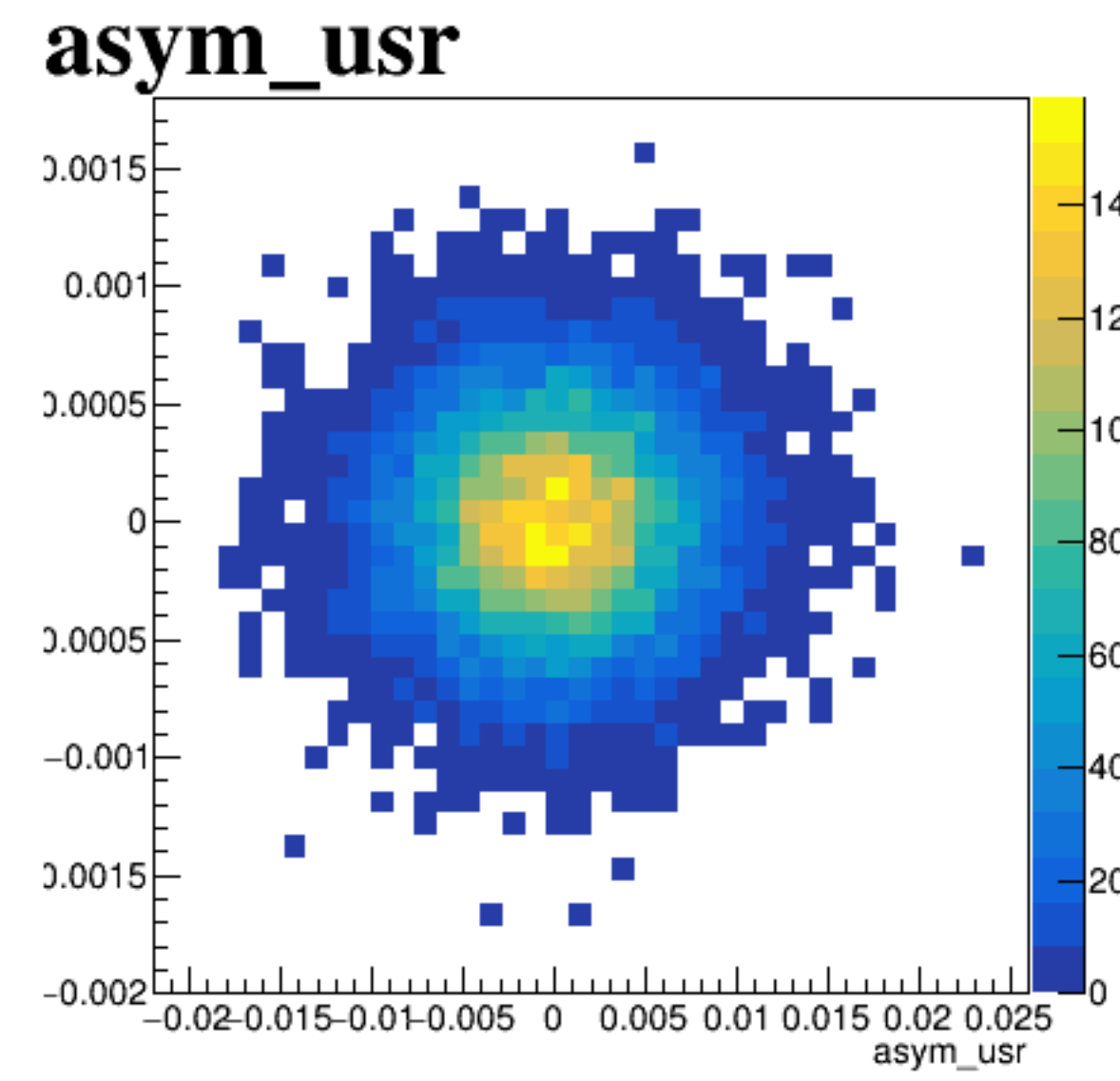
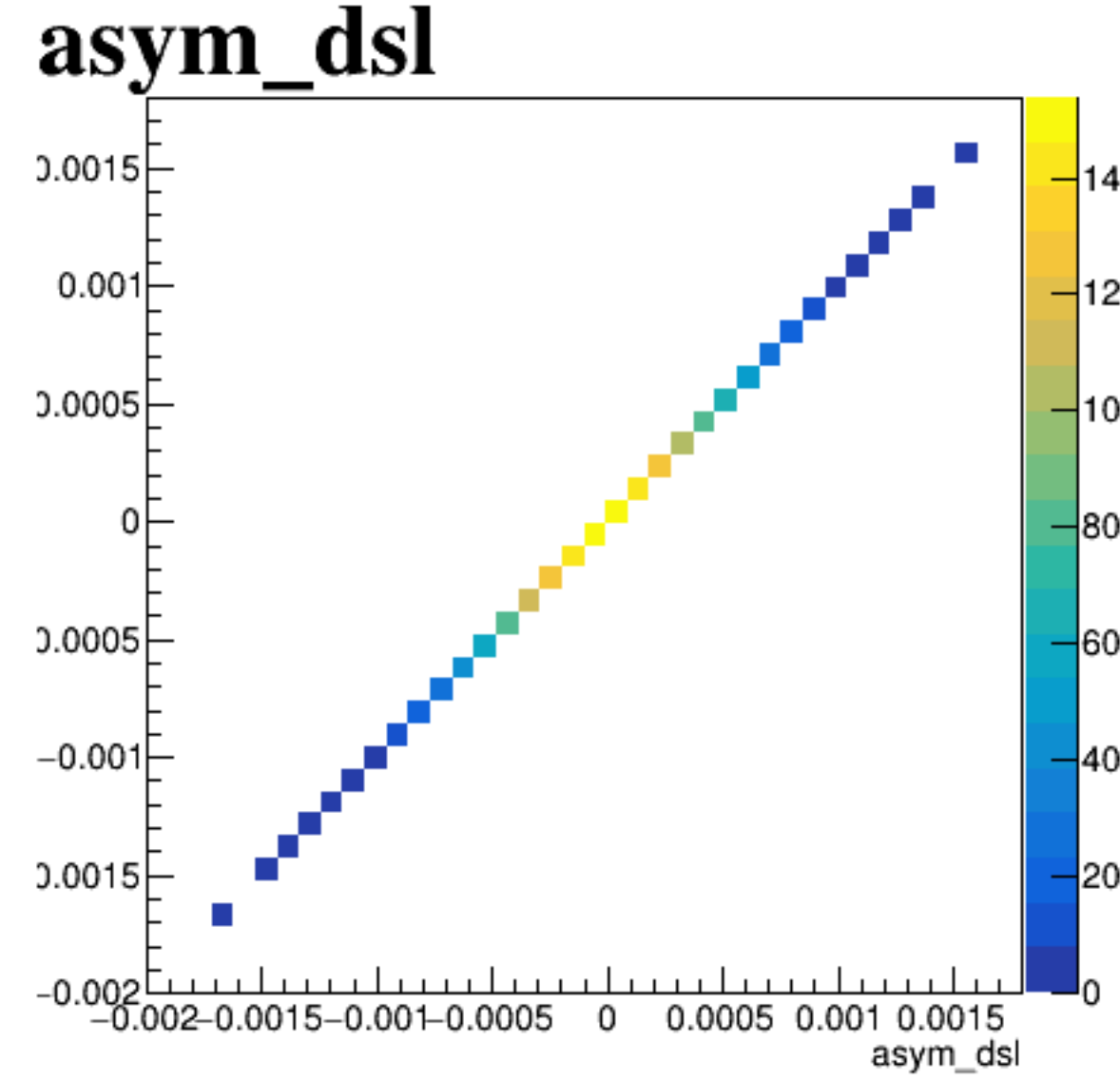
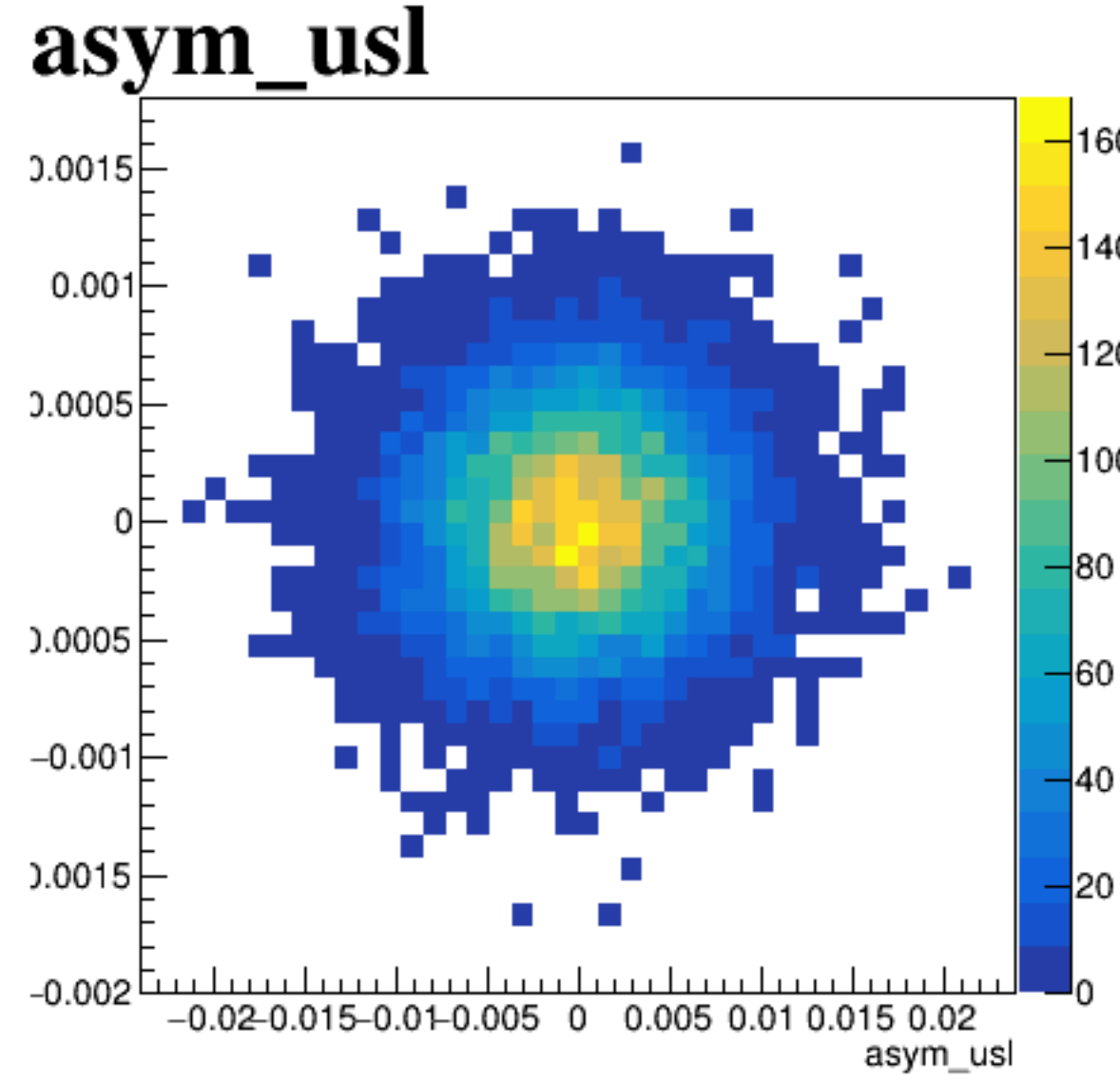




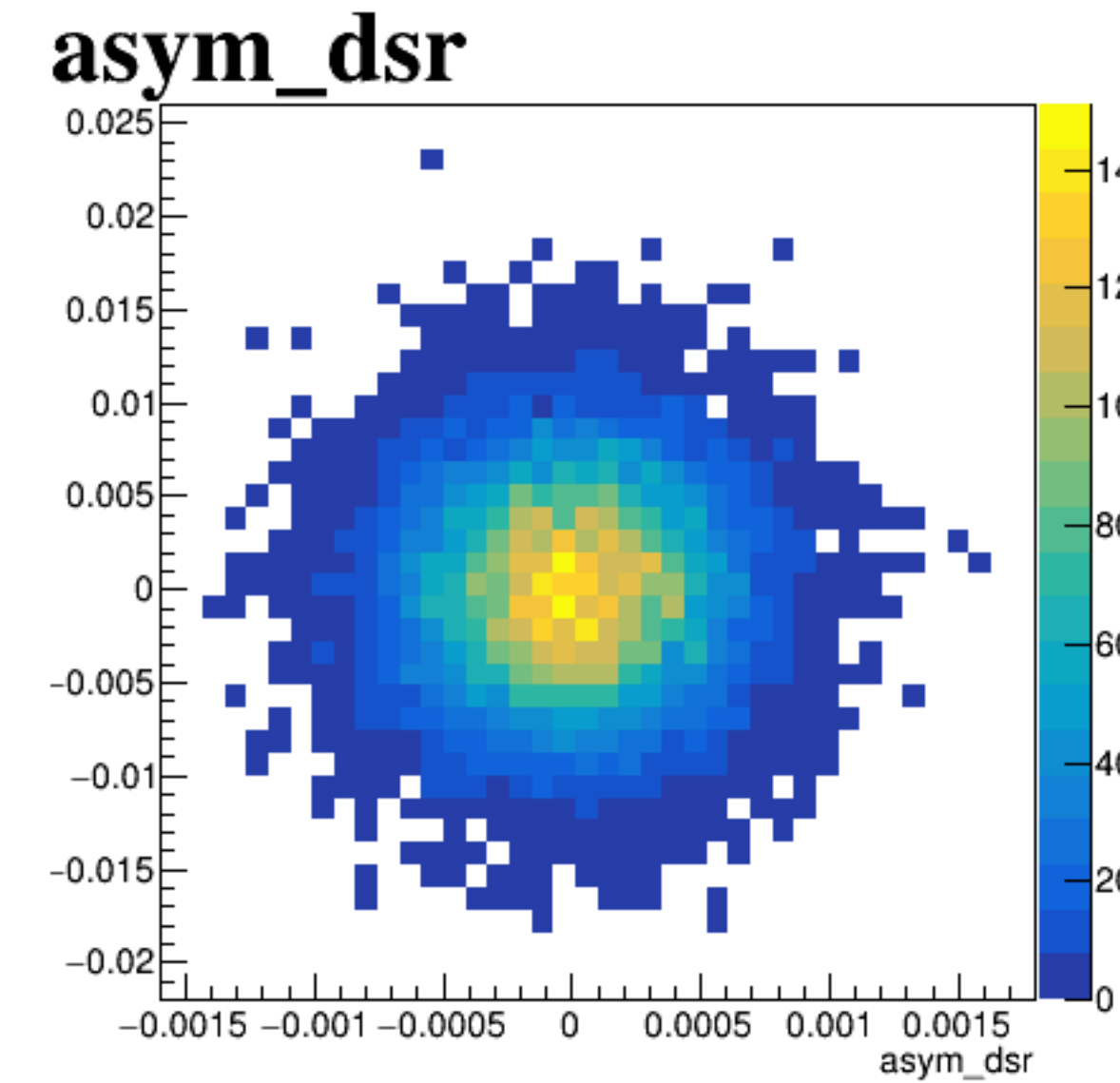
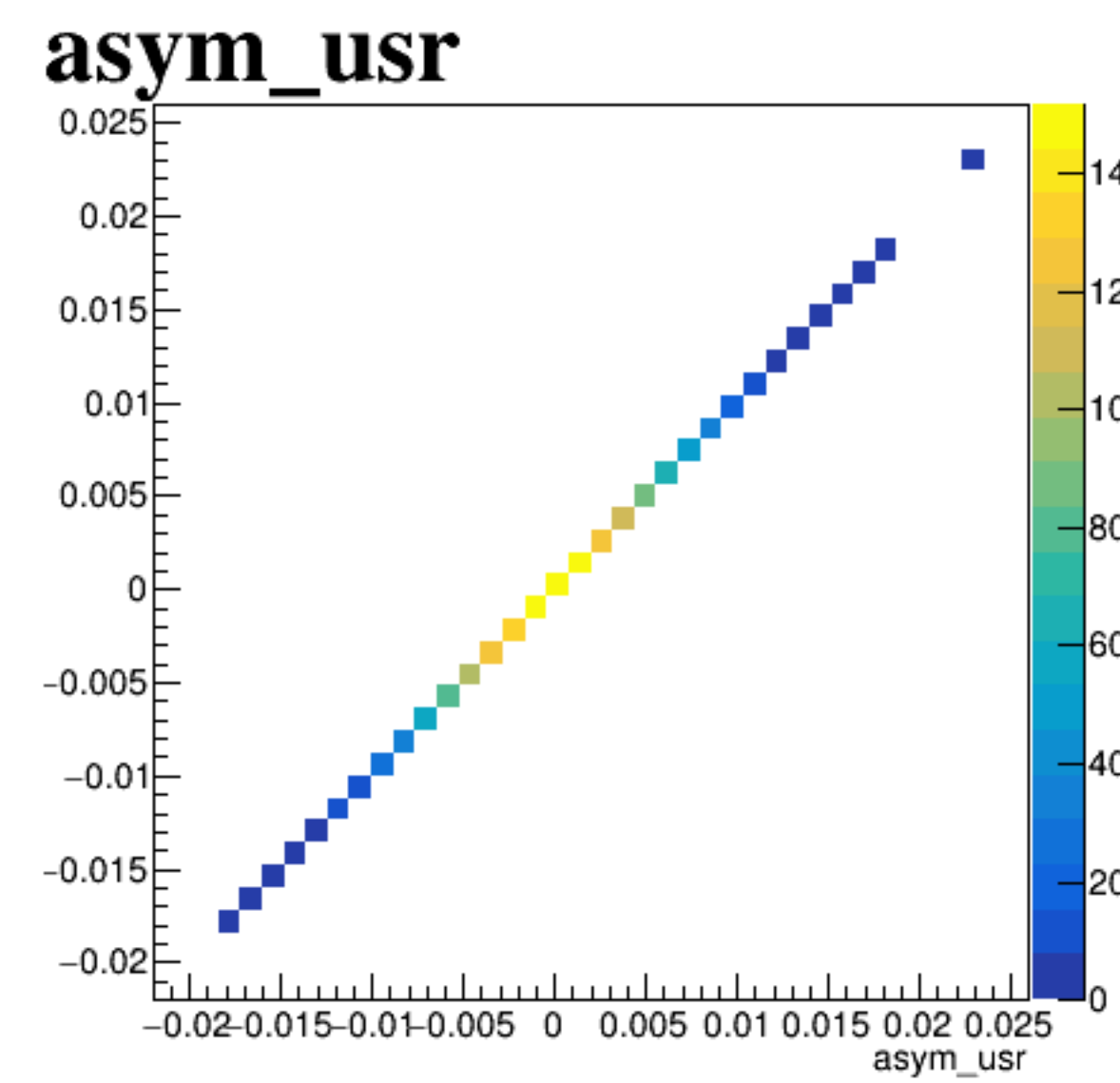
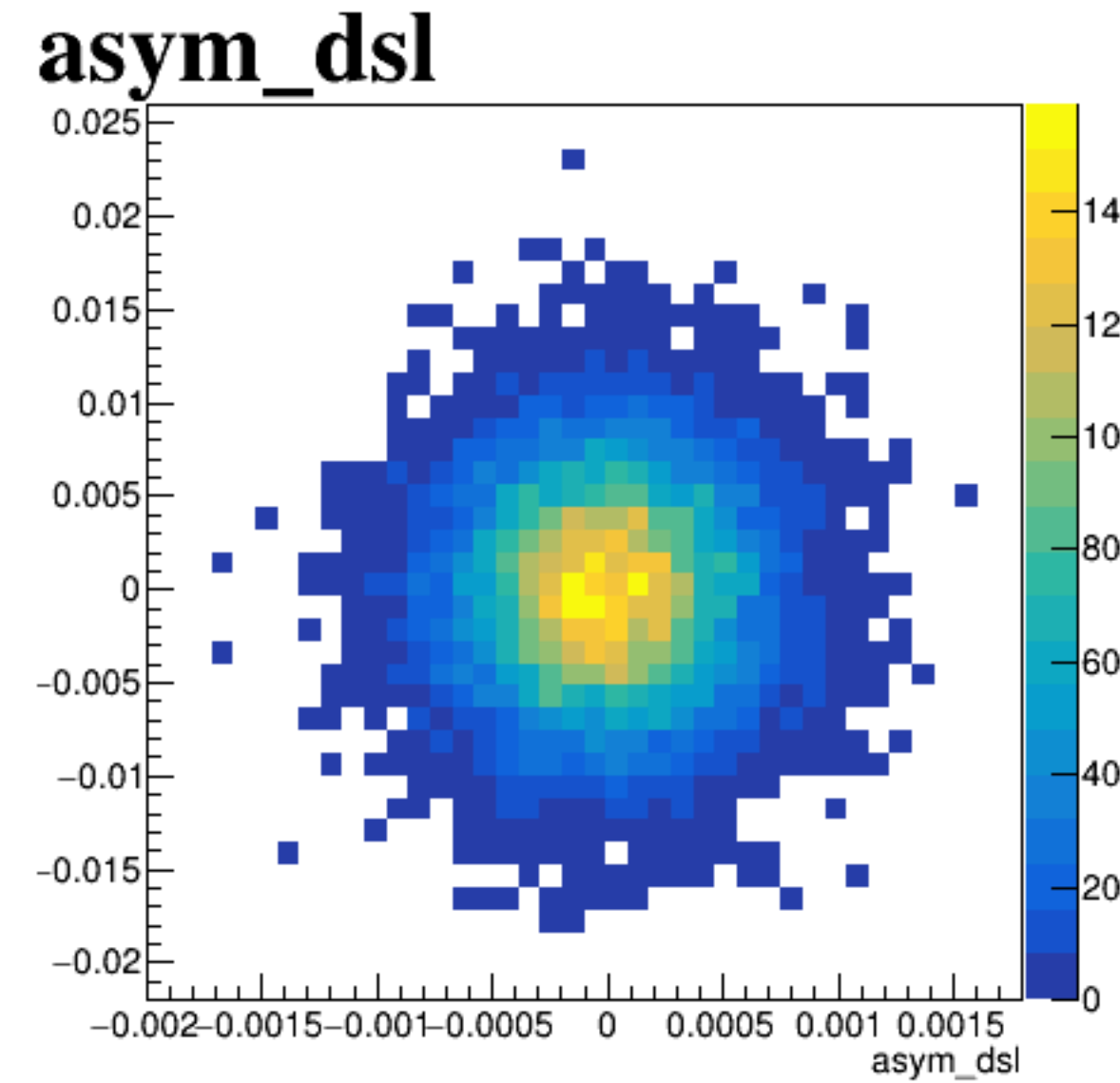
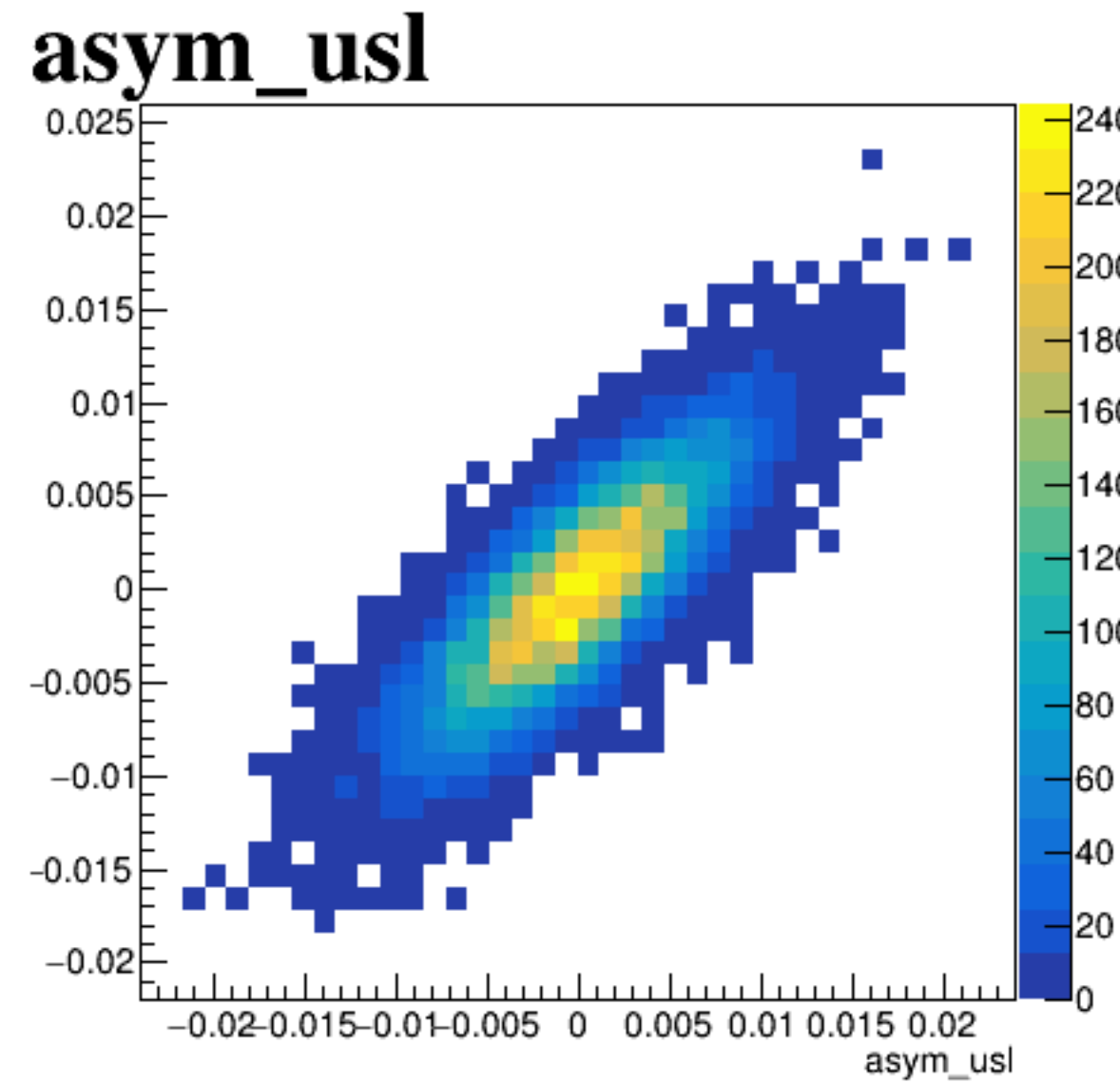
**asym\_usl**



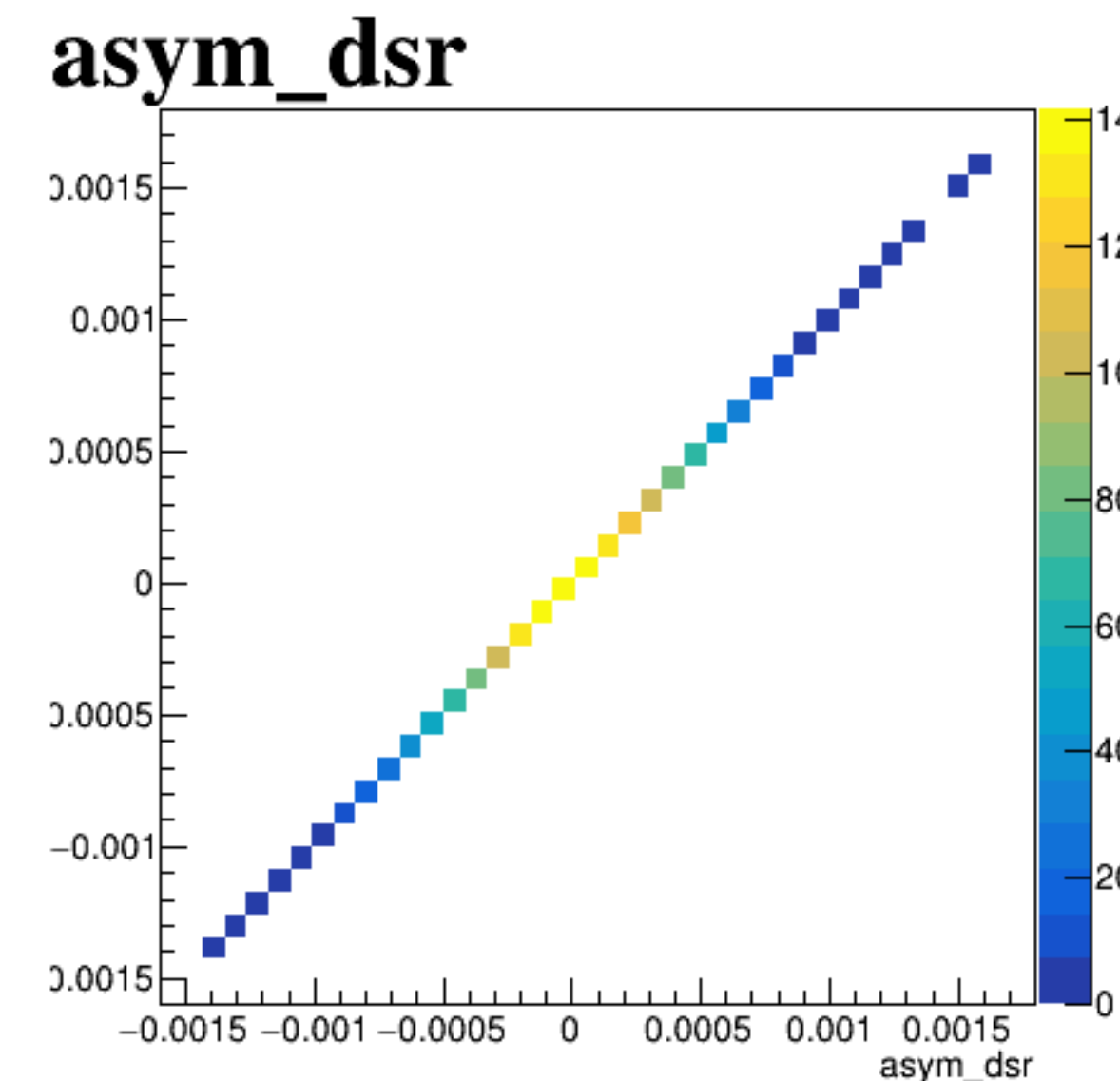
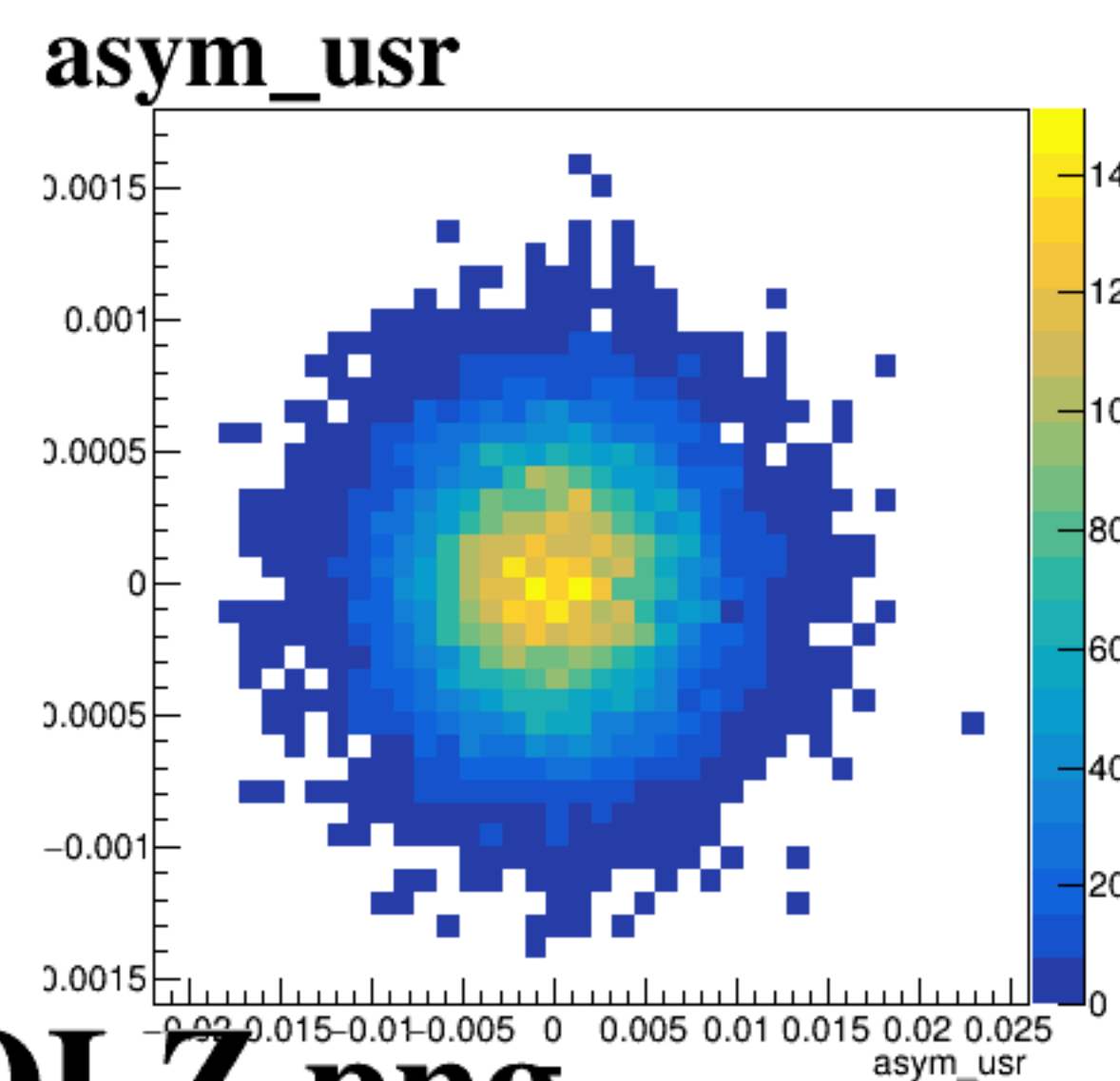
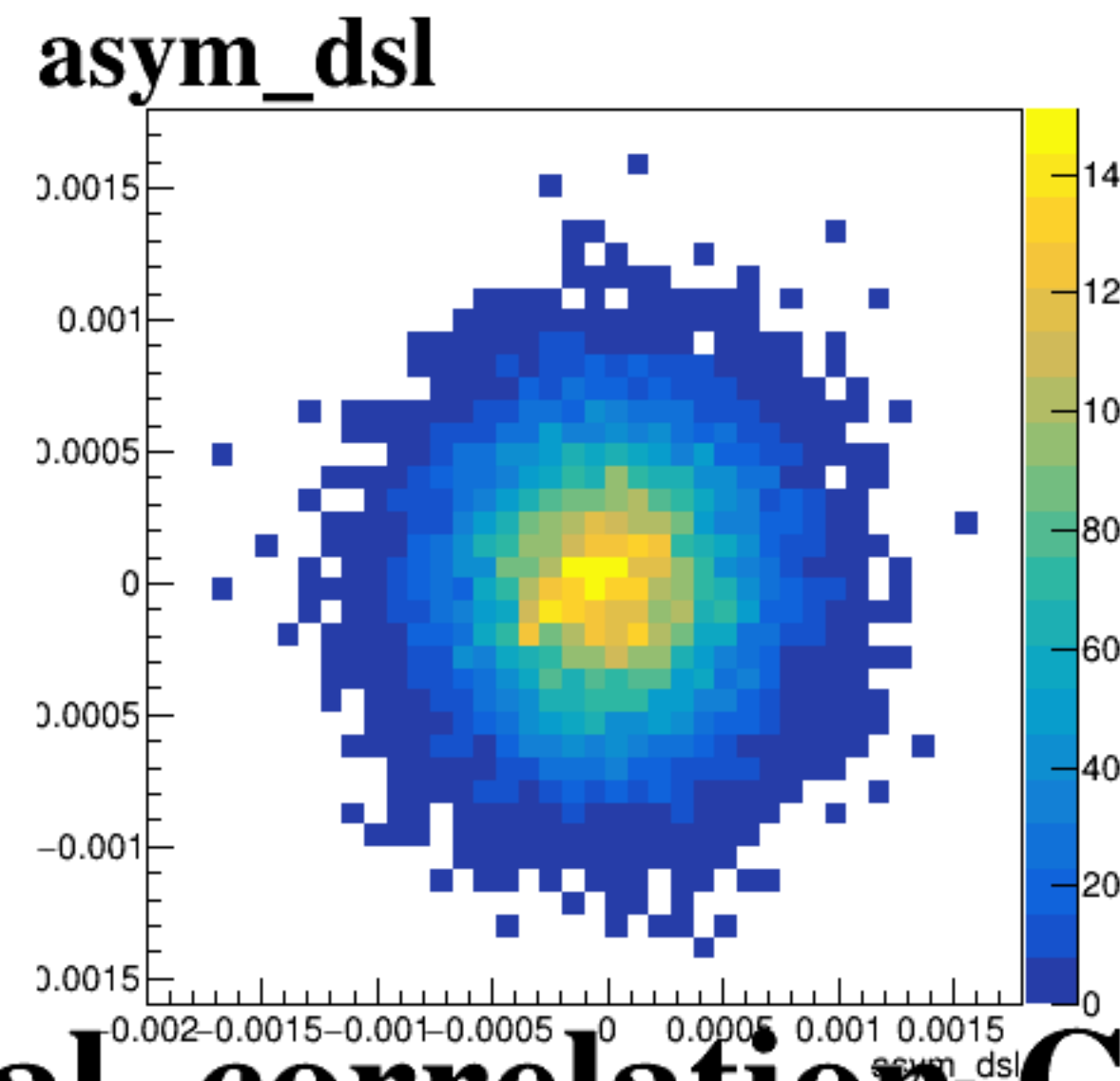
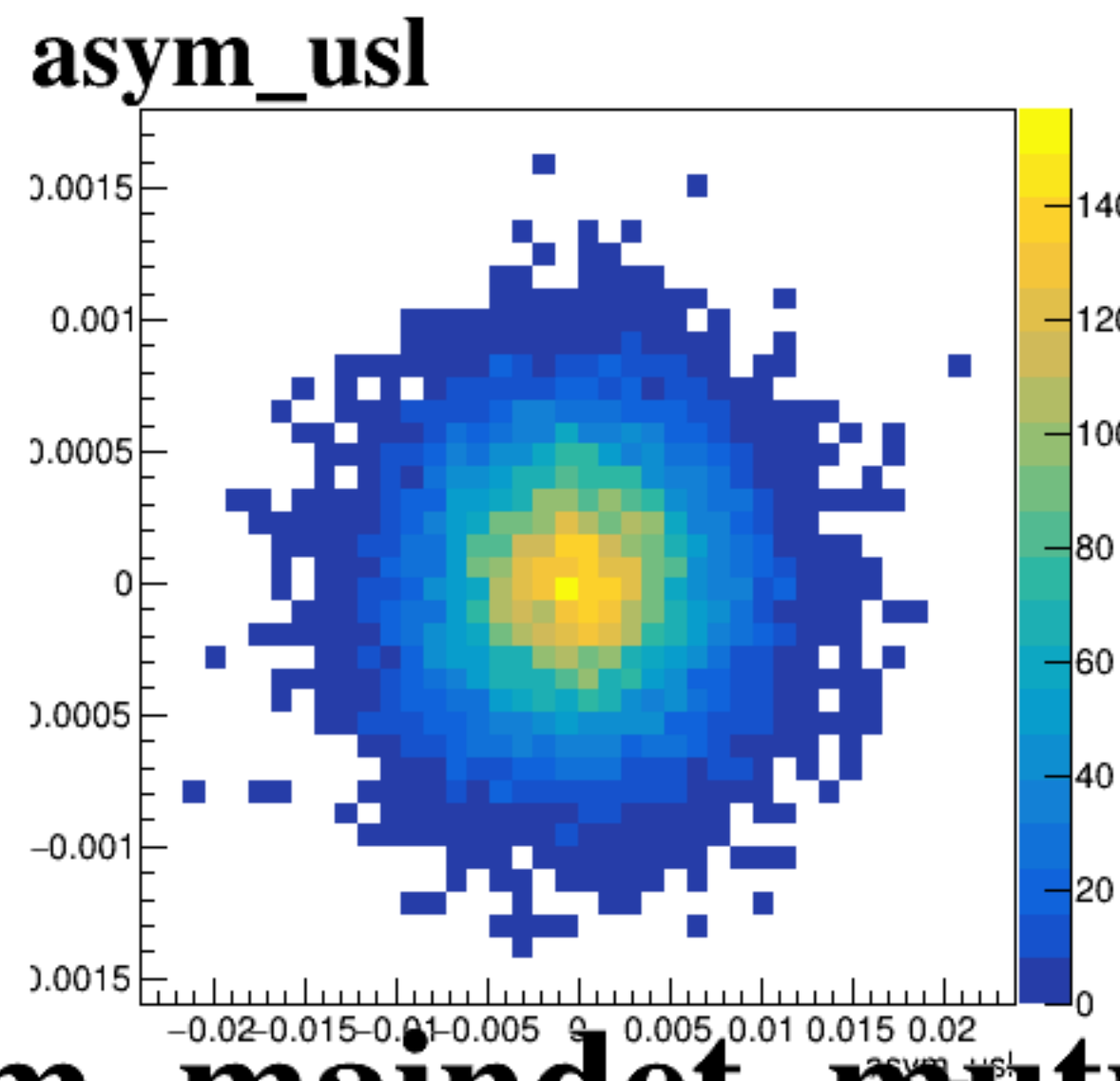
**asym\_dsl**



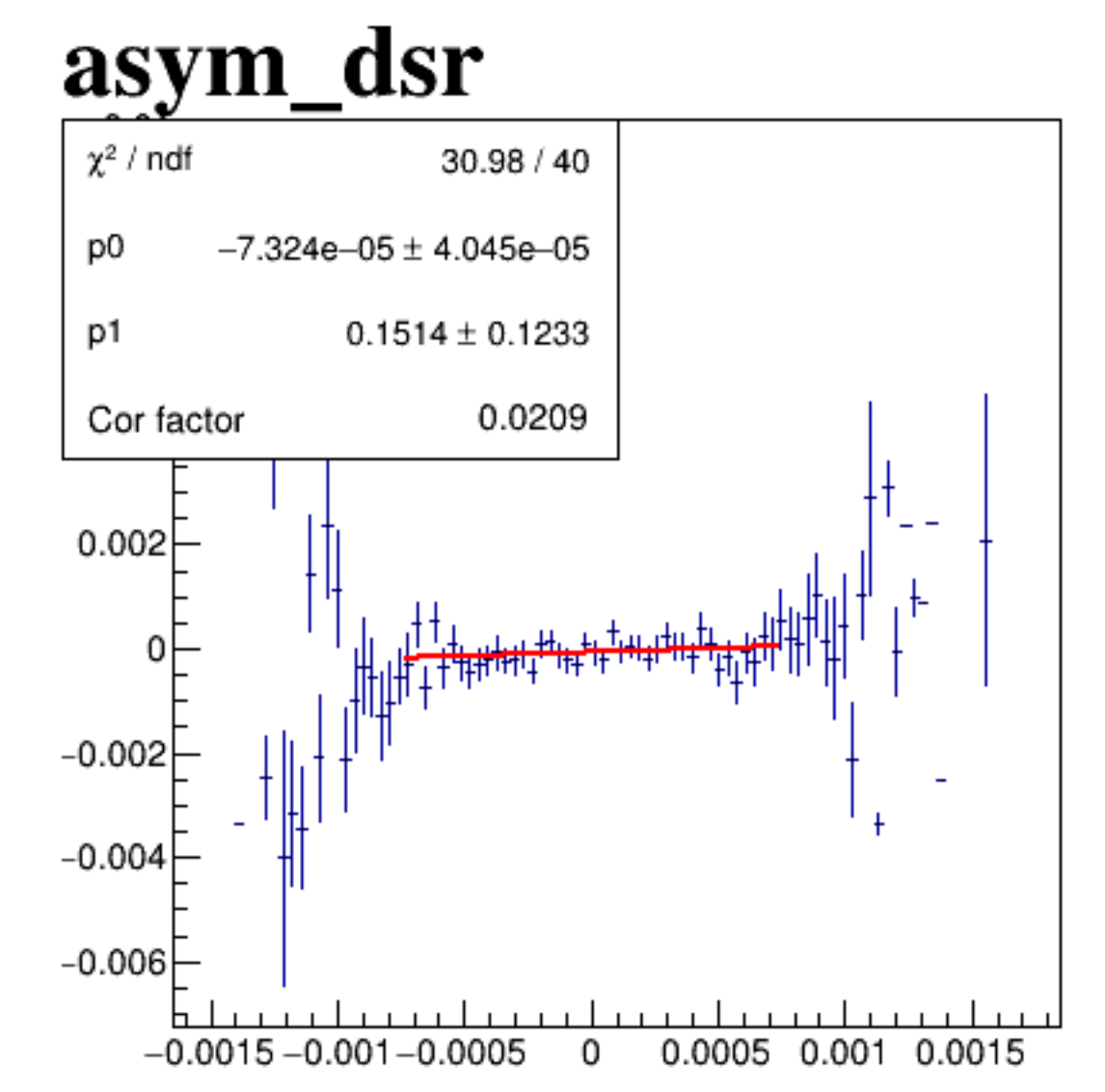
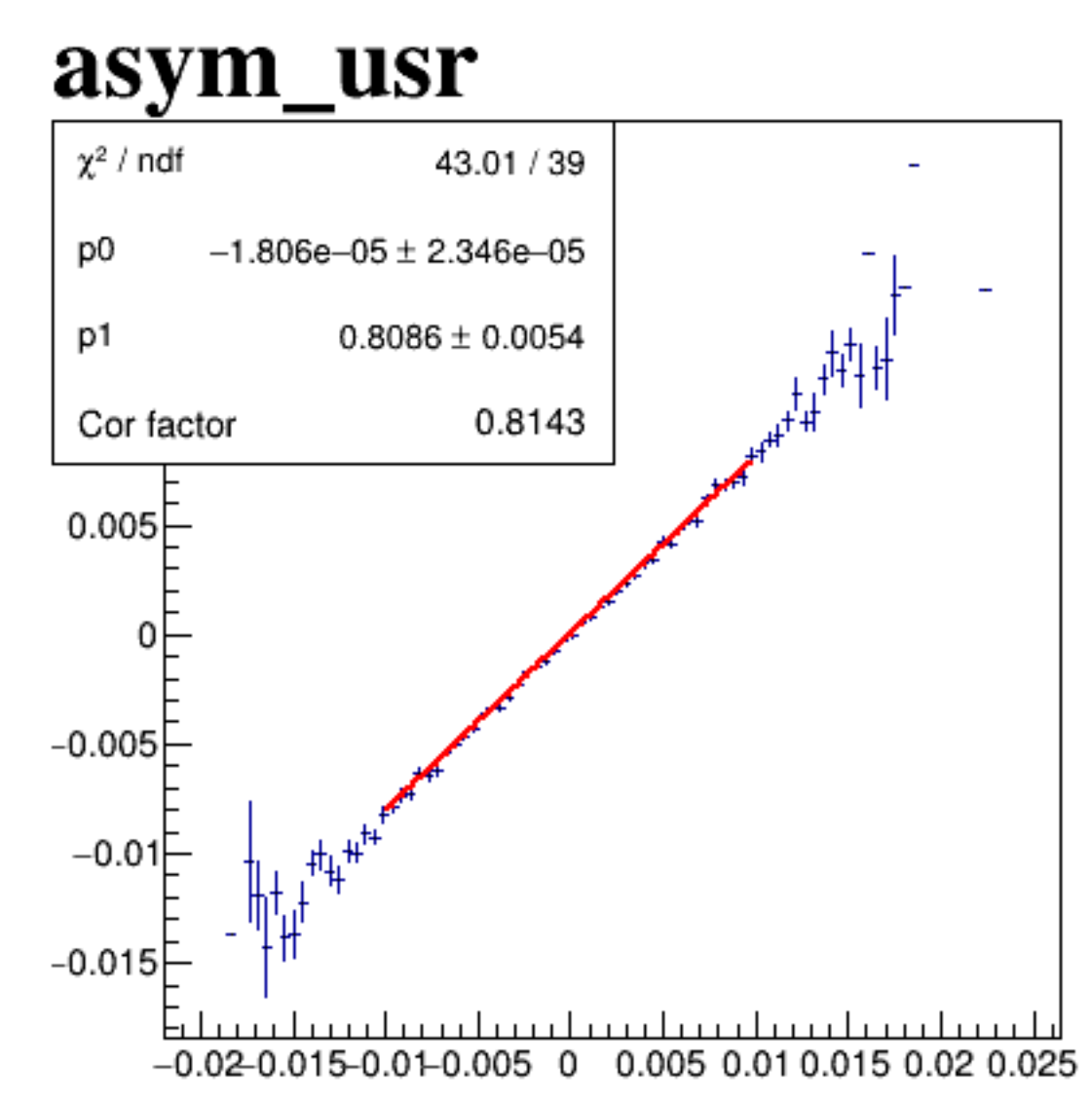
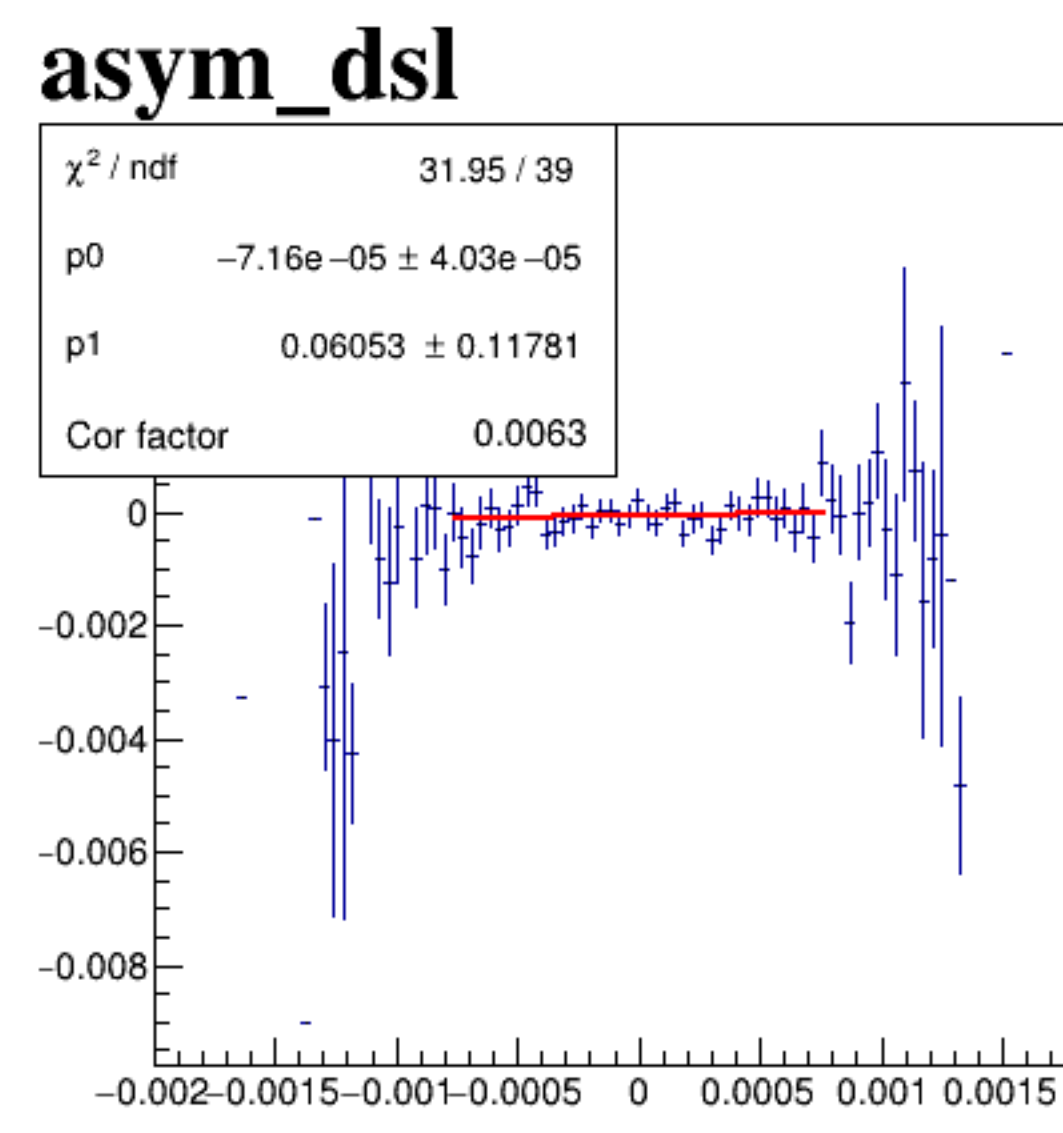
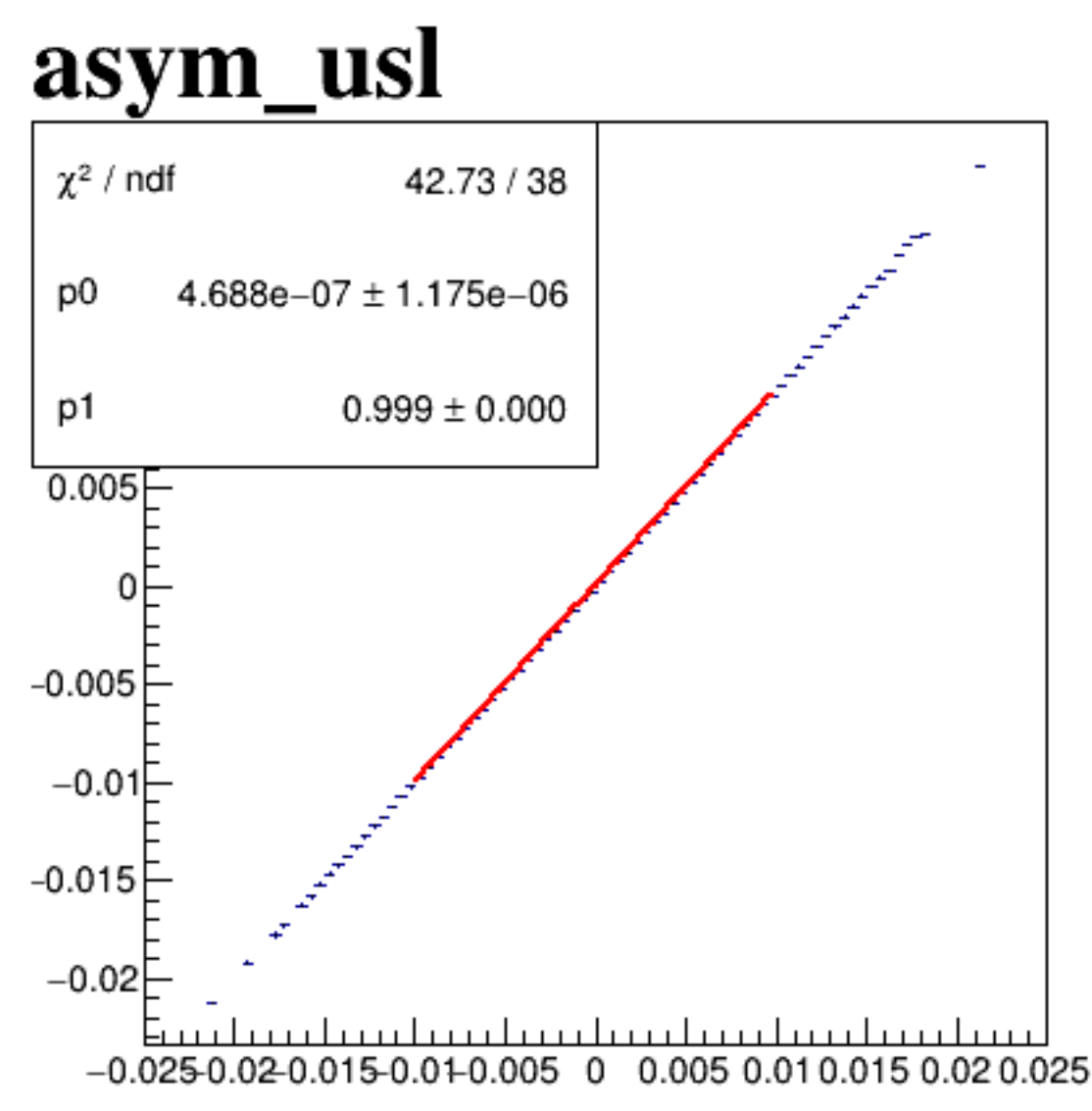
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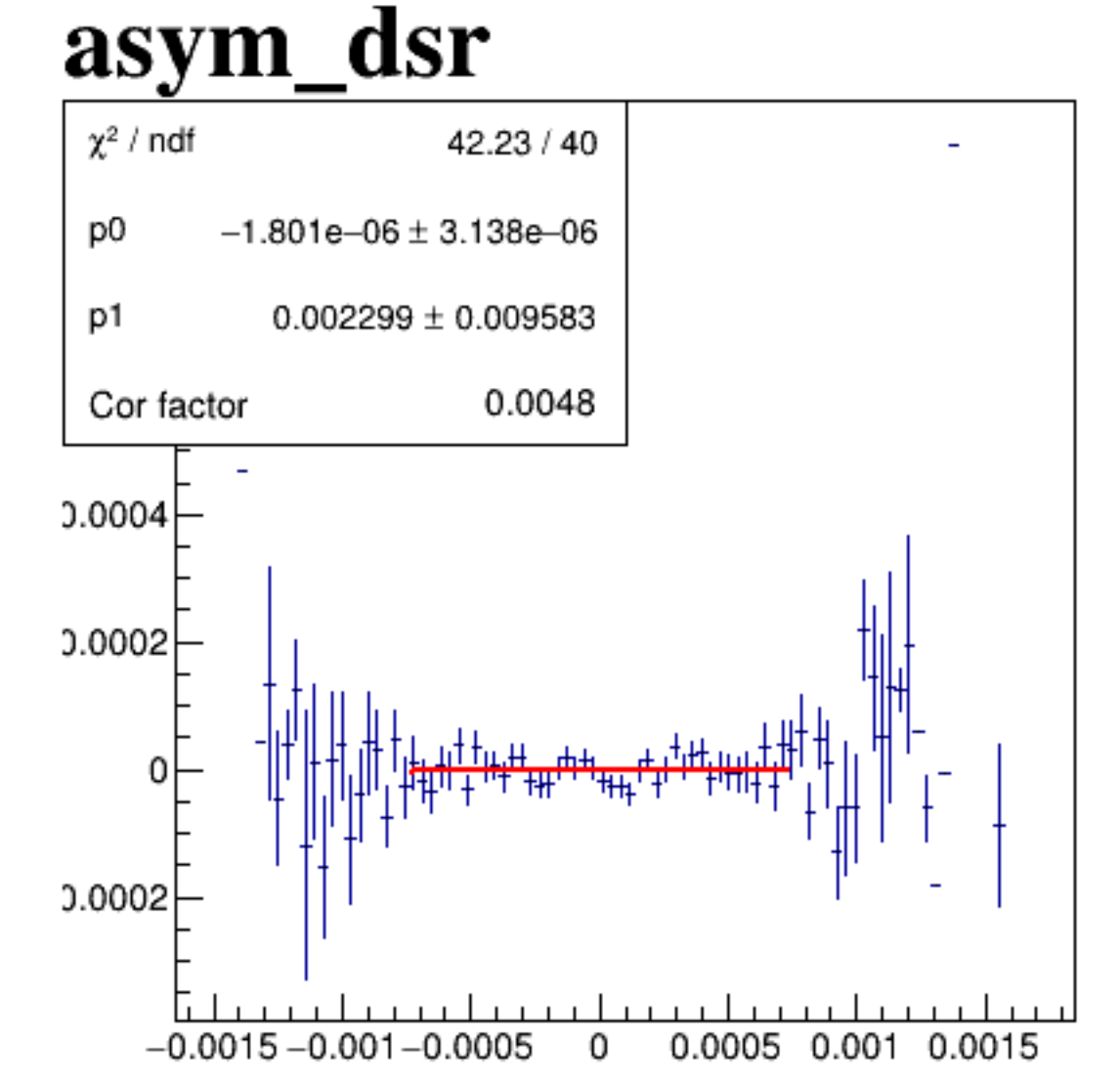
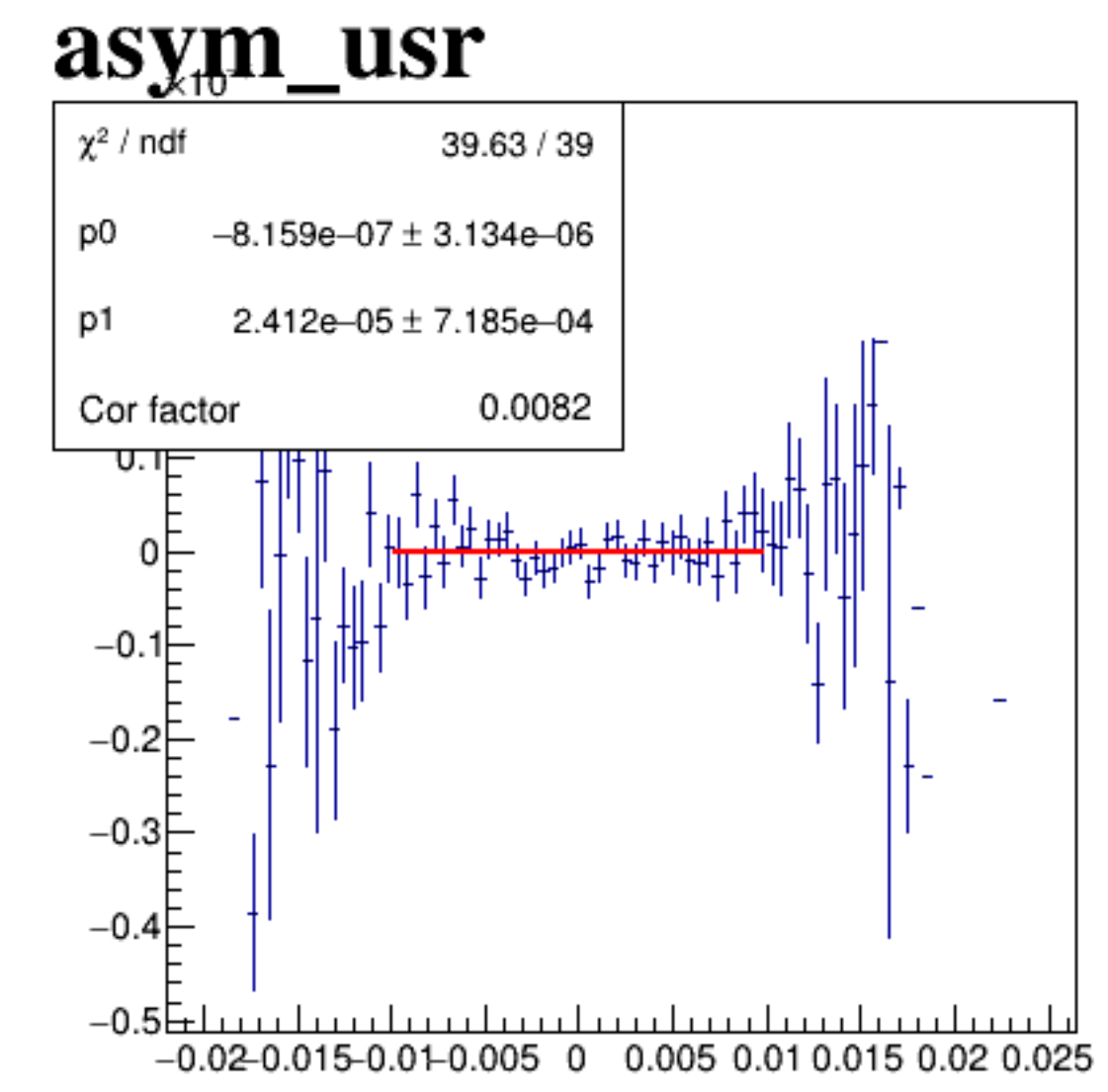
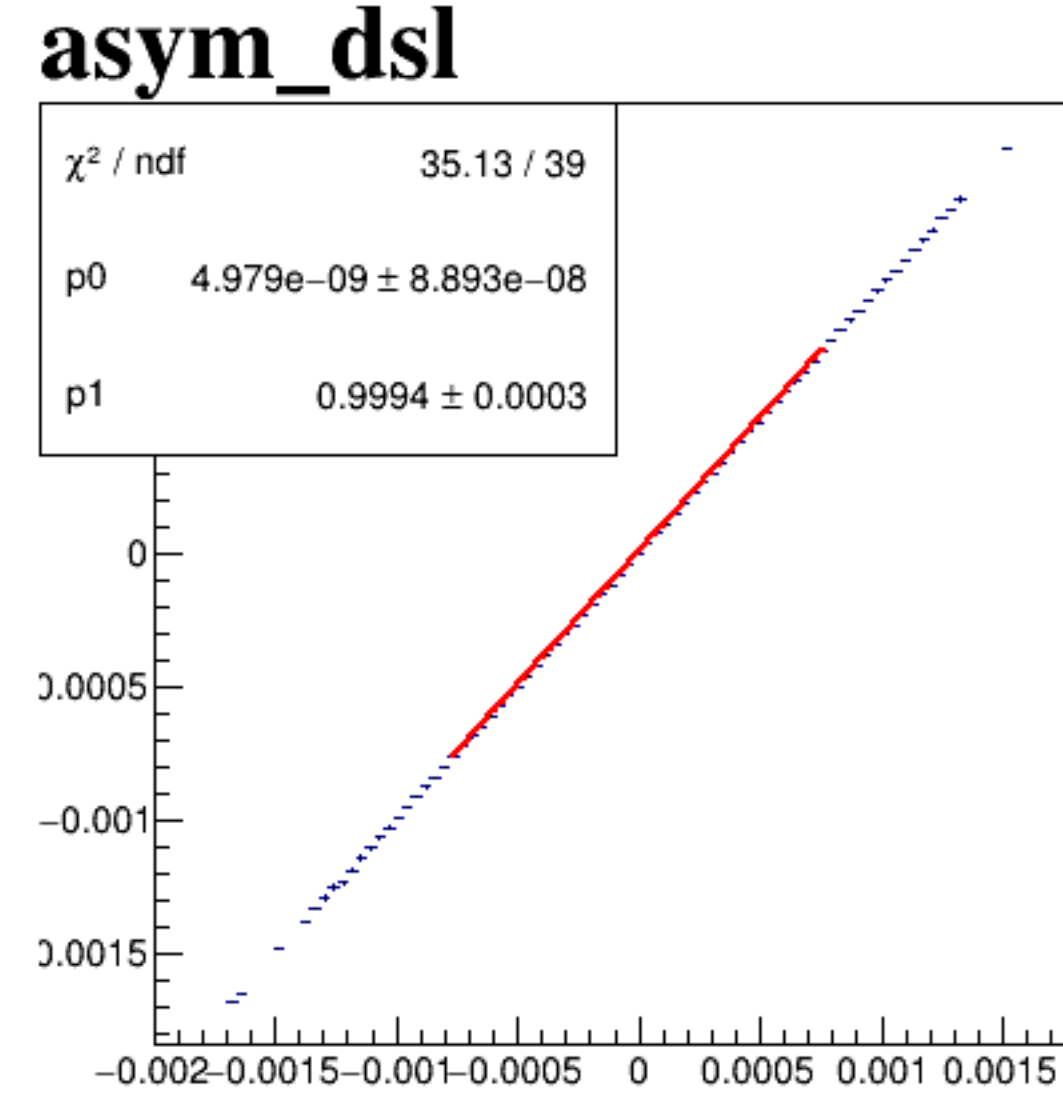
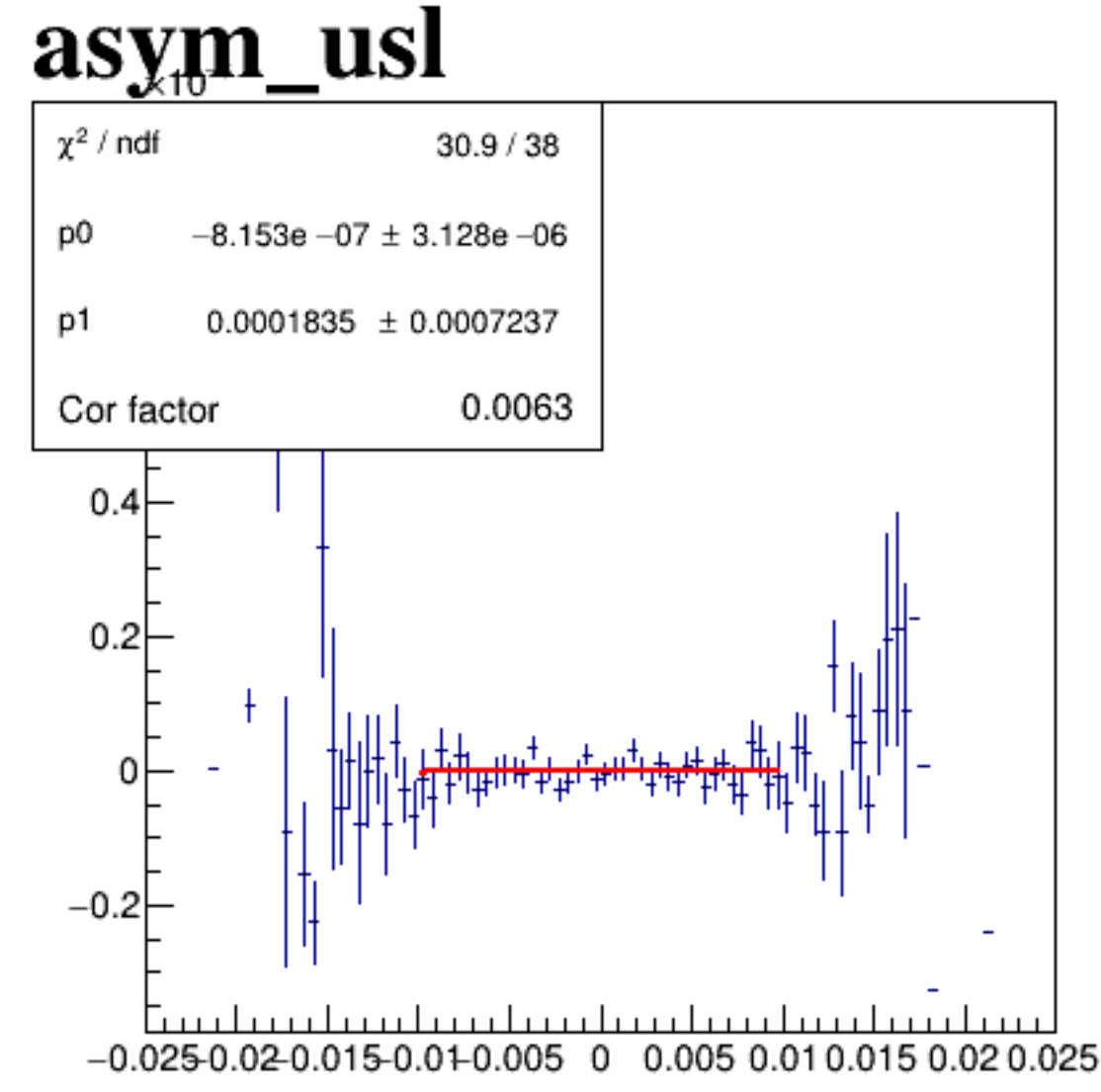
**asym\_dsr**



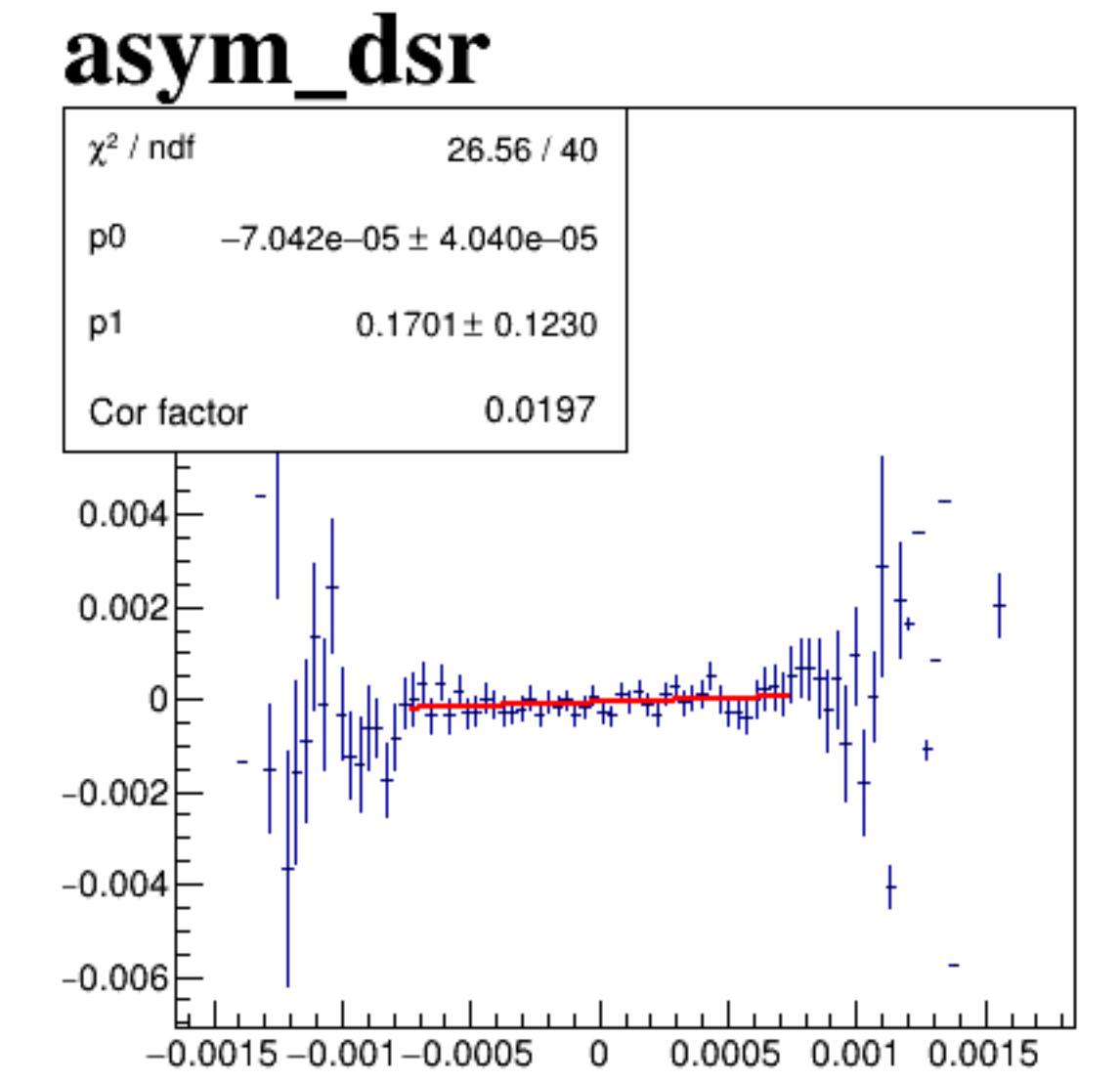
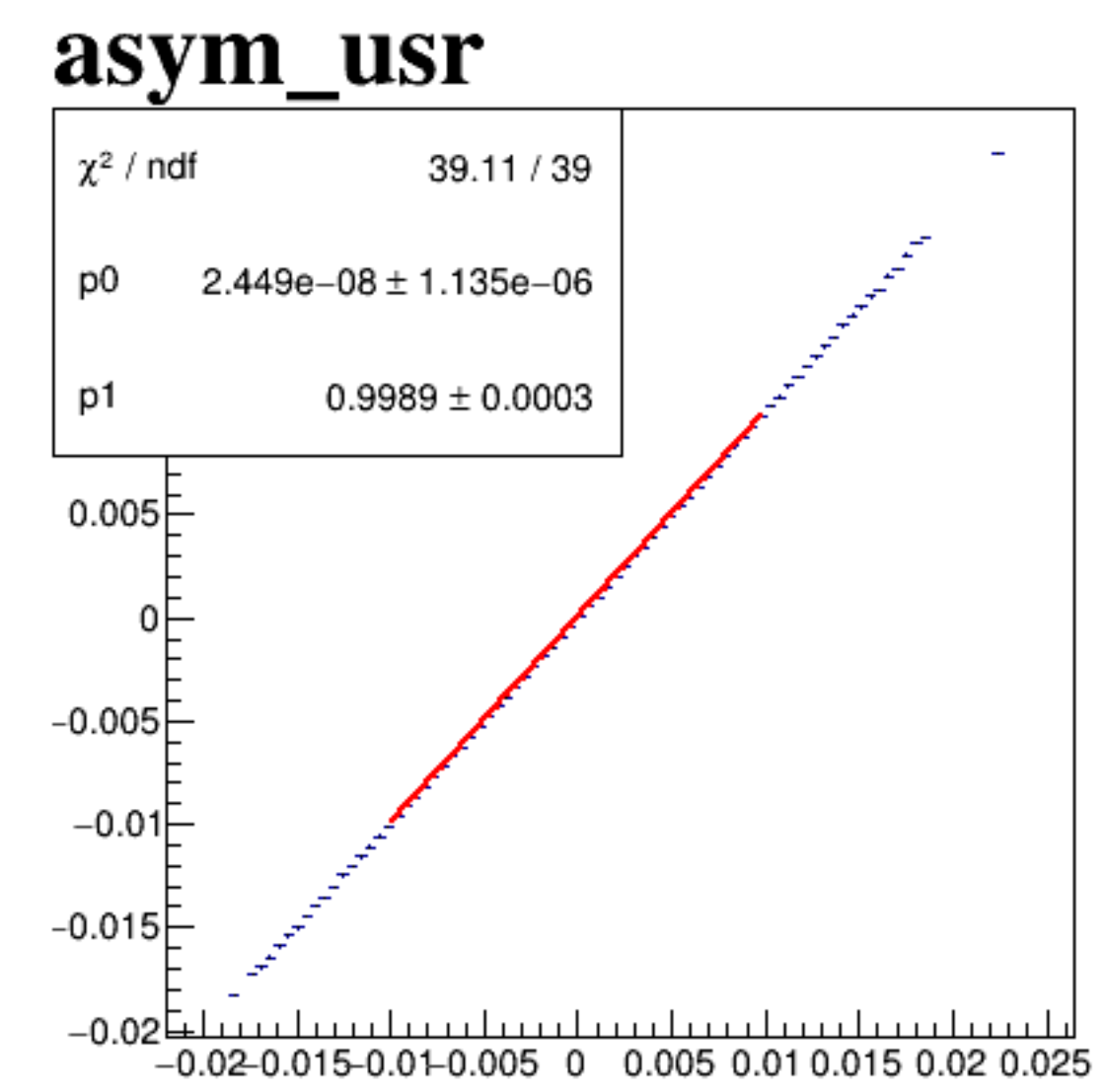
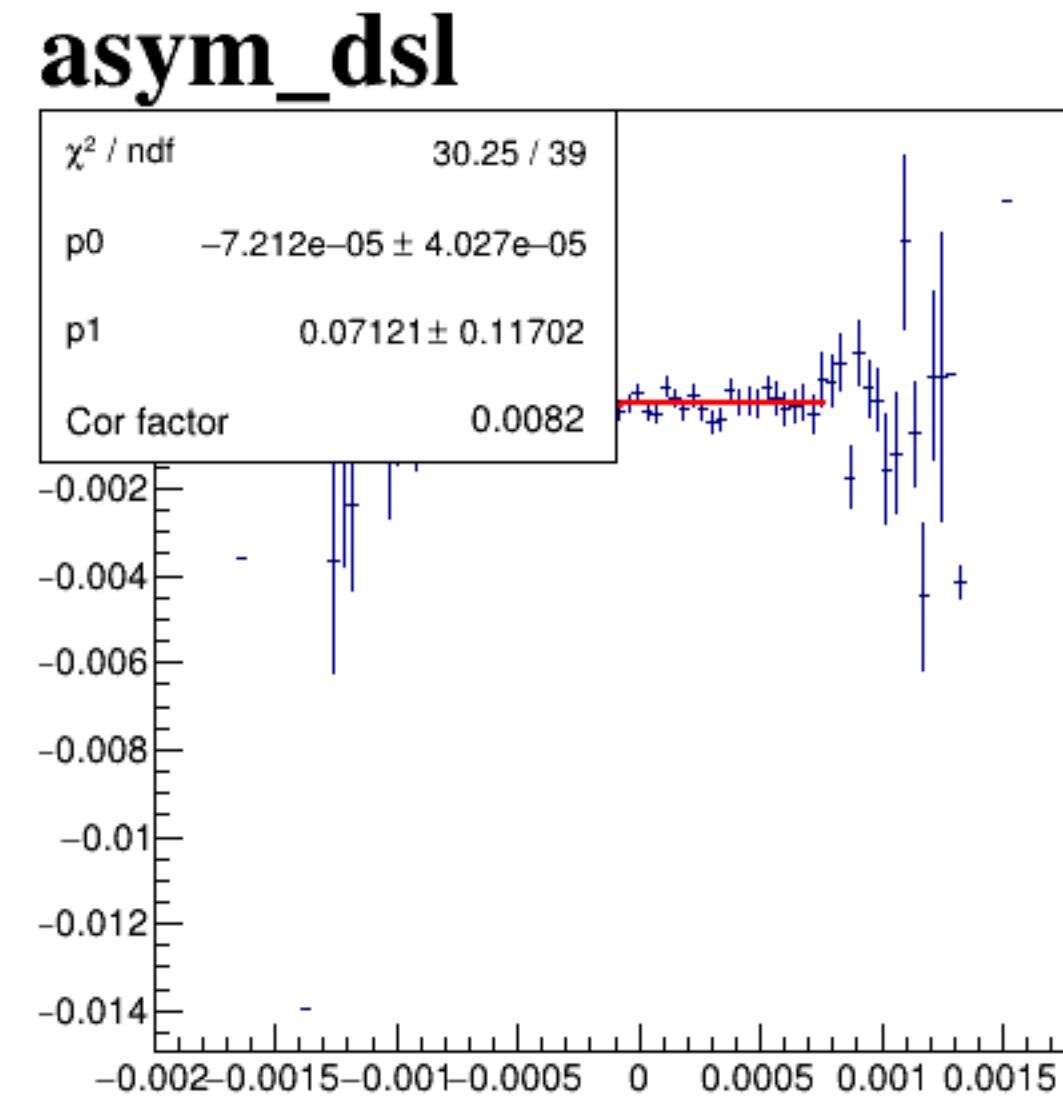
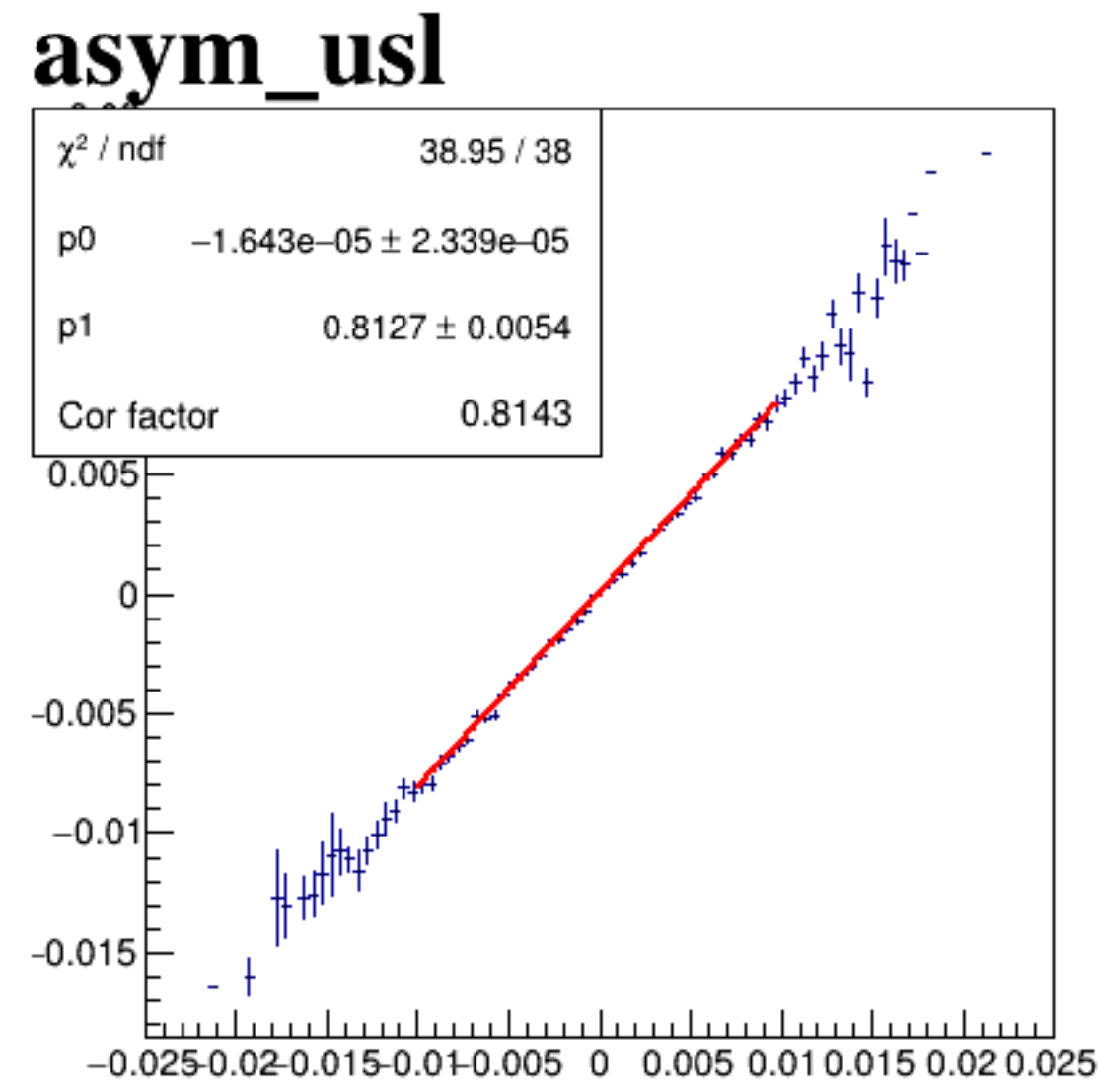
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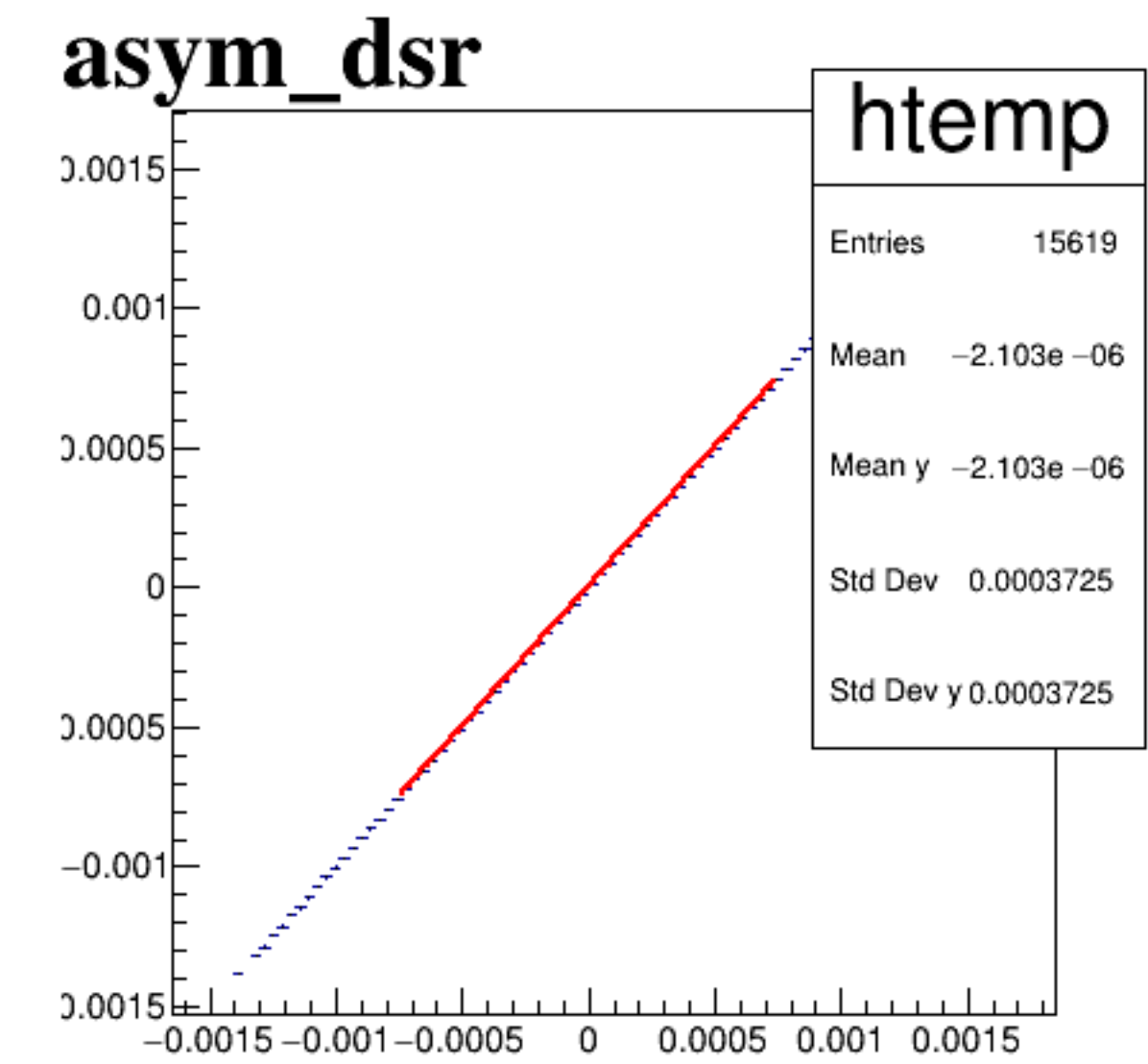
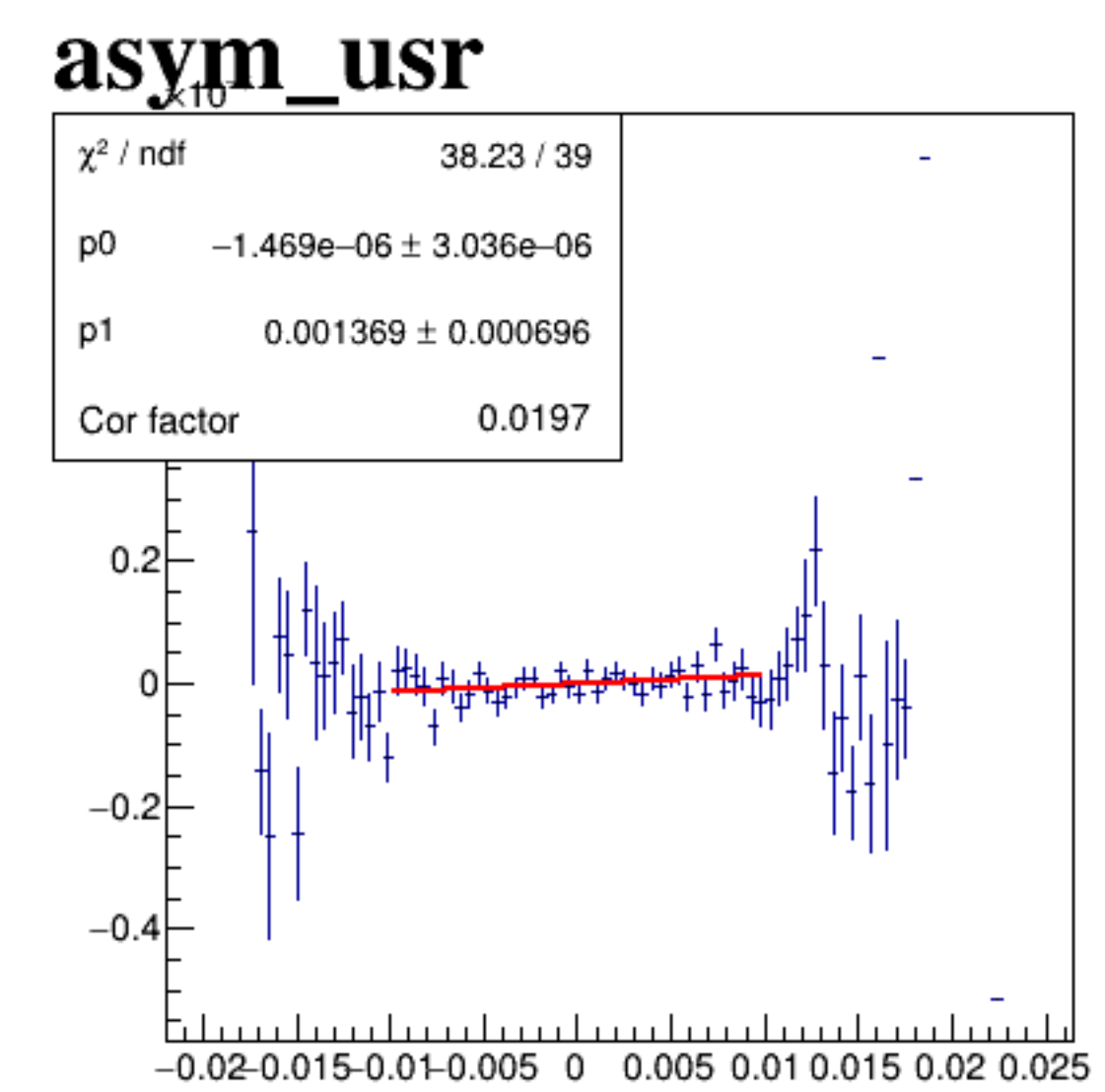
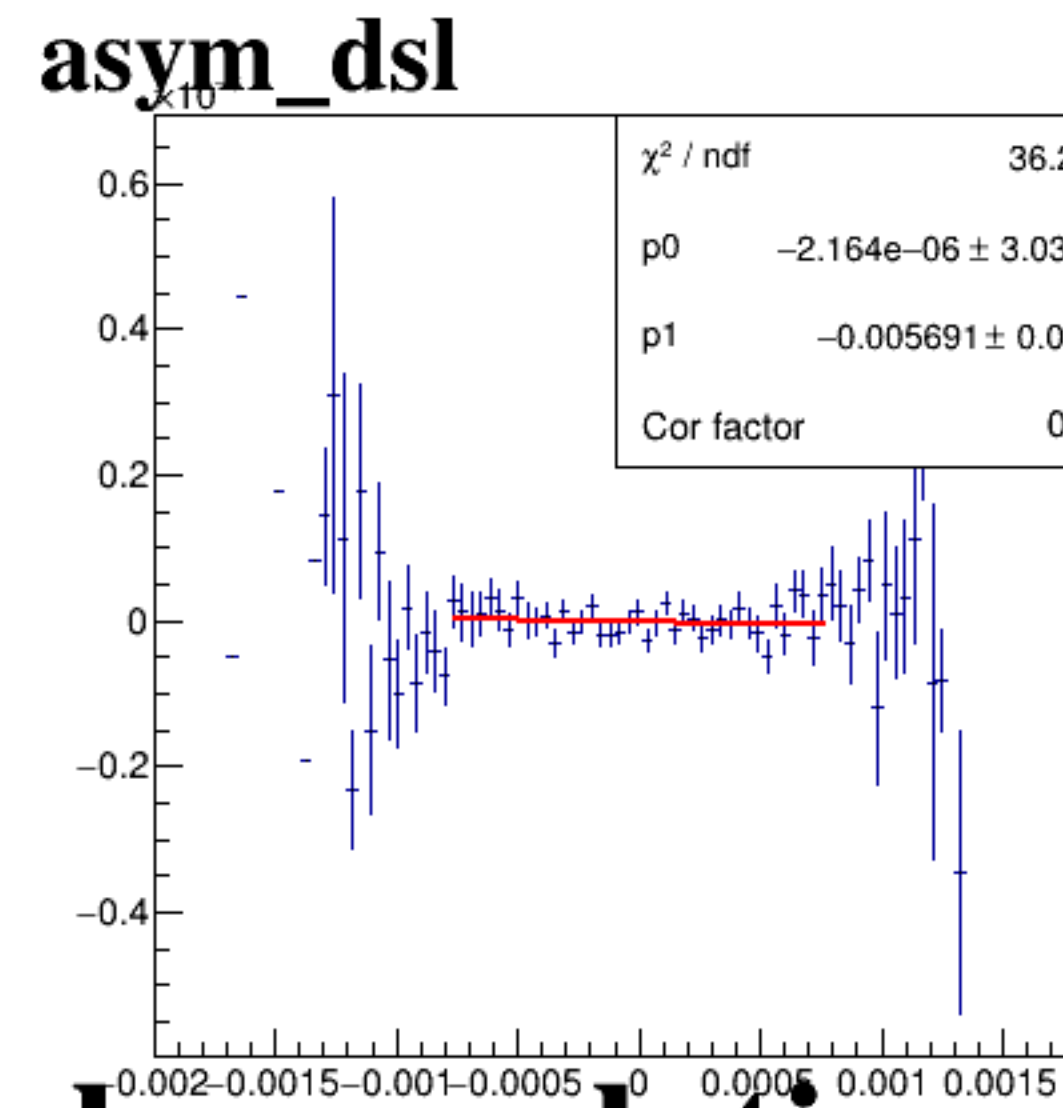
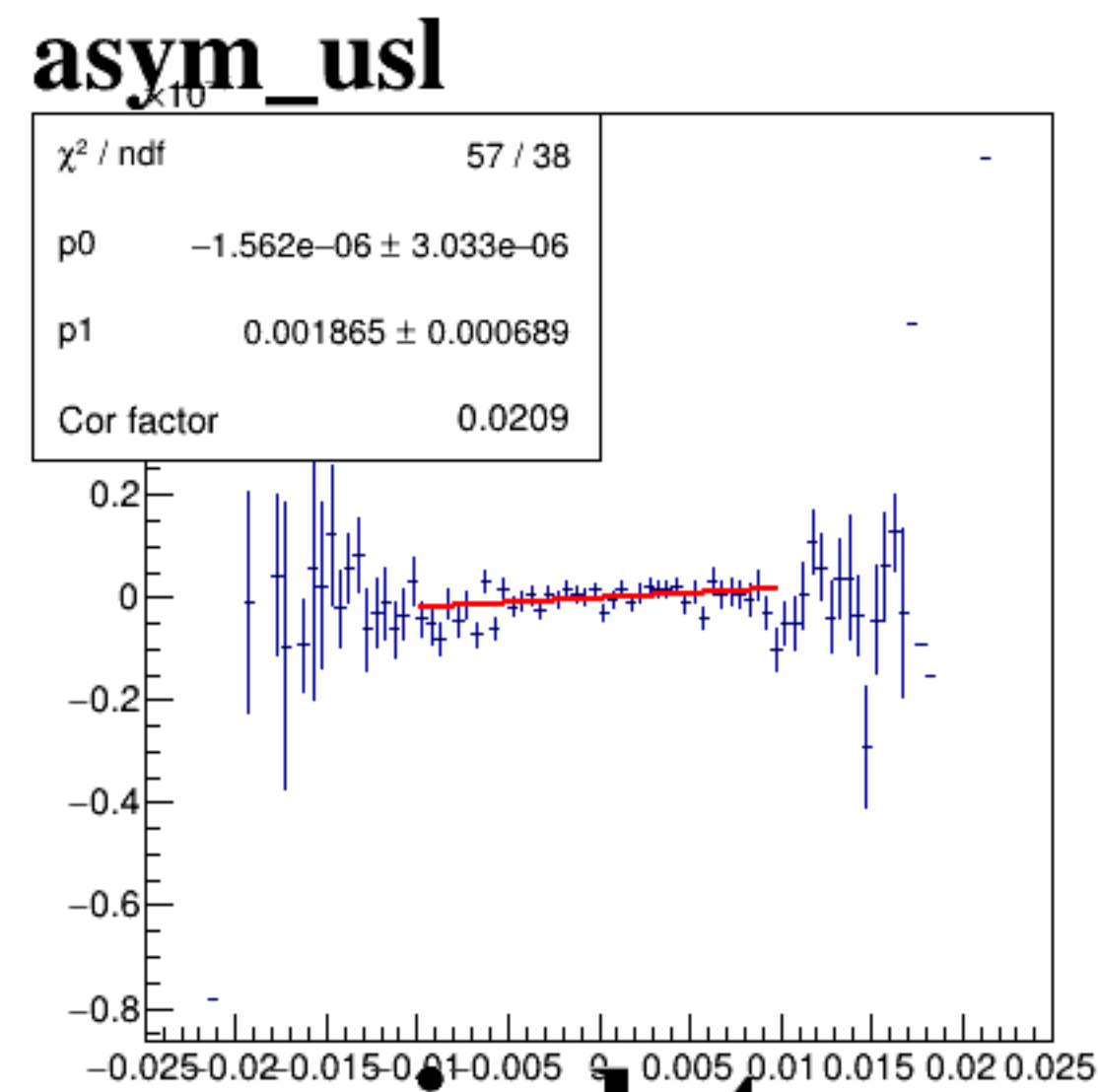
asym\_dsl



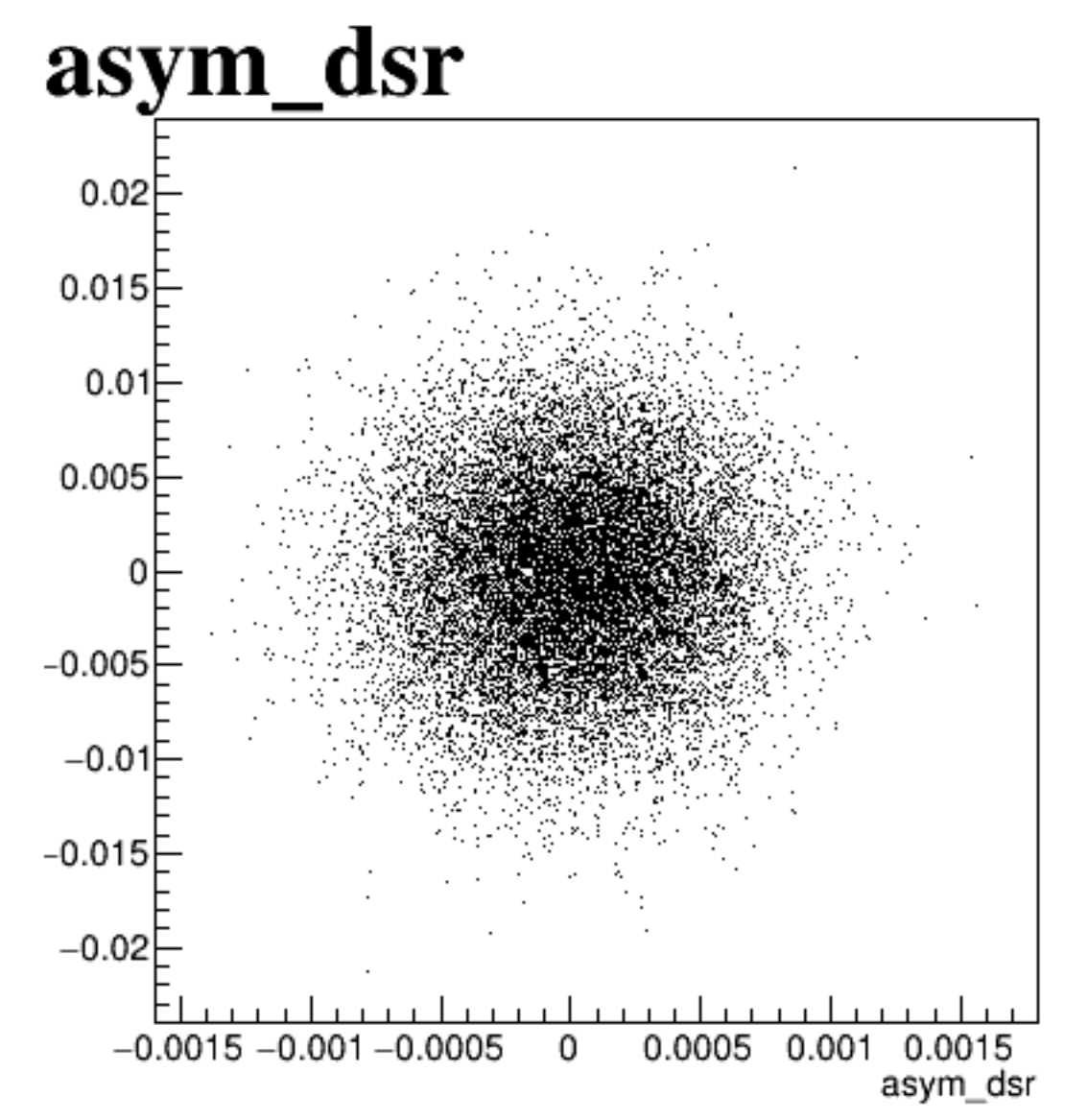
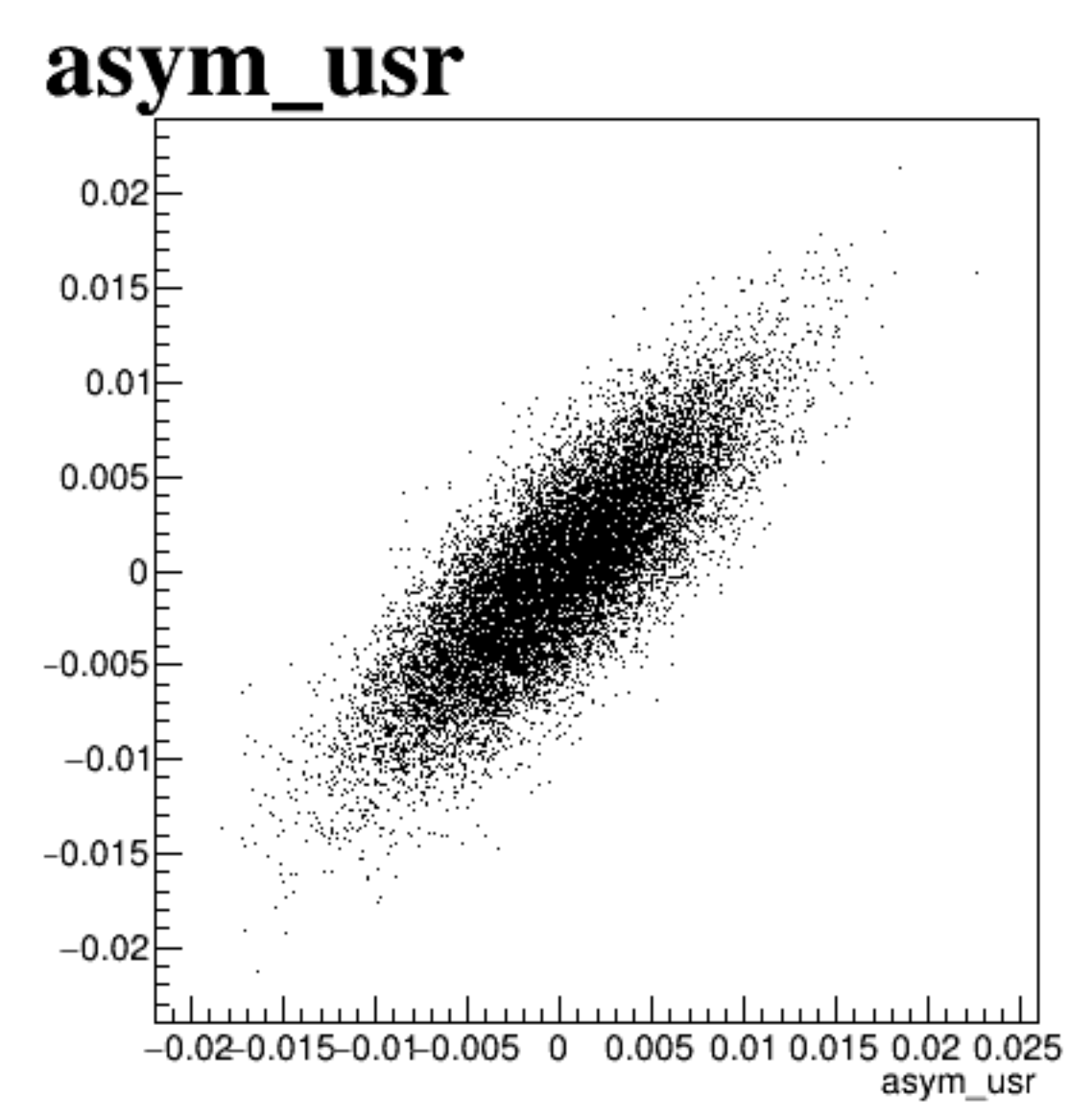
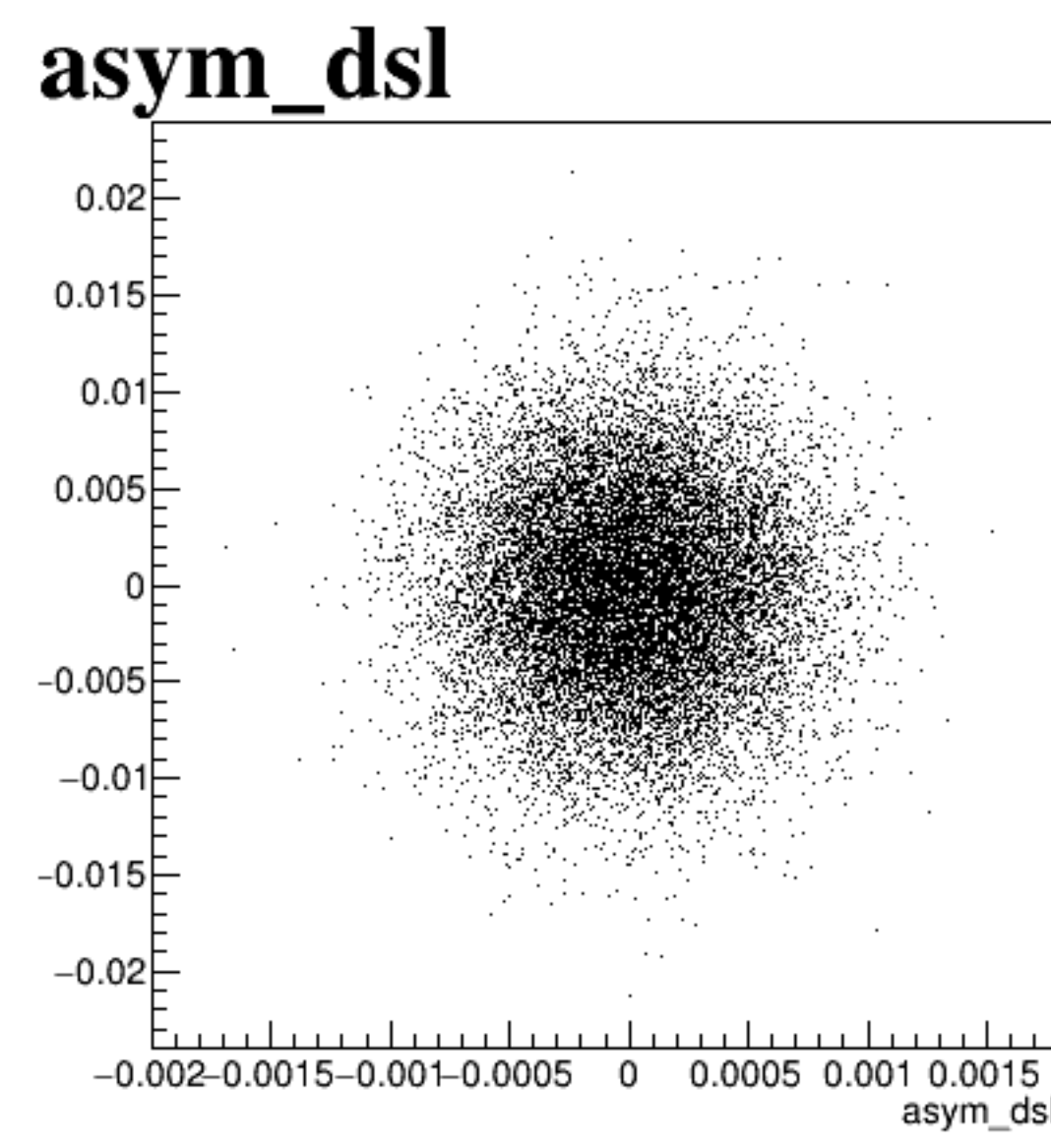
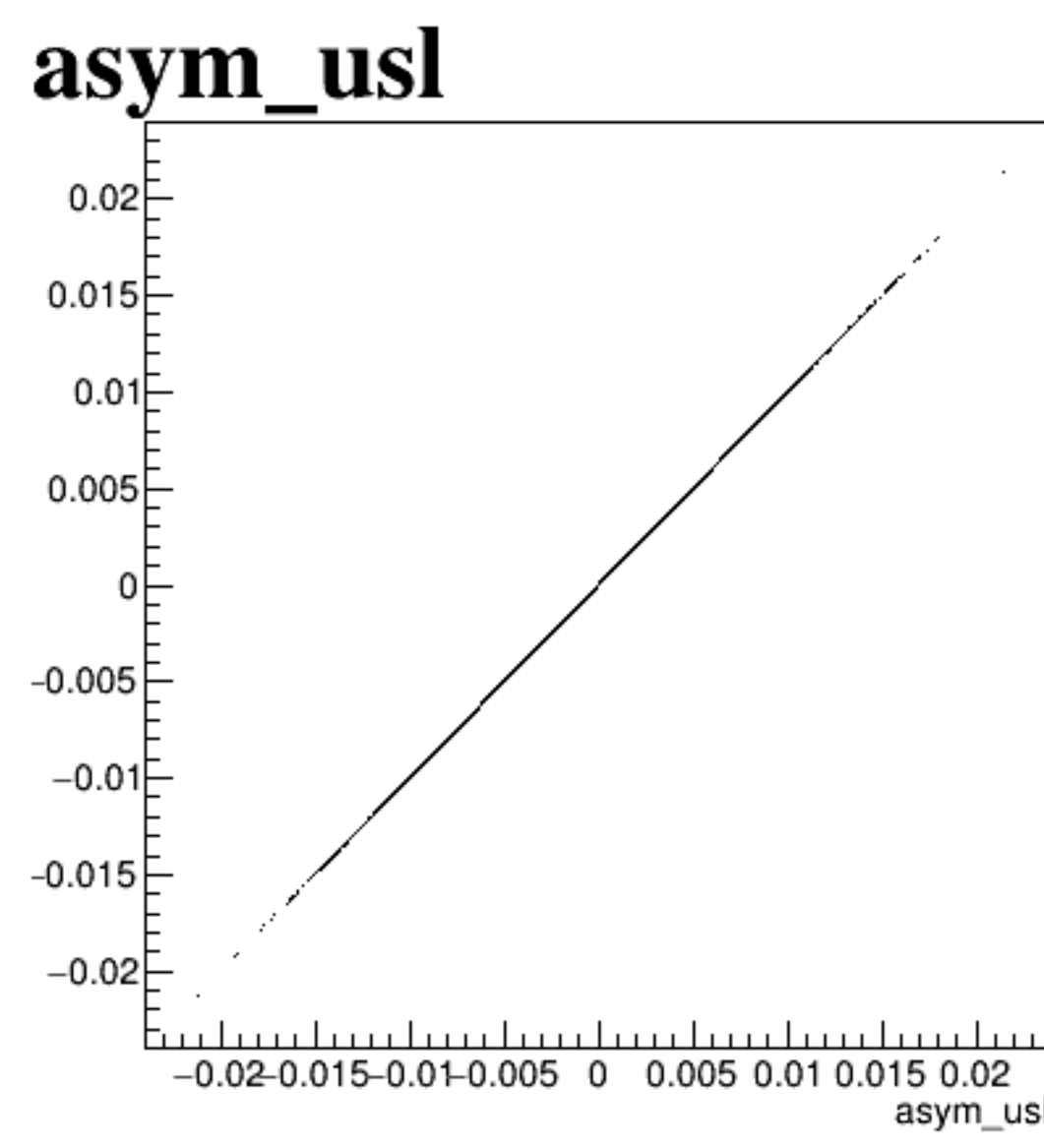
asym\_usr



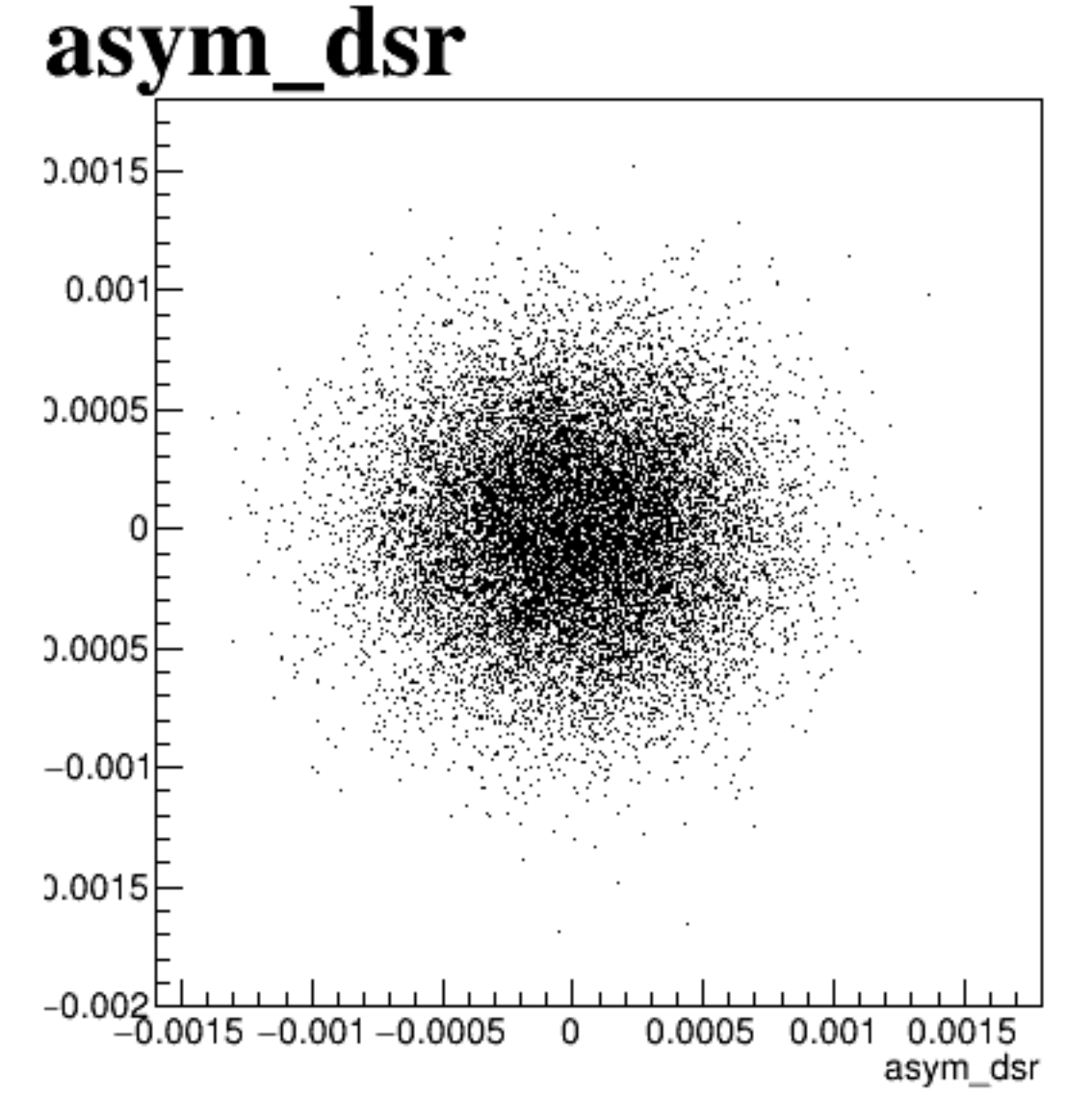
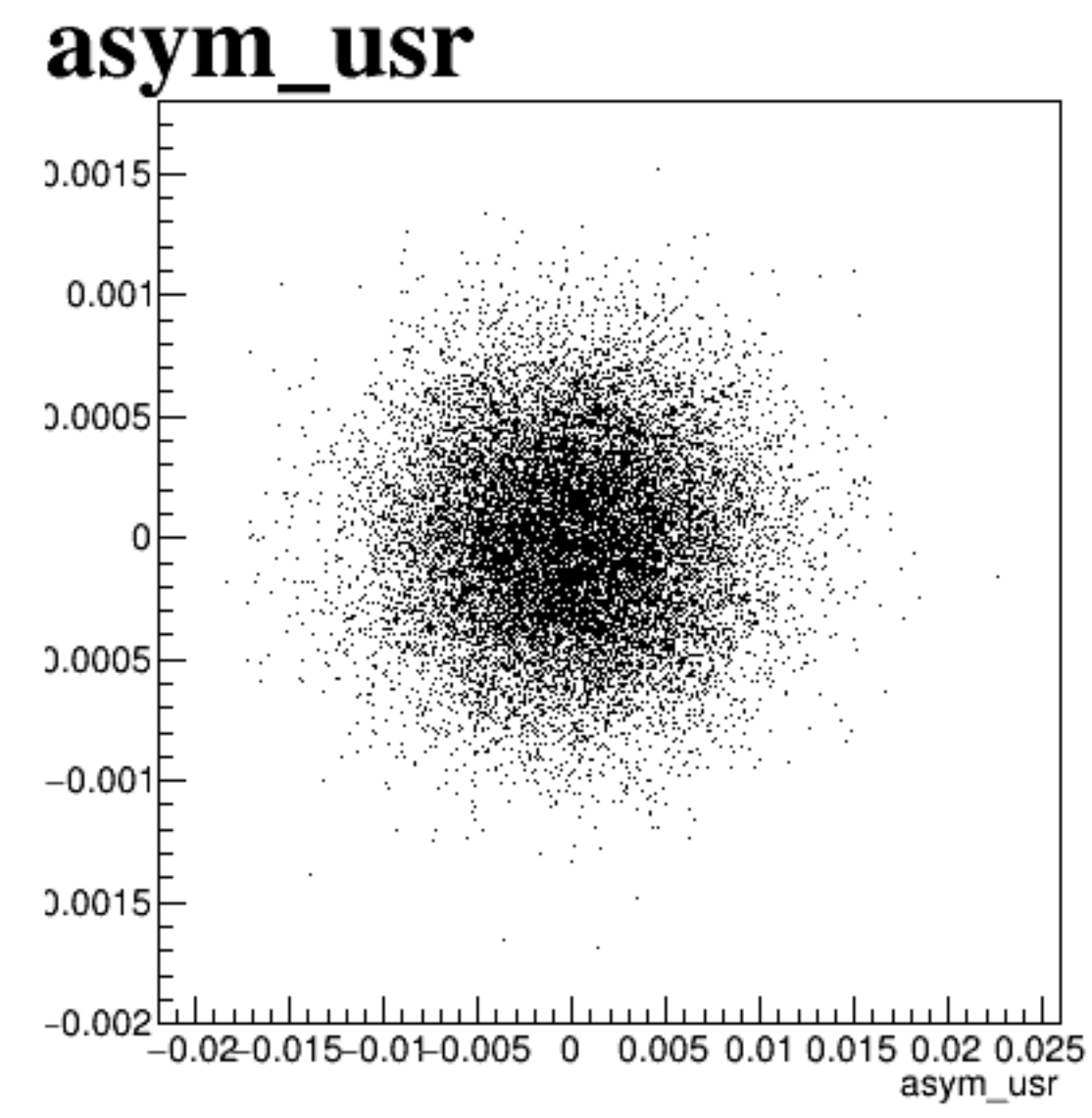
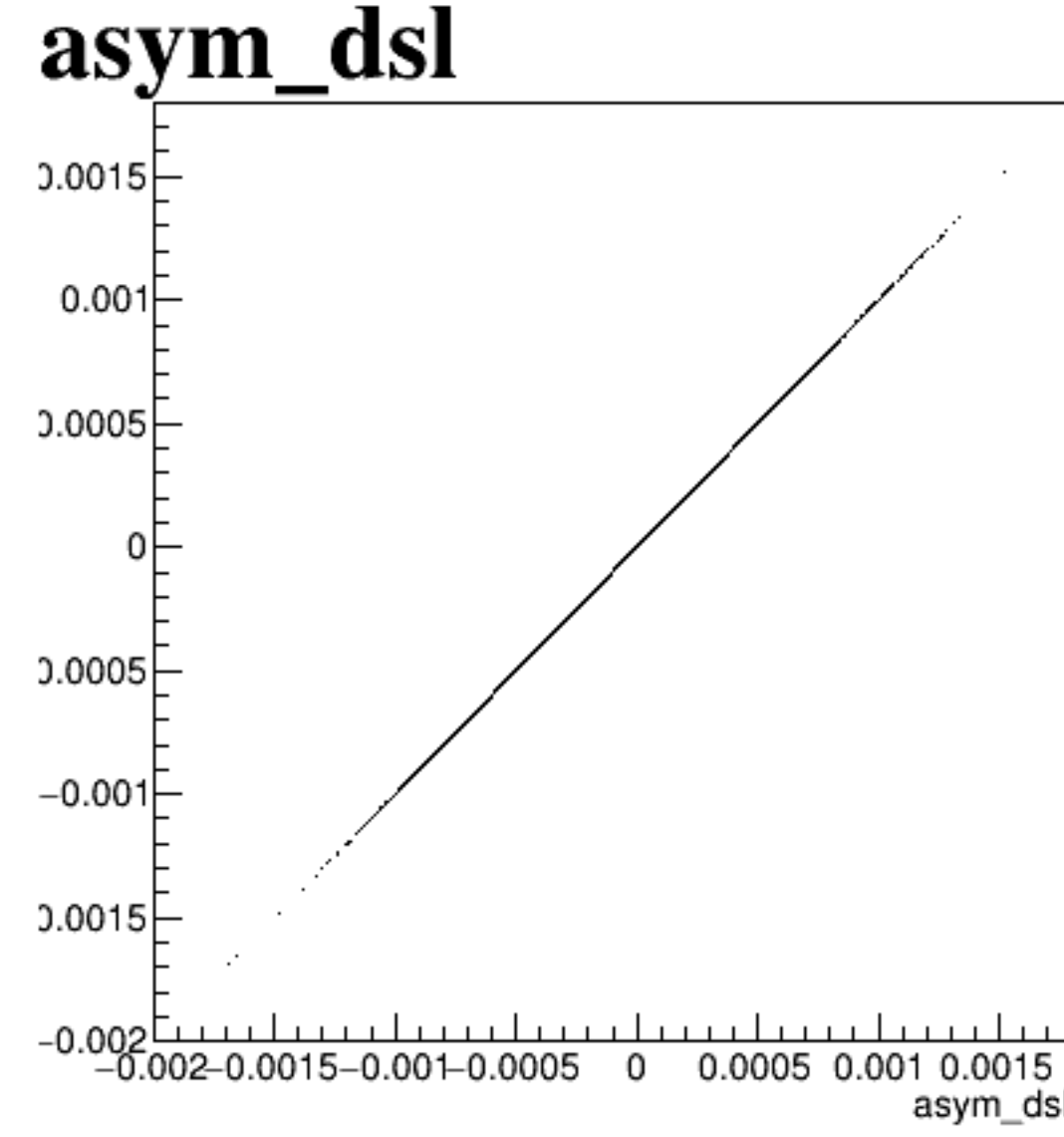
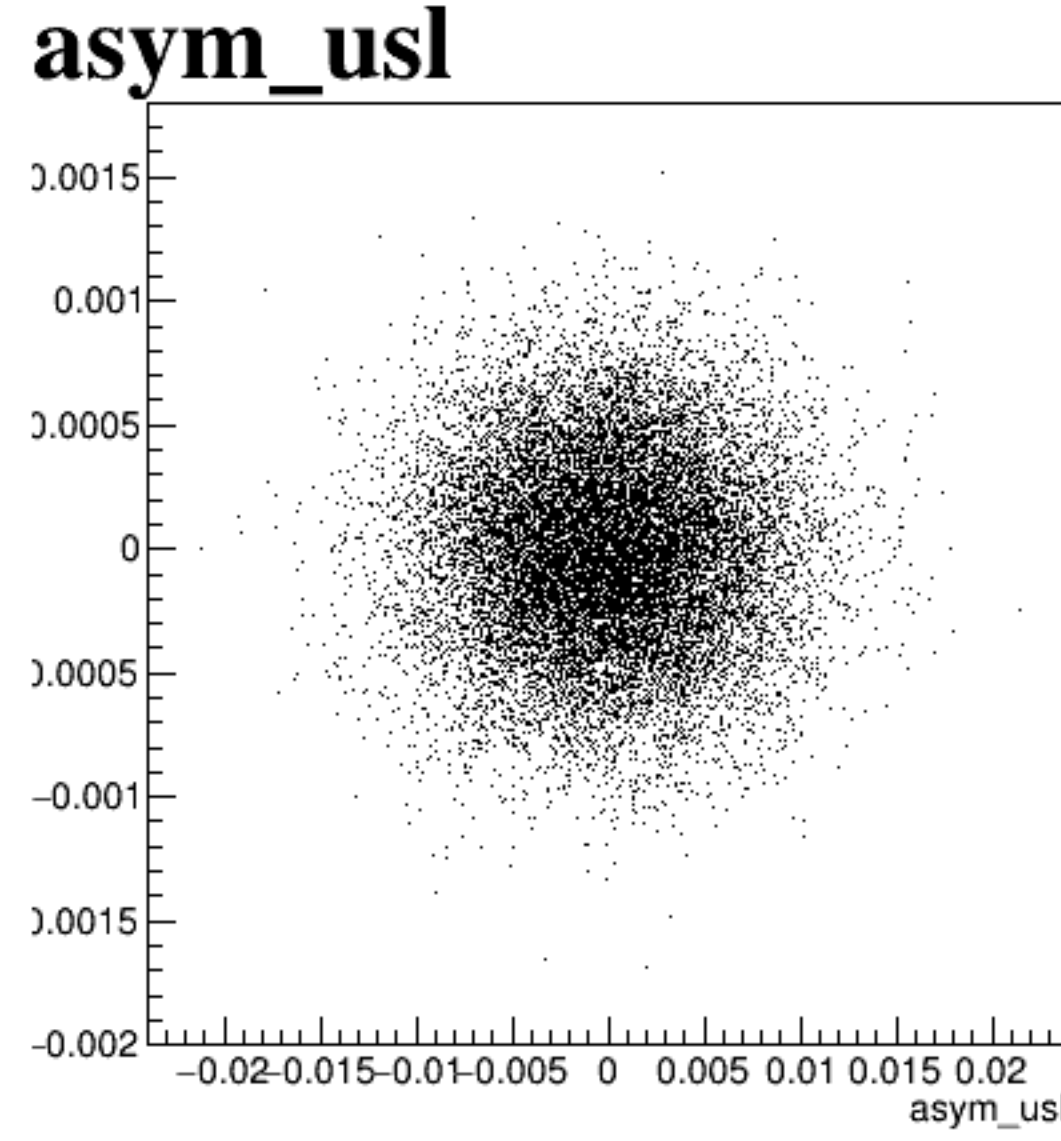
asym\_dsr



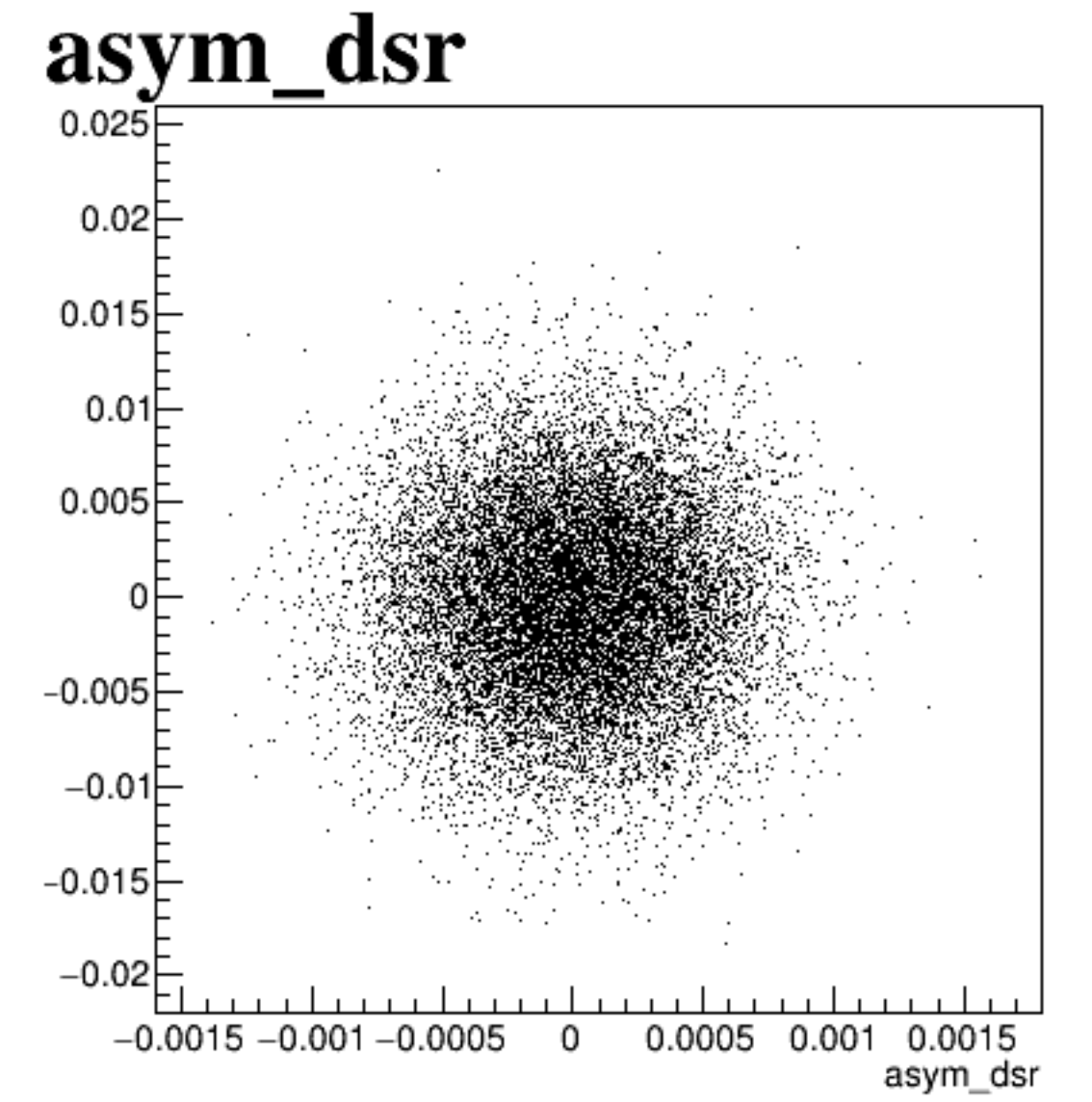
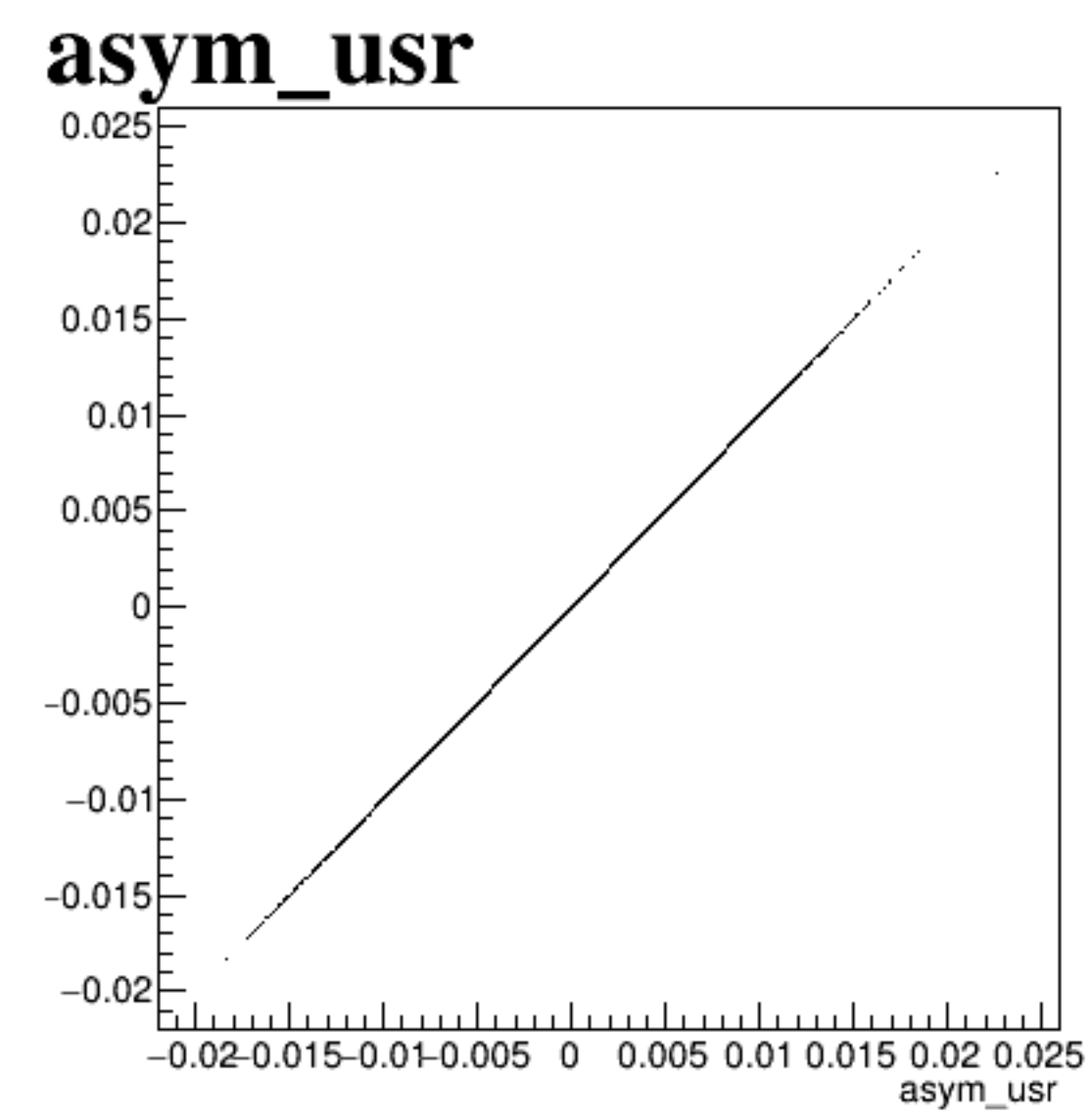
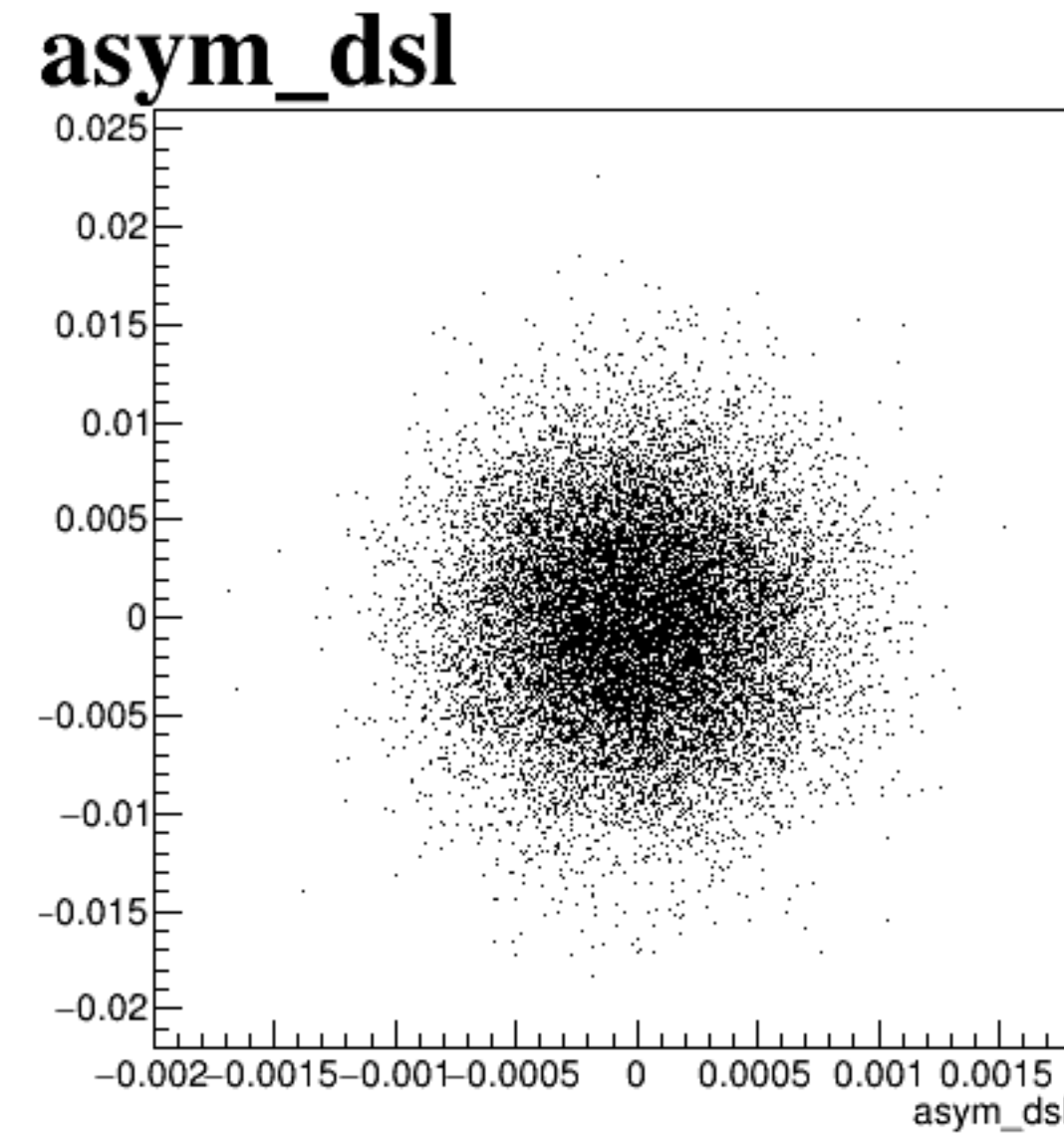
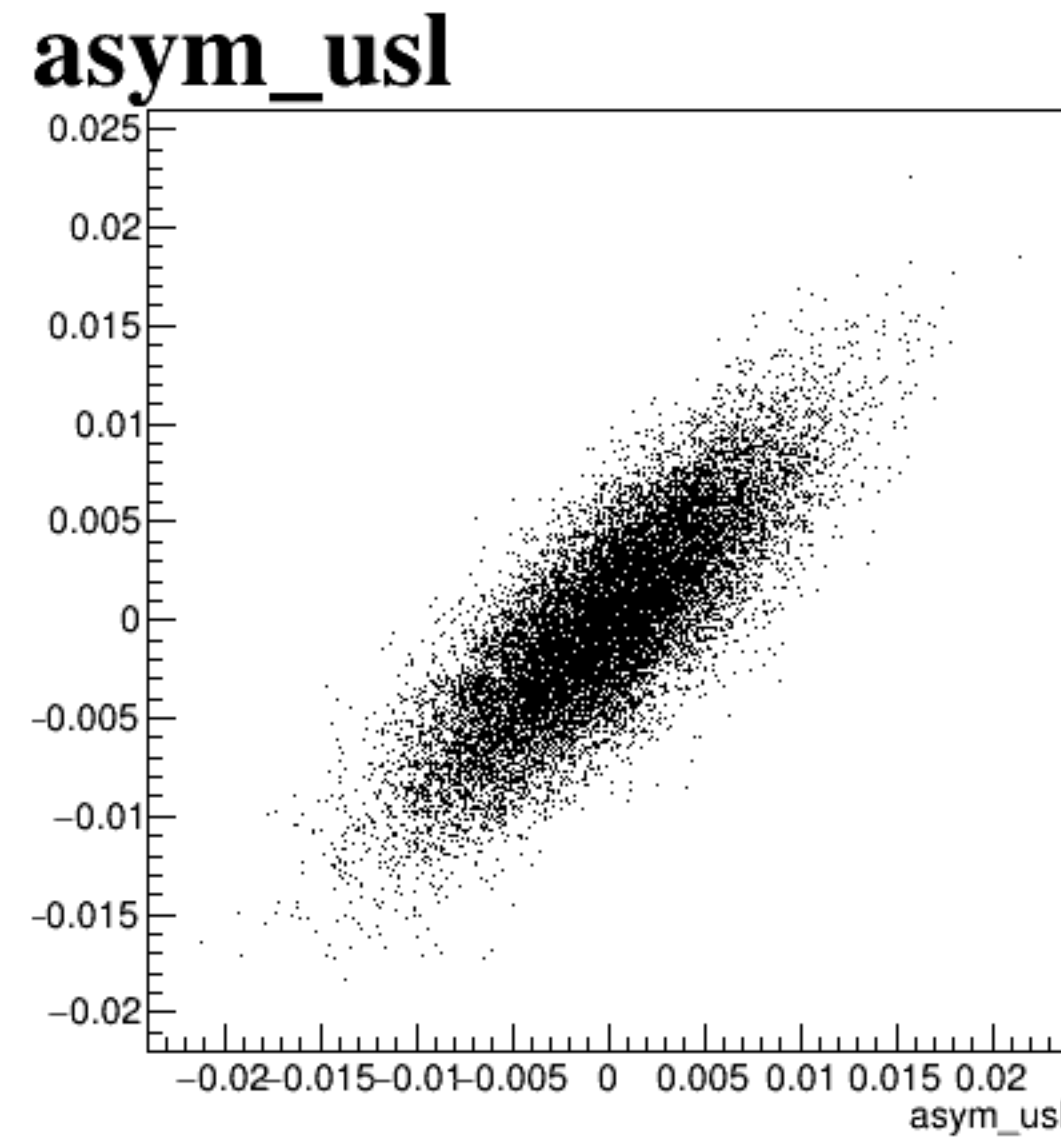
**asym\_usl**



**asym\_dsl**



**asym\_usr**



**asym\_dsr**

