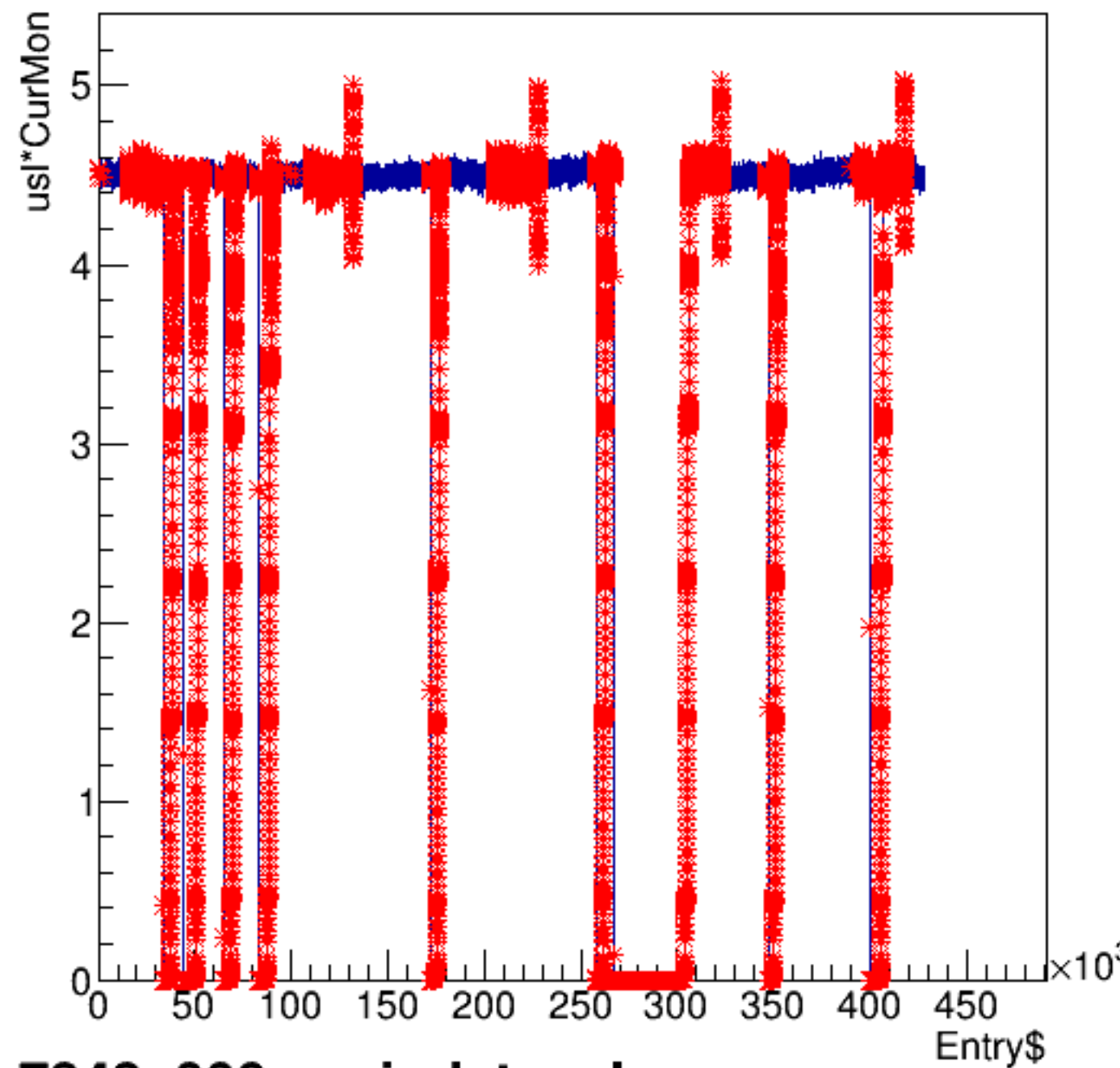
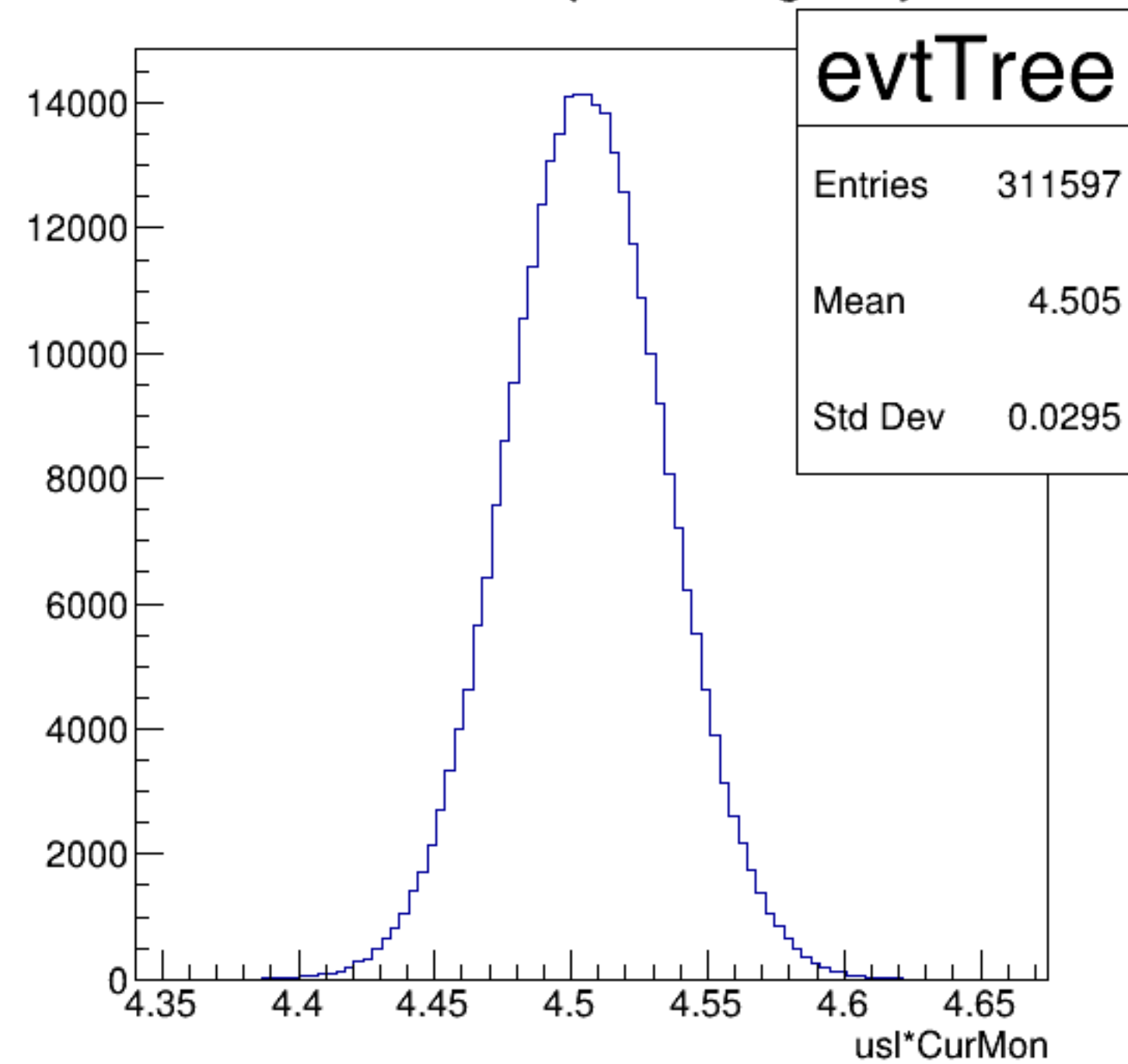


usl*CurMon:Entry\$

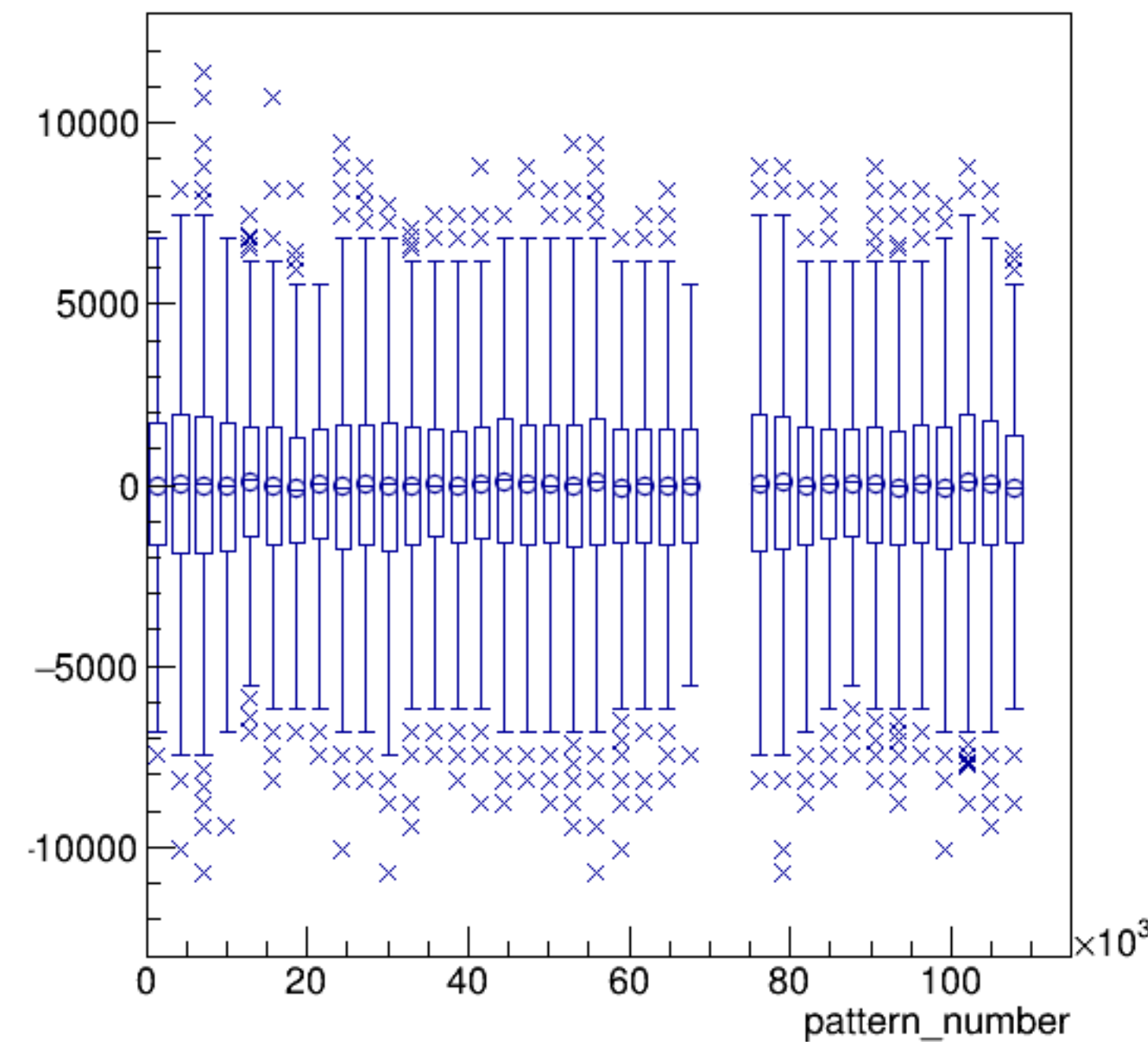


run7343_000_maindet_usl.png

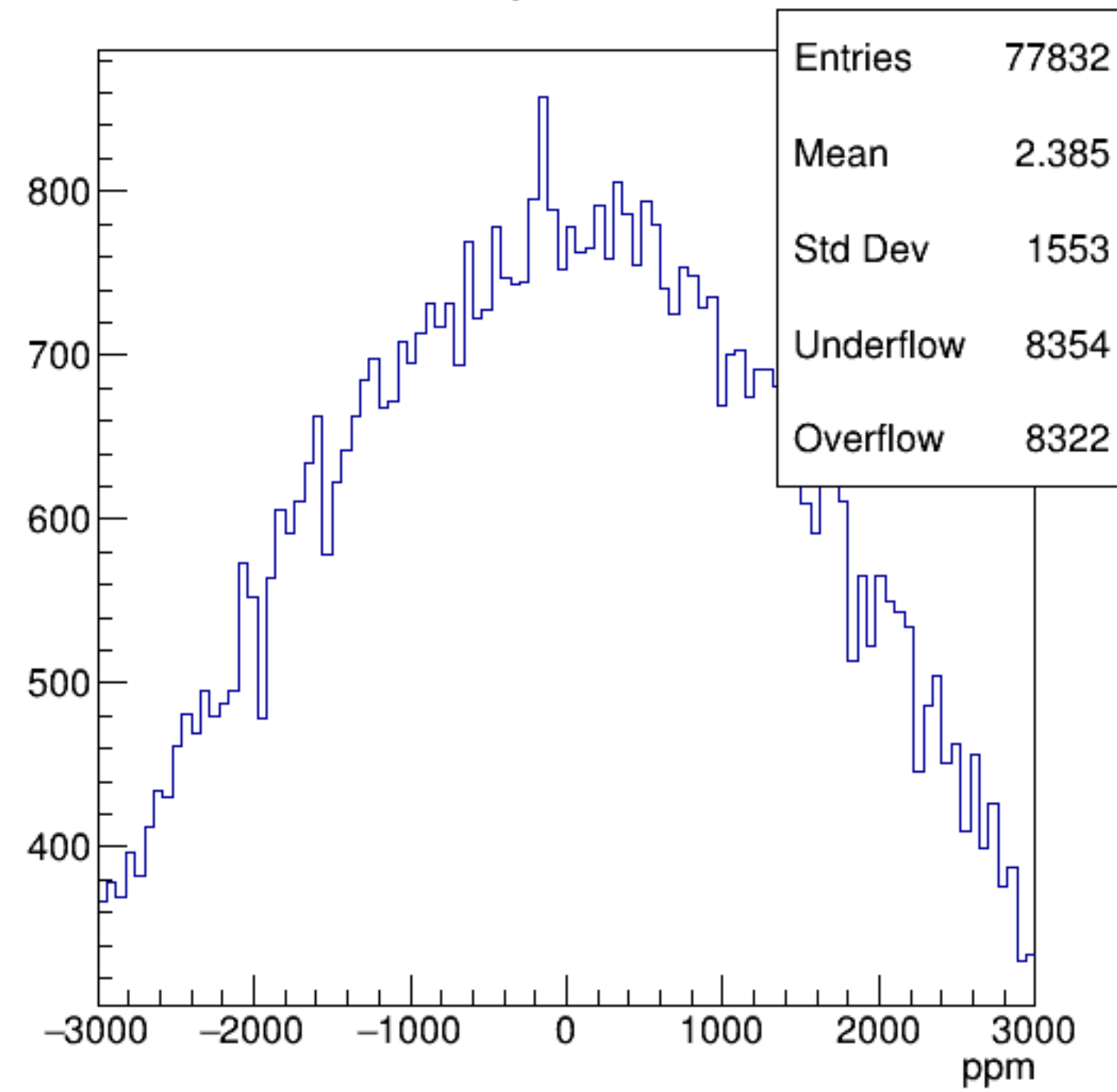
usl*CurMon {ErrorFlag==0}



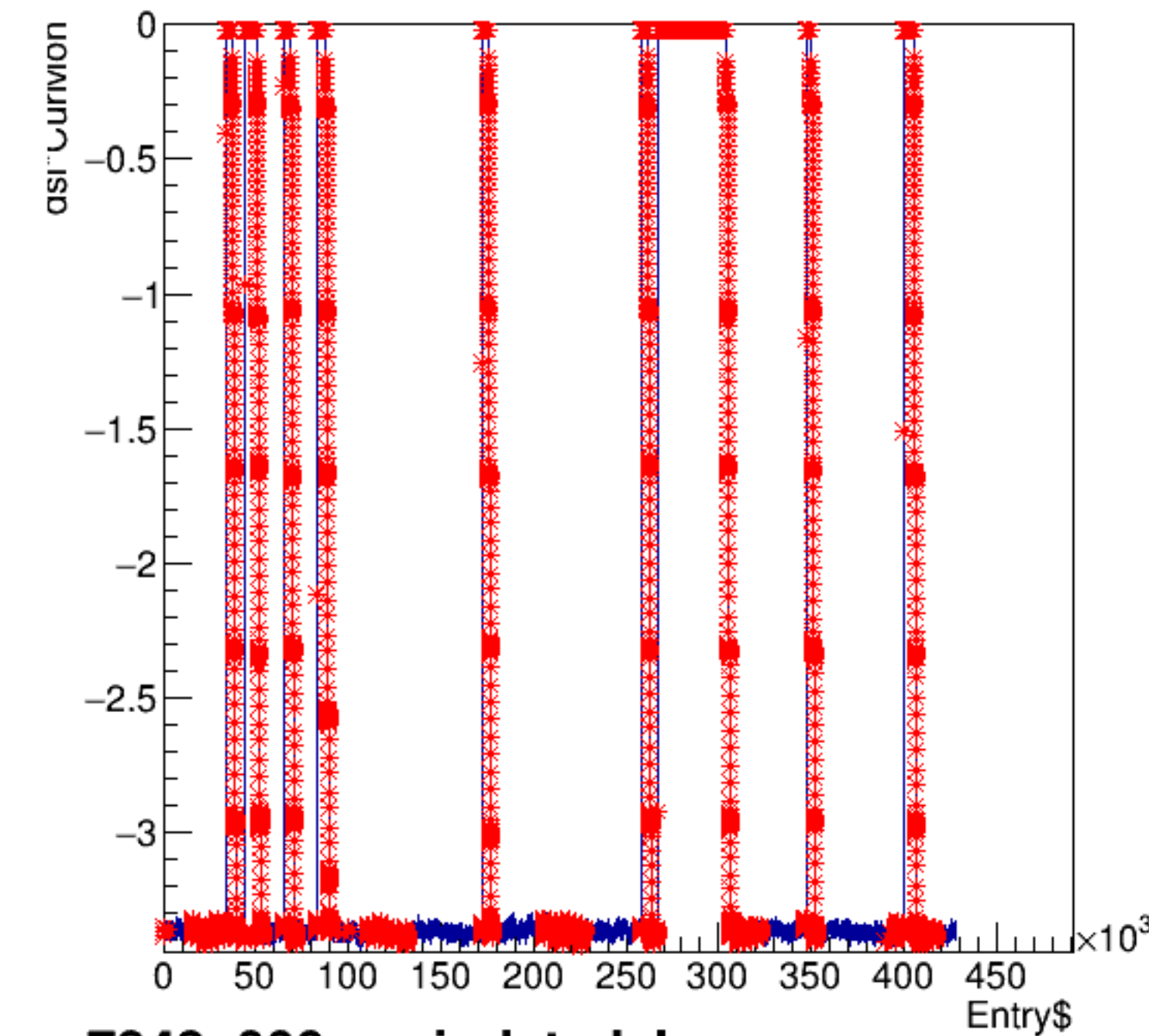
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl

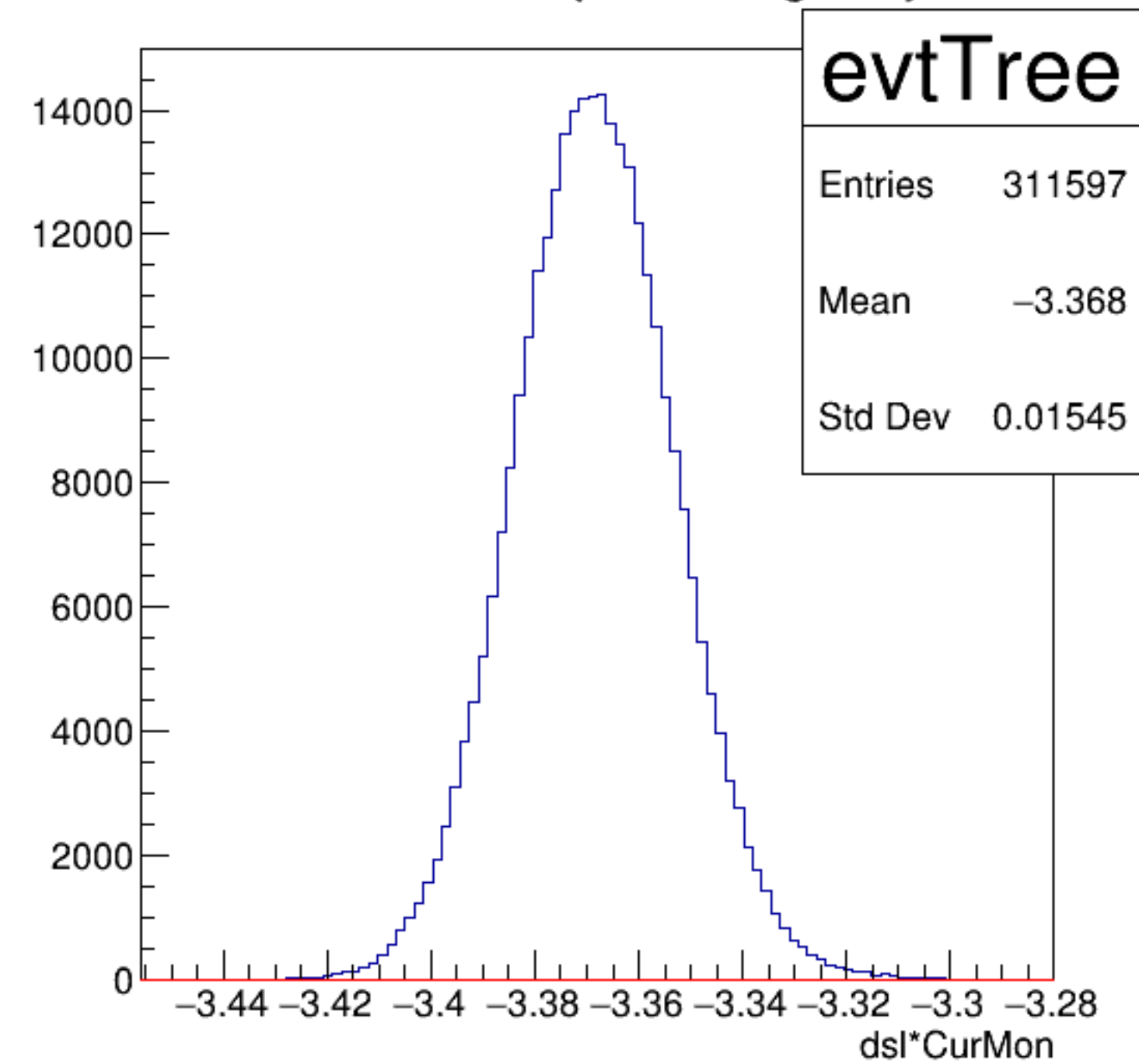


dsl*CurMon:Entry\$

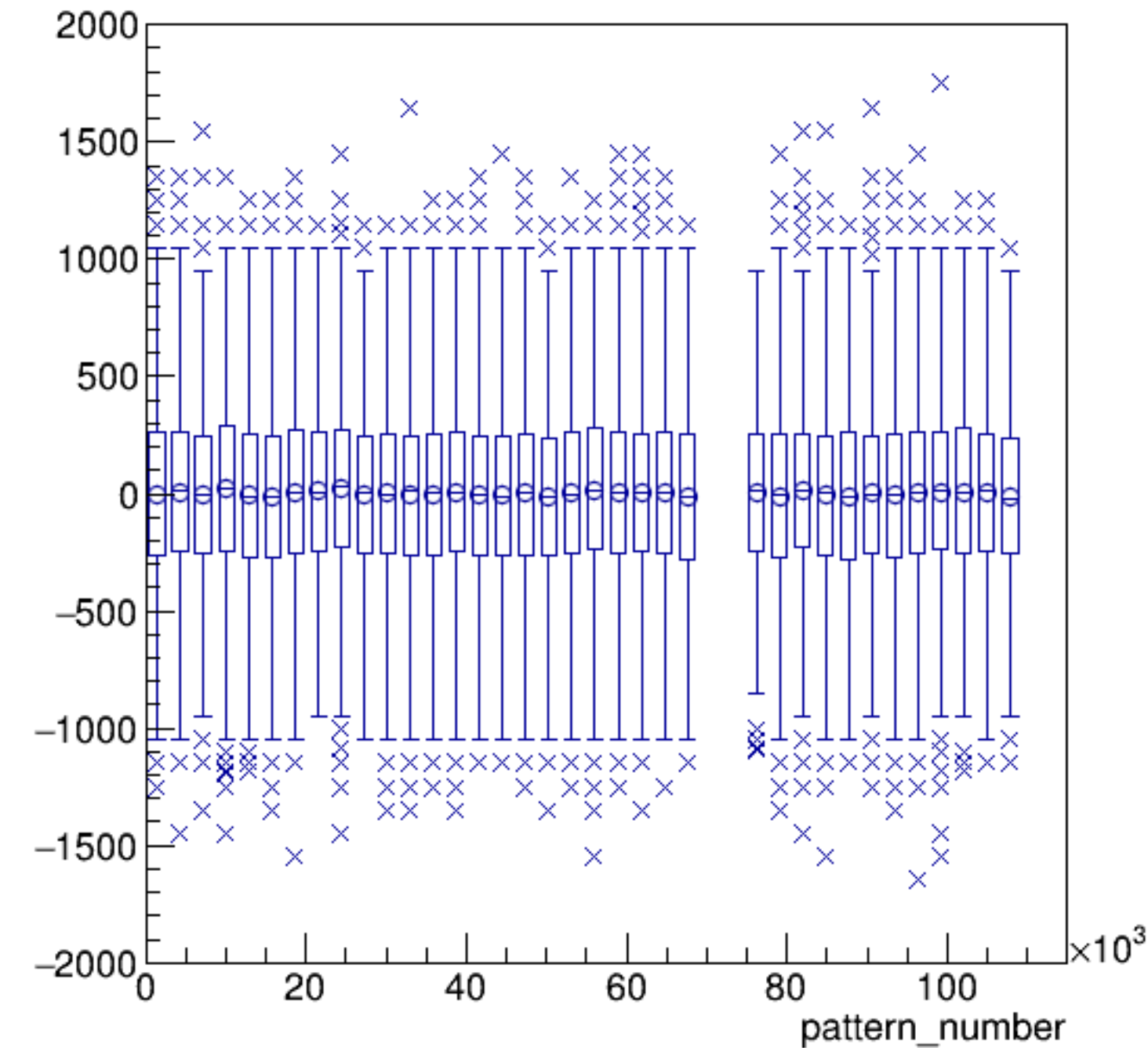


run7343_000_maindet_dsl.png

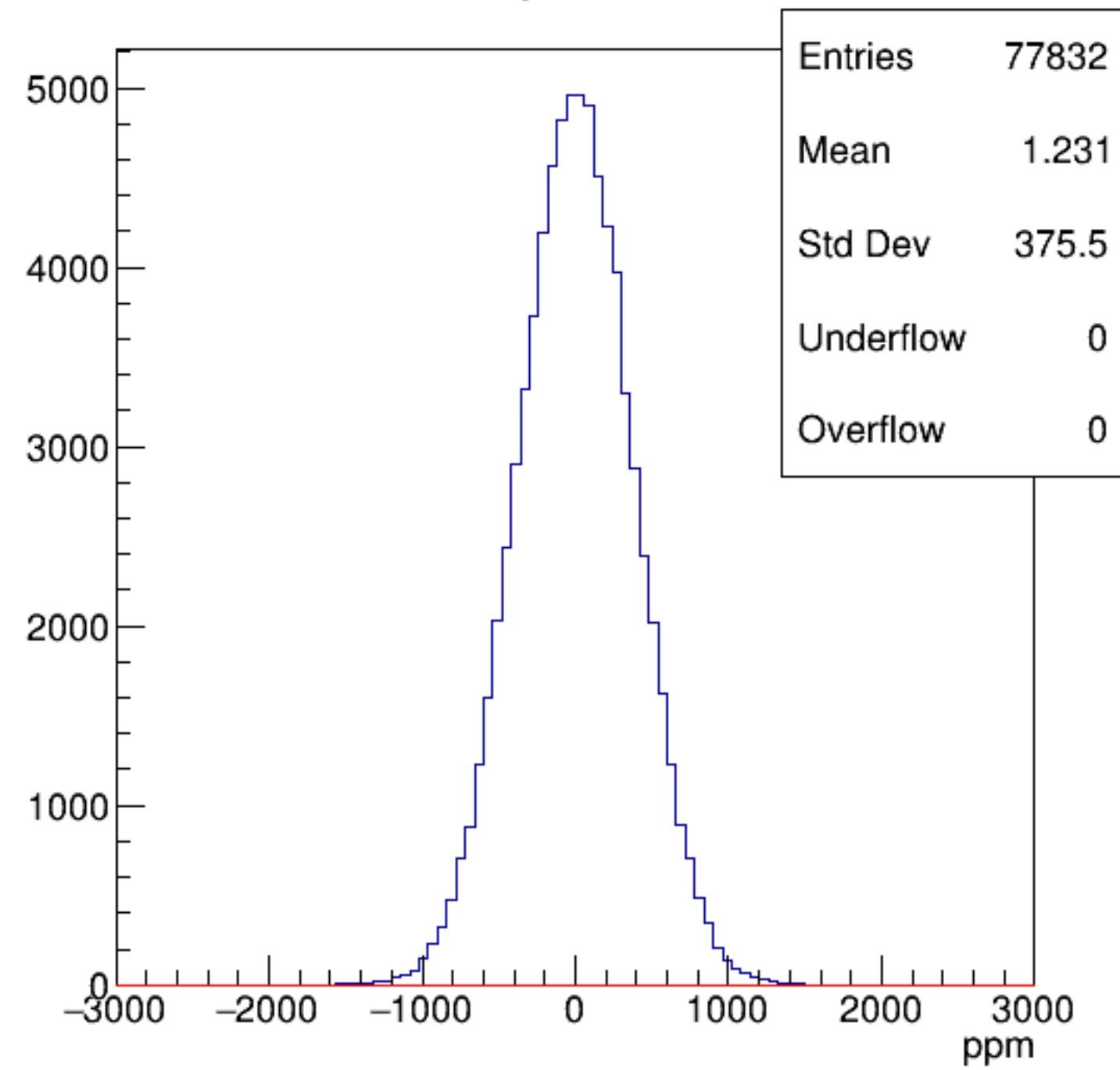
dsl*CurMon {ErrorFlag==0}



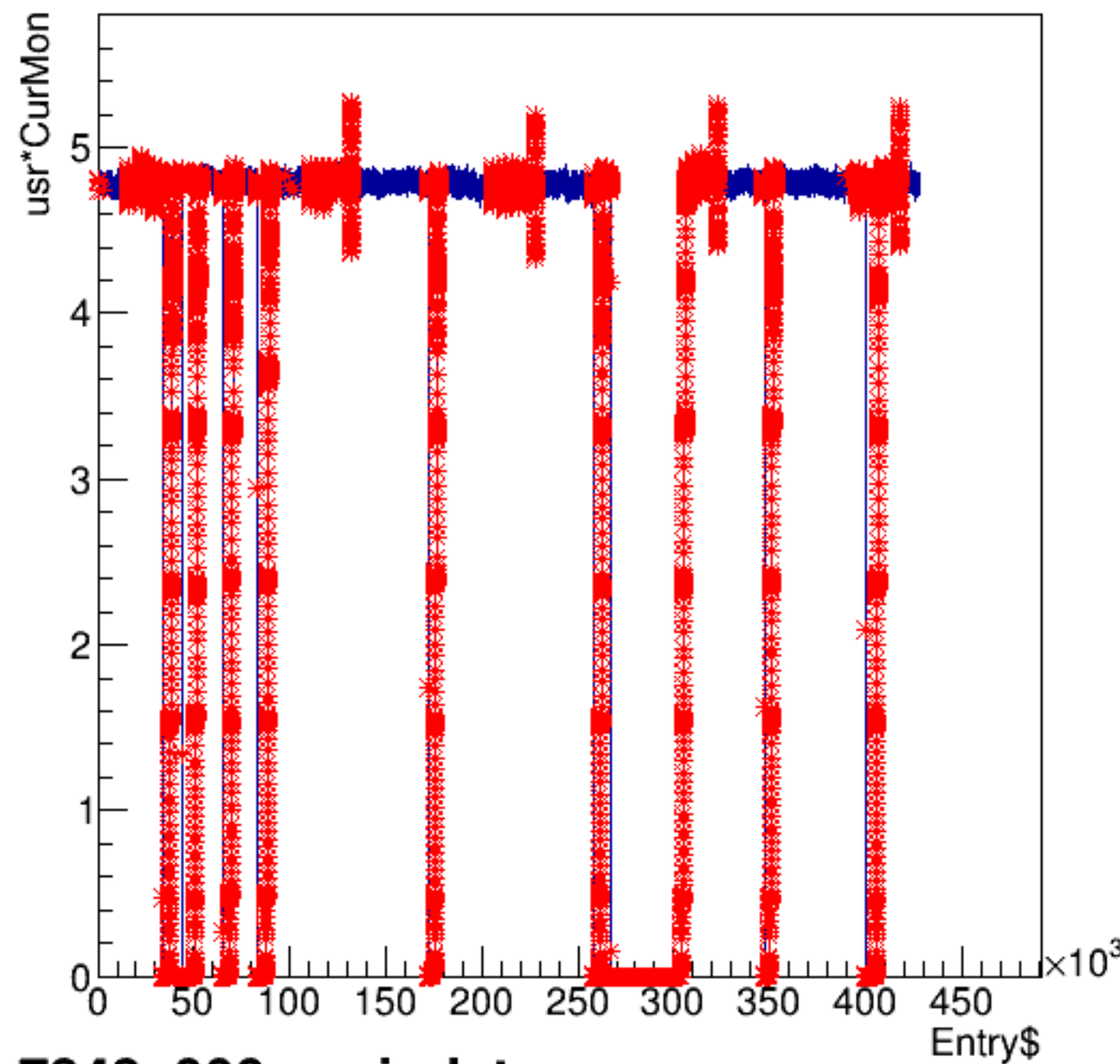
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl

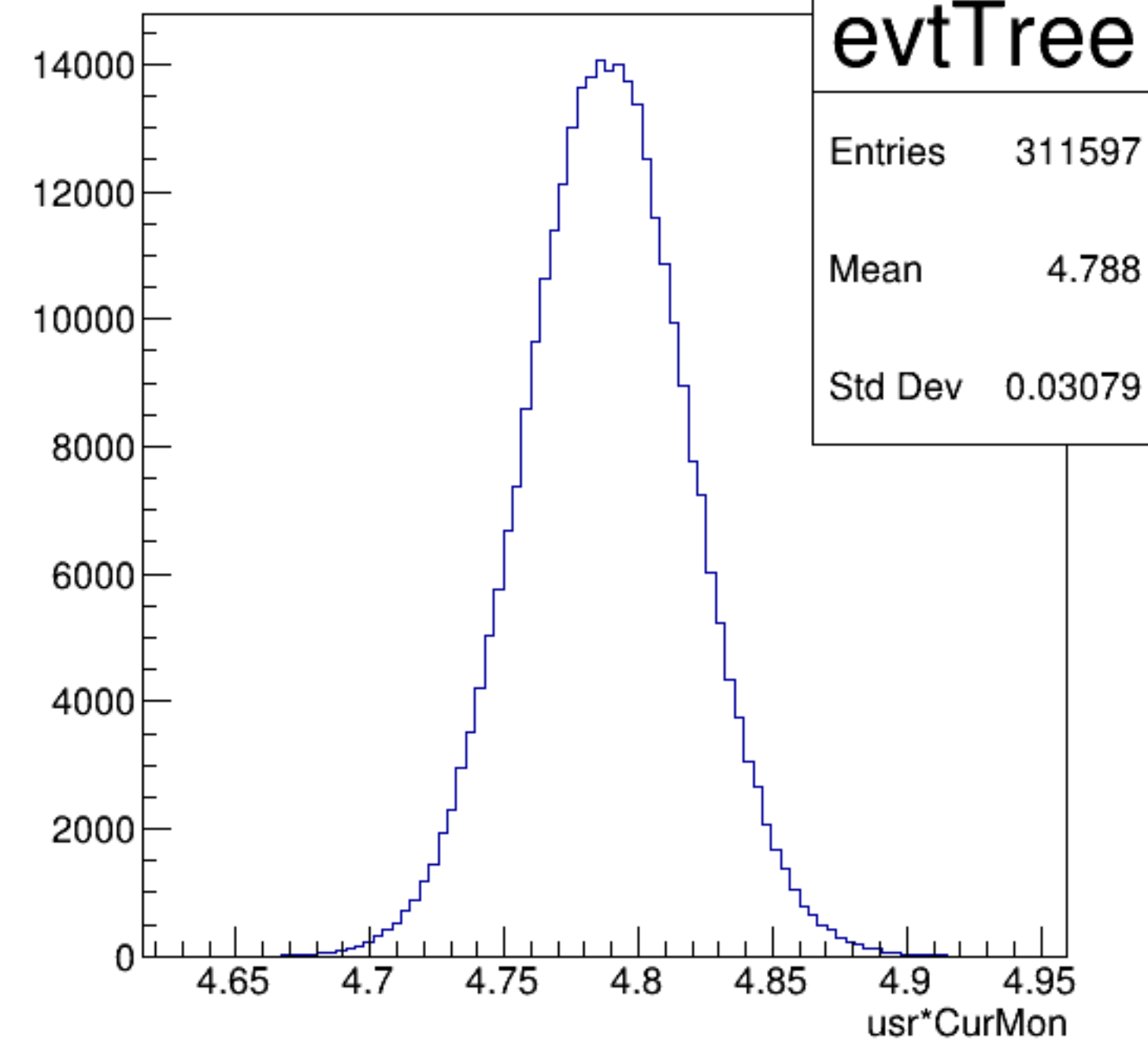


usr*CurMon:Entry\$



run7343_000_maindet_usr.png

usr*CurMon {ErrorFlag==0}



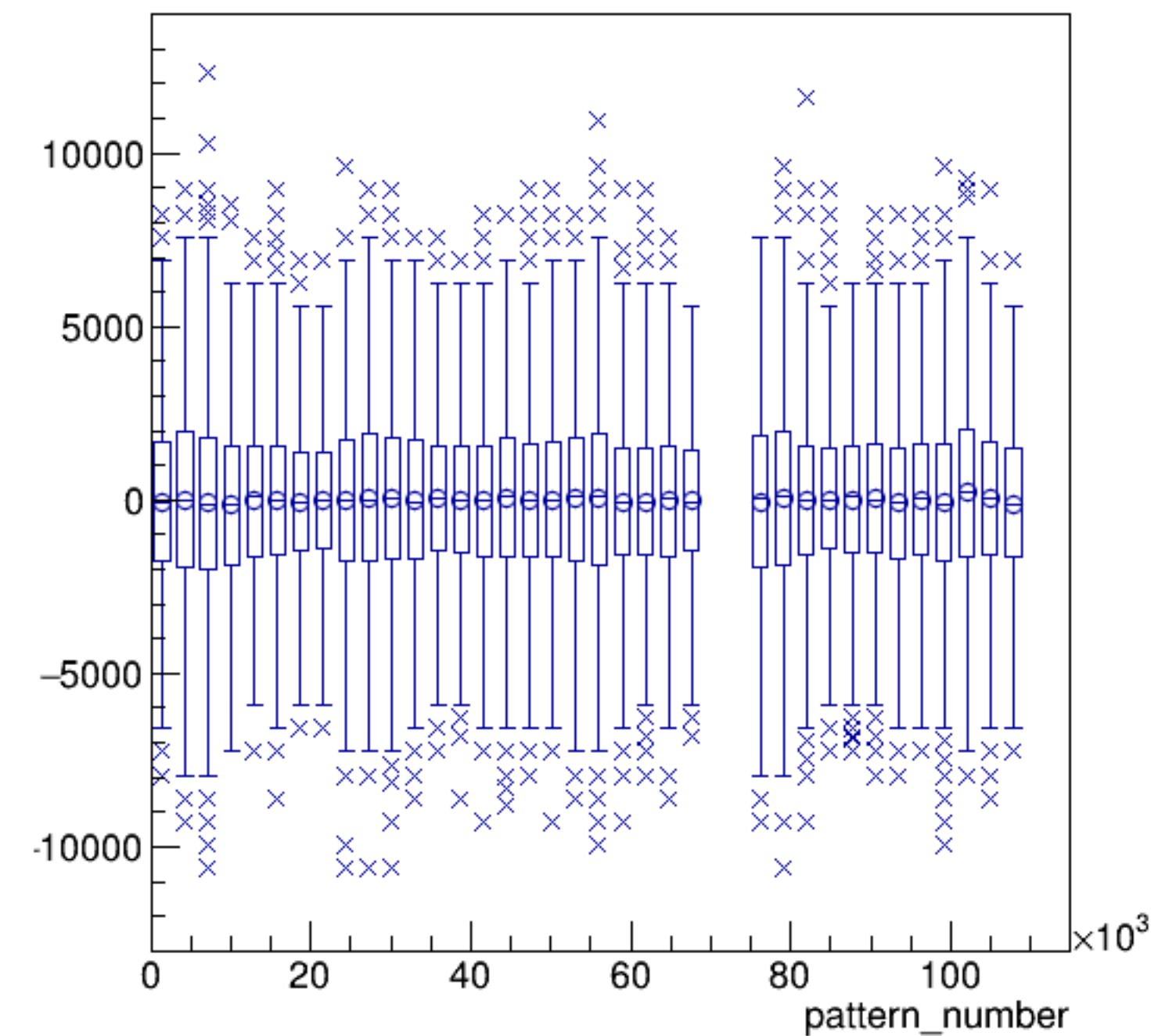
evtTree

Entries 311597

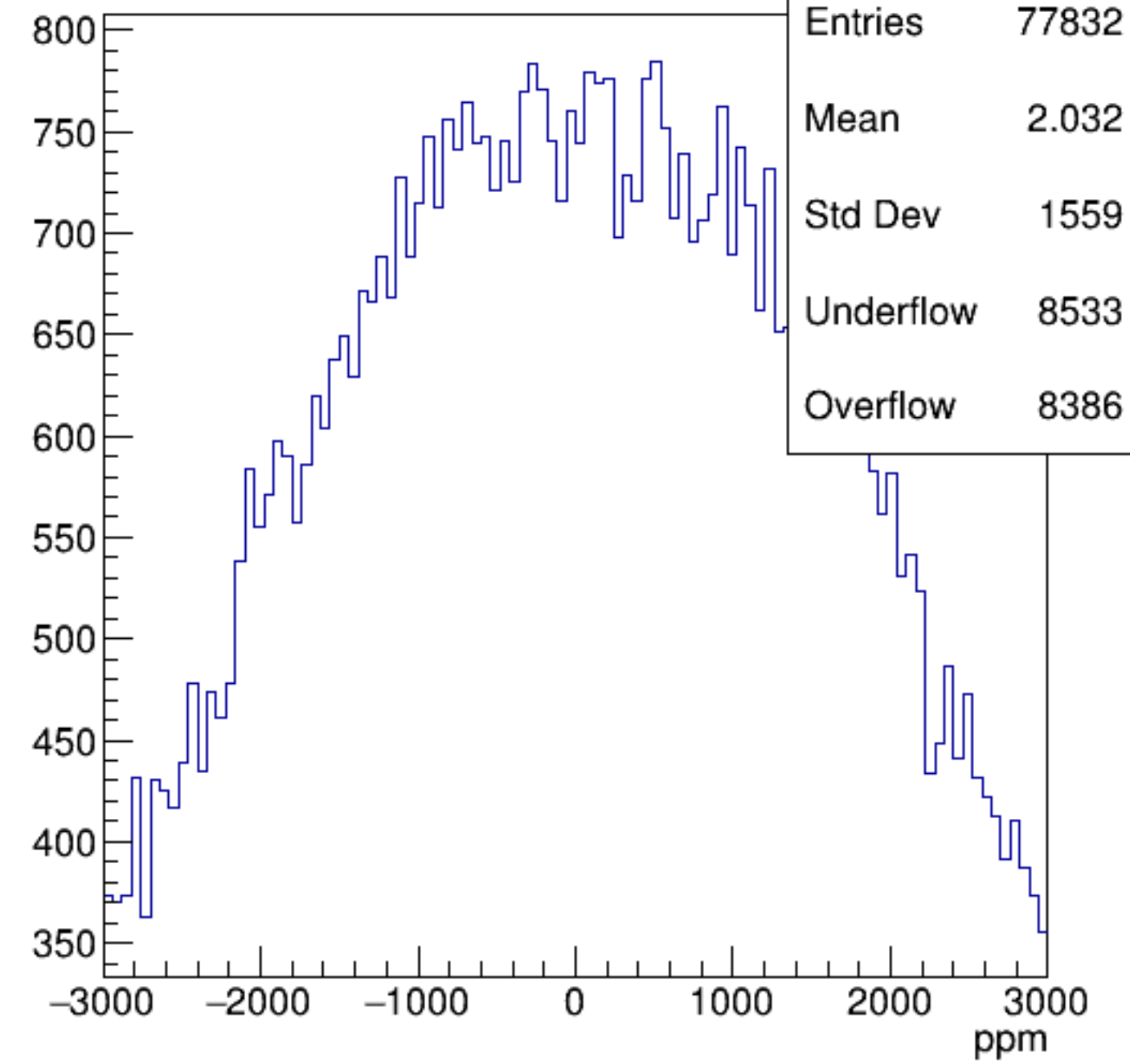
Mean 4.788

Std Dev 0.03079

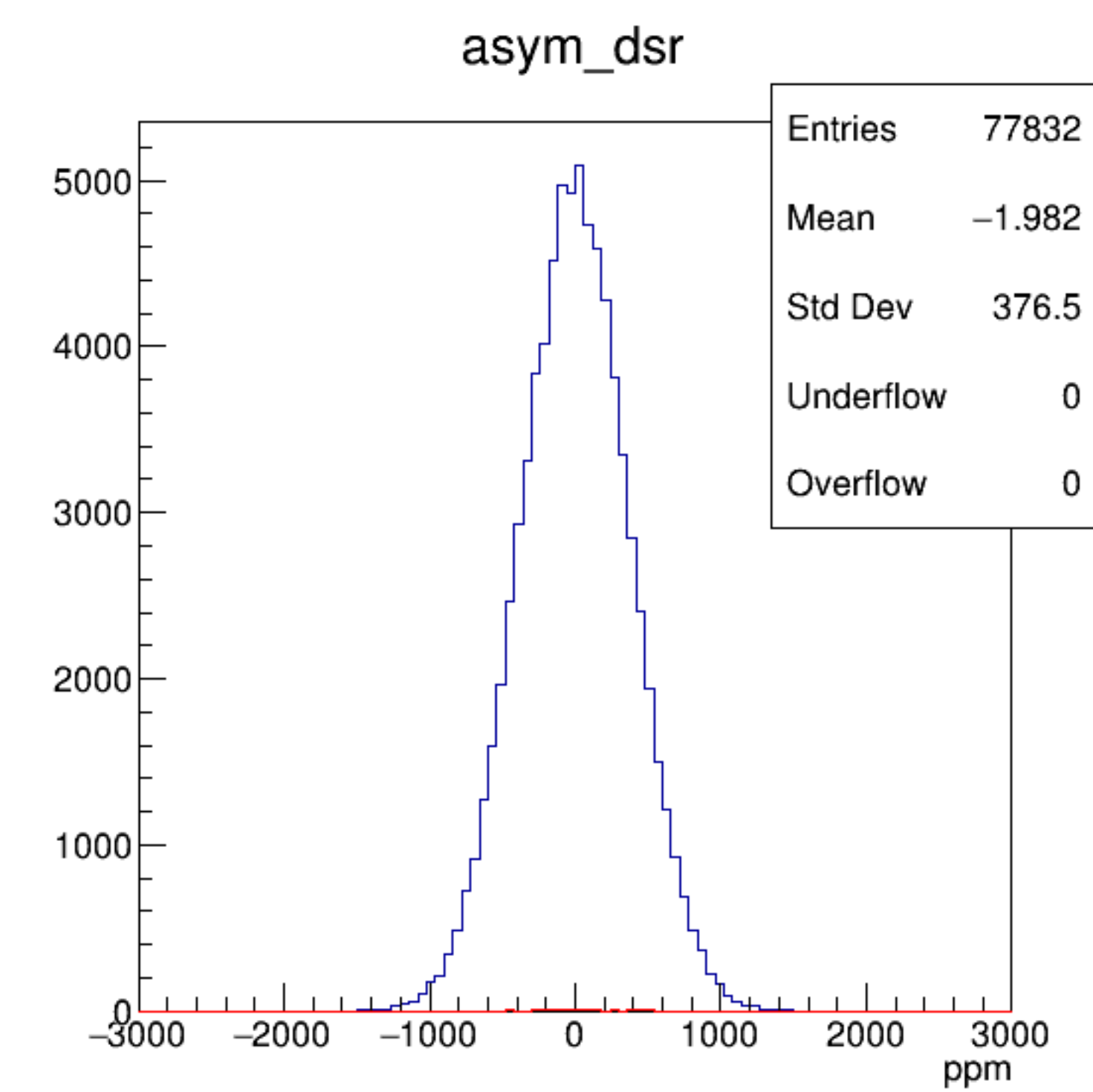
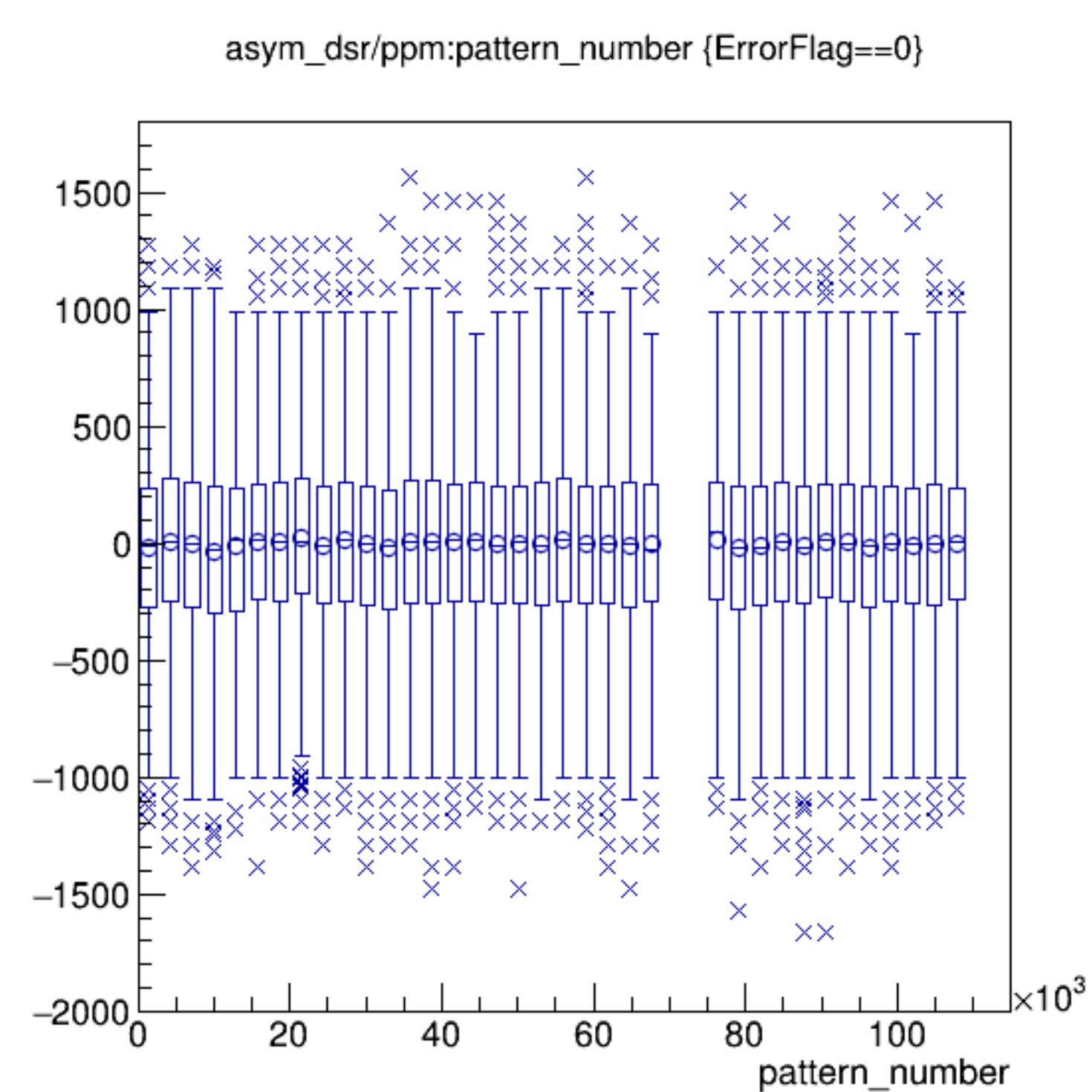
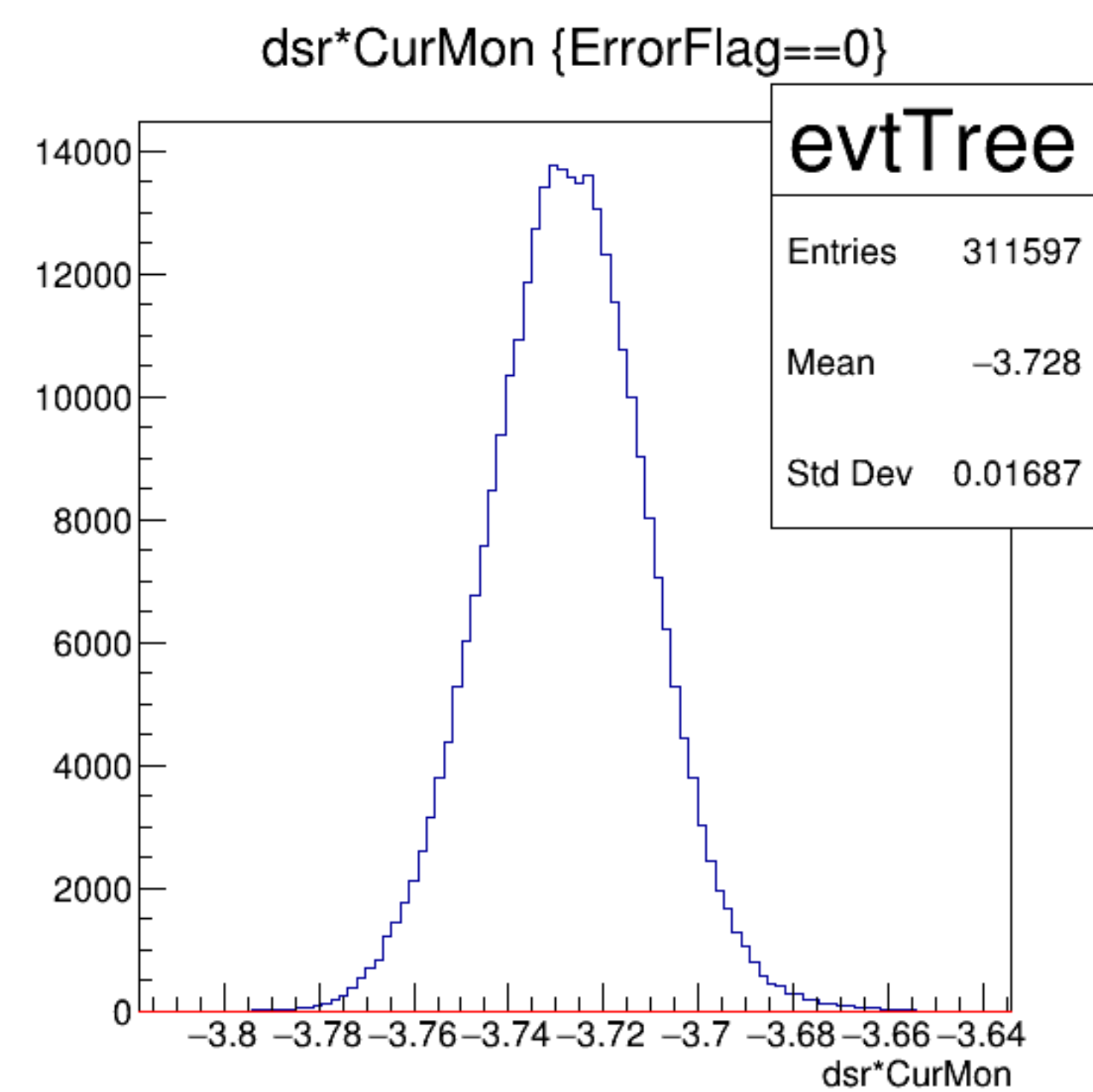
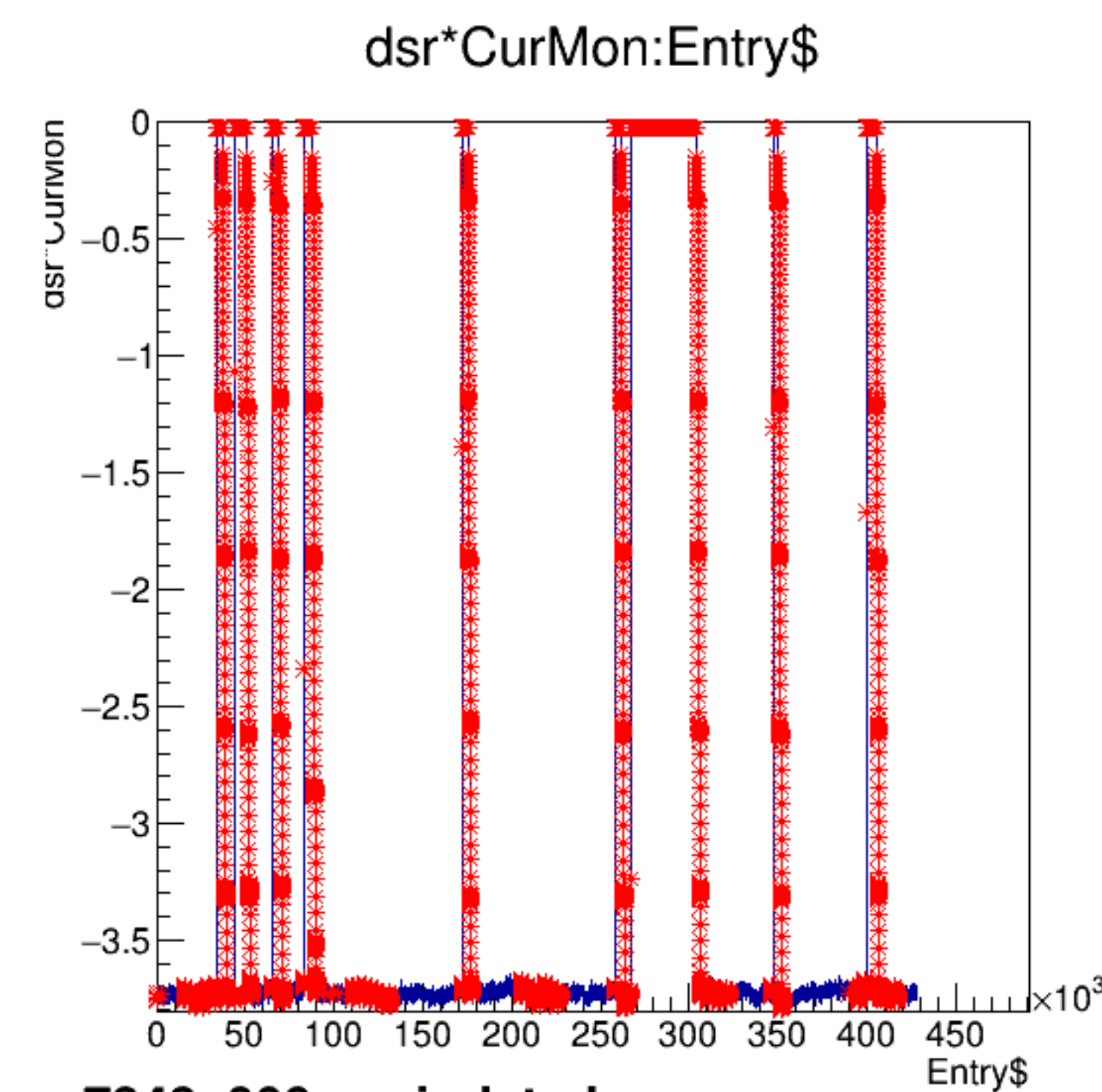
asym_usr/ppm:pattern_number {ErrorFlag==0}



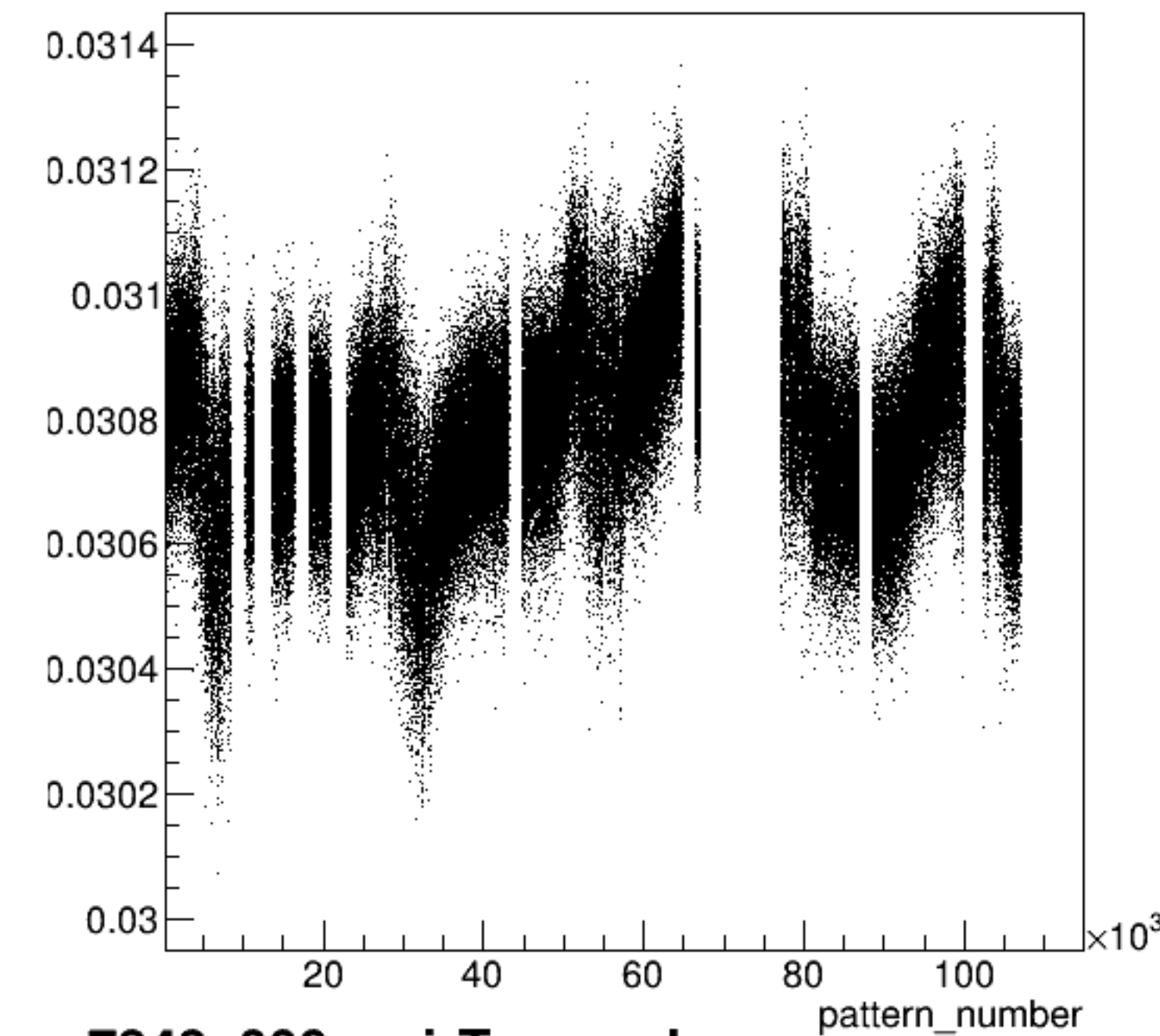
asym_usr



Entries	77832
Mean	2.032
Std Dev	1559
Underflow	8533
Overflow	8386

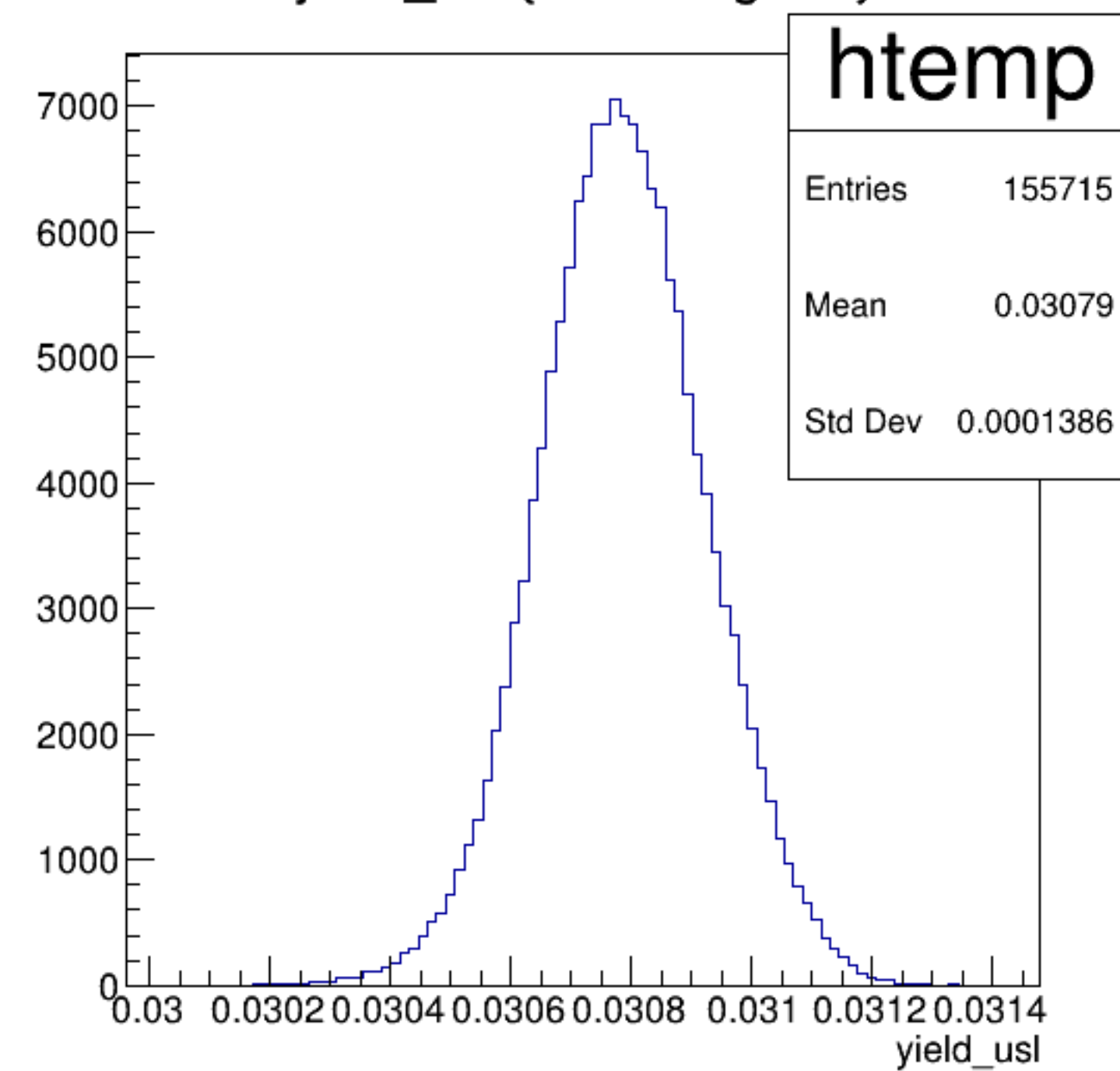


yield_usl:pattern_number {ErrorFlag==0}

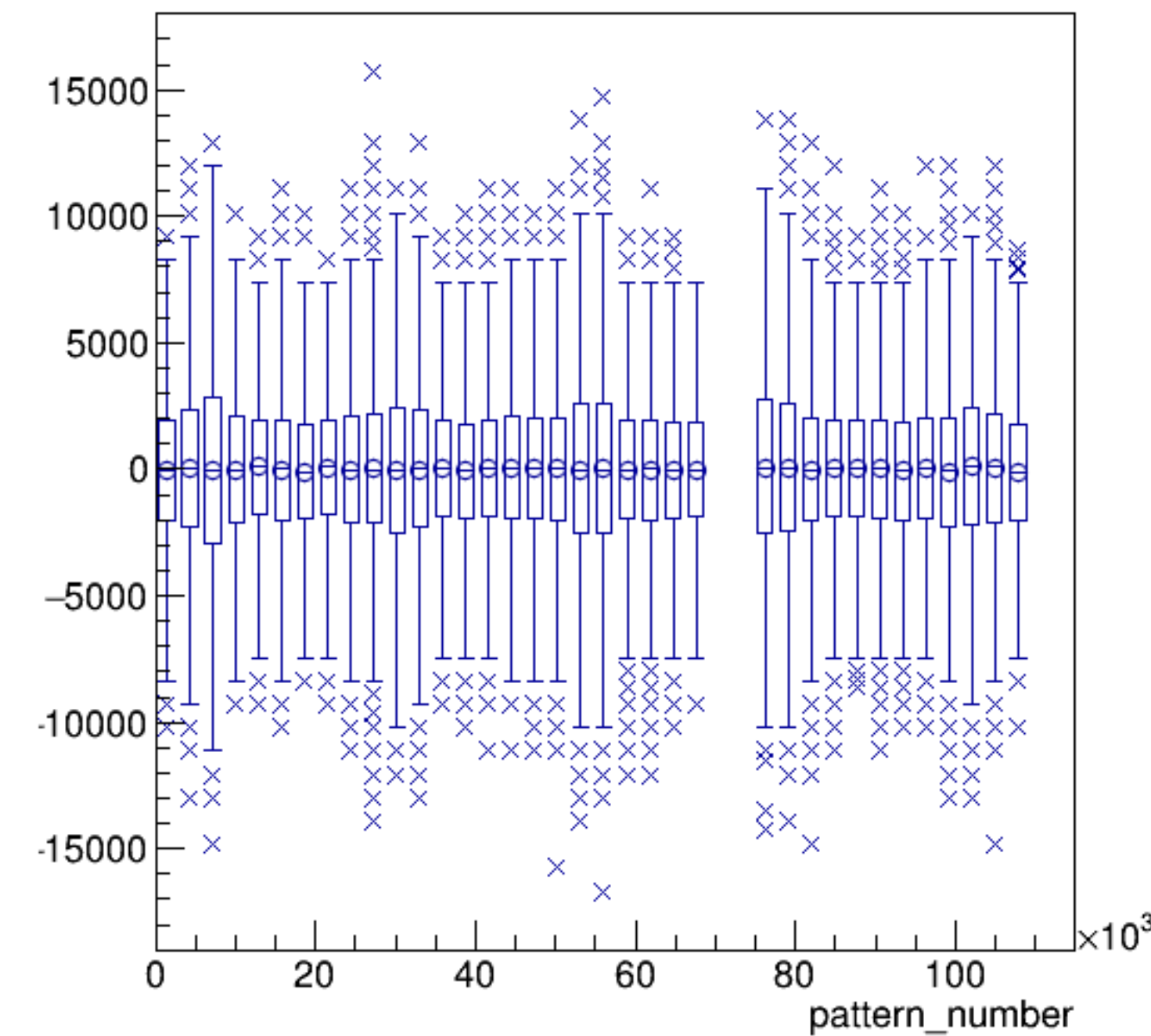


run7343_000_pairTree_usl.png

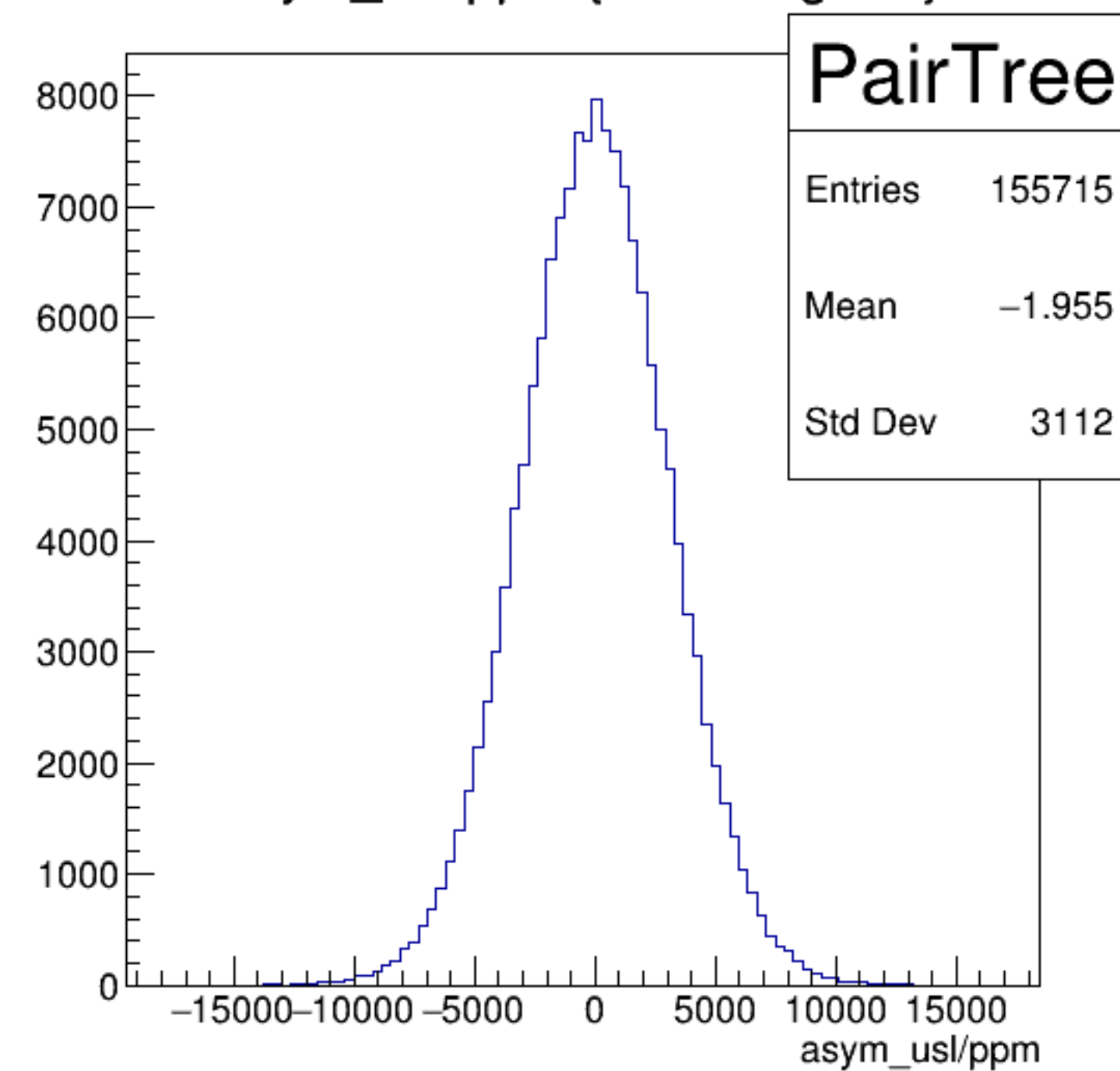
yield_usl {ErrorFlag==0}



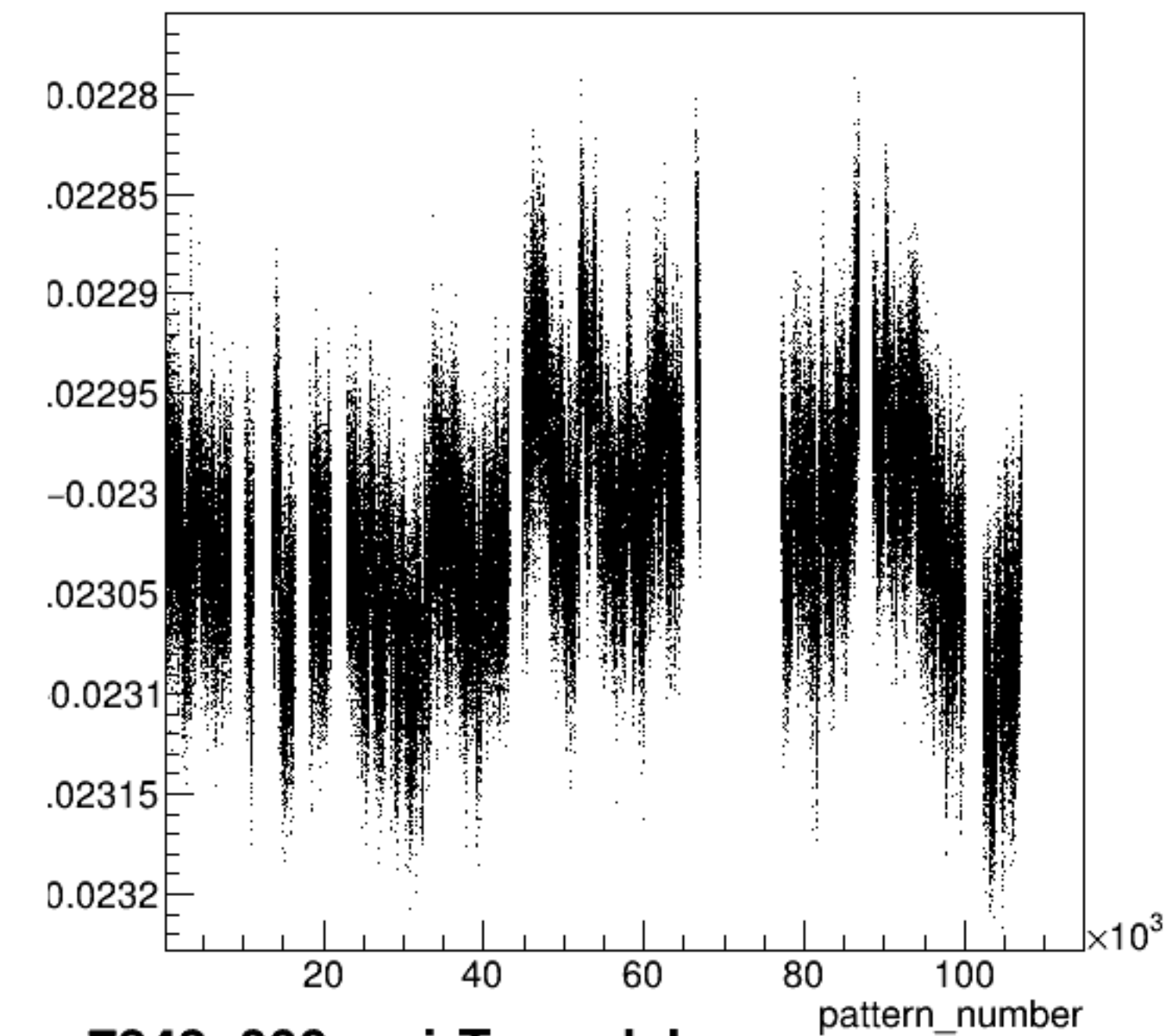
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl/ppm {ErrorFlag==0}

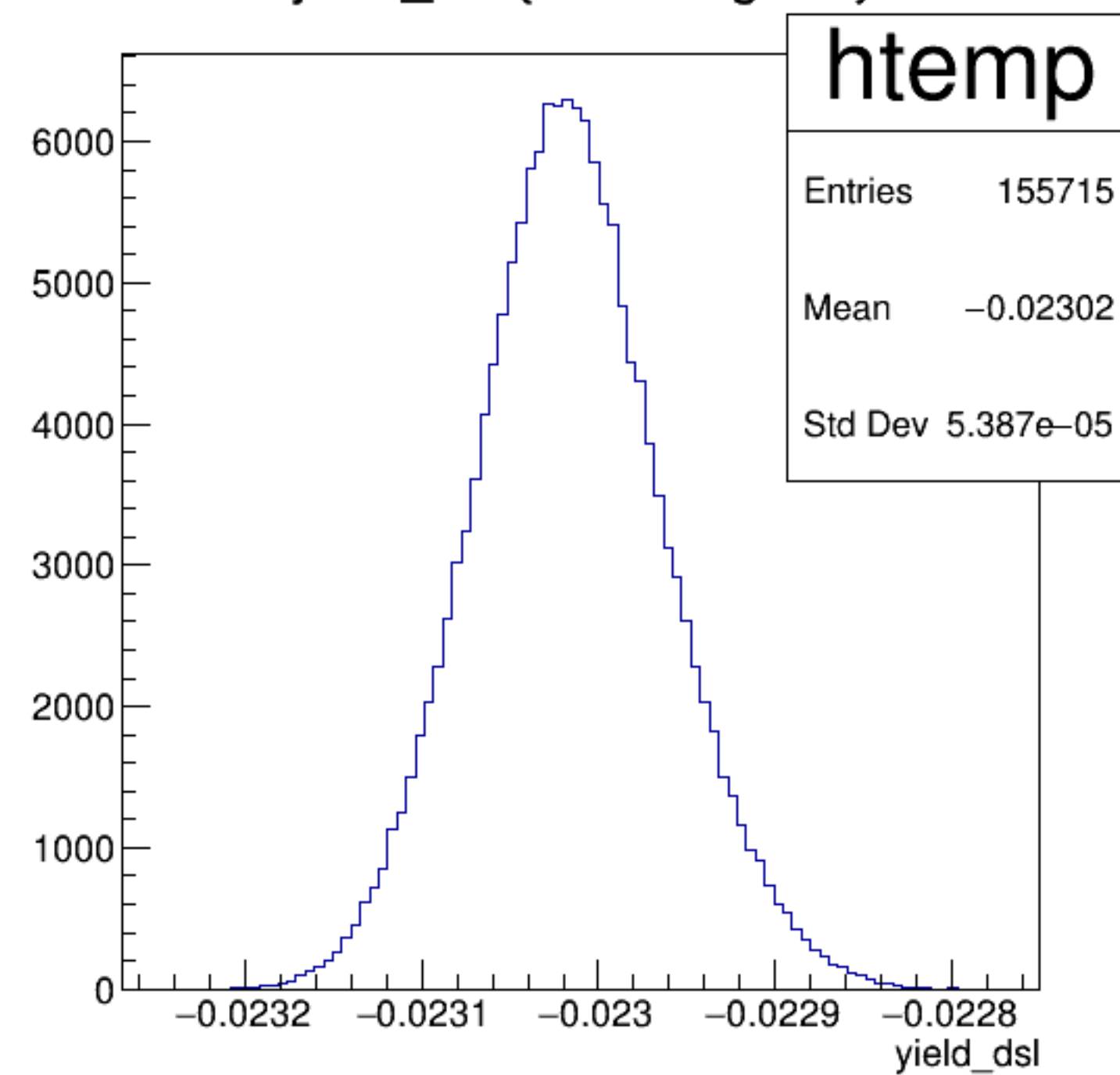


yield_dsl:pattern_number {ErrorFlag==0}

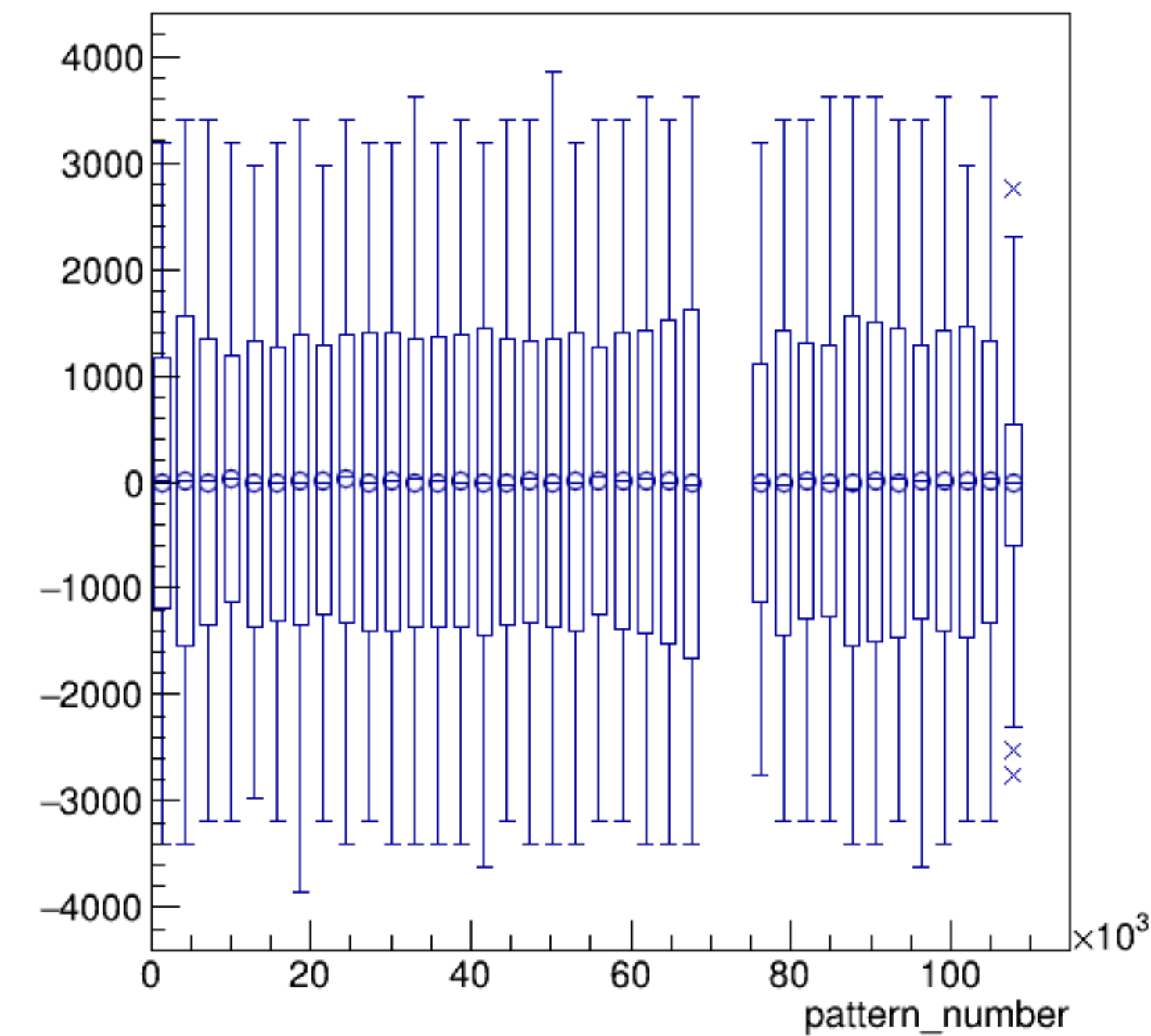


run7343_000_pairTree_dsl.png

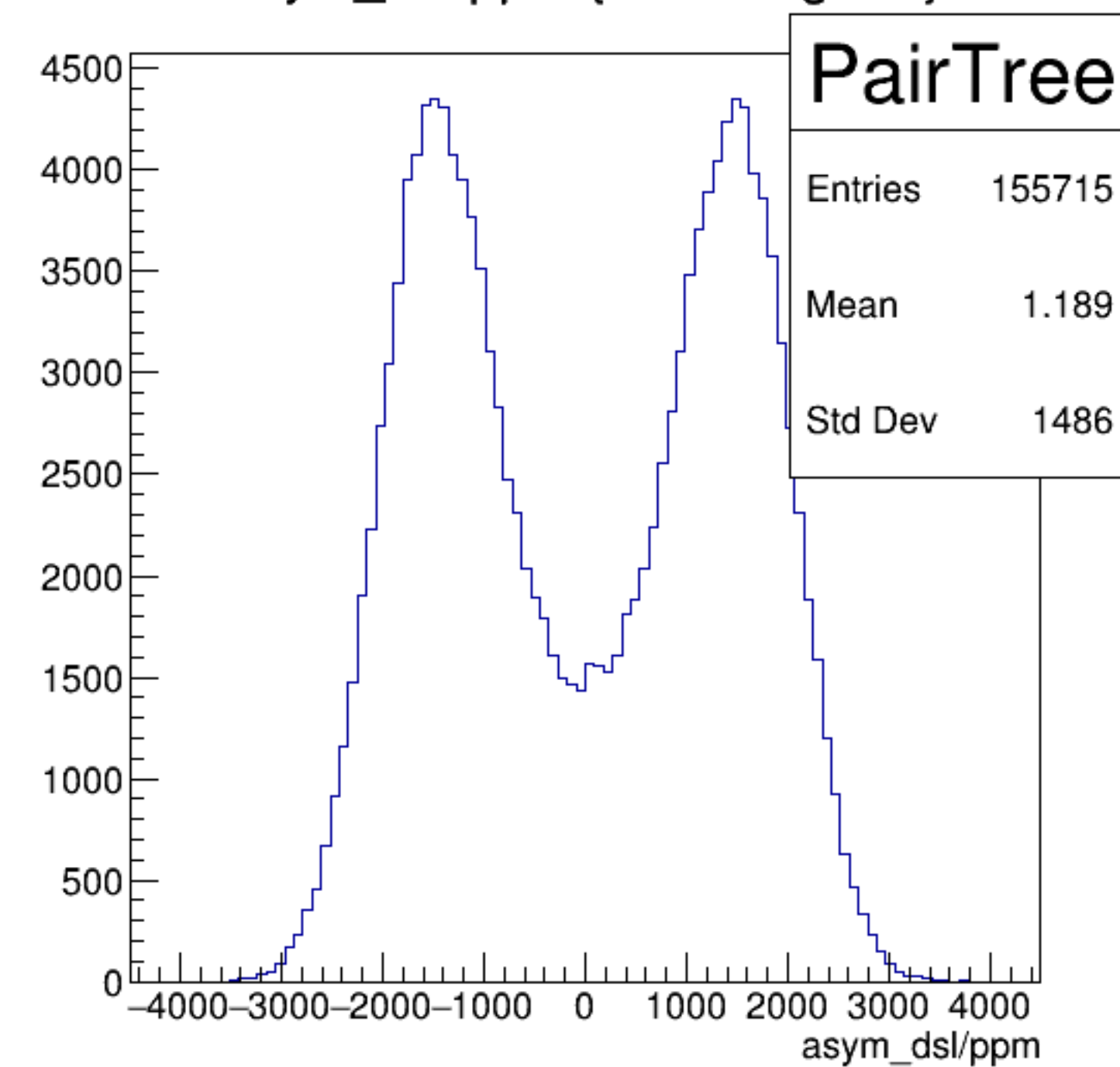
yield_dsl {ErrorFlag==0}

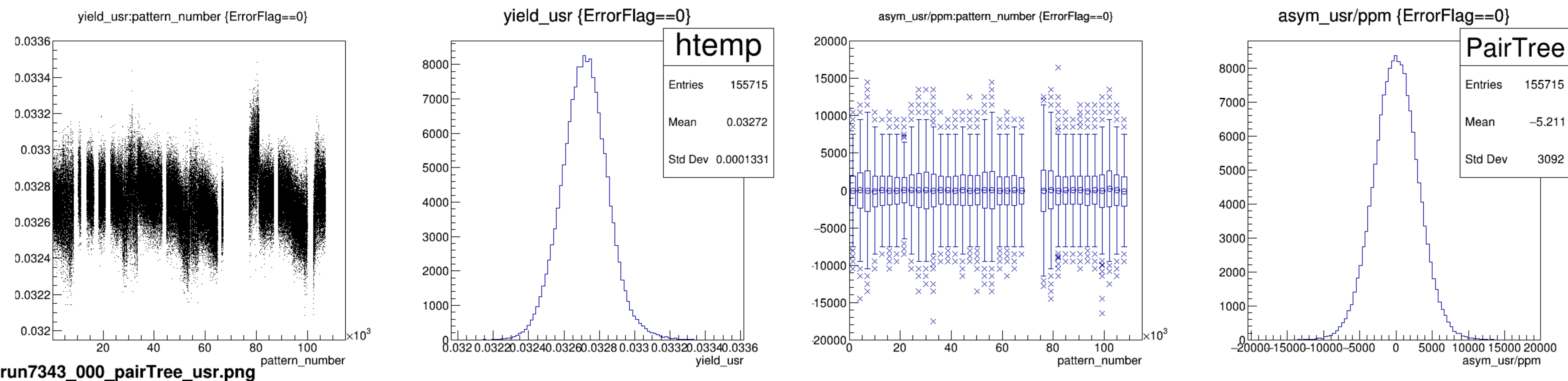


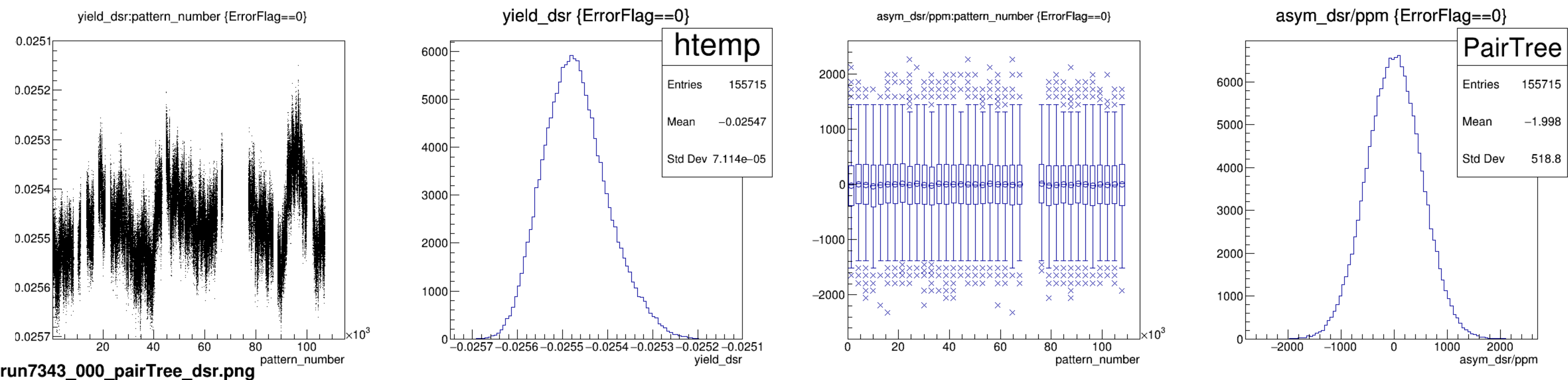
asym_dsl/ppm:pattern_number {ErrorFlag==0}



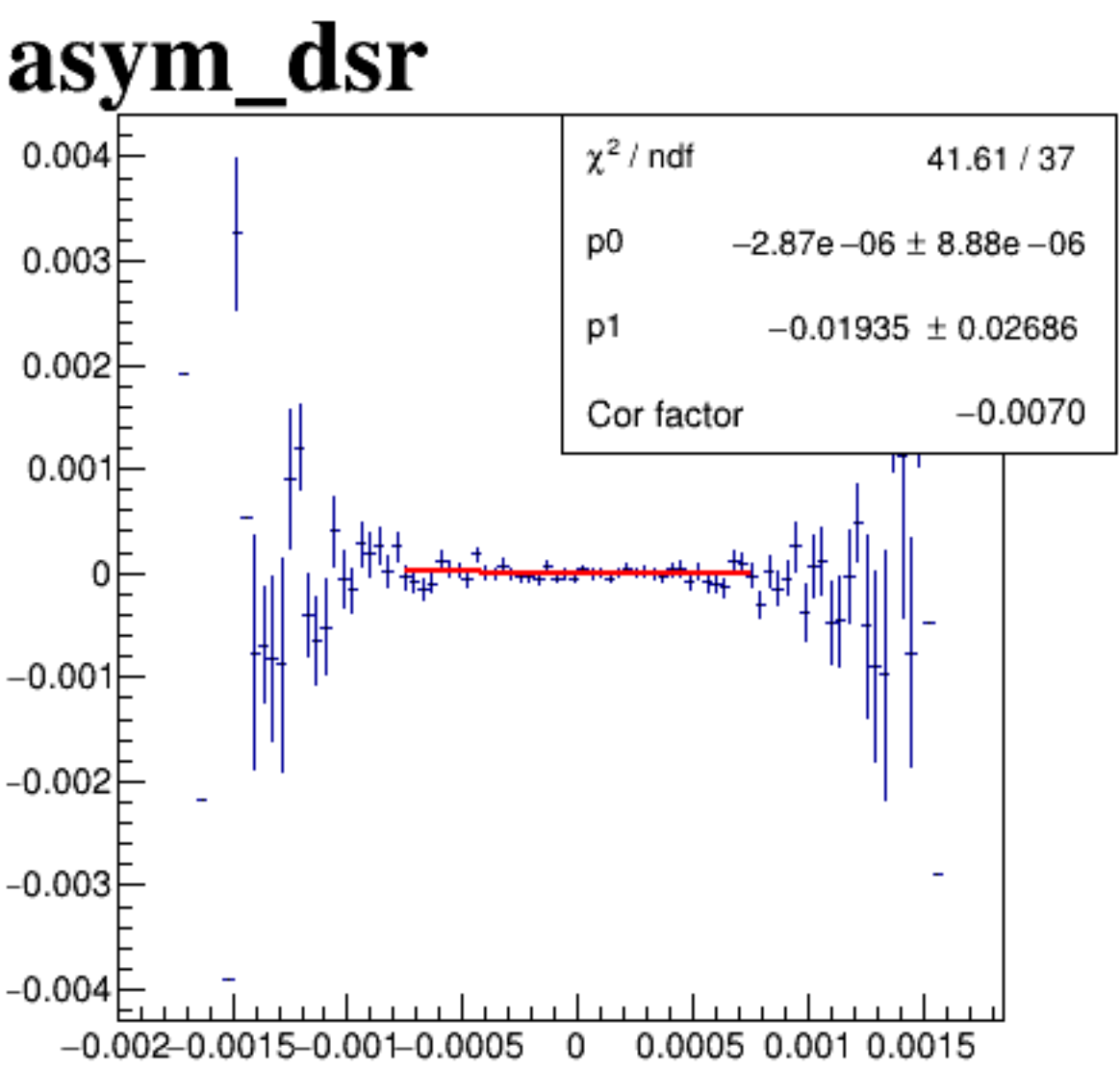
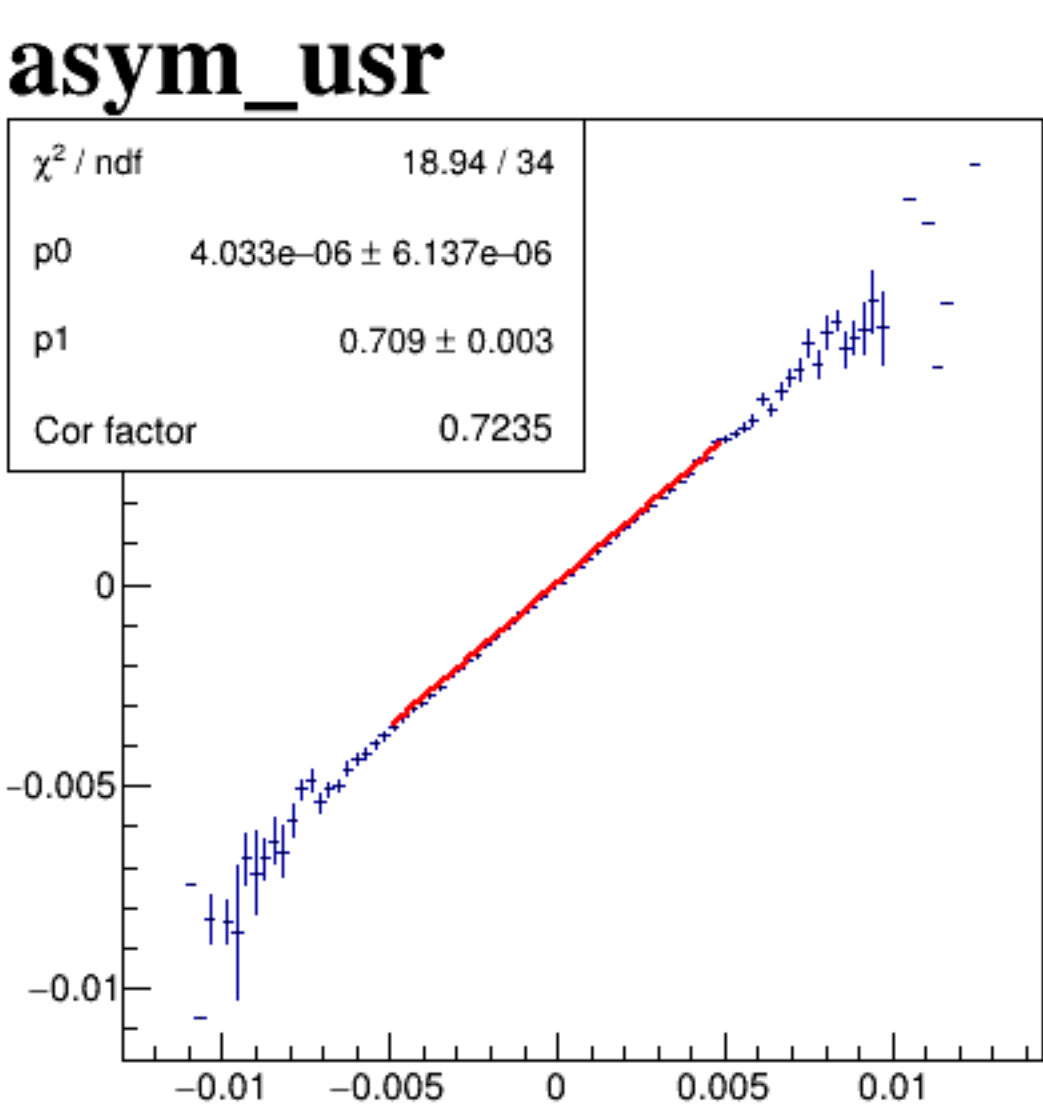
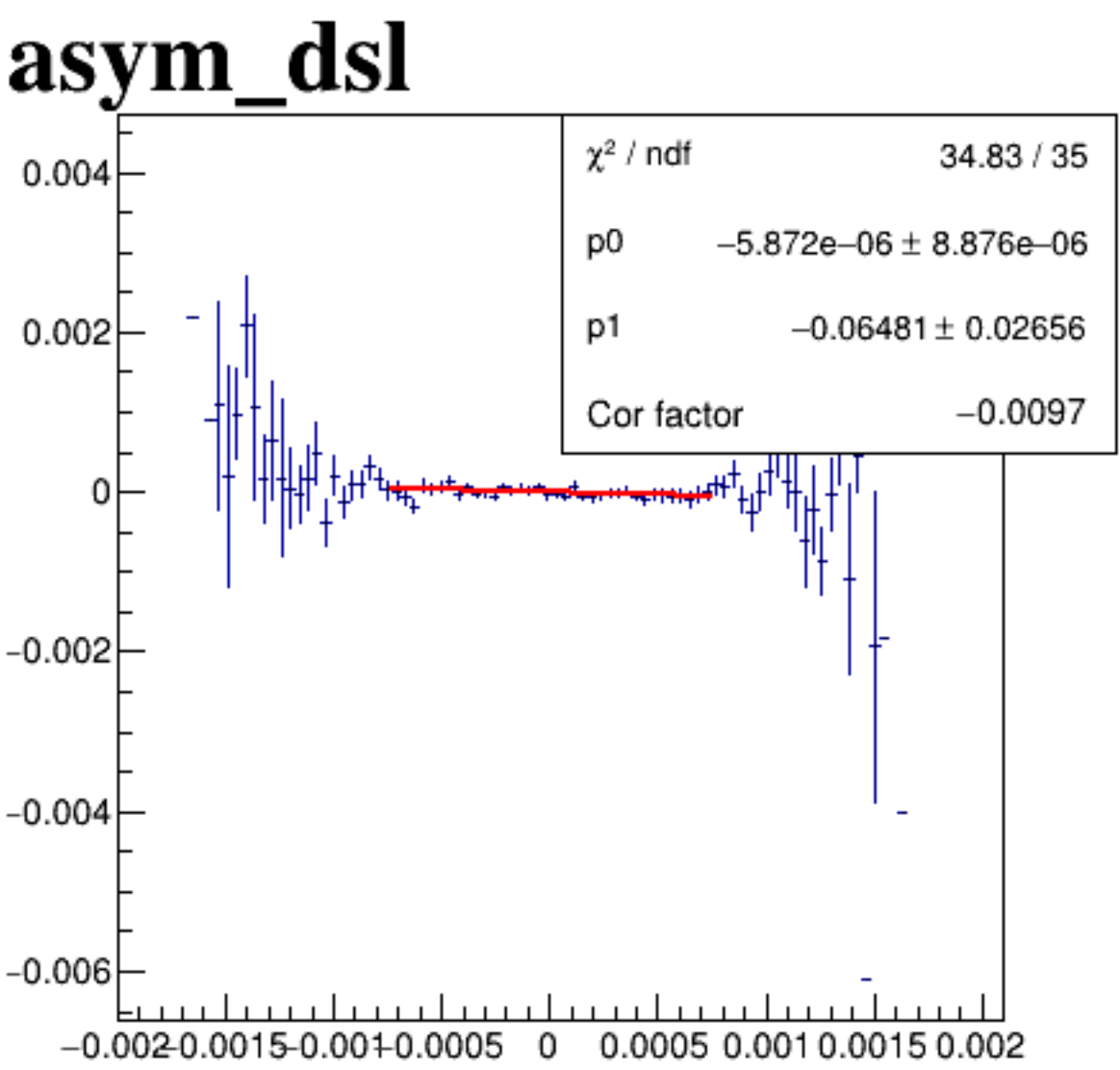
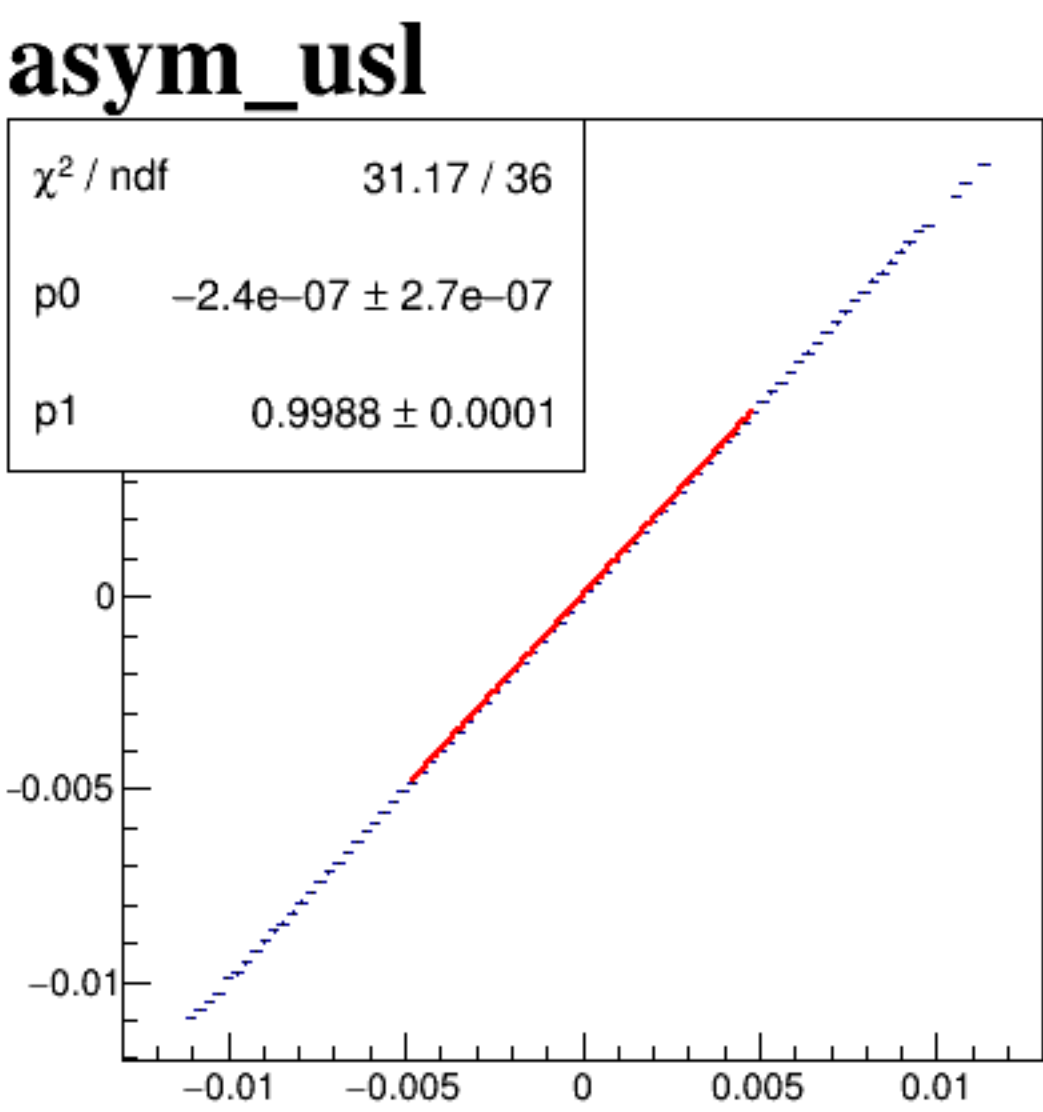
asym_dsl/ppm {ErrorFlag==0}



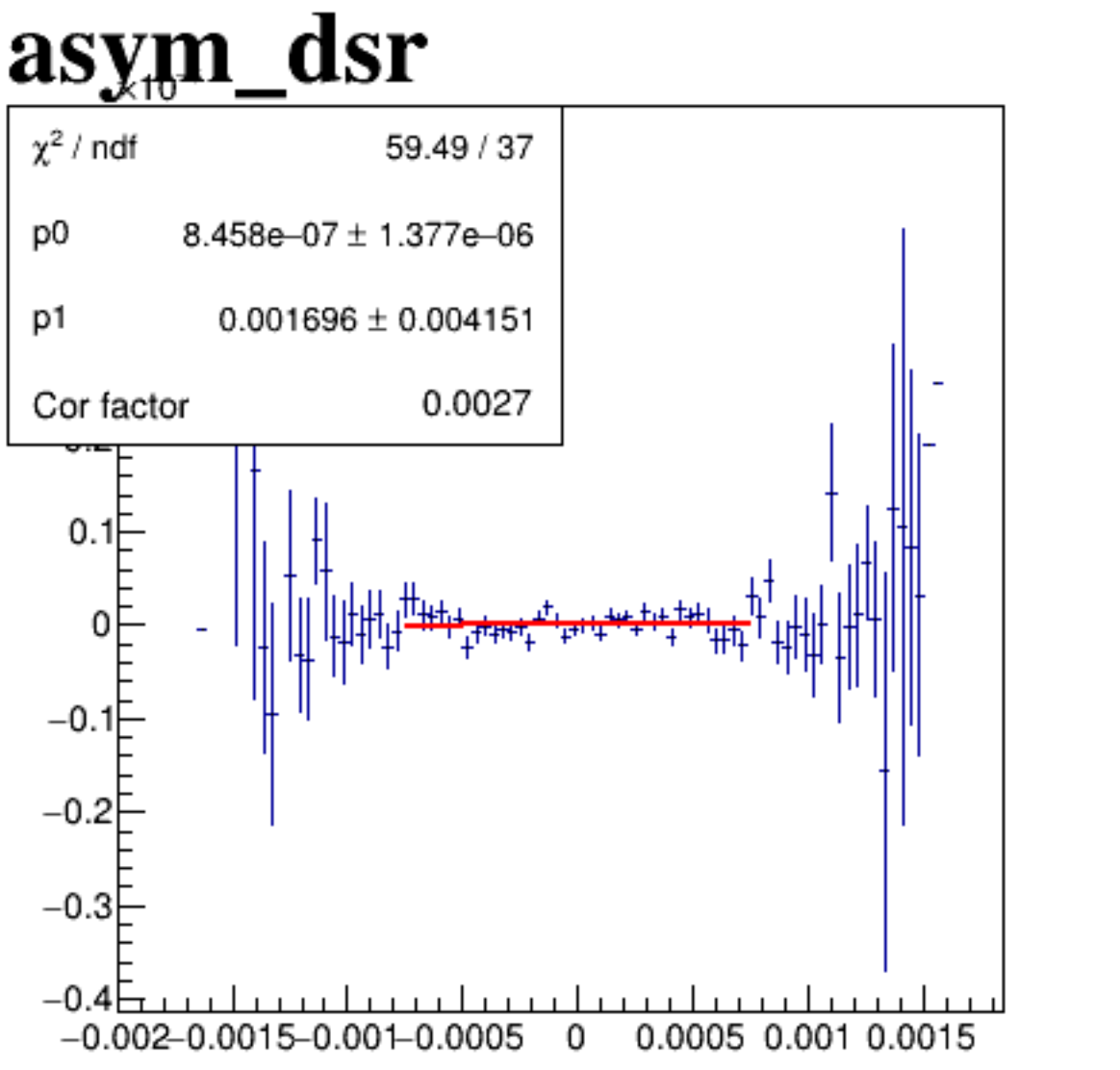
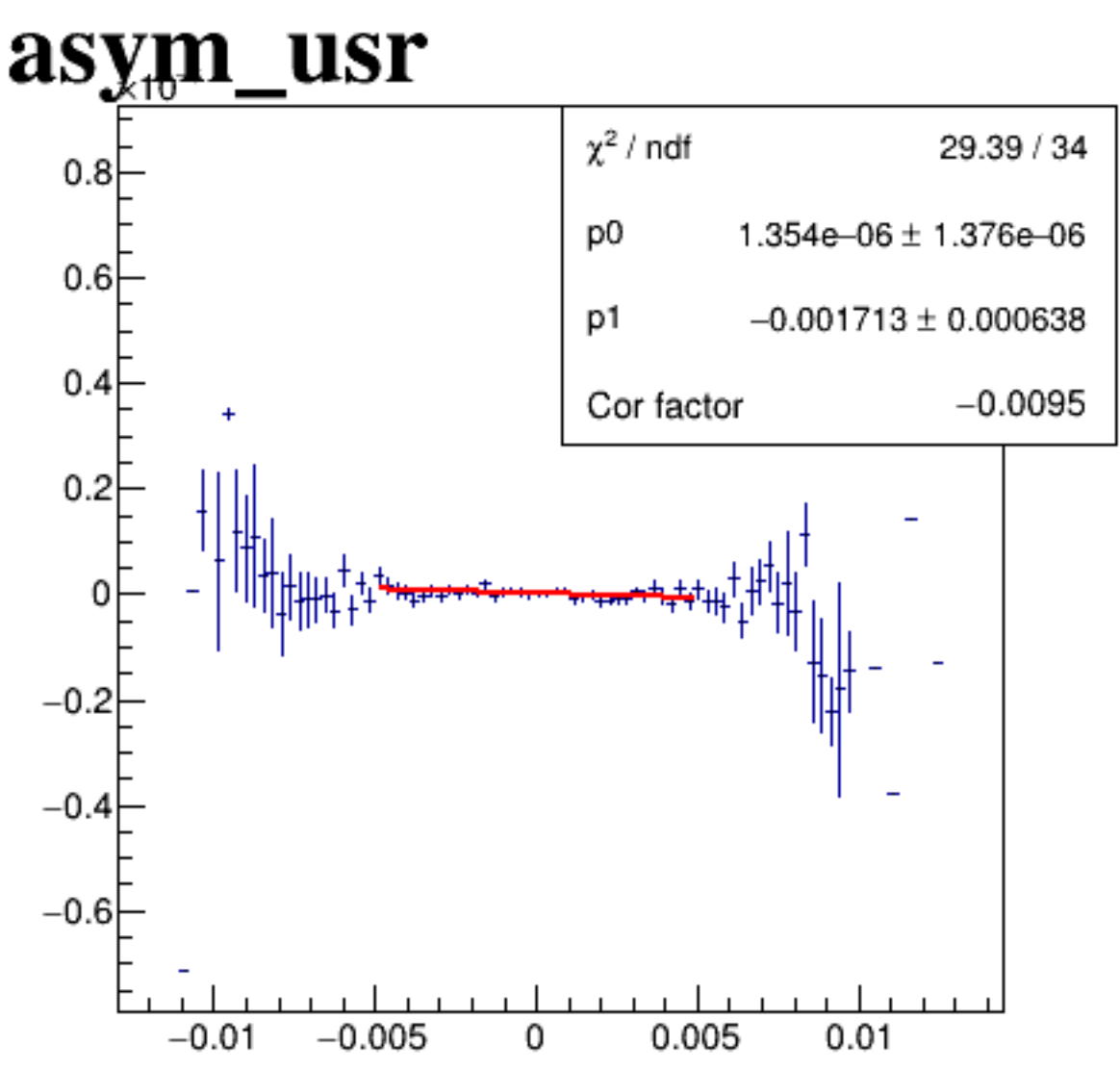
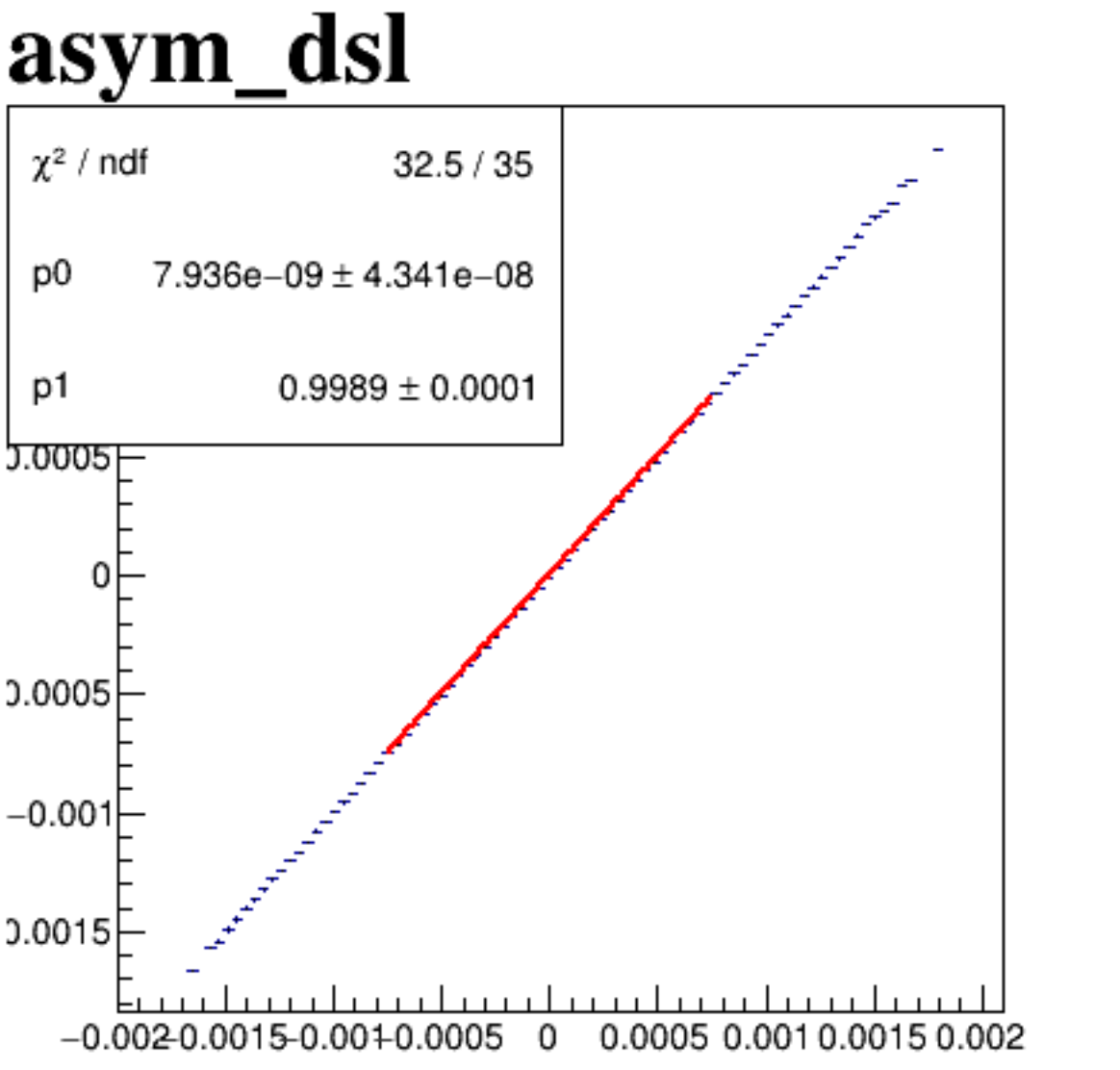
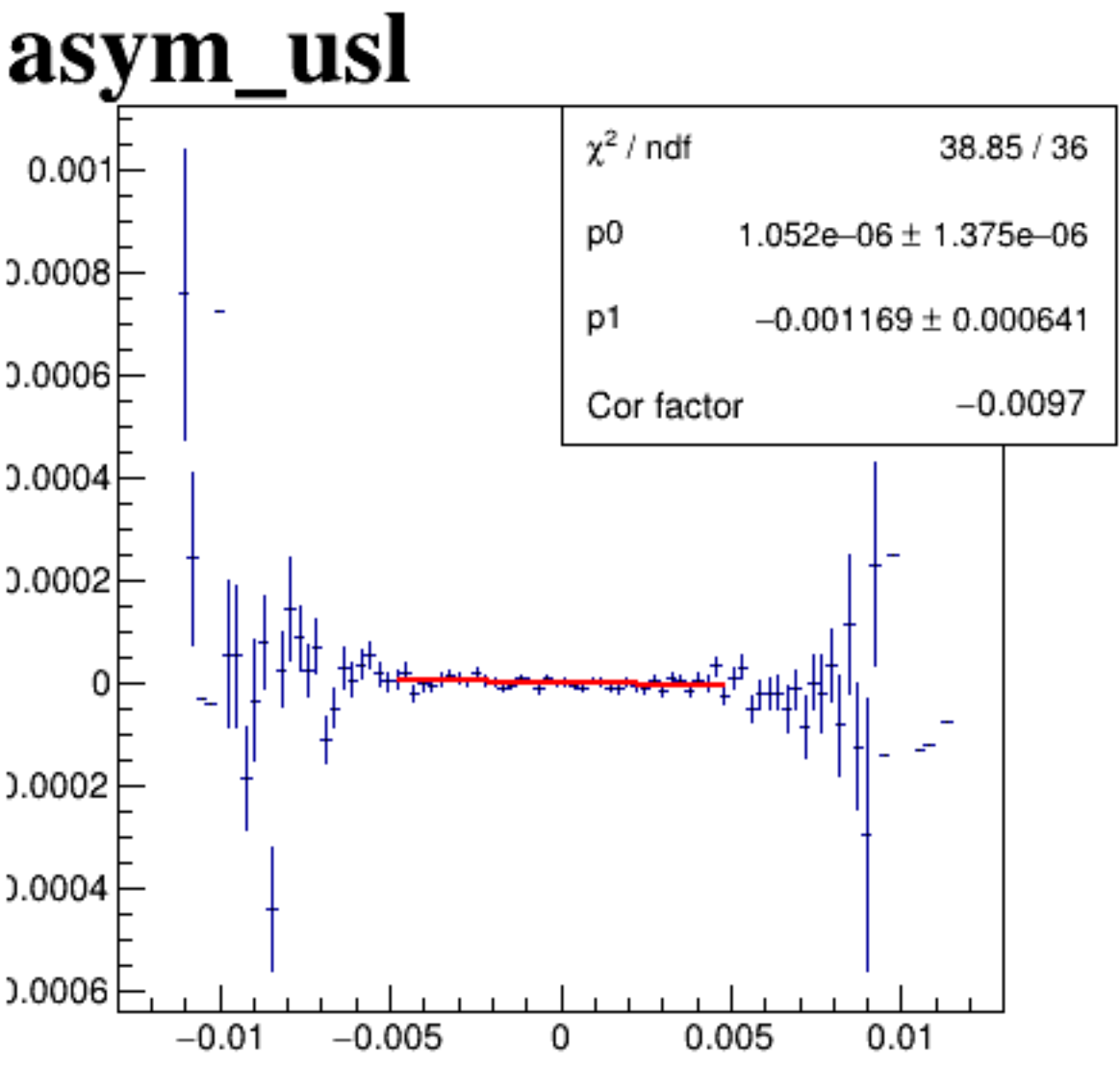




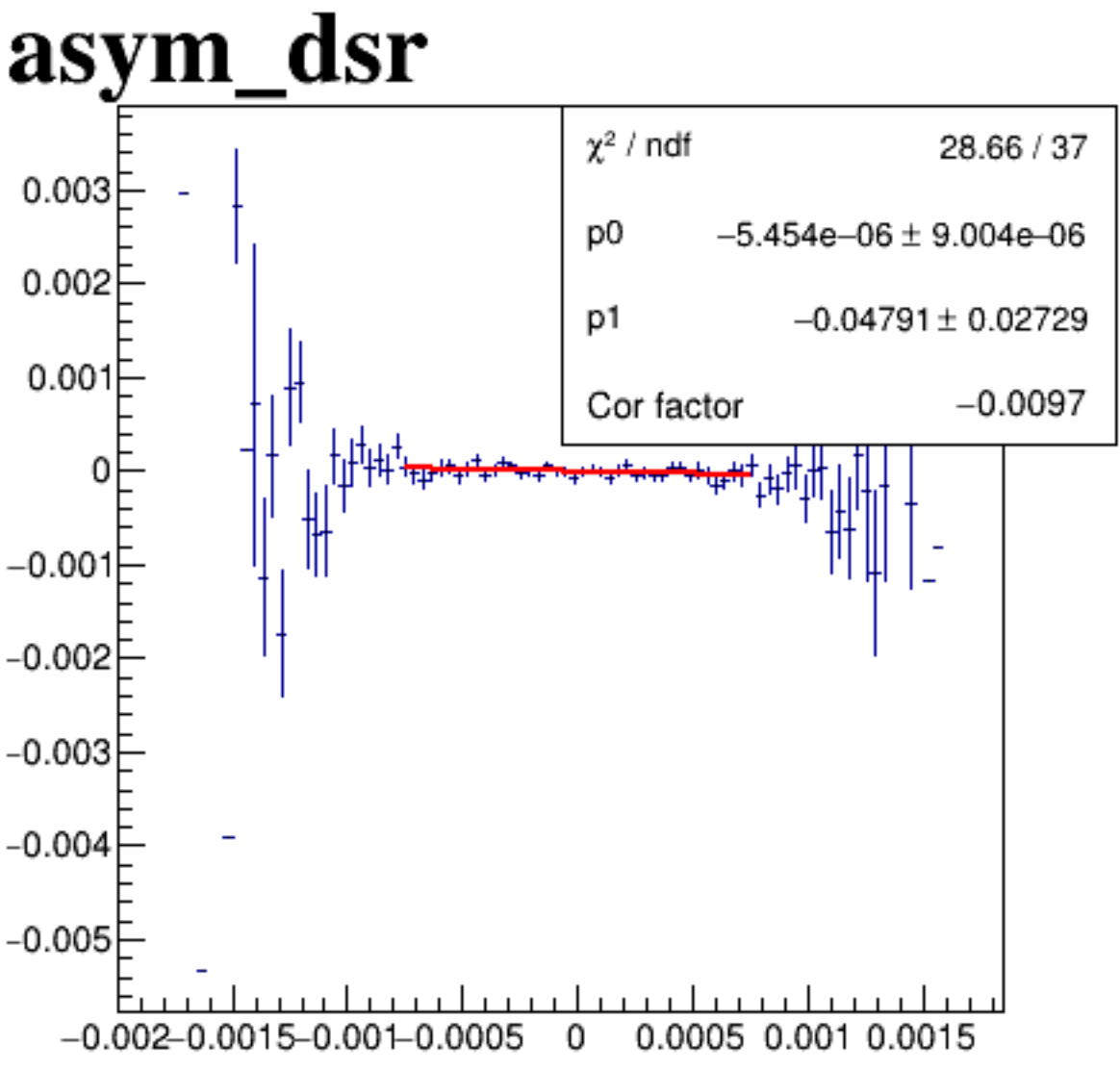
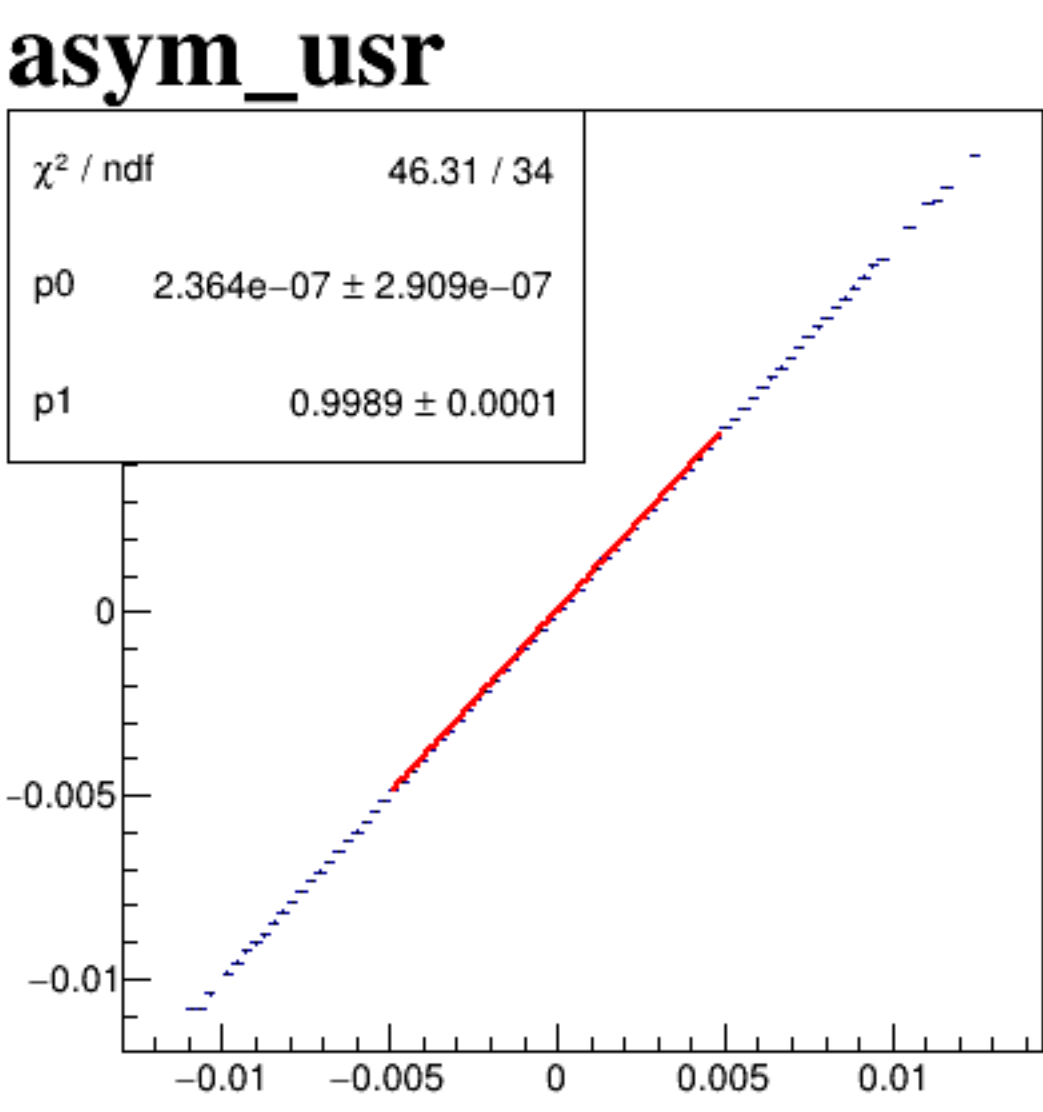
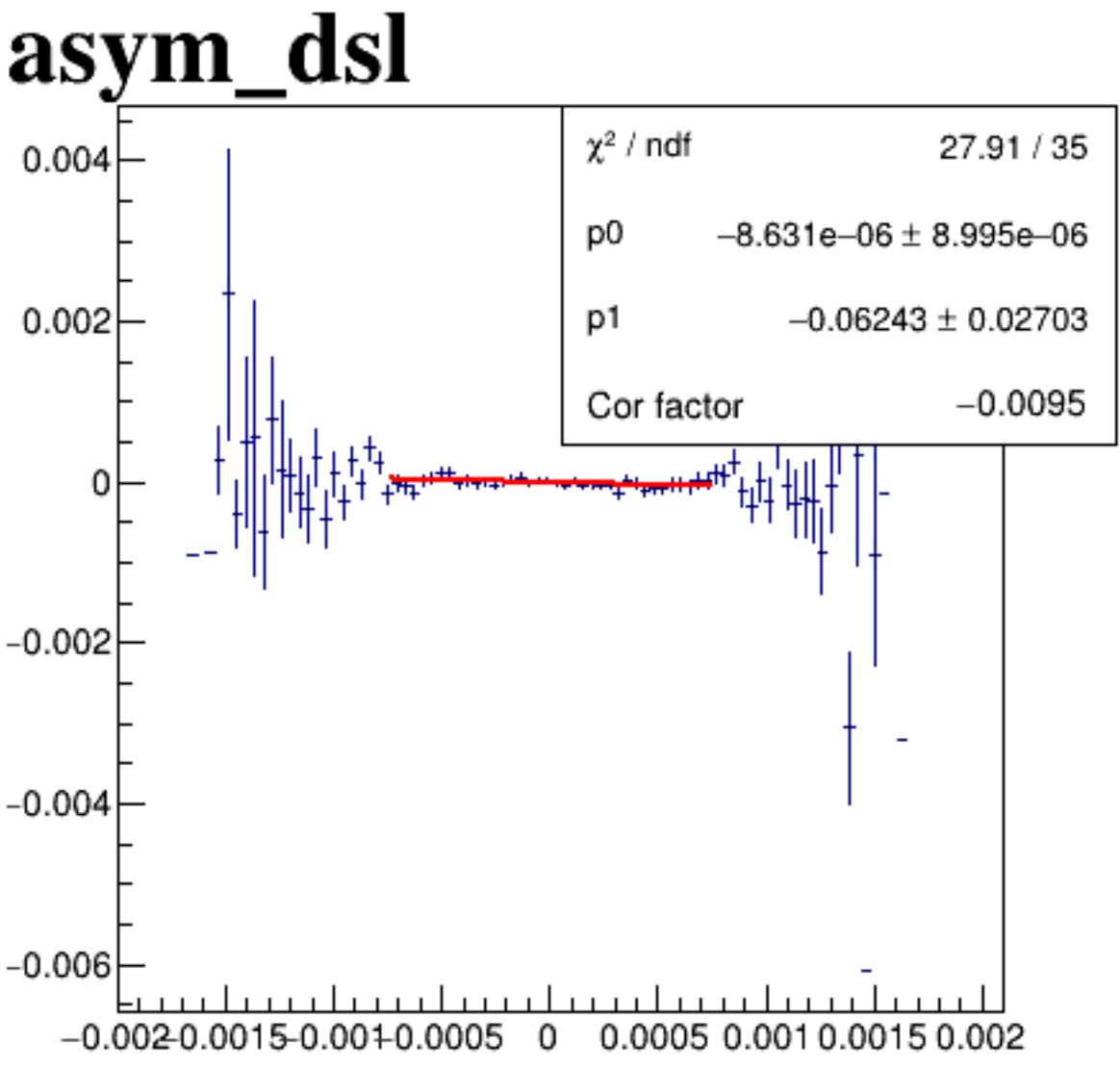
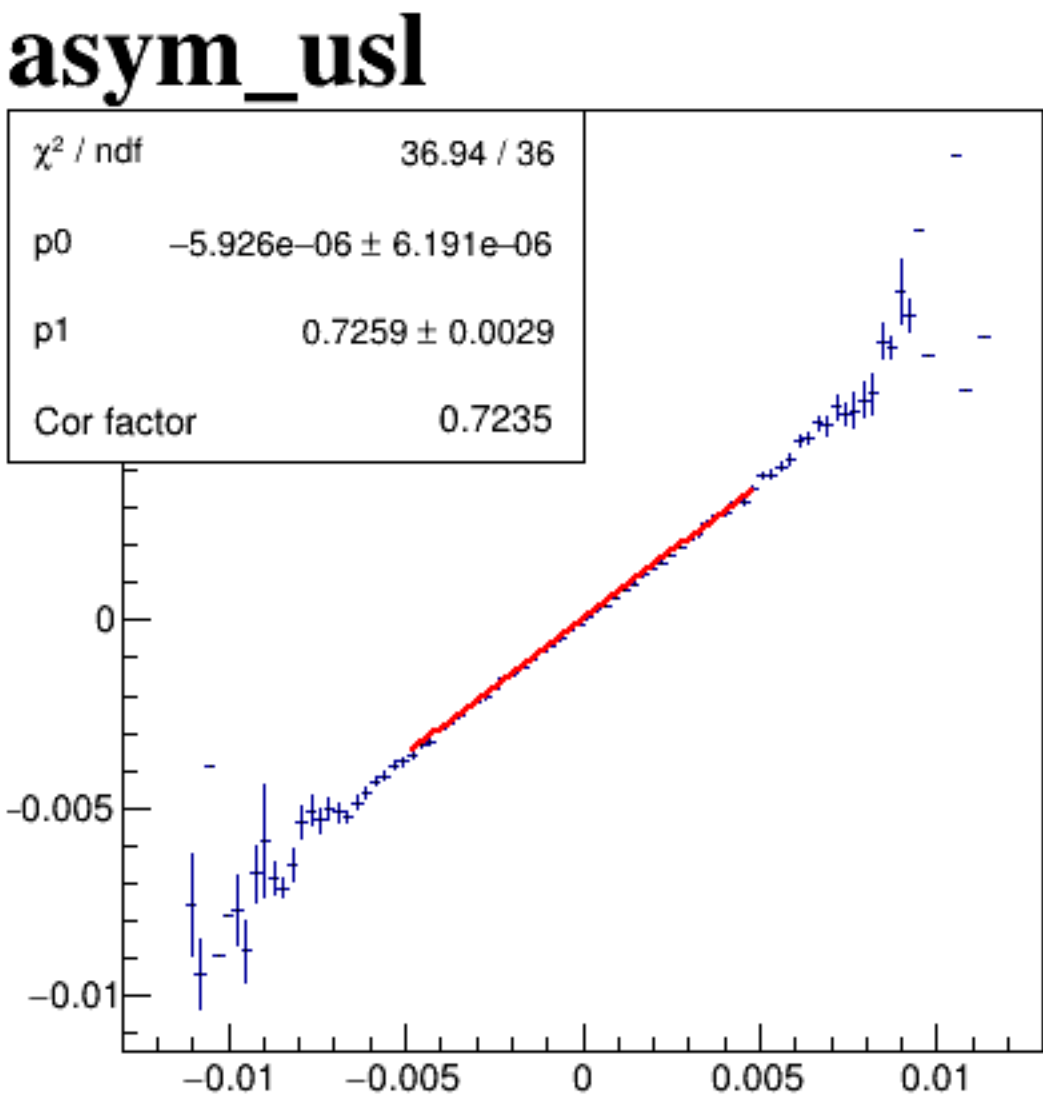
asym_usl



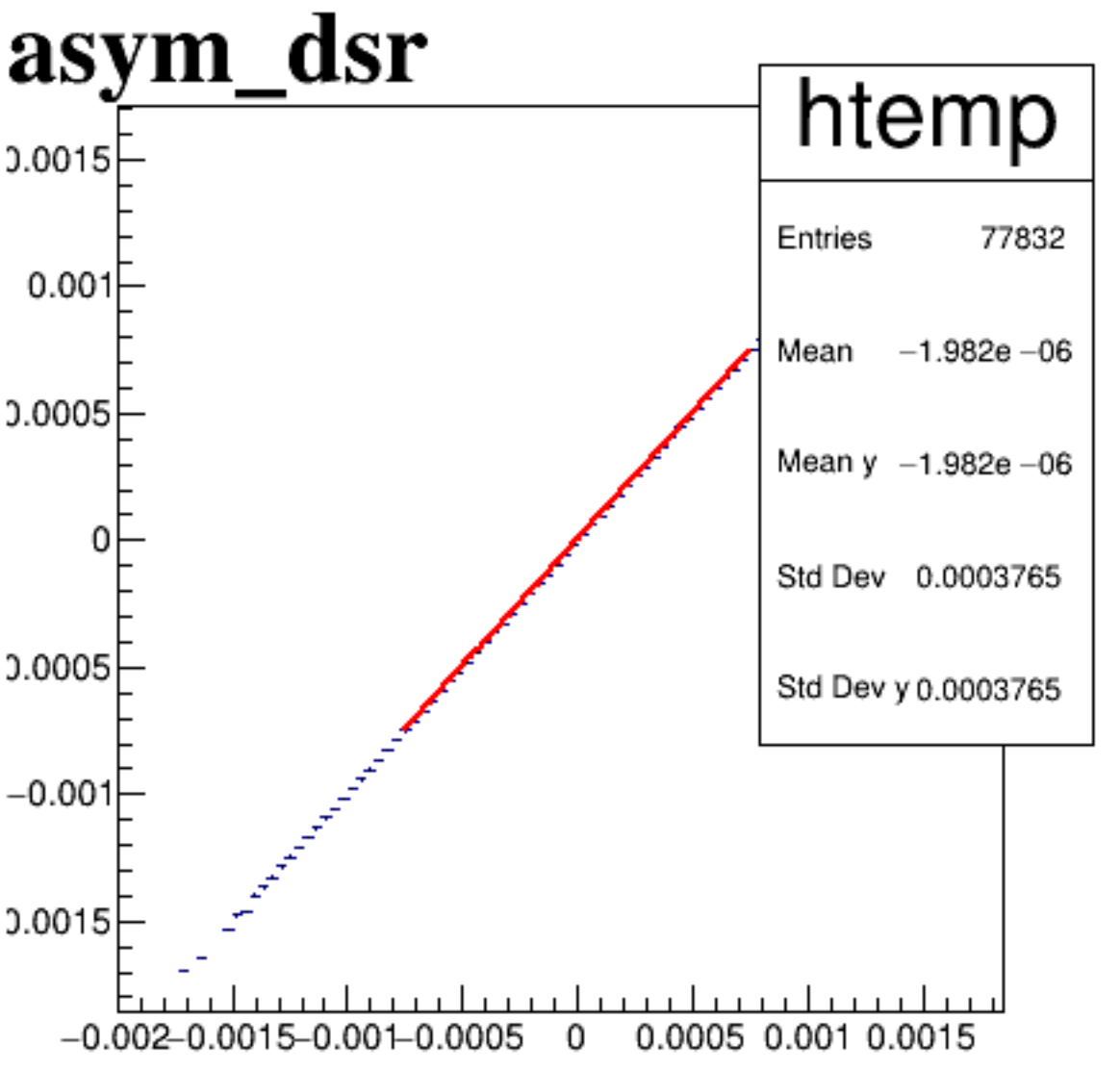
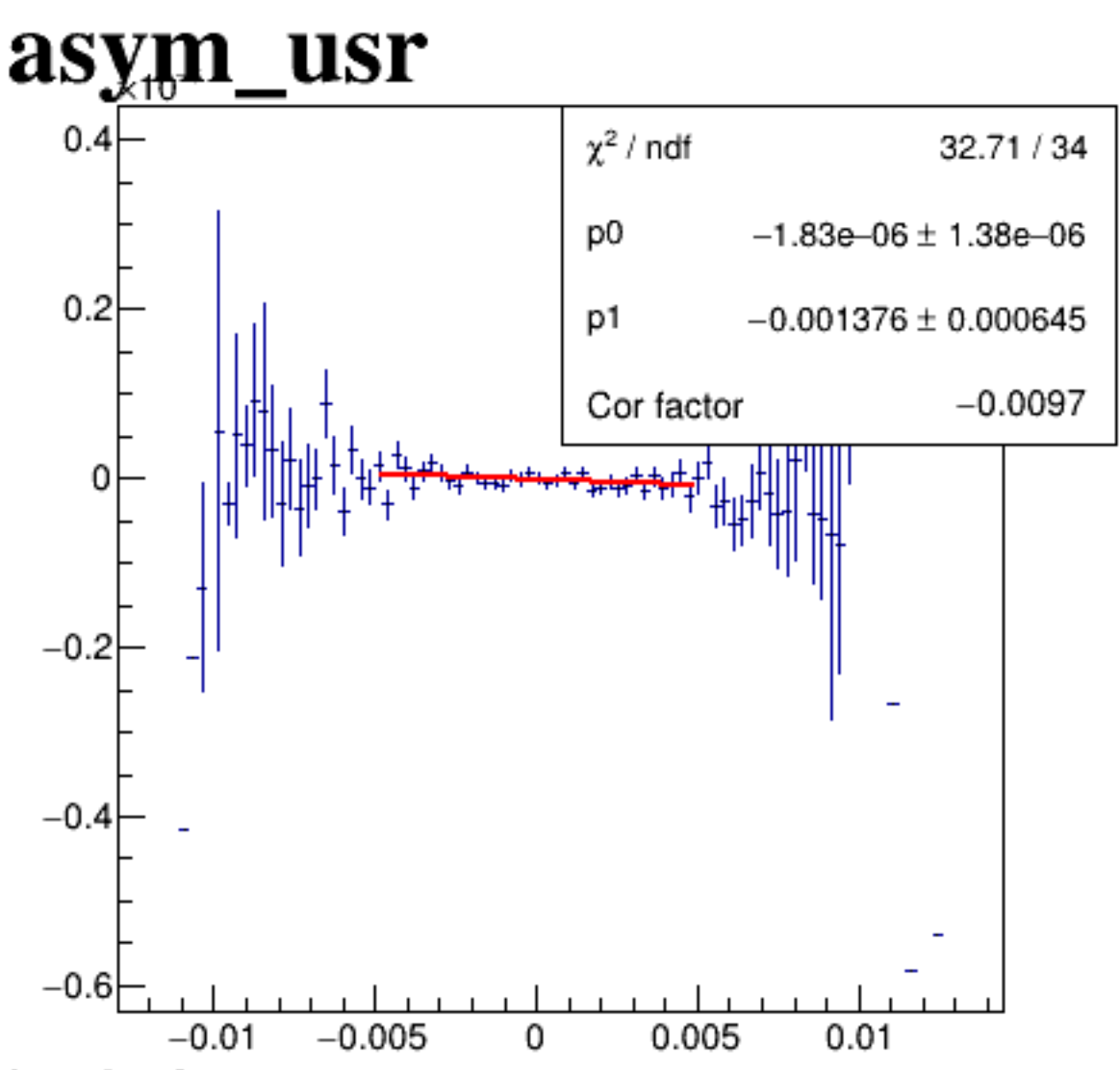
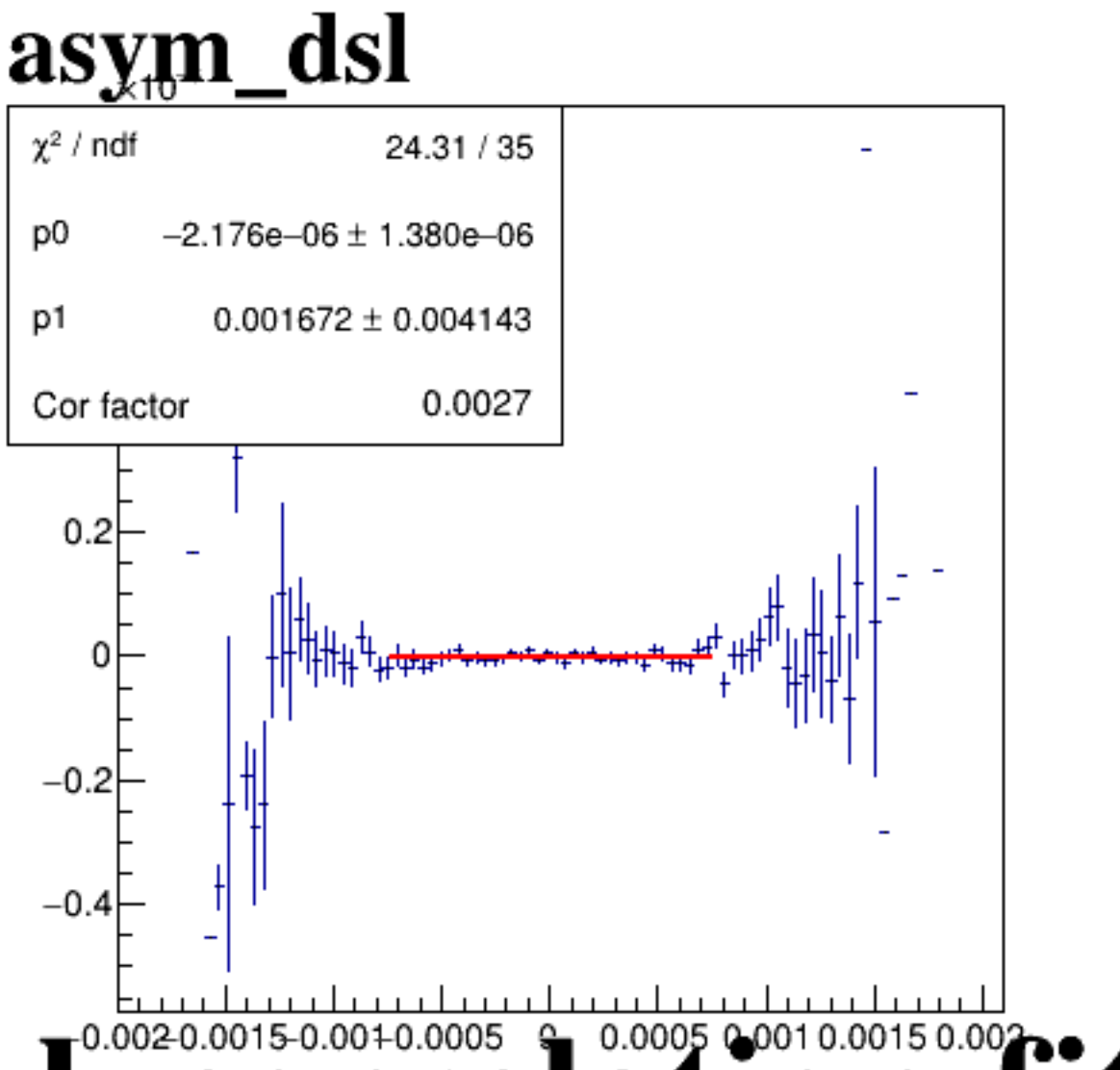
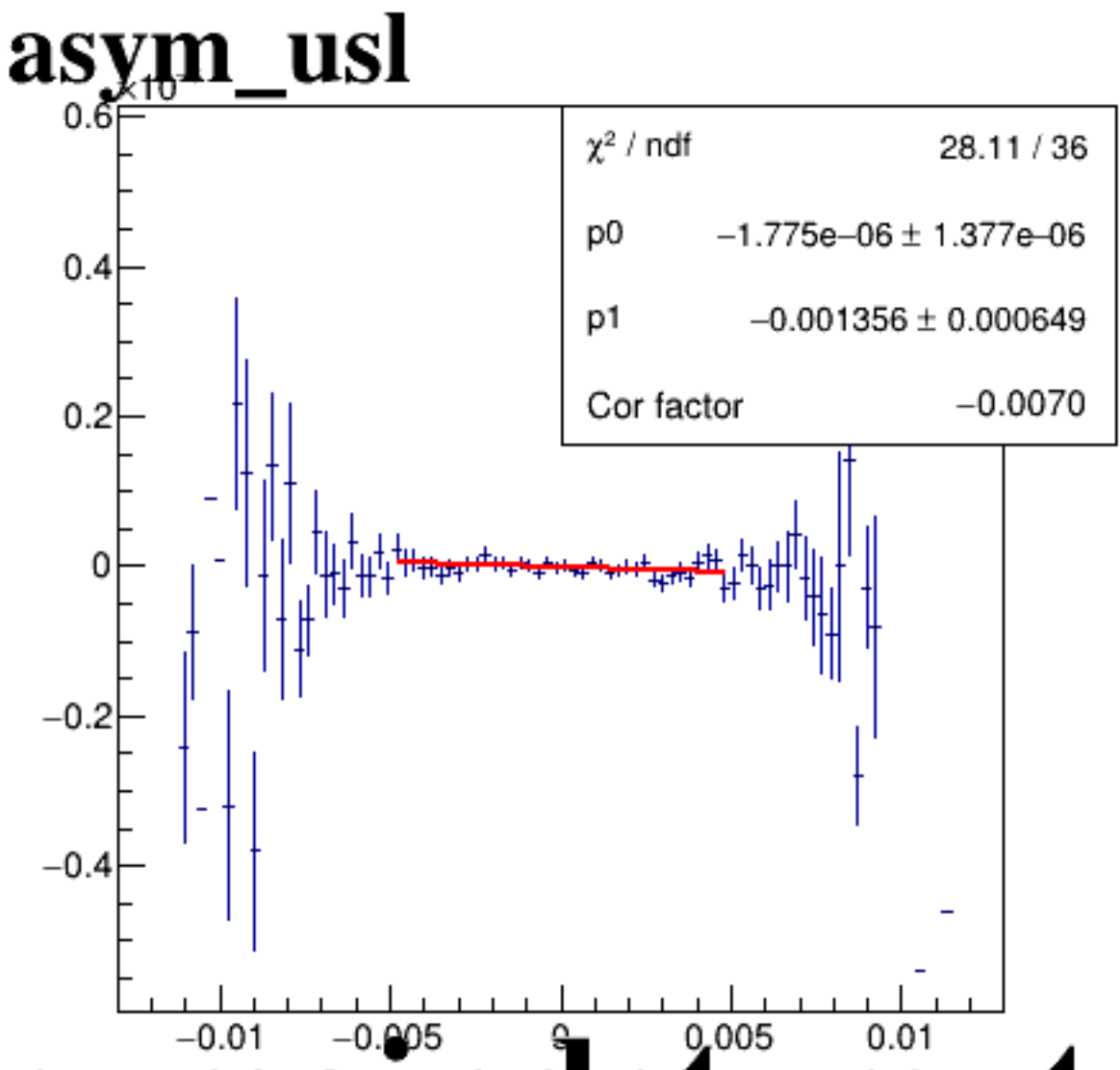
asym_dsl



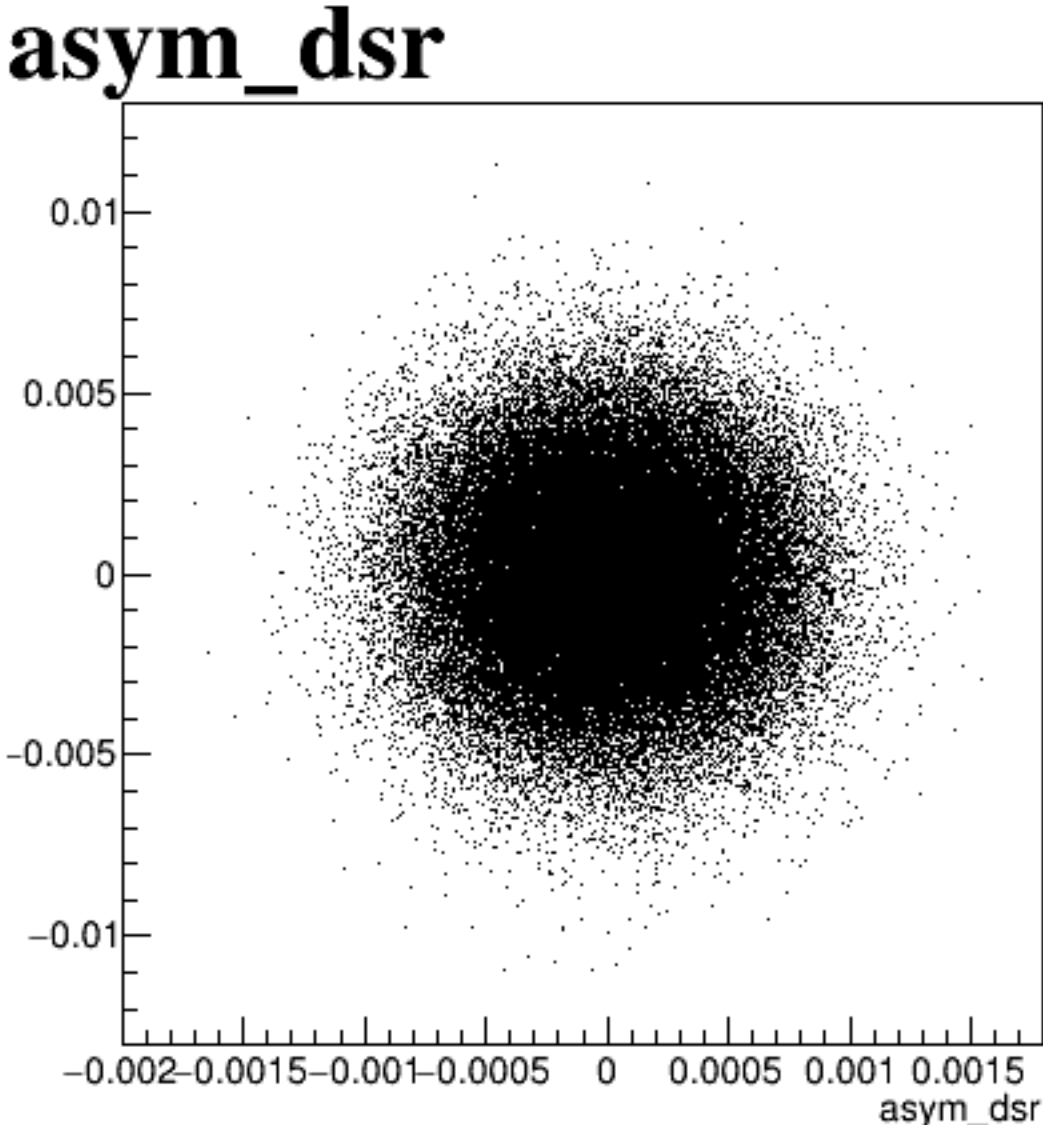
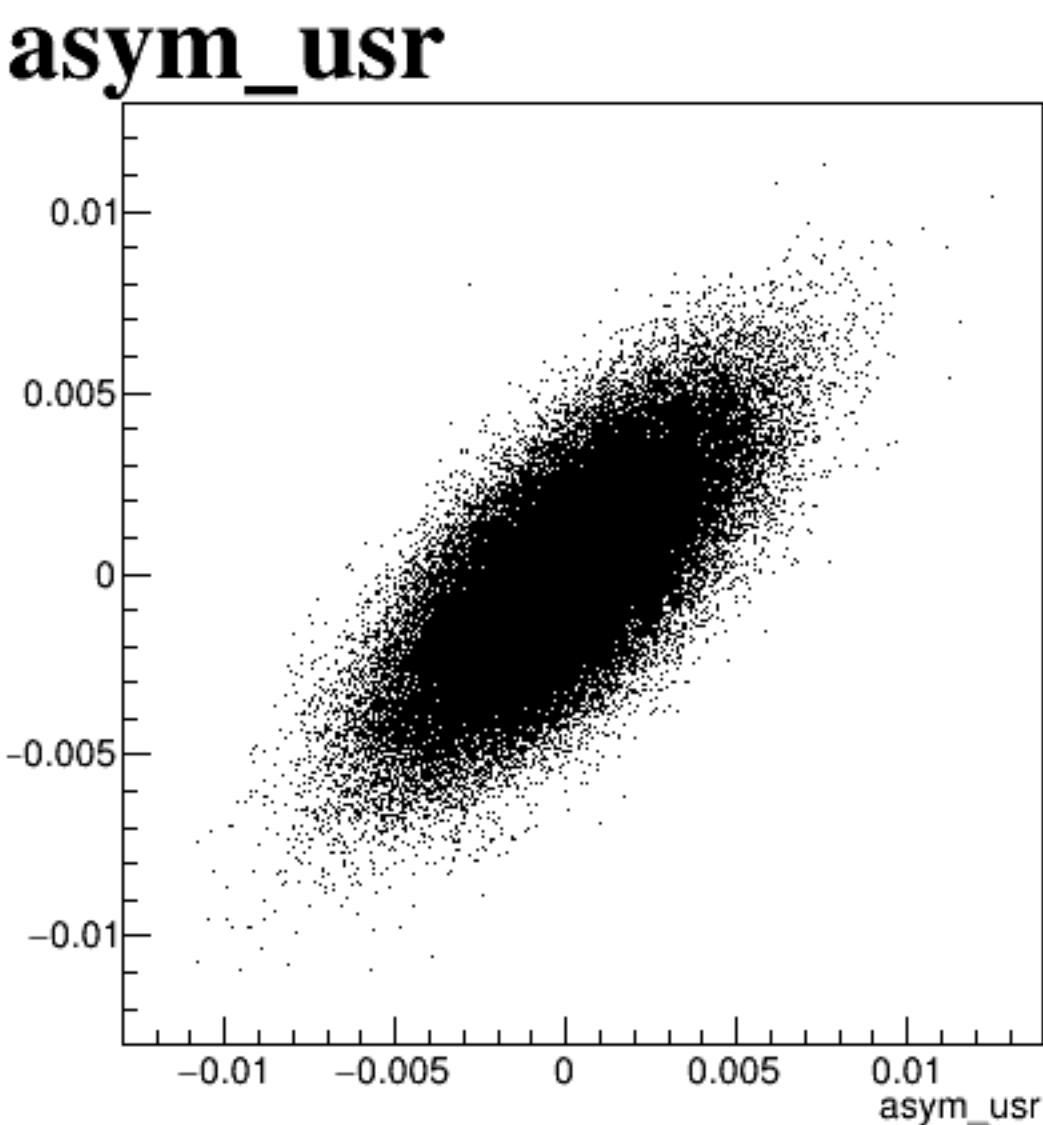
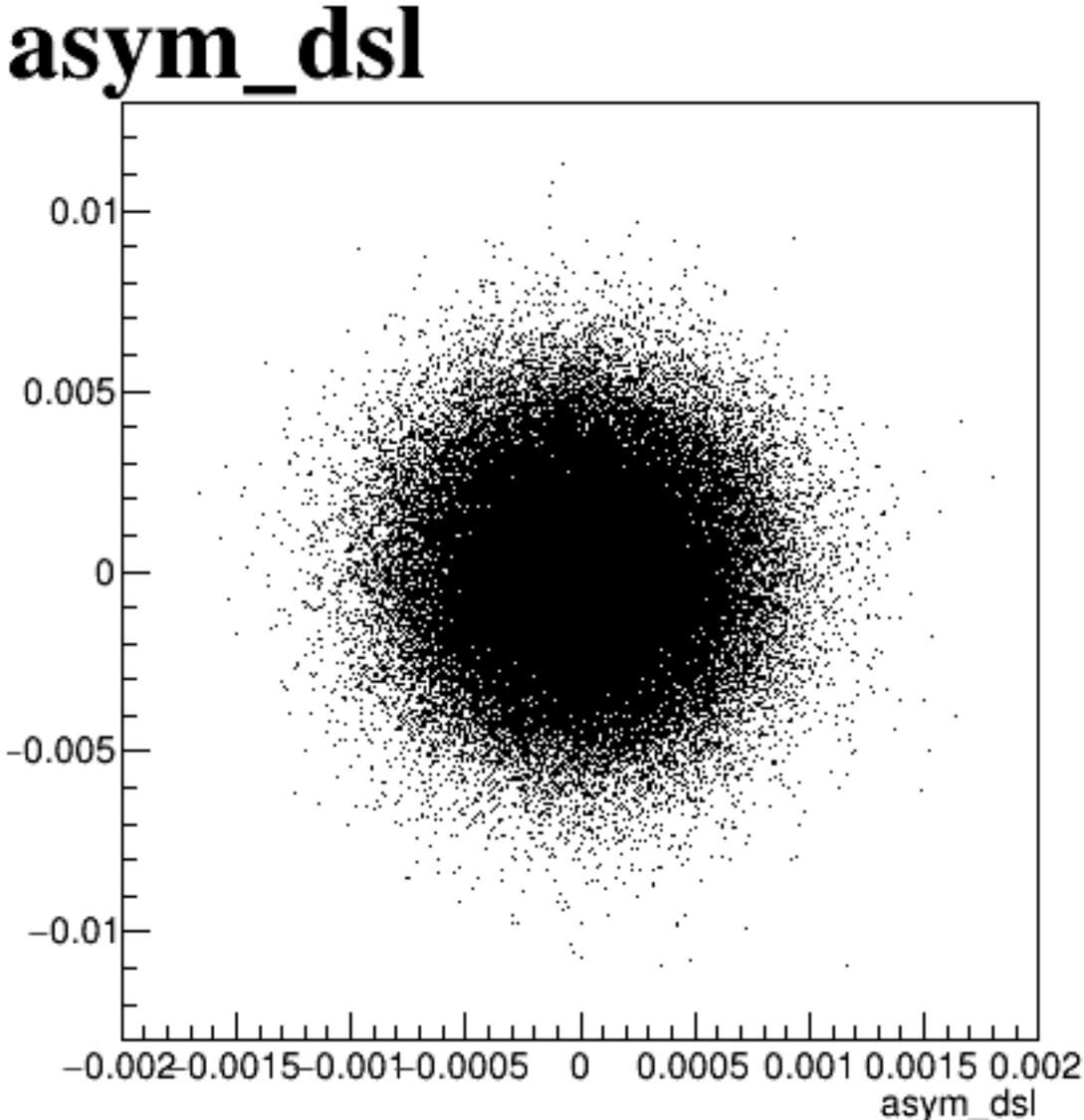
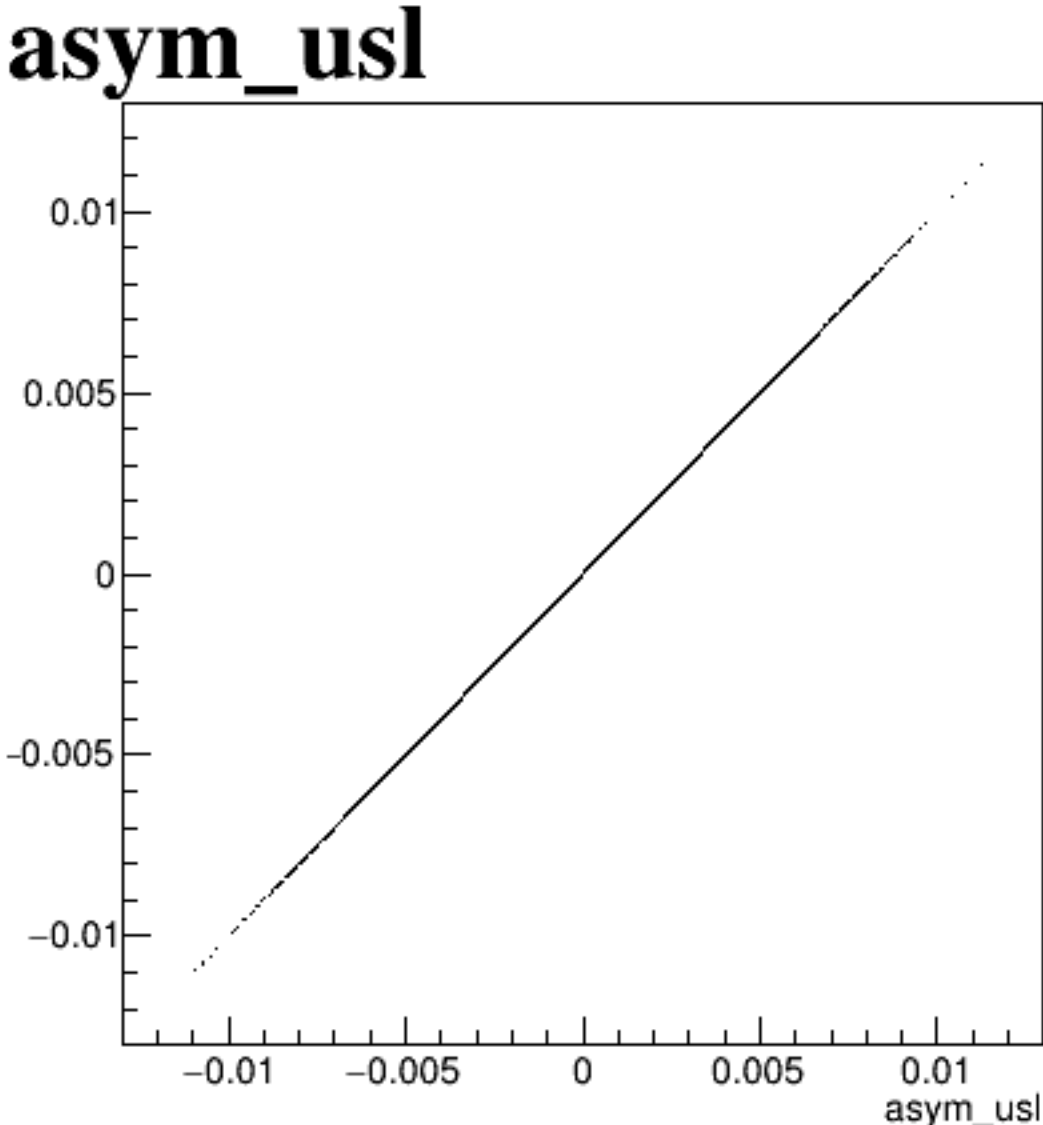
asym_usr



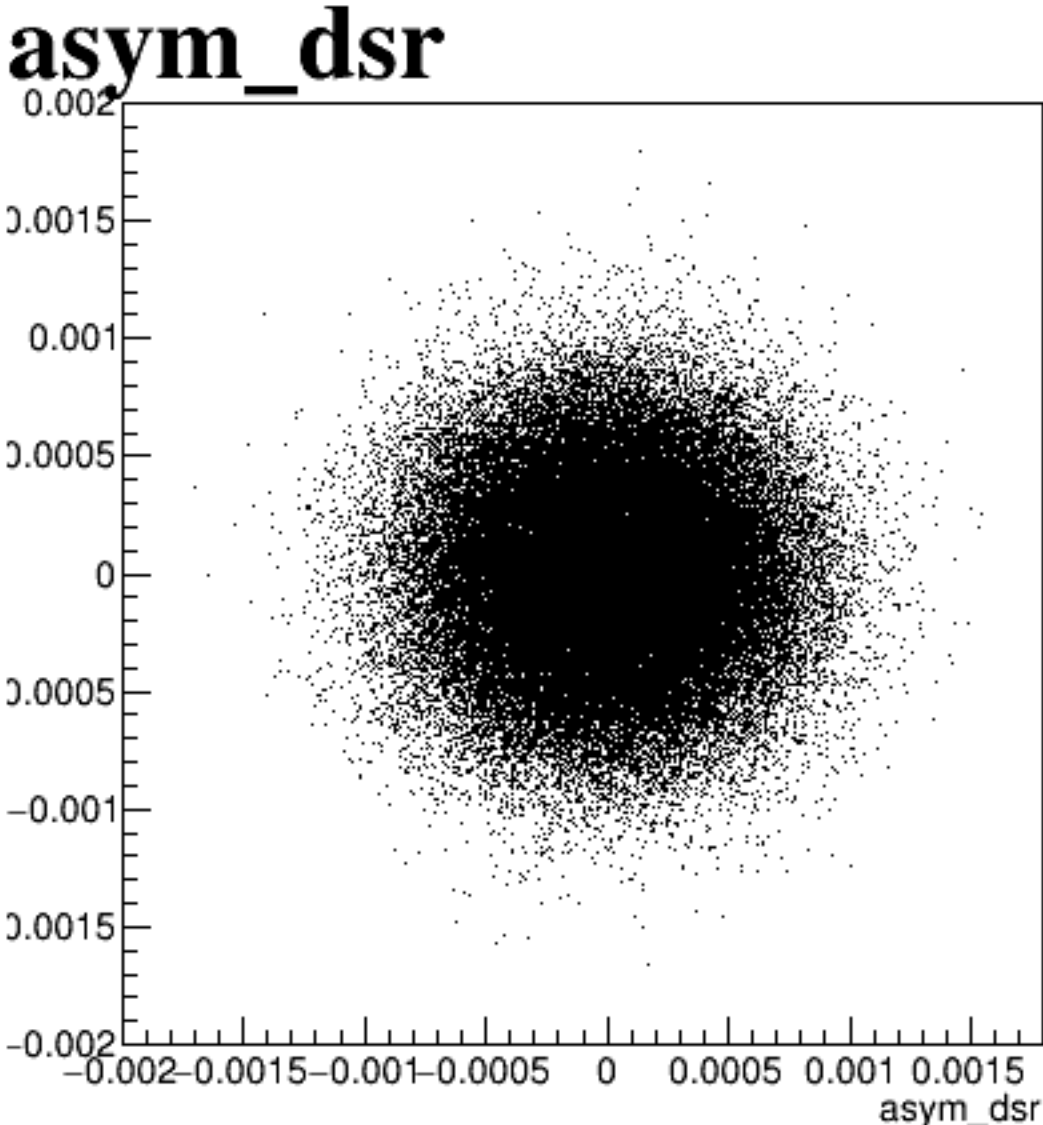
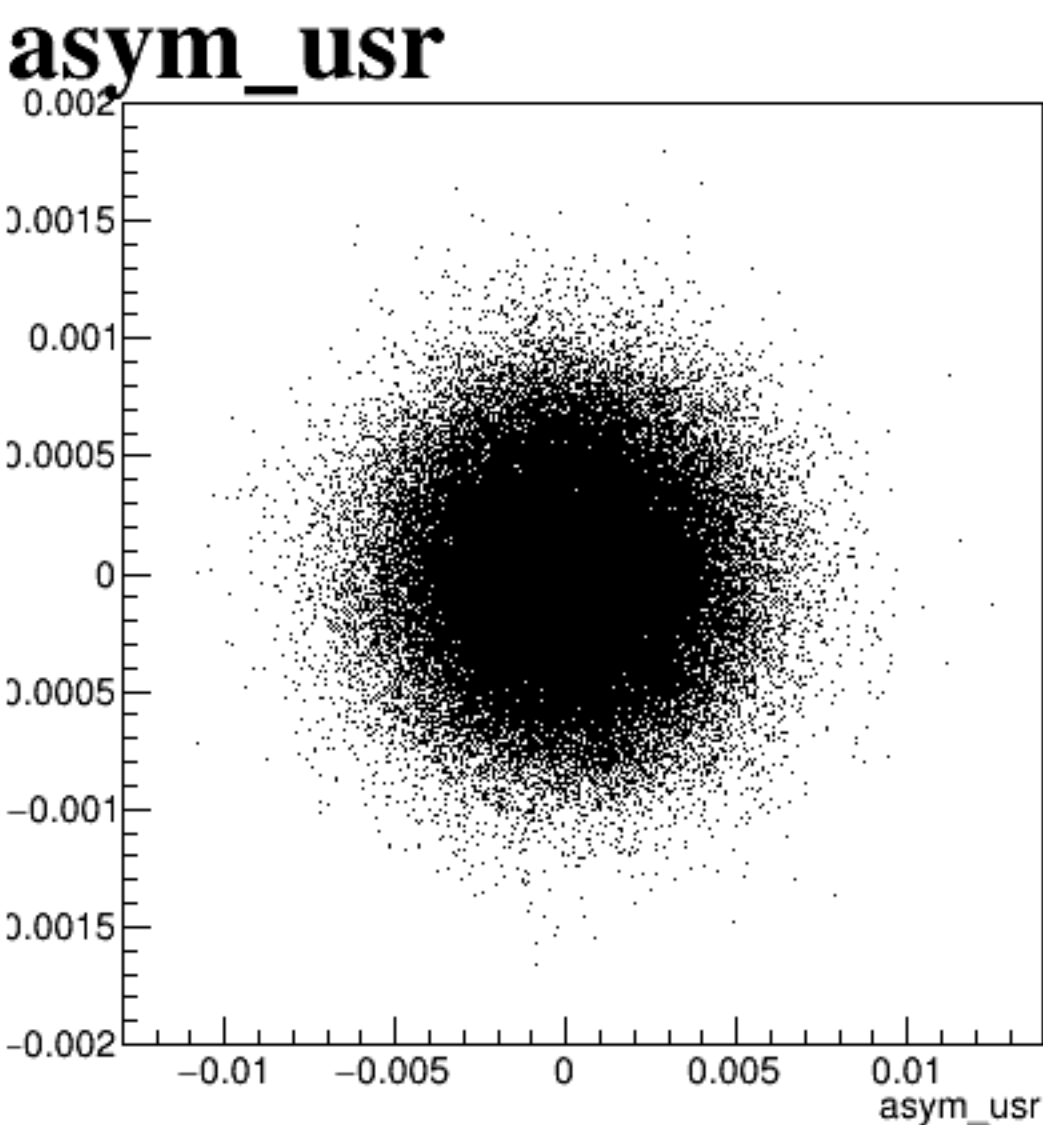
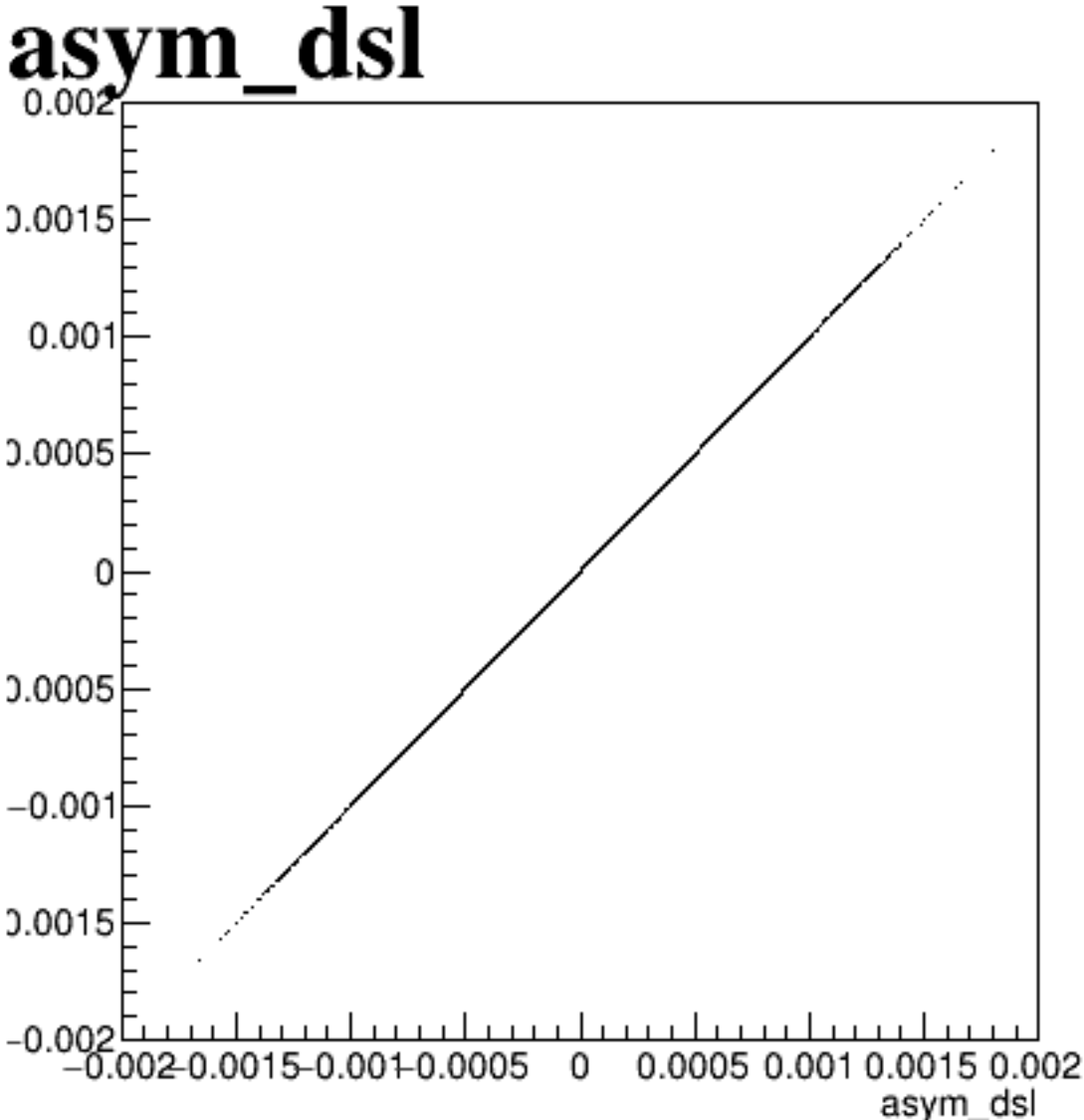
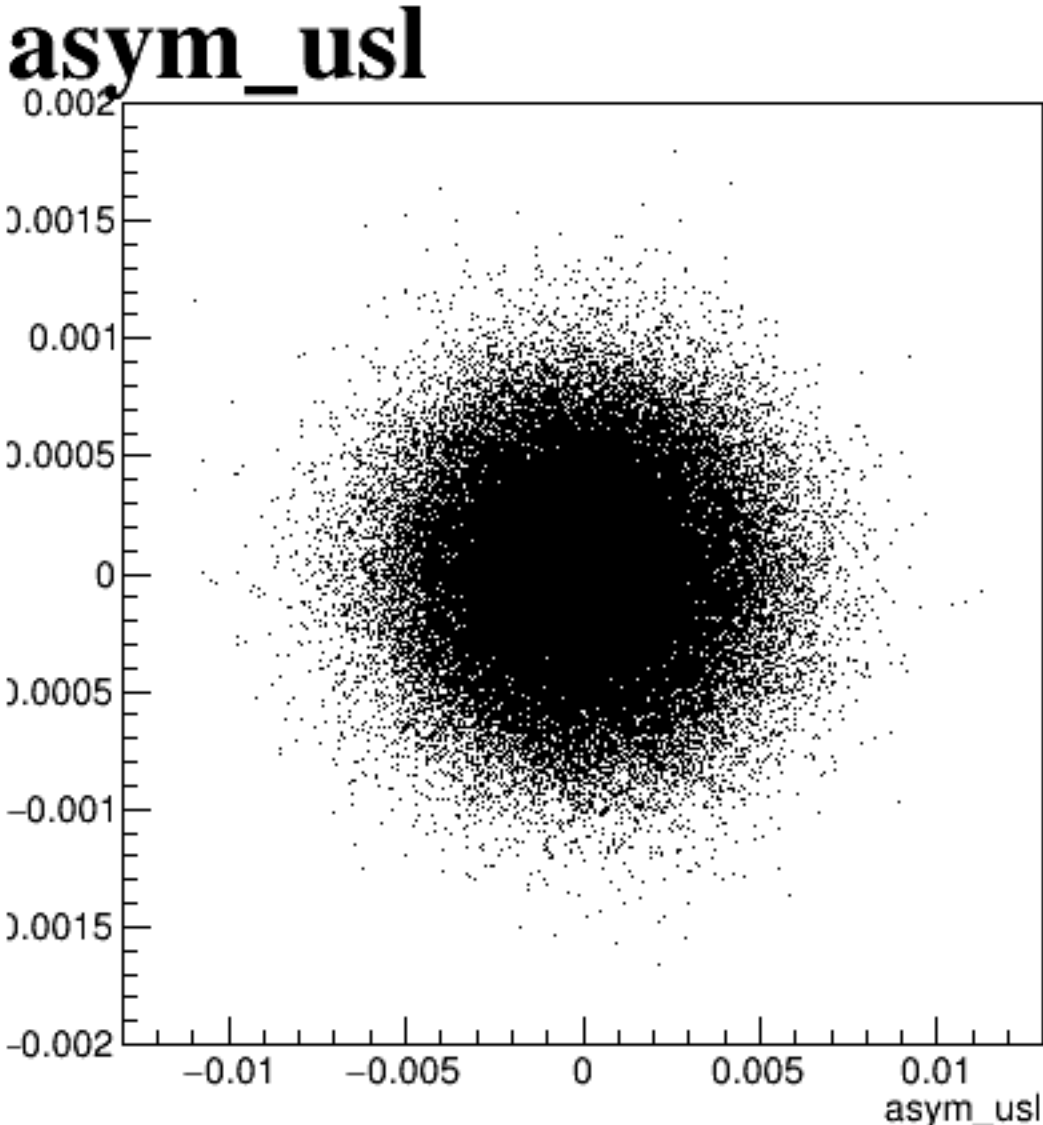
asym_dsr



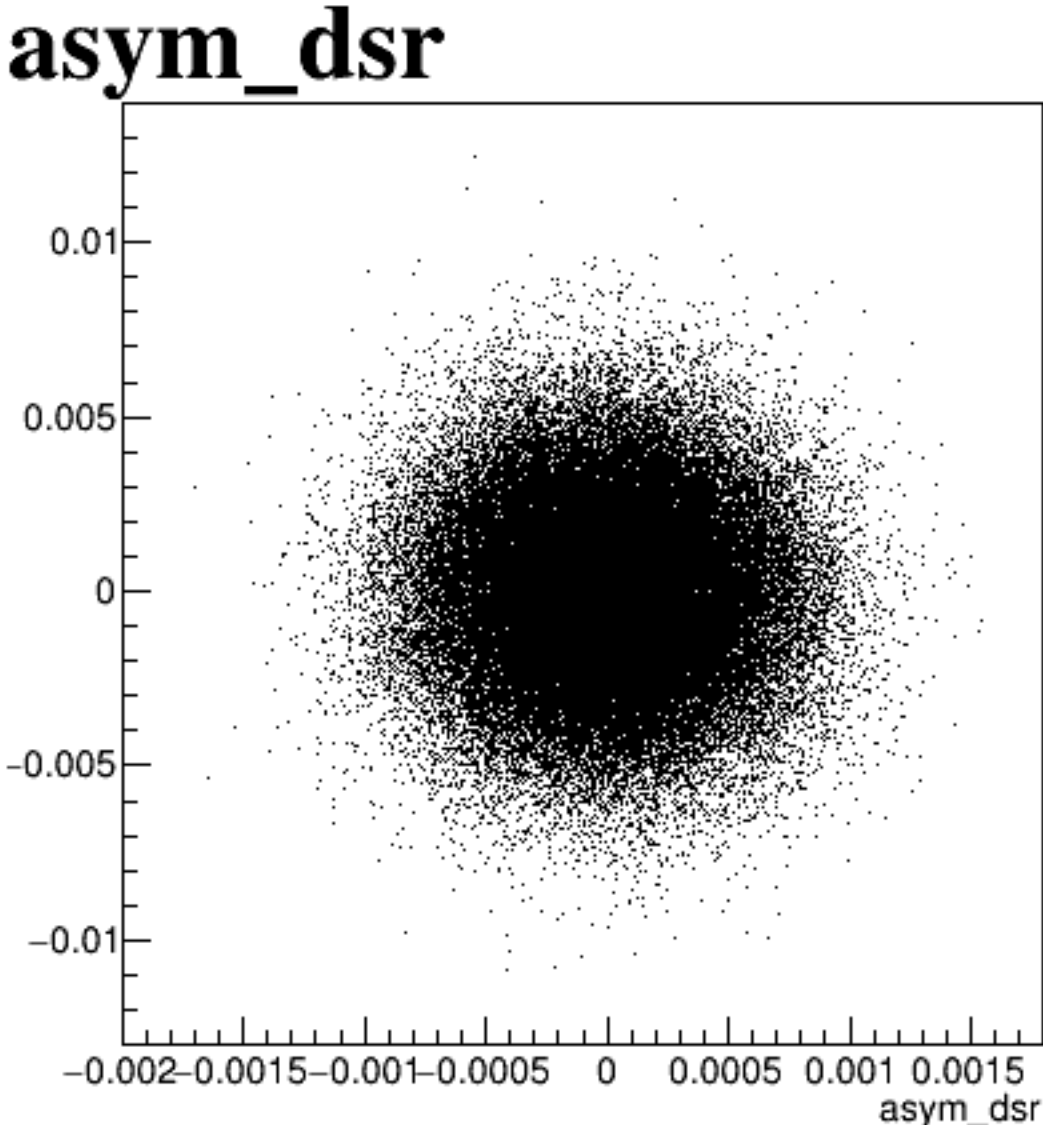
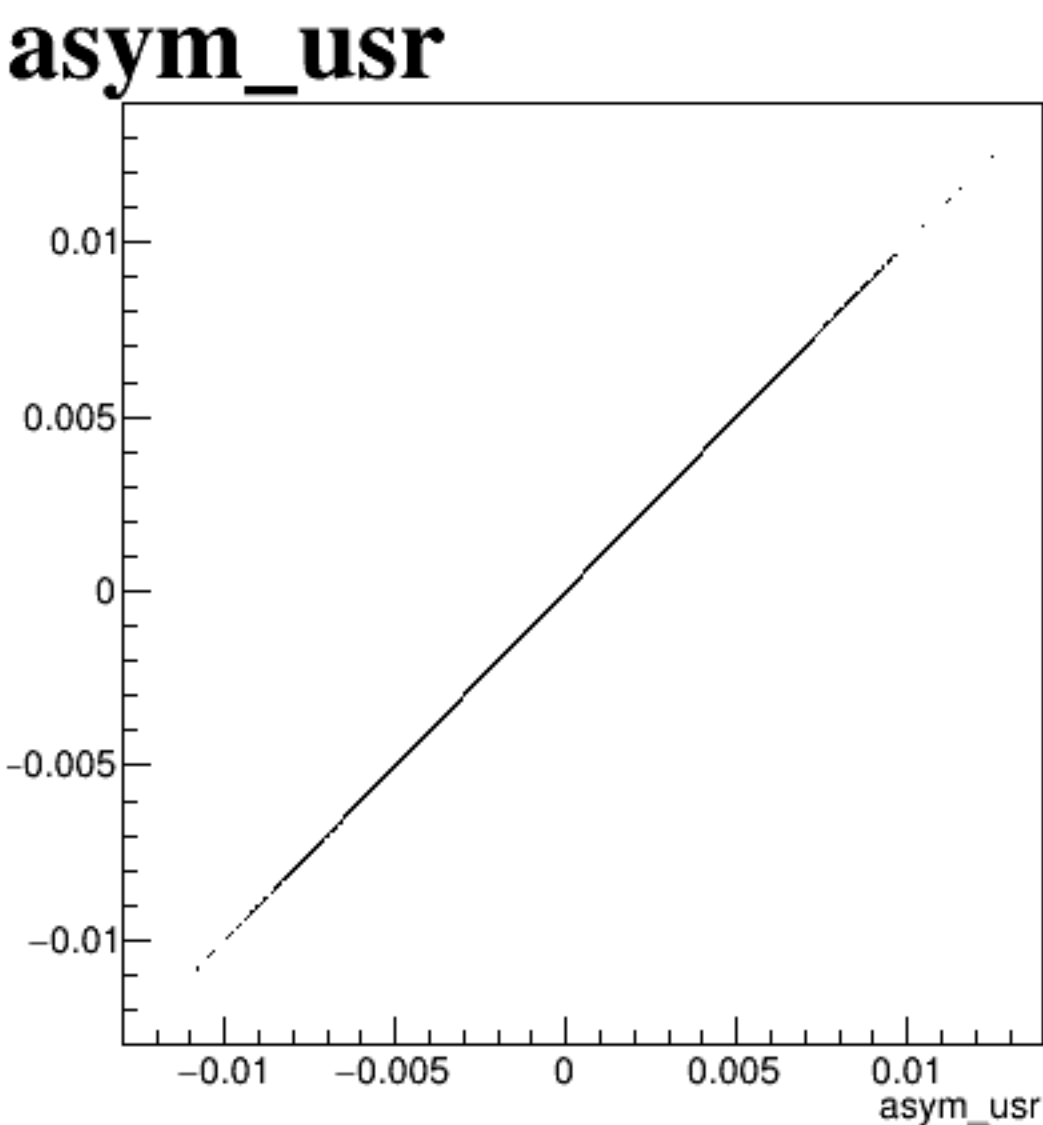
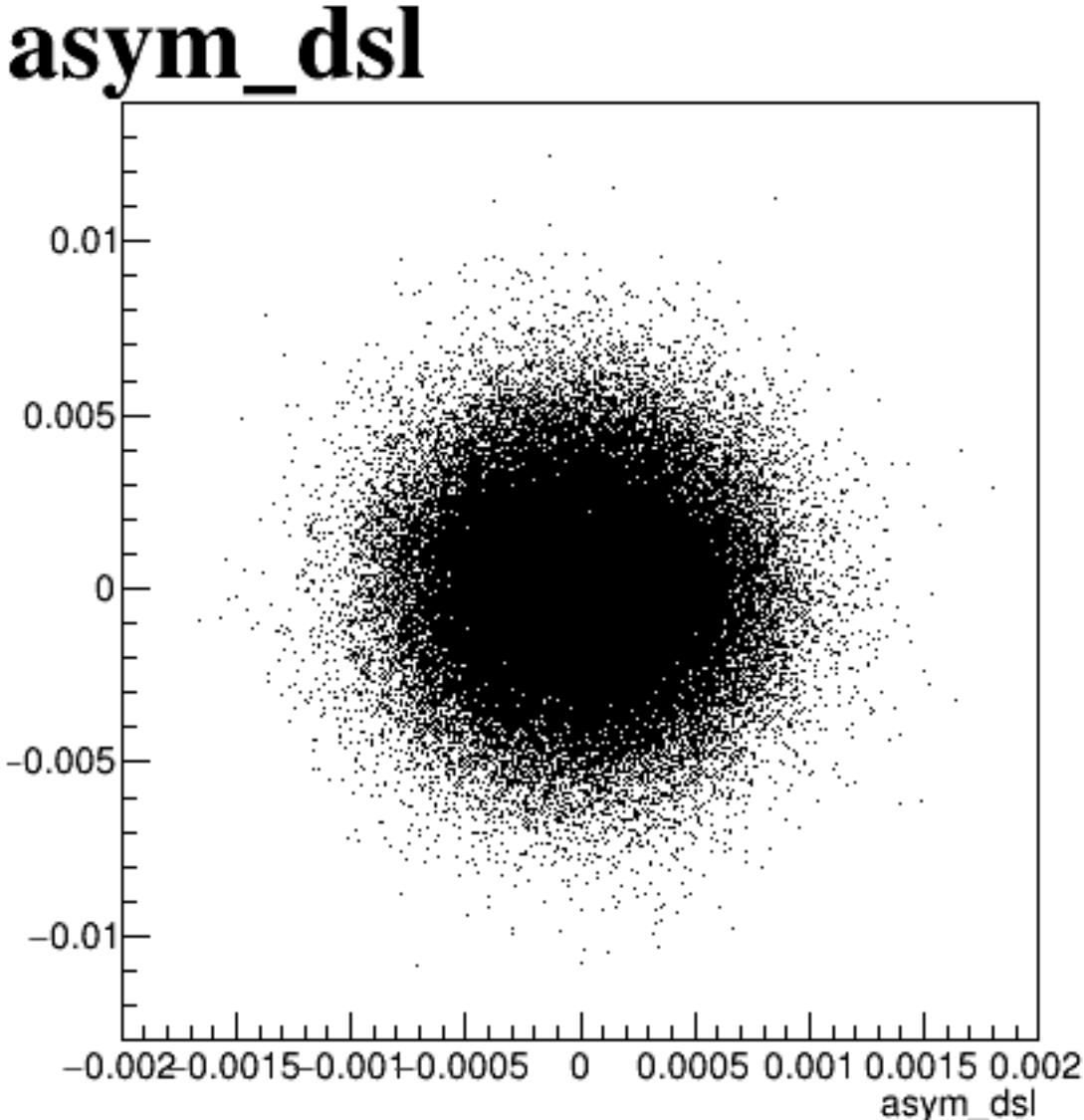
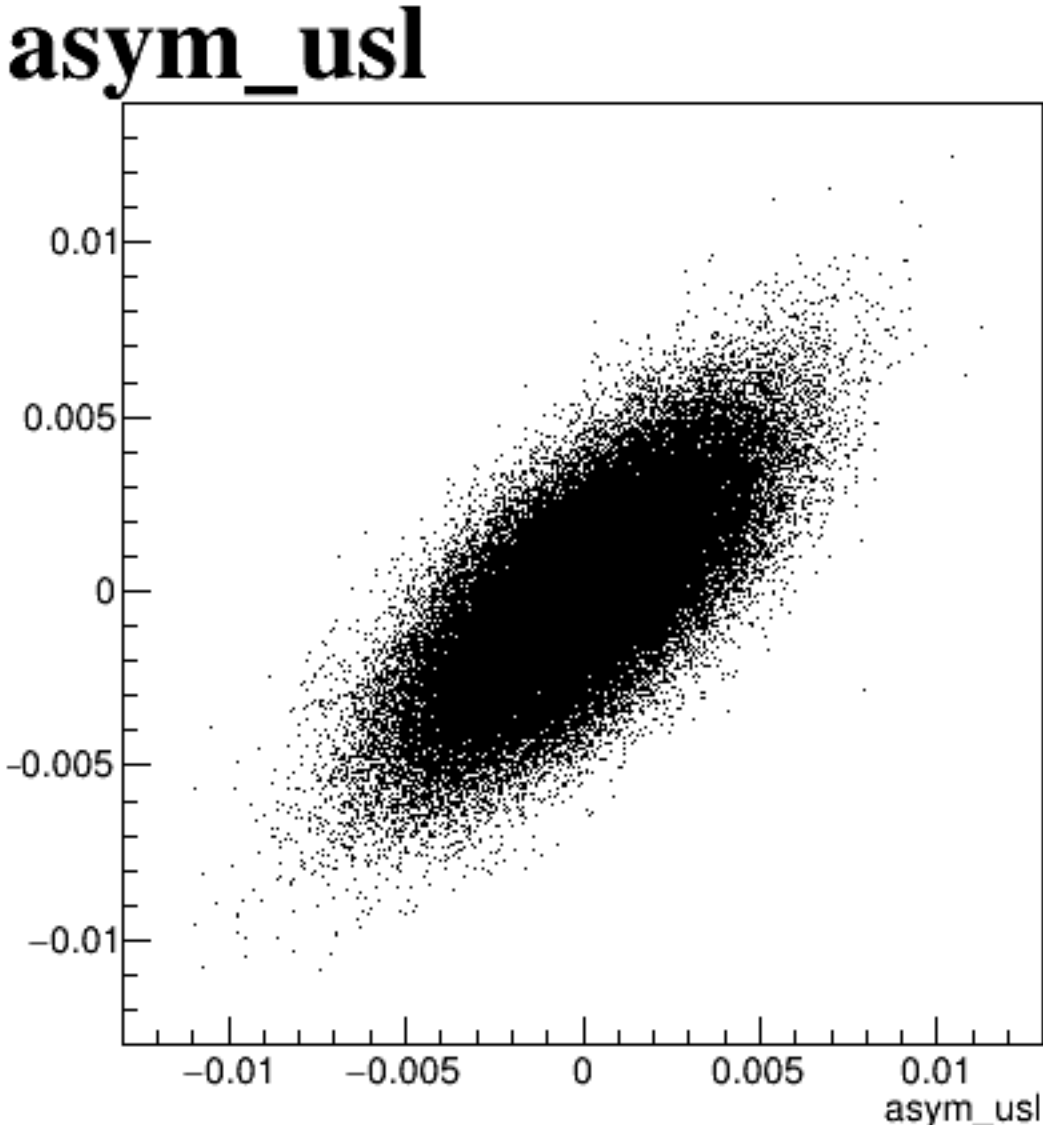
asym_usl



asym_dsl



asym_usr



asym_dsr

