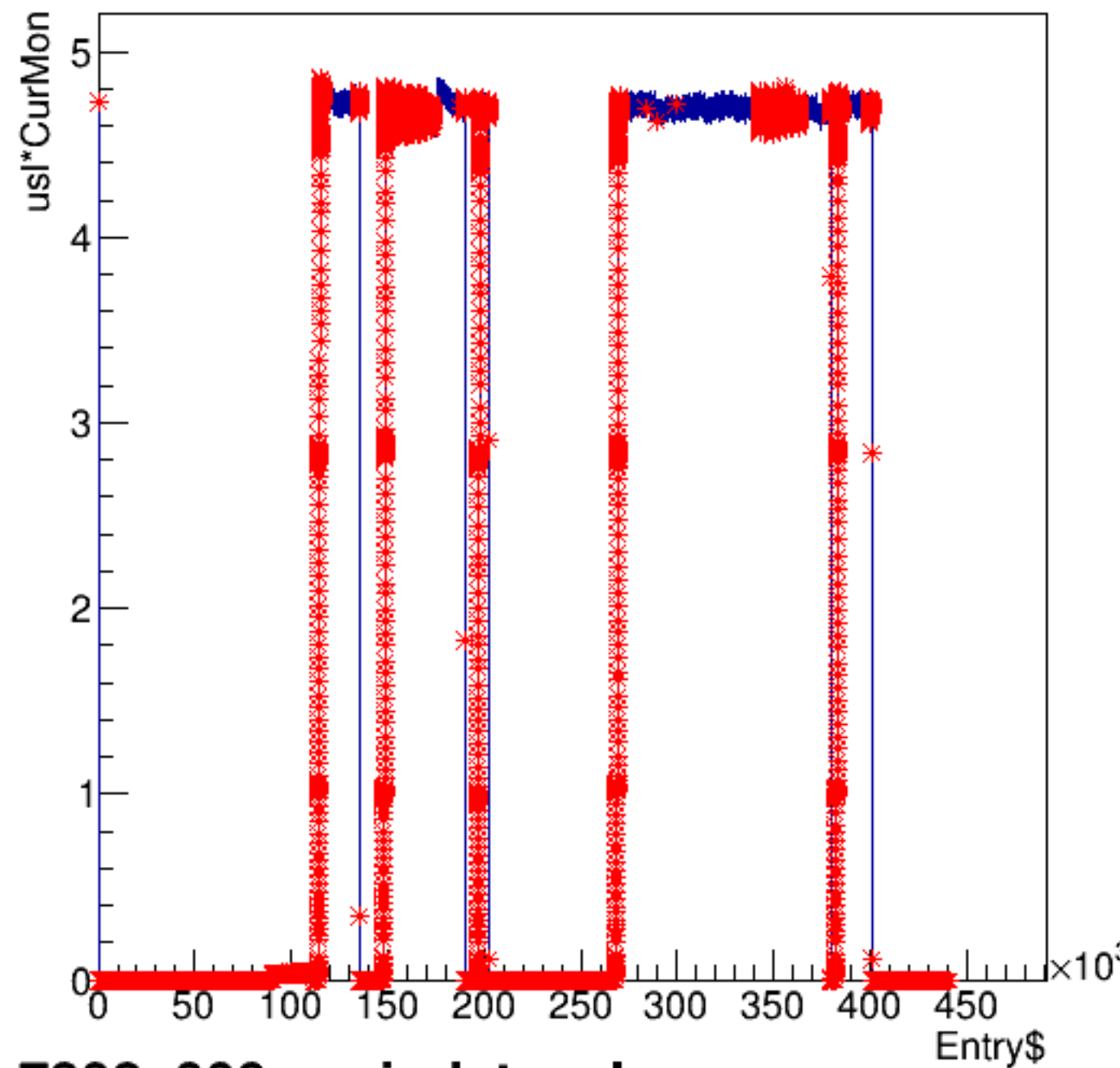
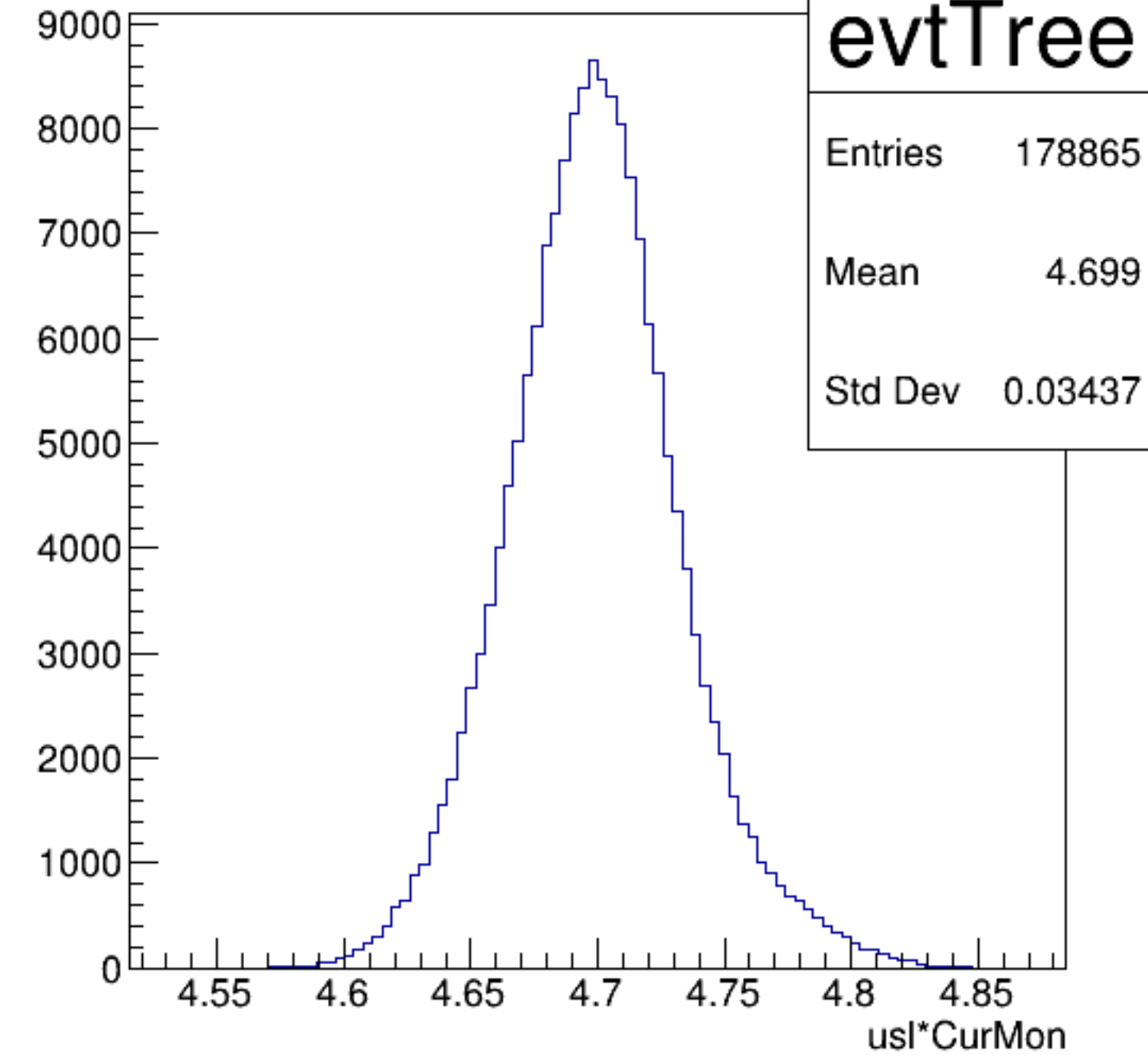


usl*CurMon:Entry\$



run7392_000_maindet_usl.png

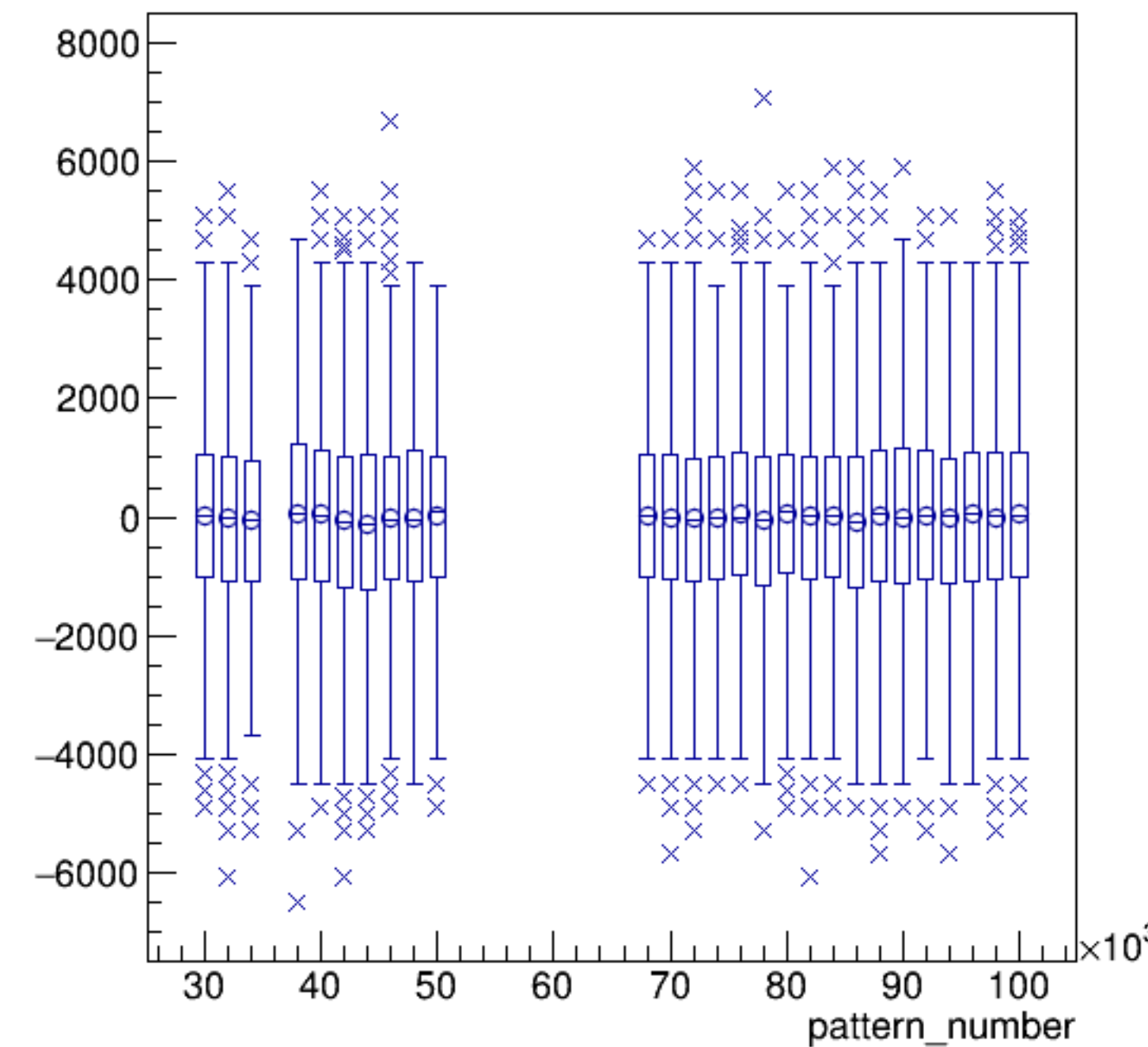
usl*CurMon {ErrorFlag==0}



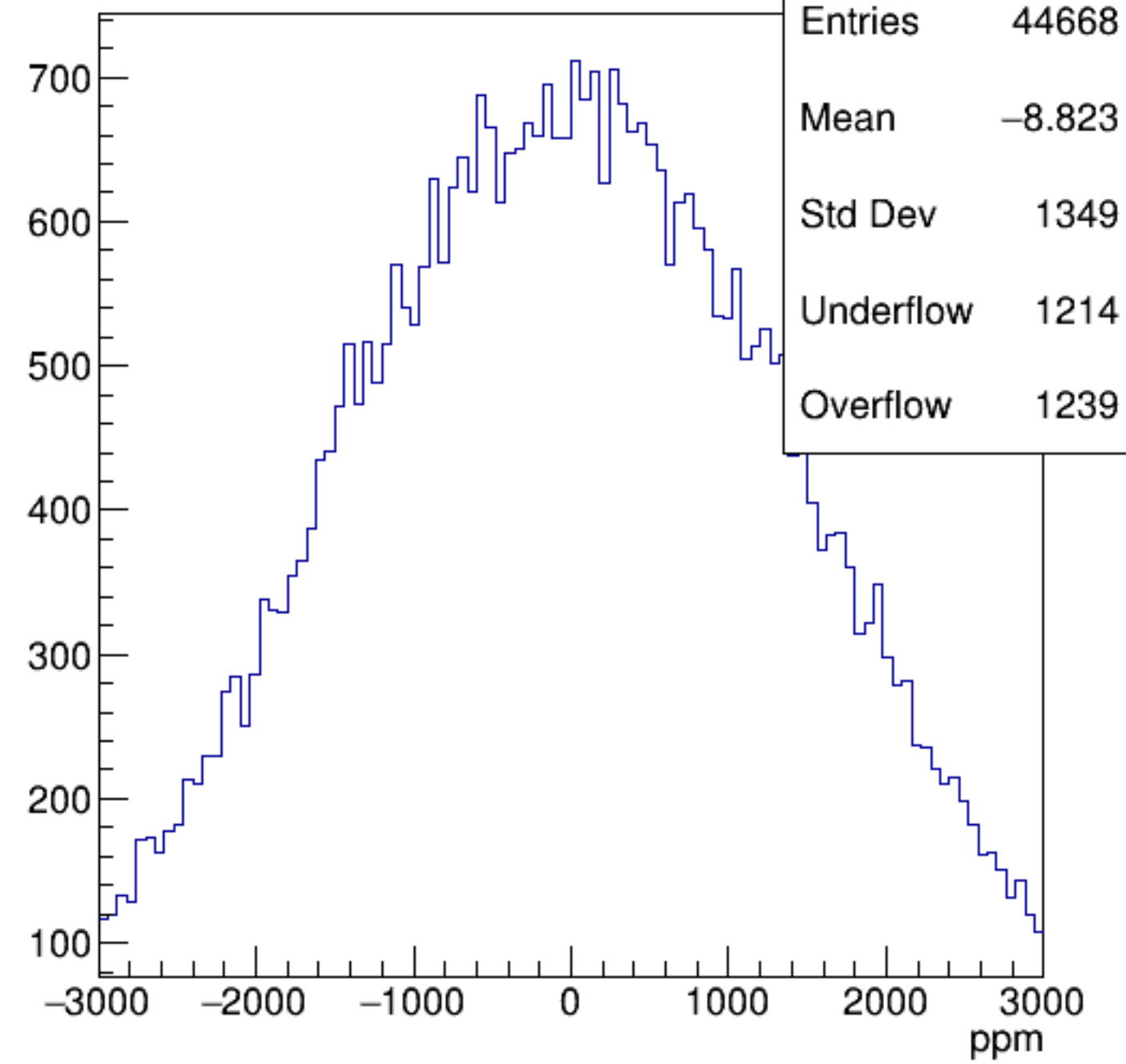
evtTree

Entries	178865
Mean	4.699
Std Dev	0.03437

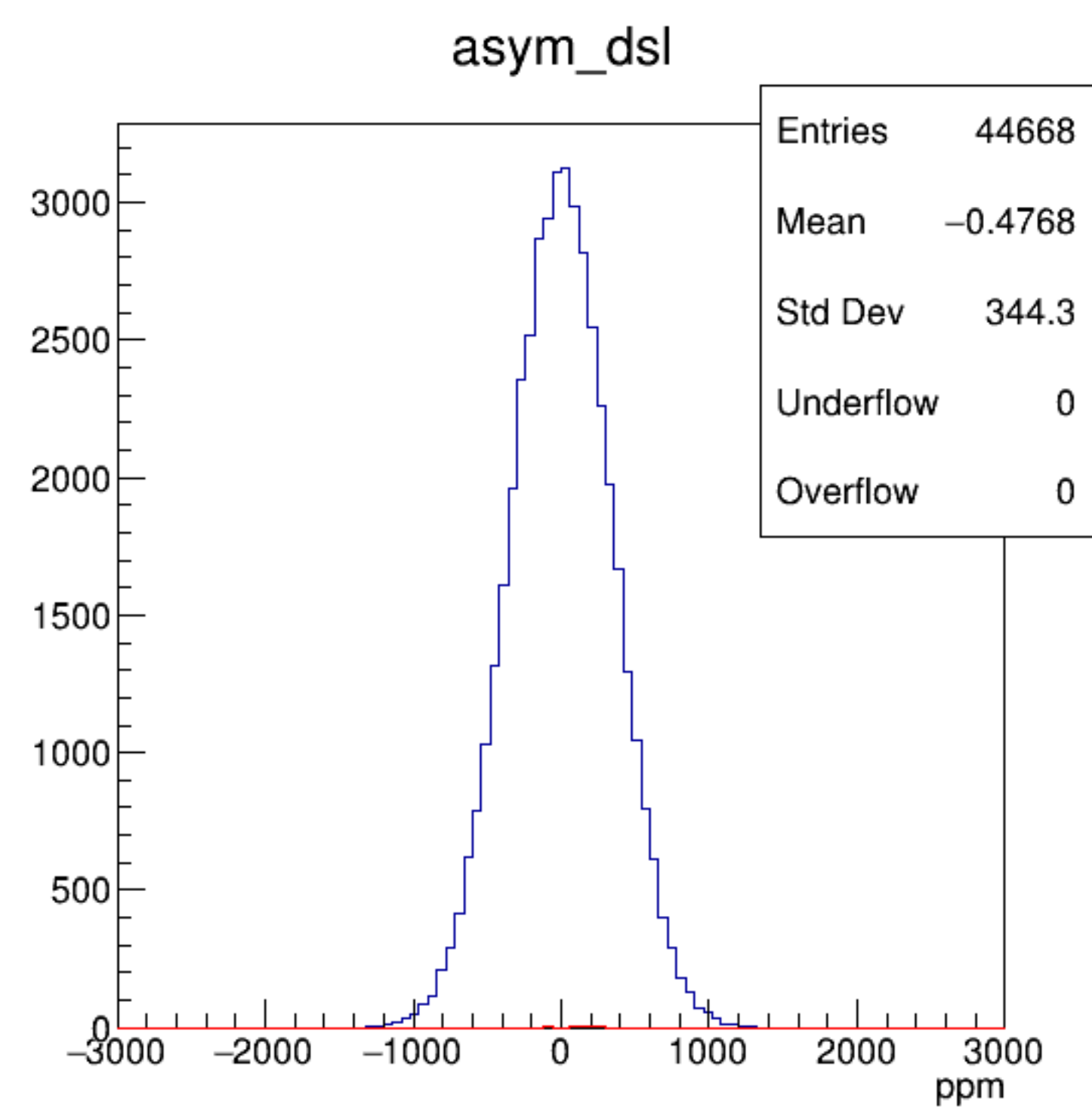
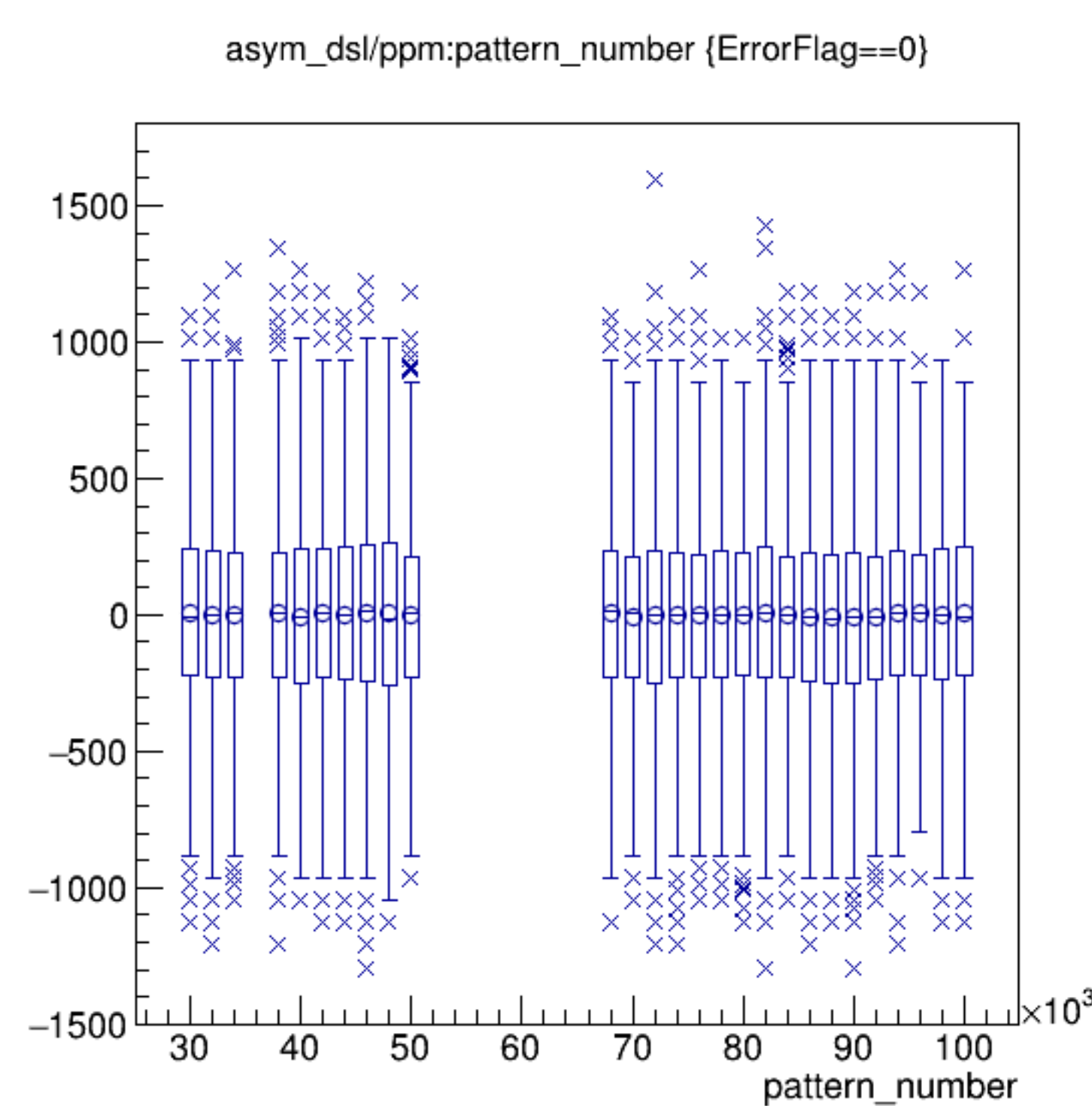
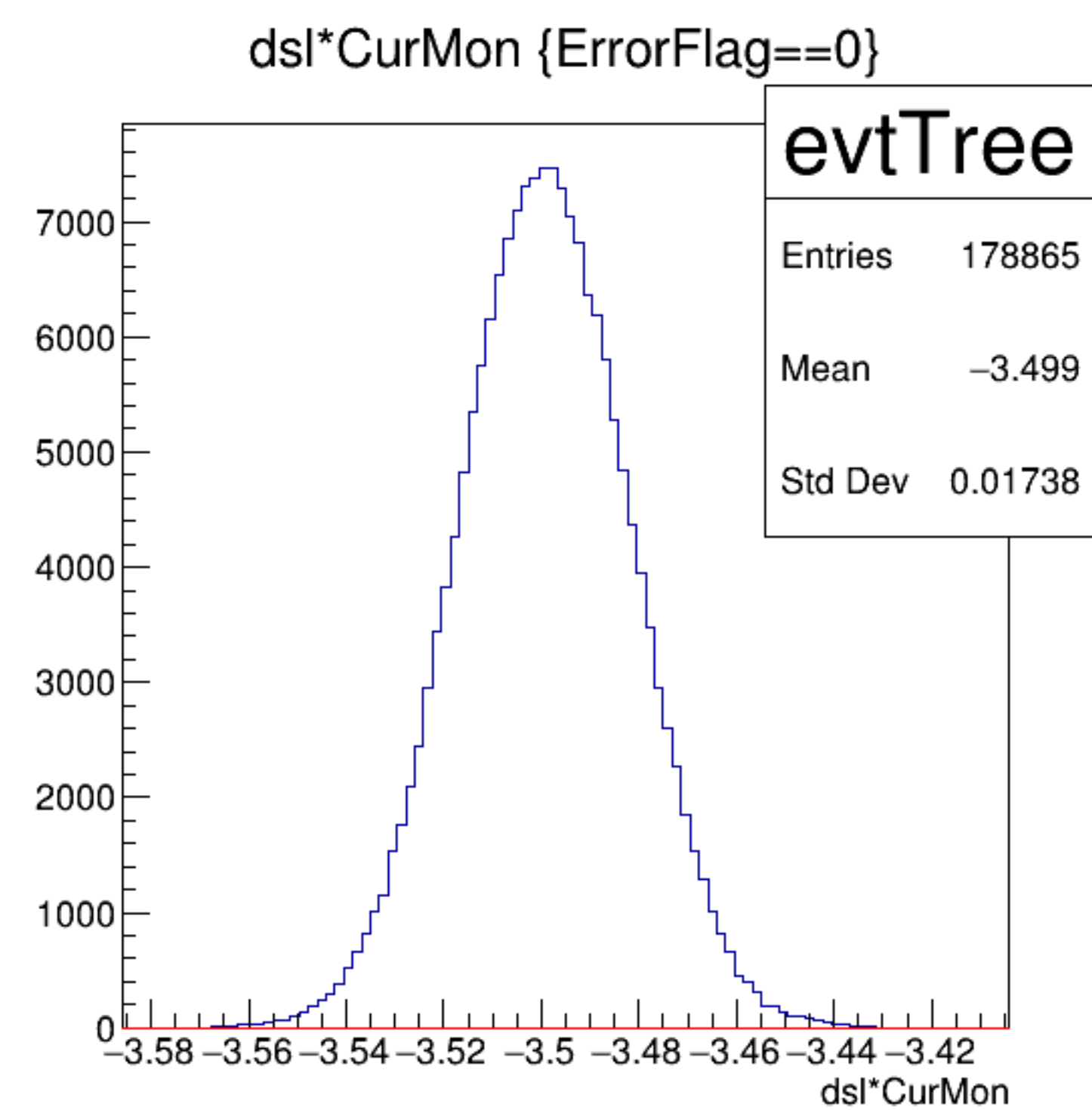
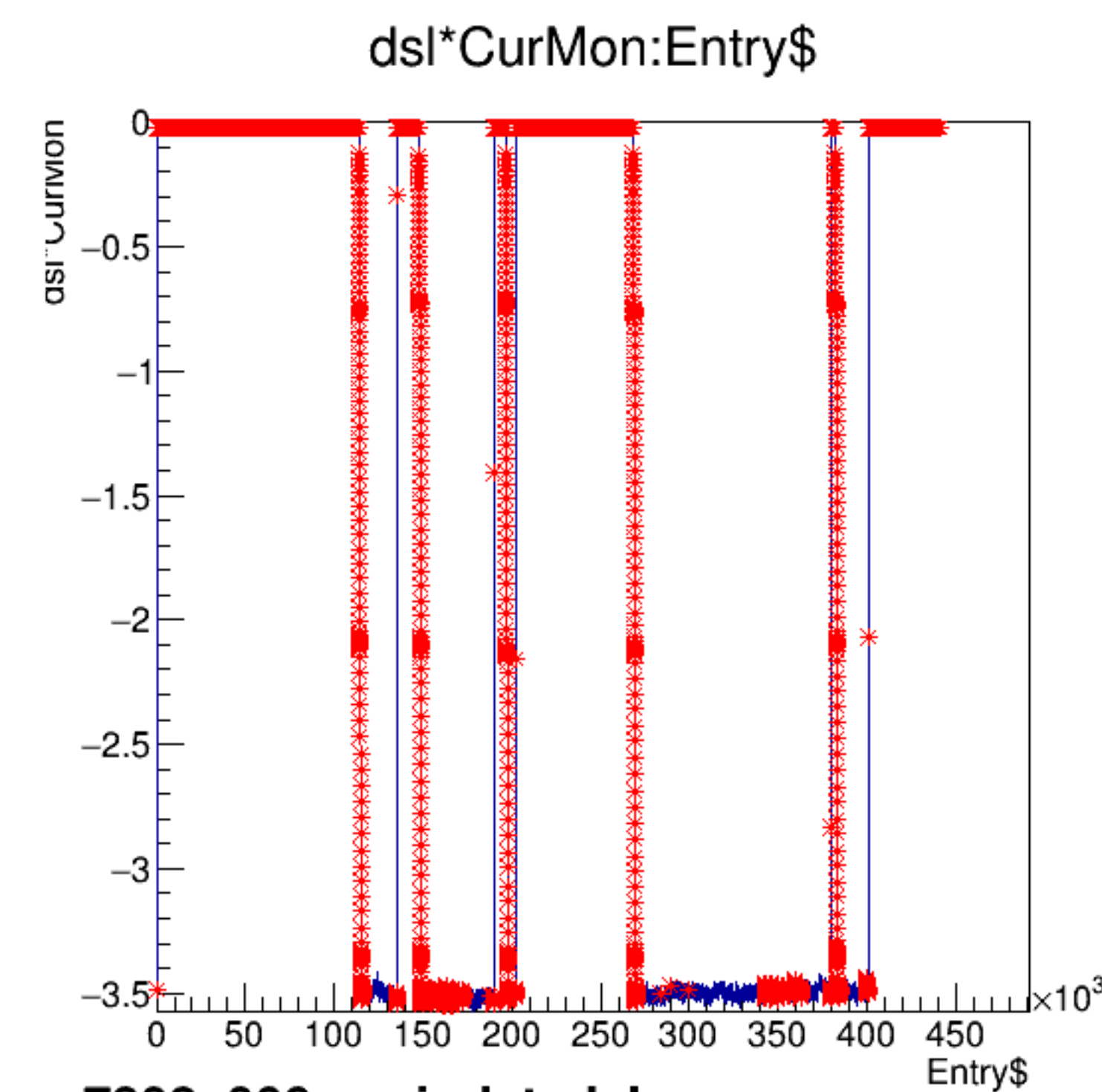
asym_usl/ppm:pattern_number {ErrorFlag==0}



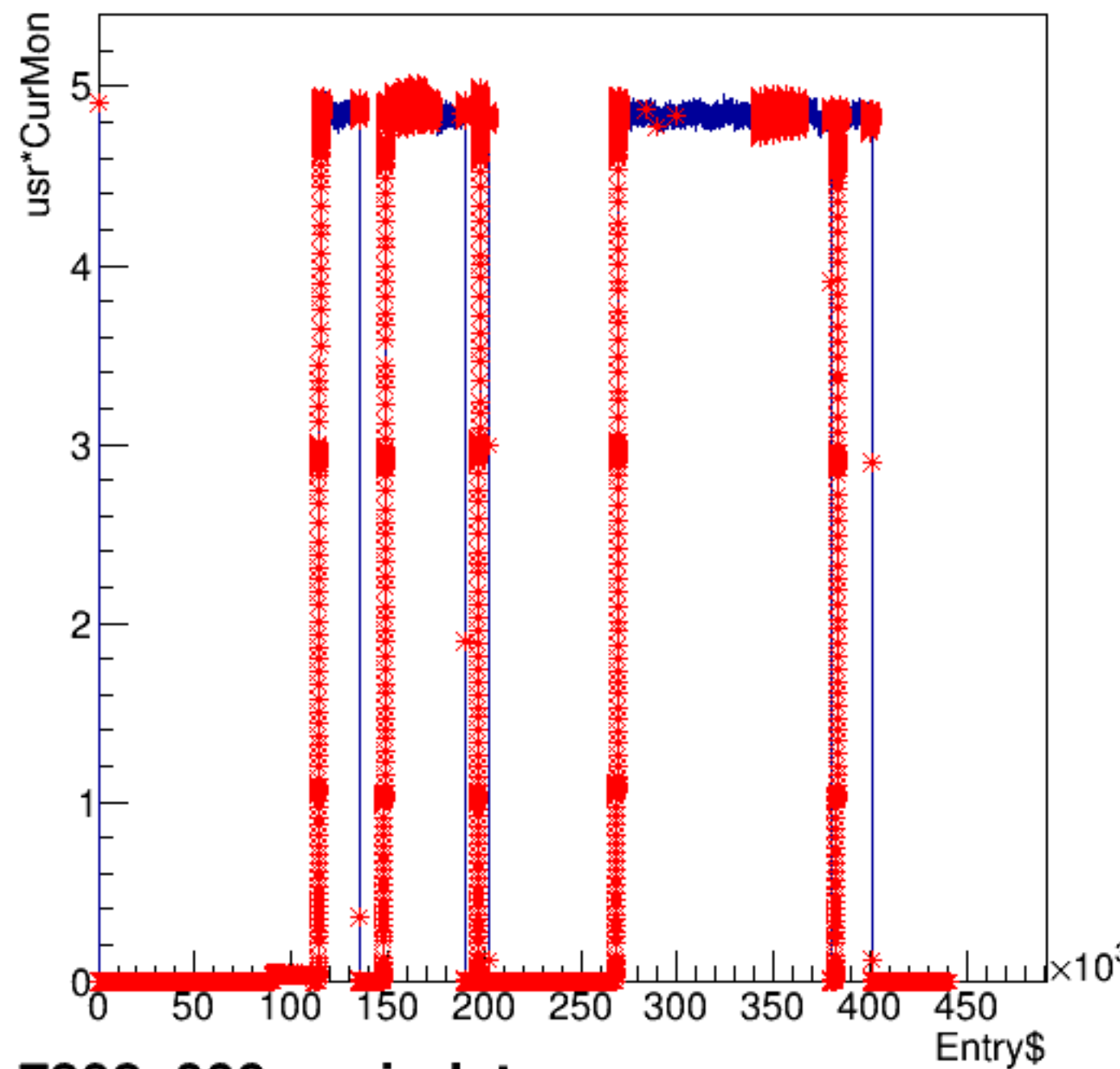
asym_usl



Entries	44668
Mean	-8.823
Std Dev	1349
Underflow	1214
Overflow	1239

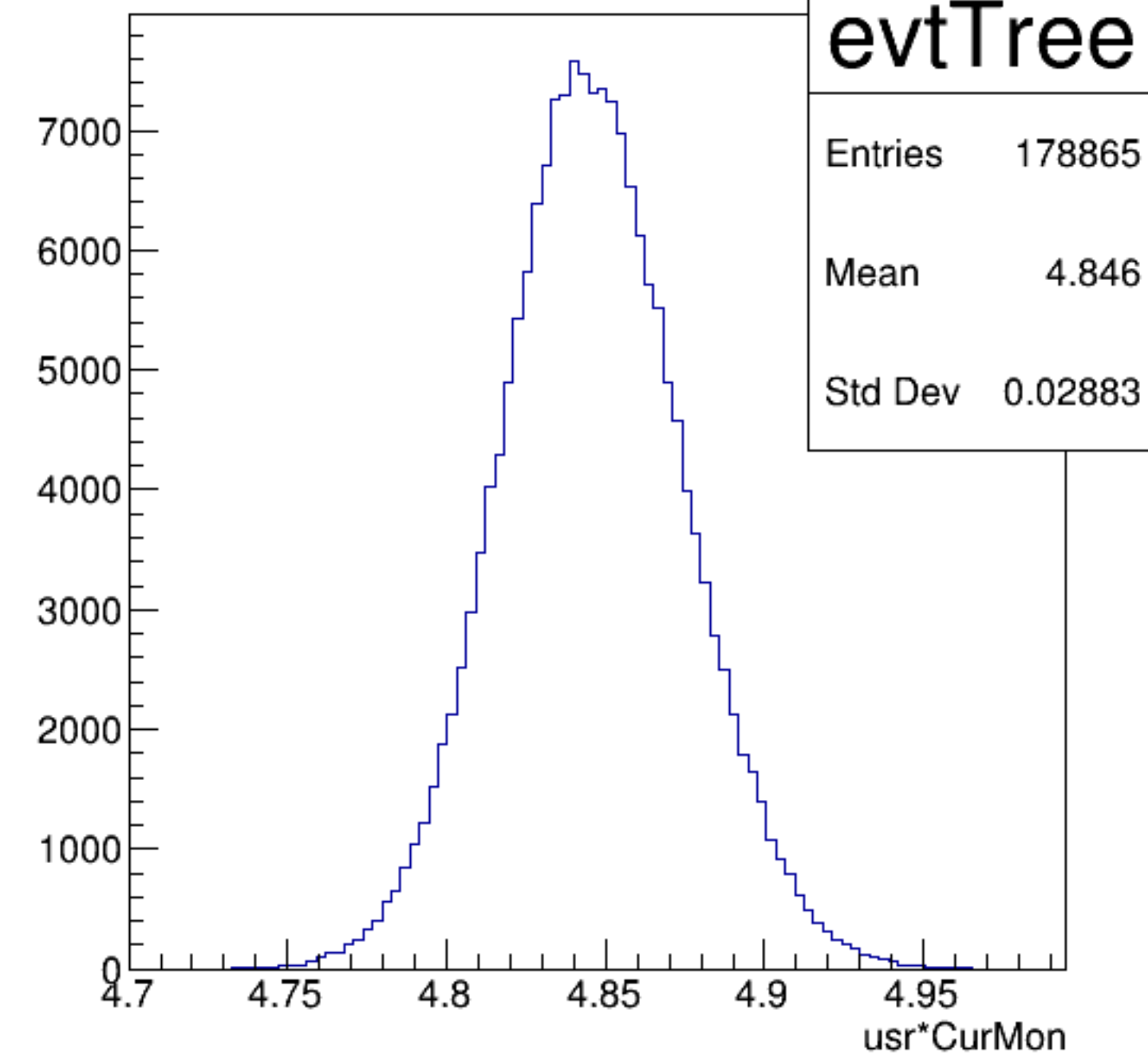


usr*CurMon:Entry\$



run7392_000_maindet_usr.png

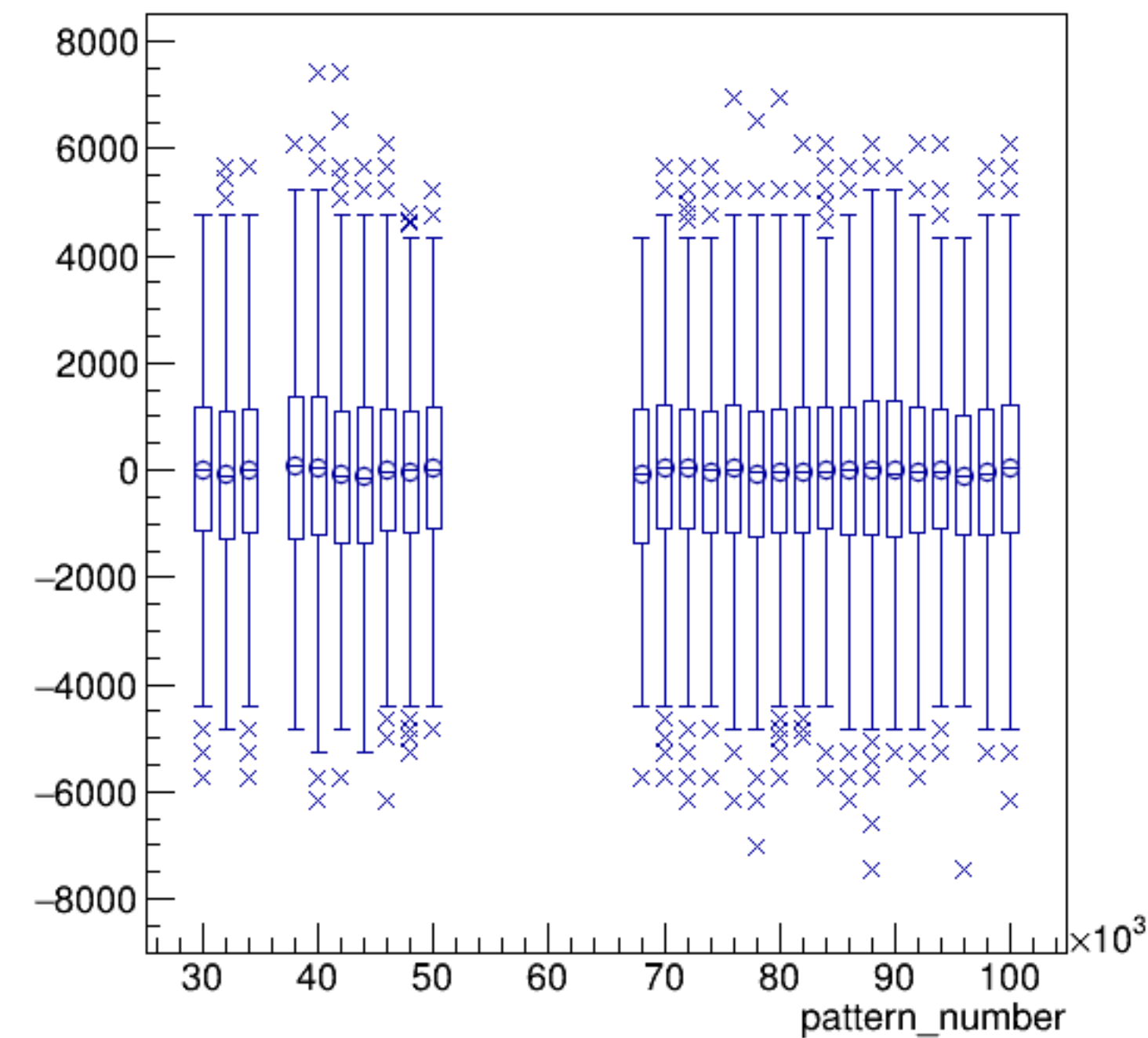
usr*CurMon {ErrorFlag==0}



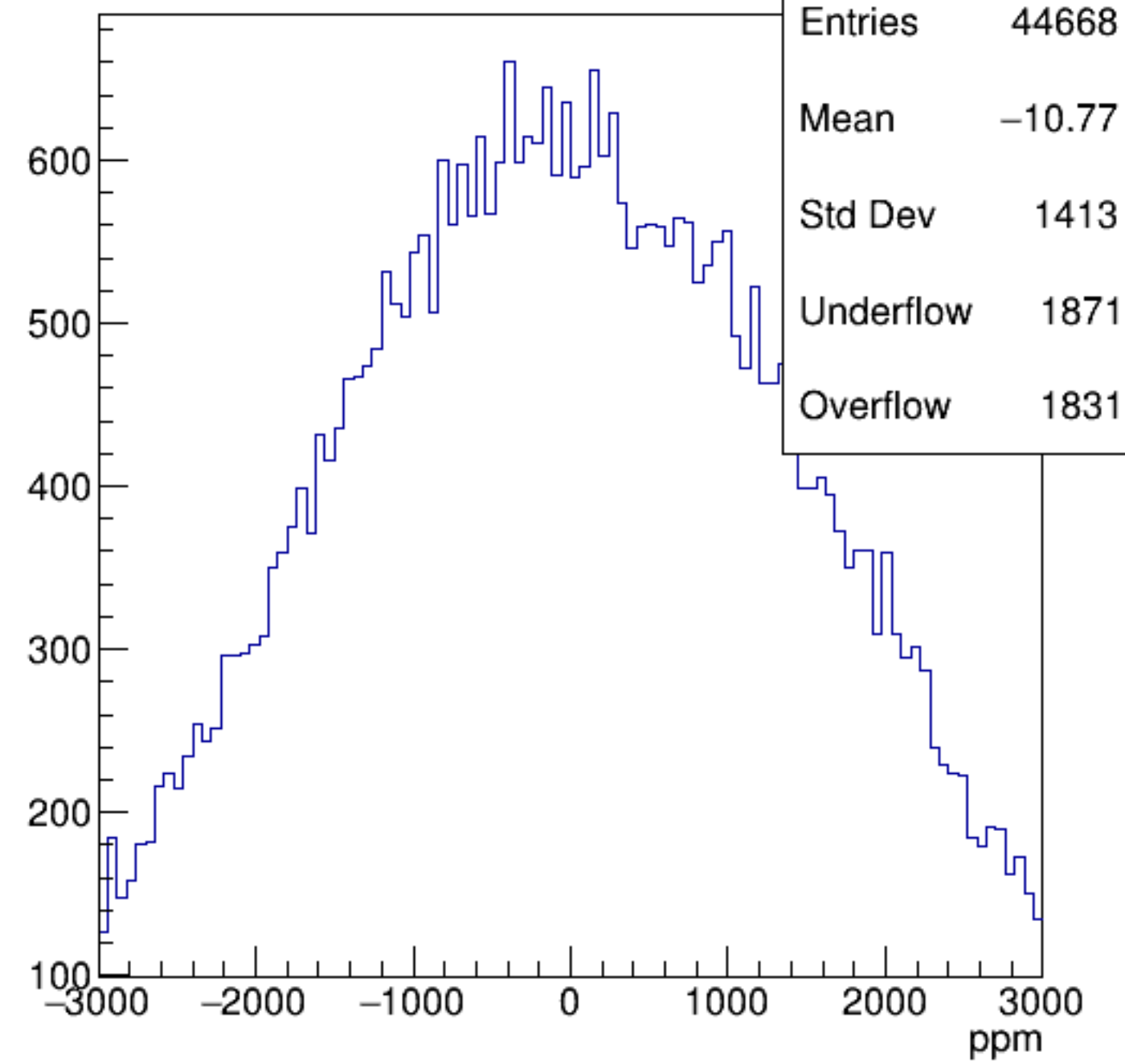
evtTree

Entries	178865
Mean	4.846
Std Dev	0.02883

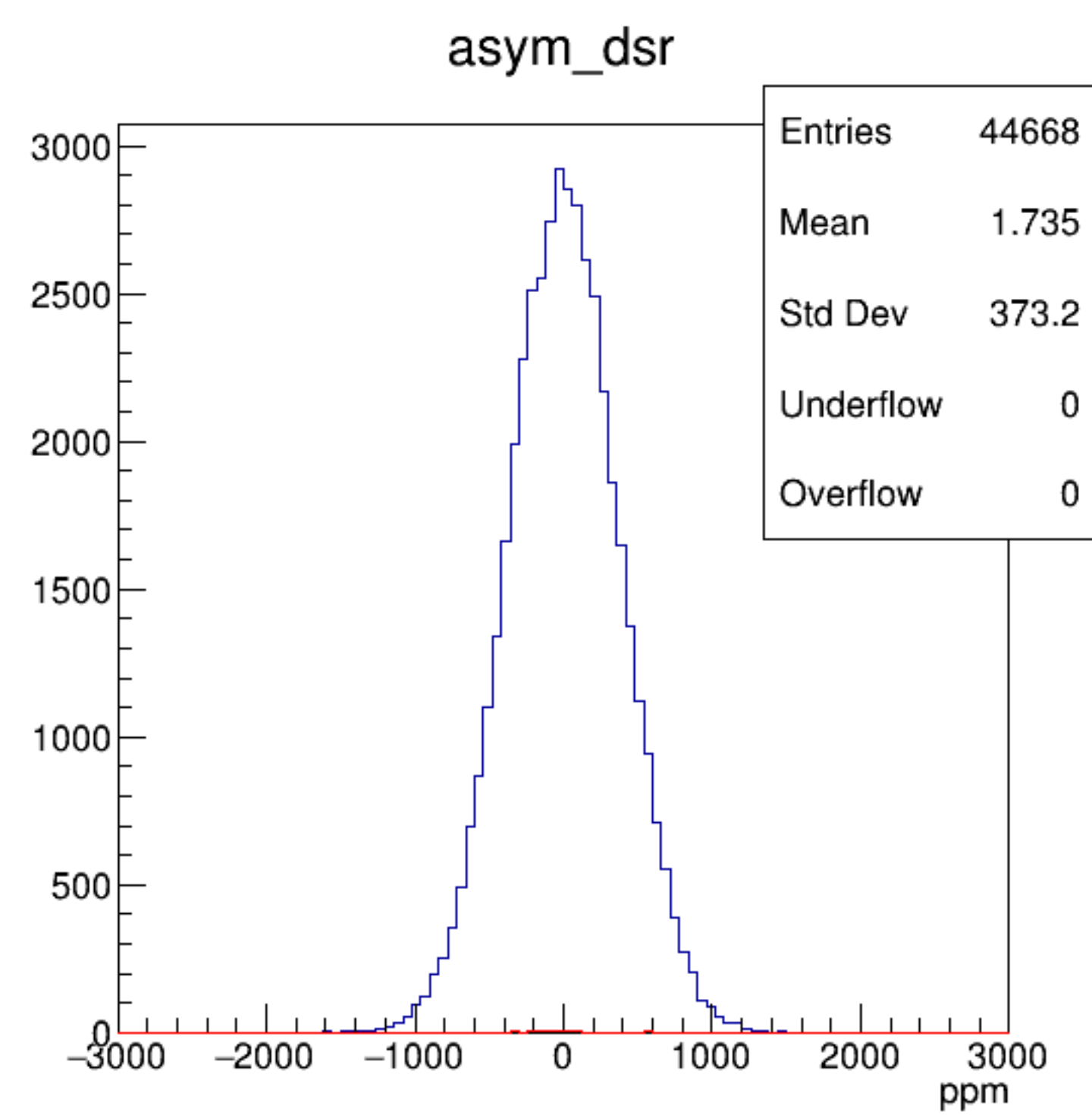
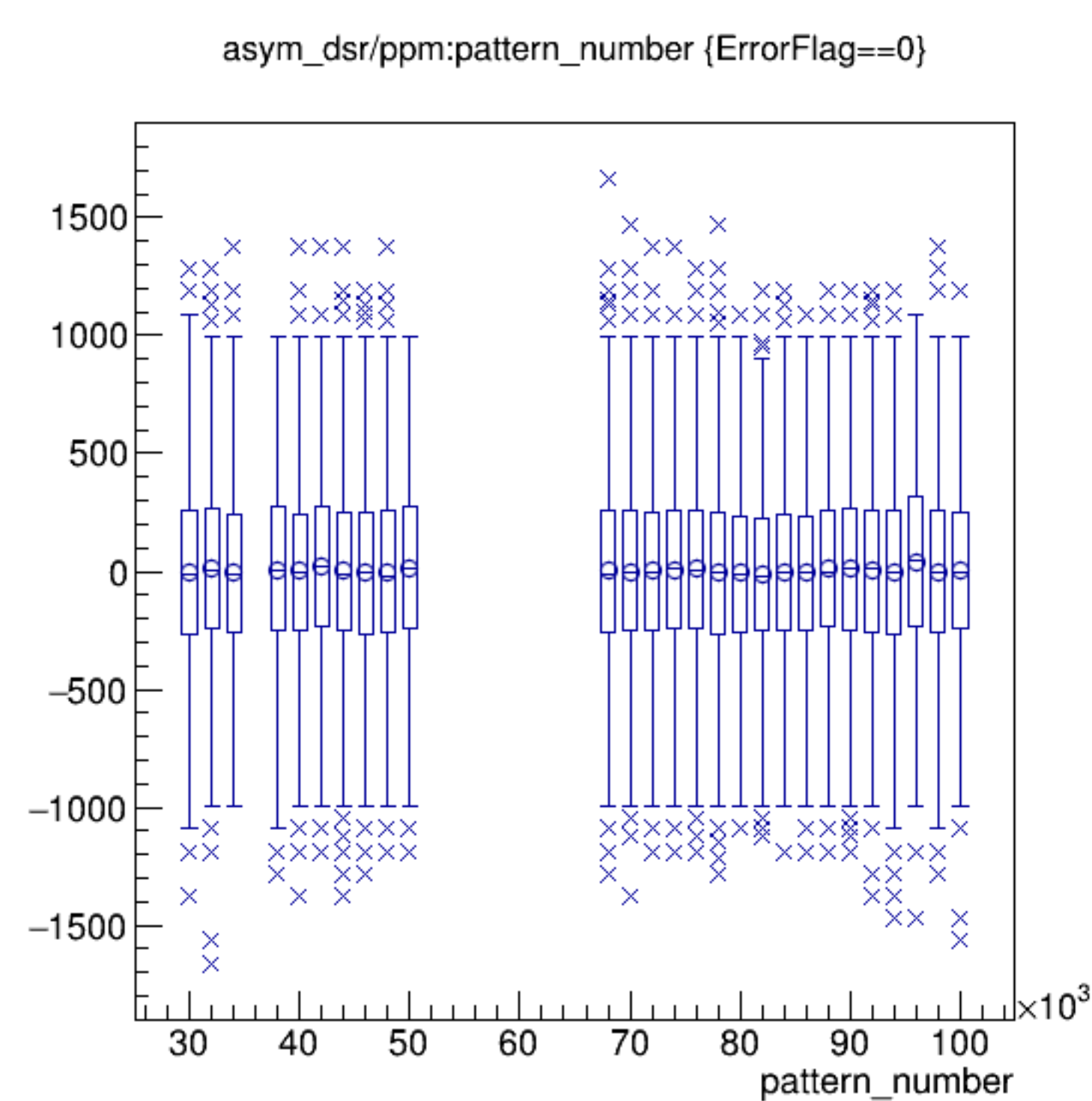
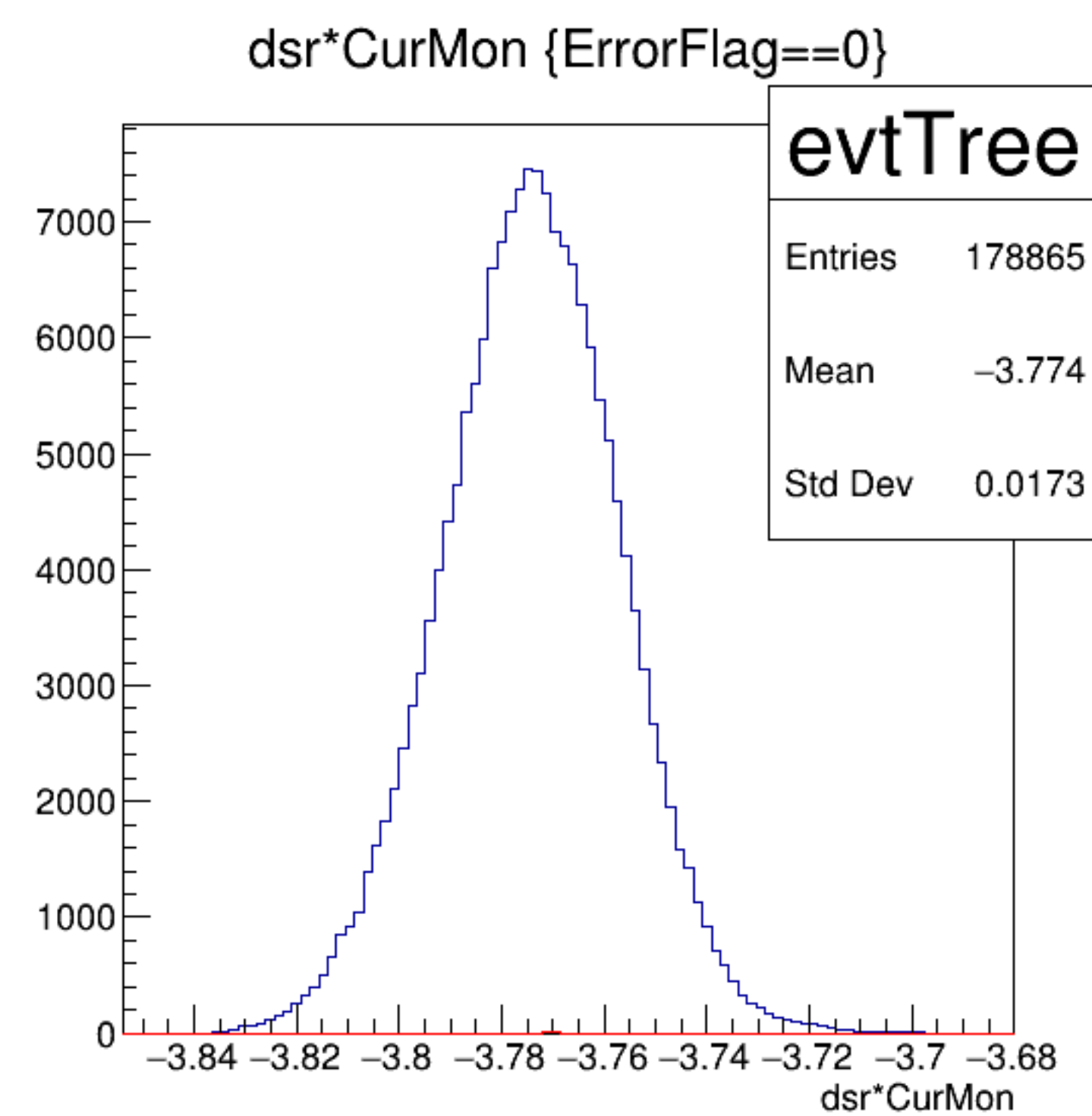
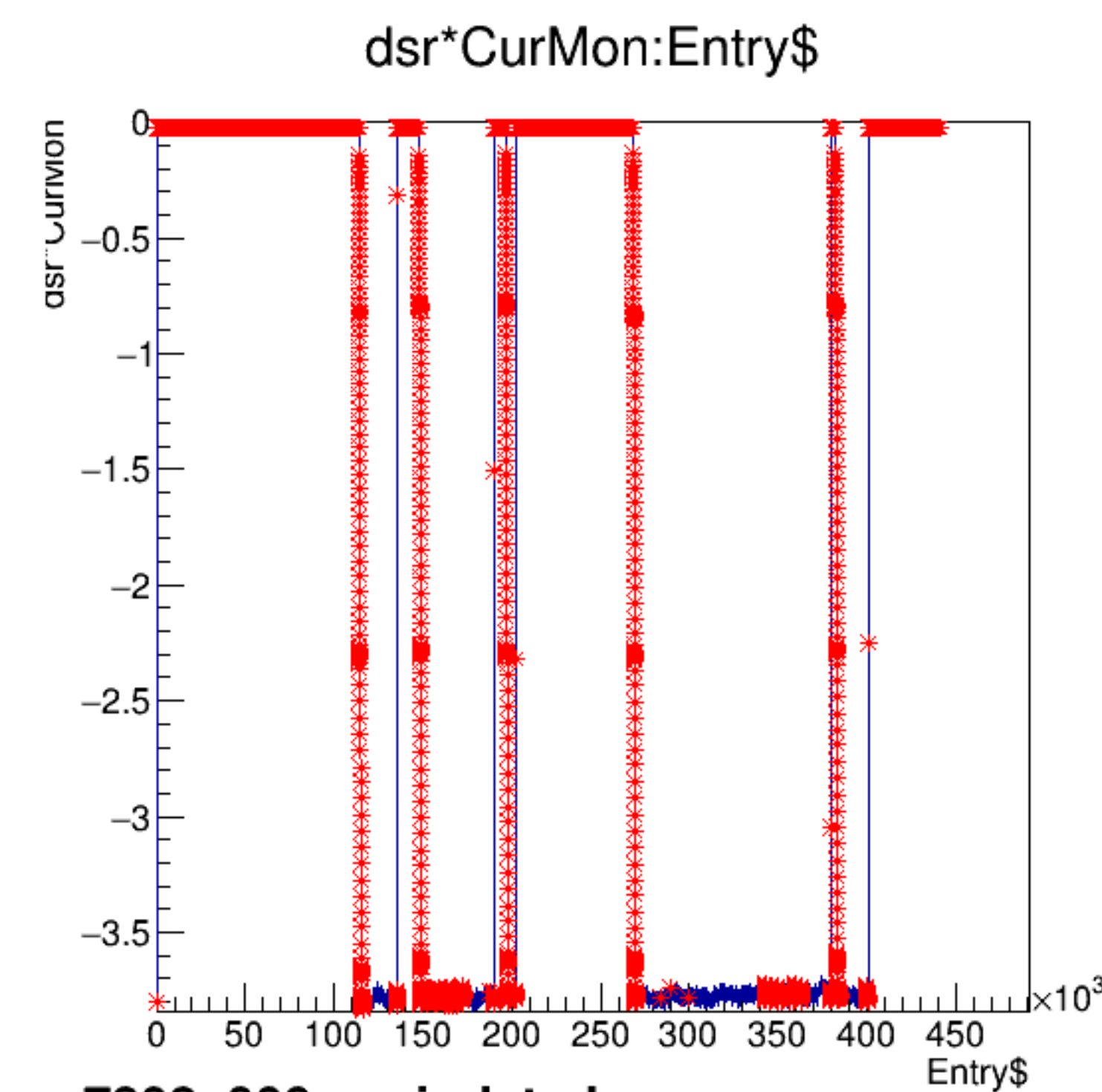
asym_usr/ppm:pattern_number {ErrorFlag==0}



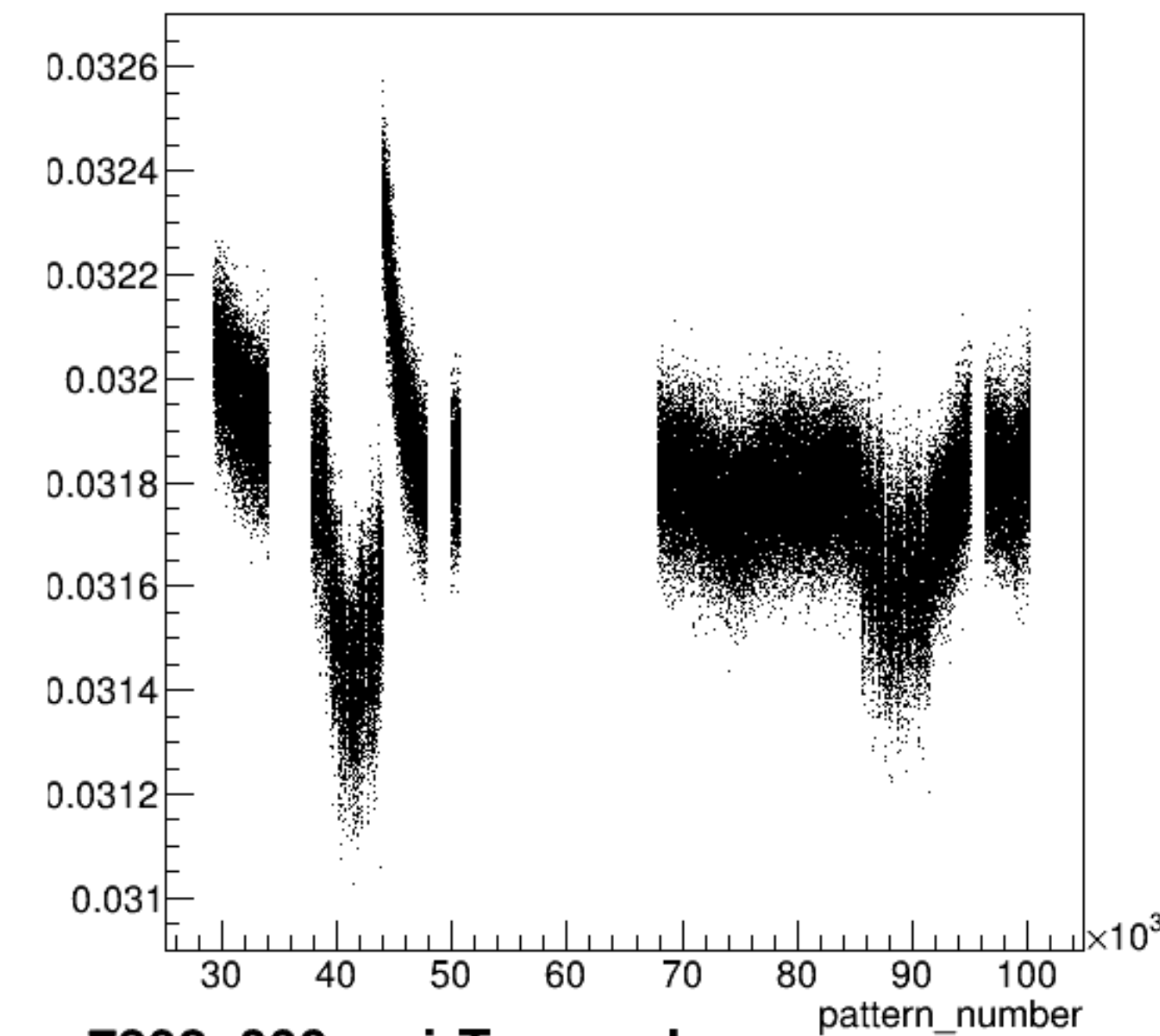
asym_usr



Entries	44668
Mean	-10.77
Std Dev	1413
Underflow	1871
Overflow	1831

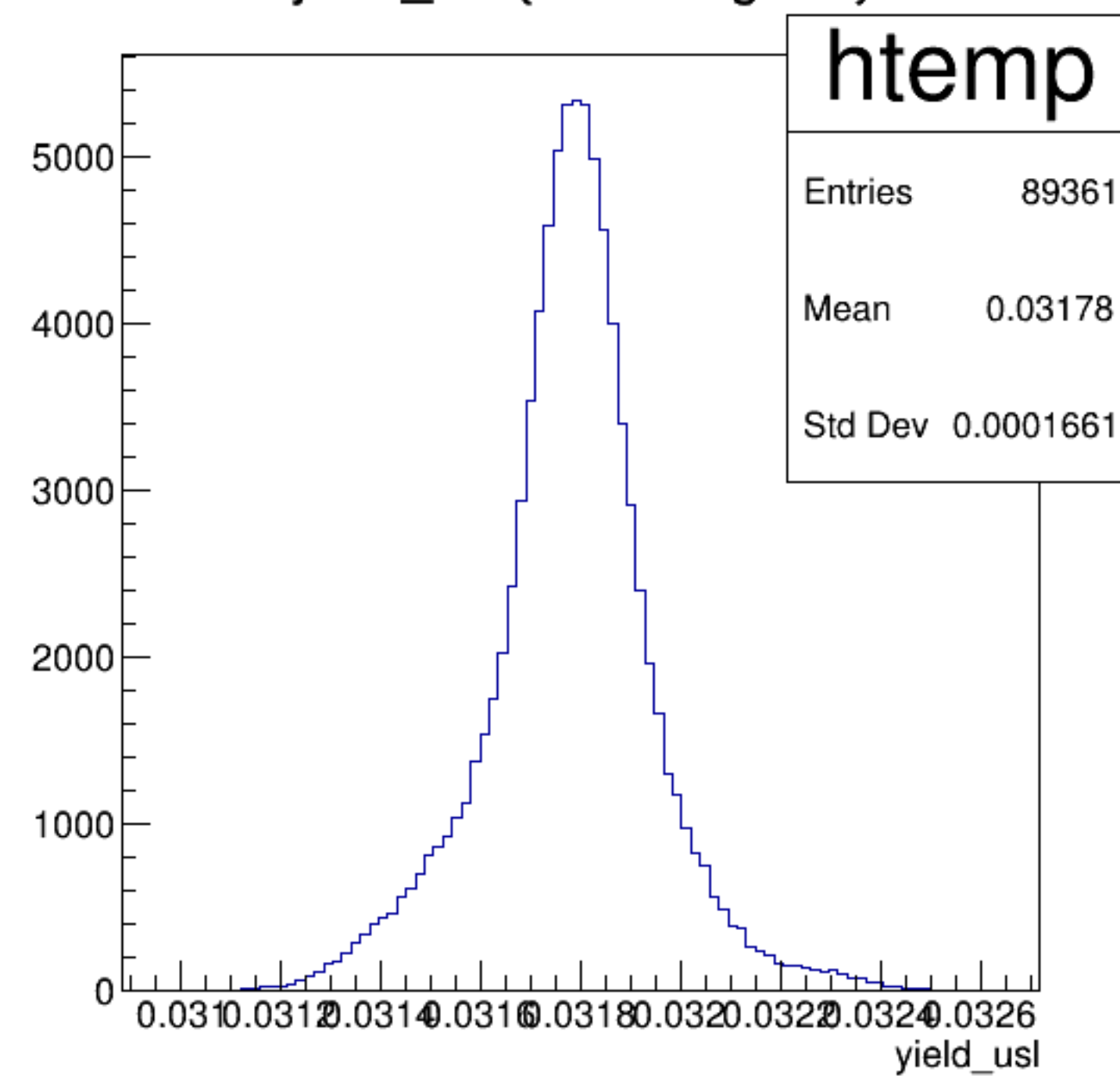


yield_usl:pattern_number {ErrorFlag==0}

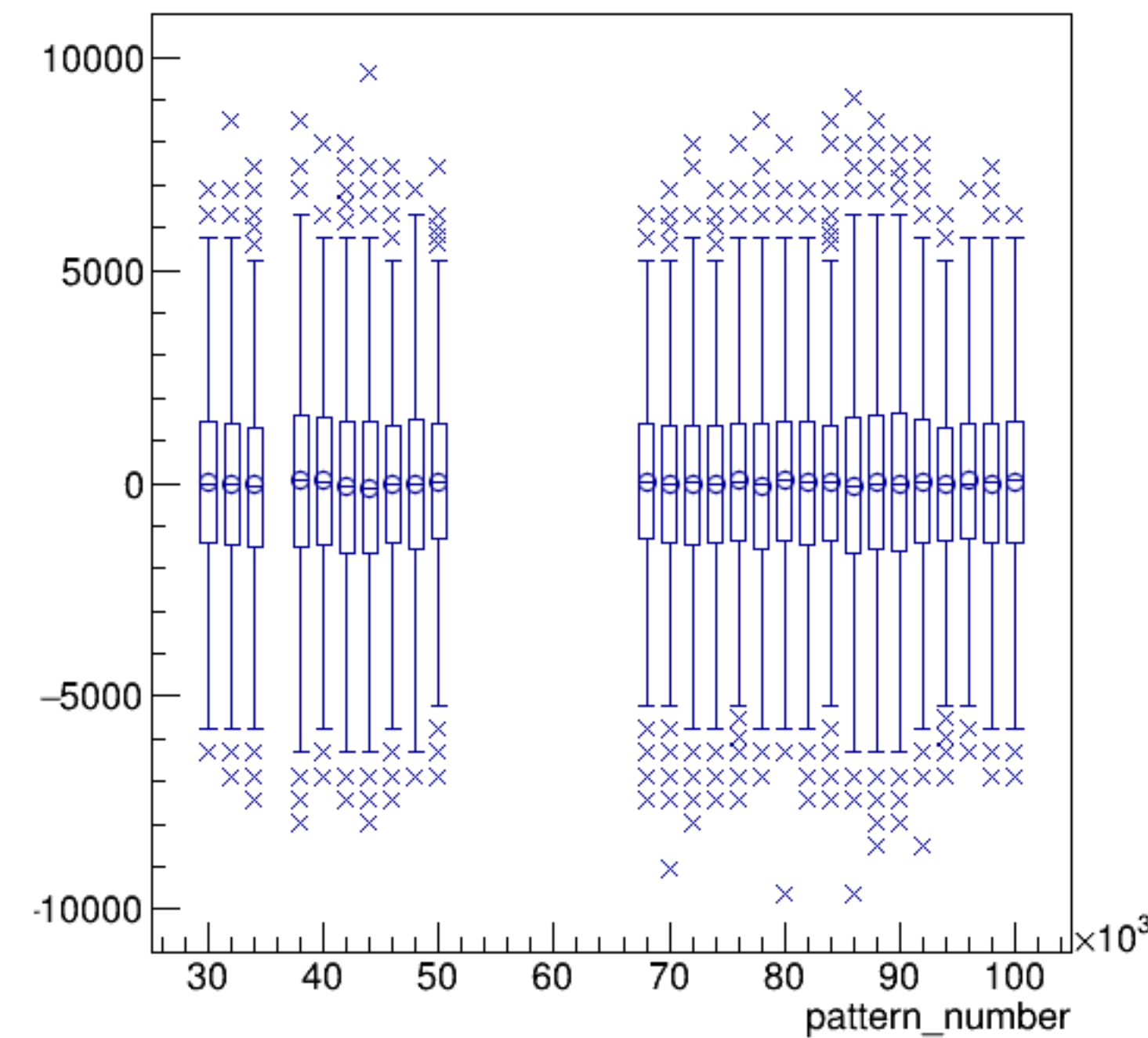


run7392_000_pairTree_usl.png

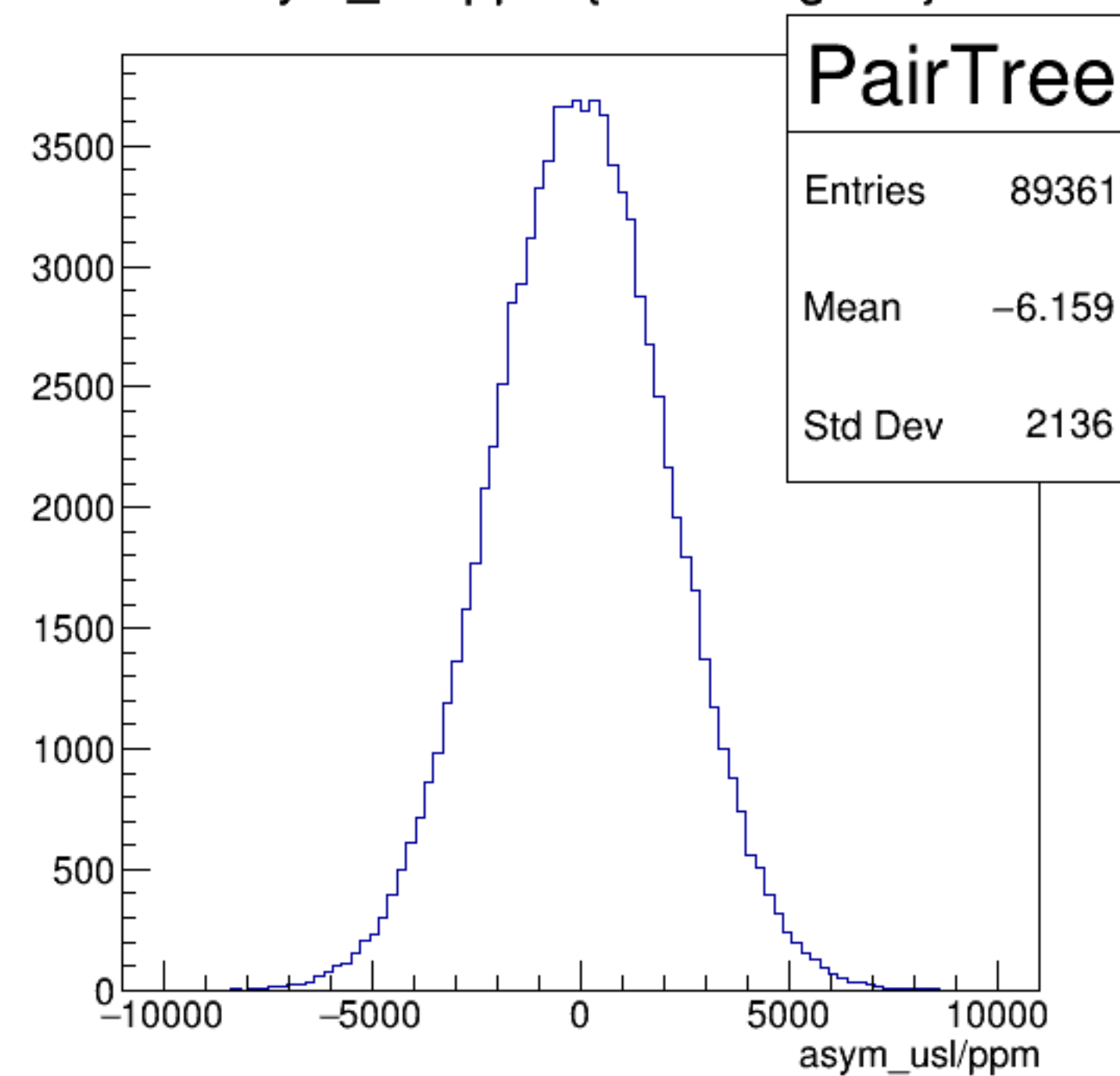
yield_usl {ErrorFlag==0}



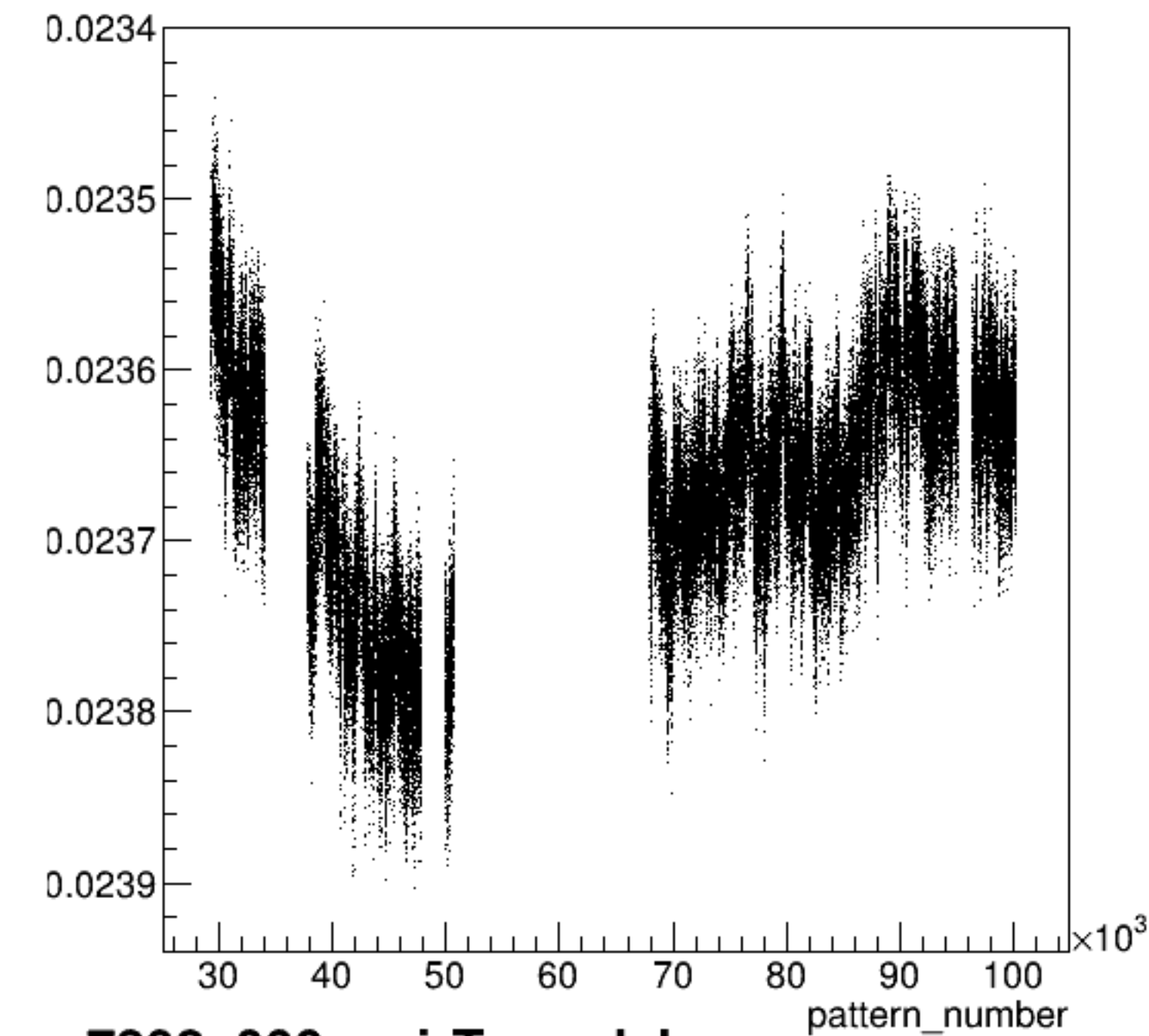
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl/ppm {ErrorFlag==0}

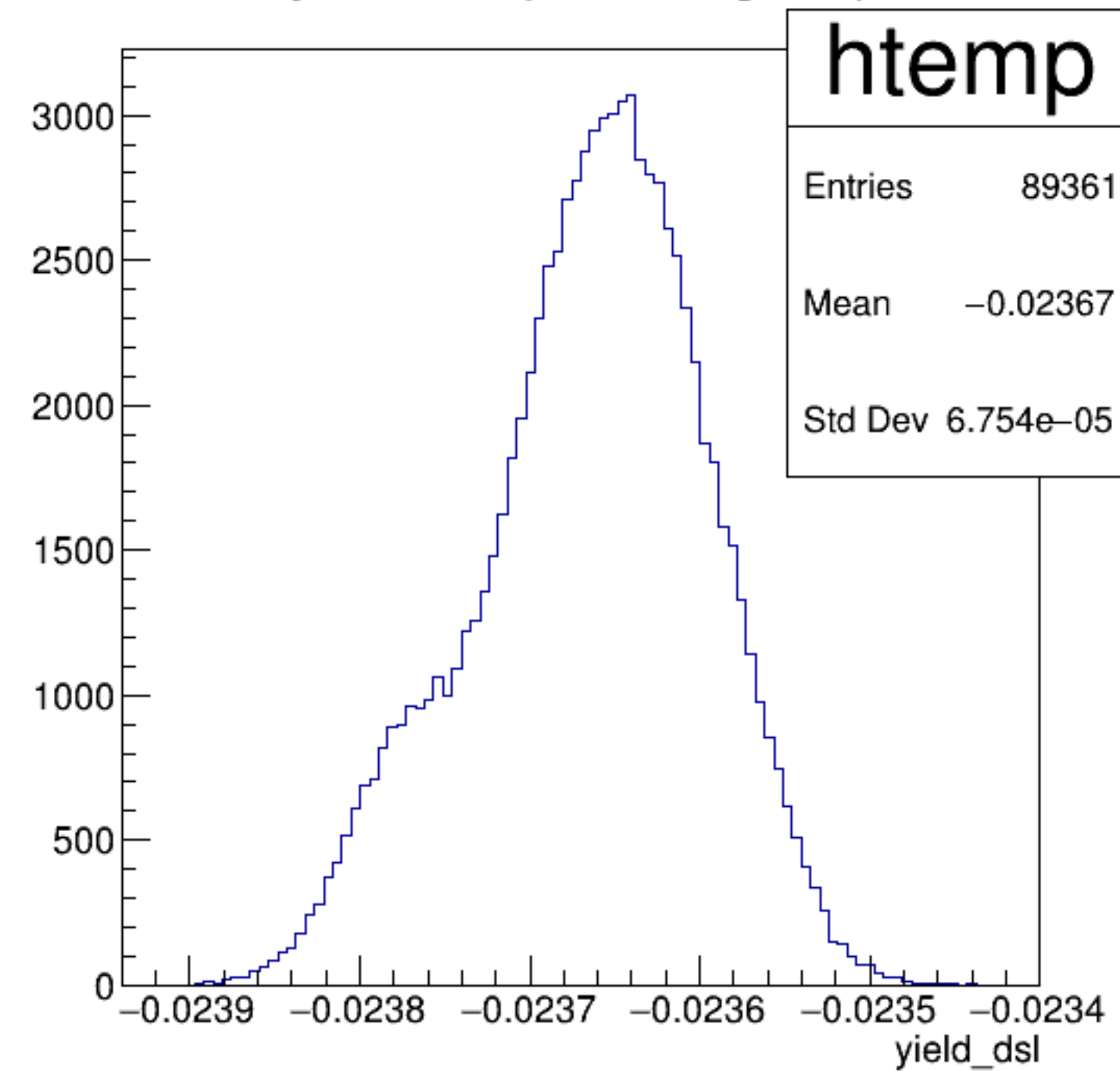


yield_dsl:pattern_number {ErrorFlag==0}

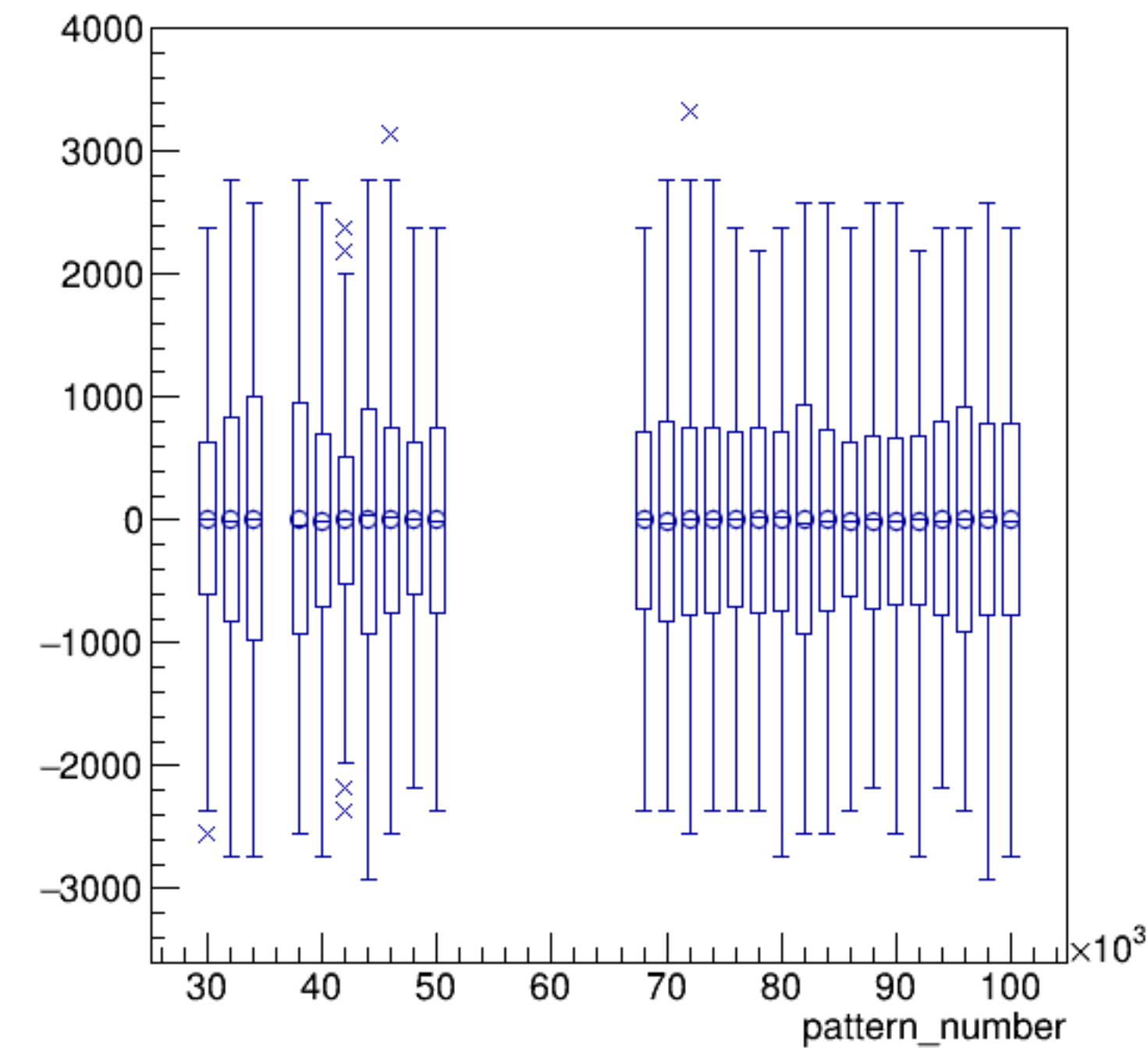


run7392_000_pairTree_dsl.png

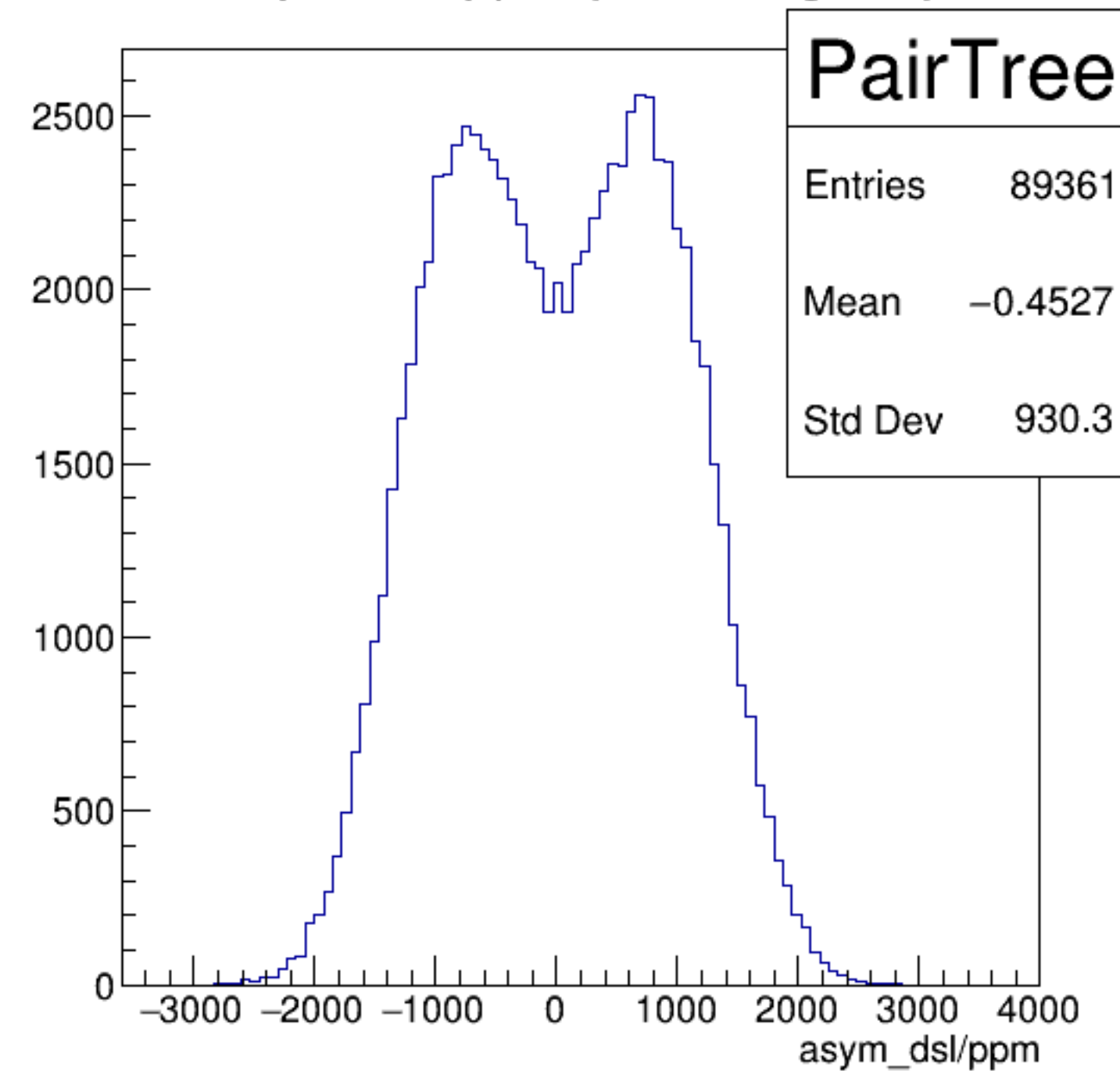
yield_dsl {ErrorFlag==0}



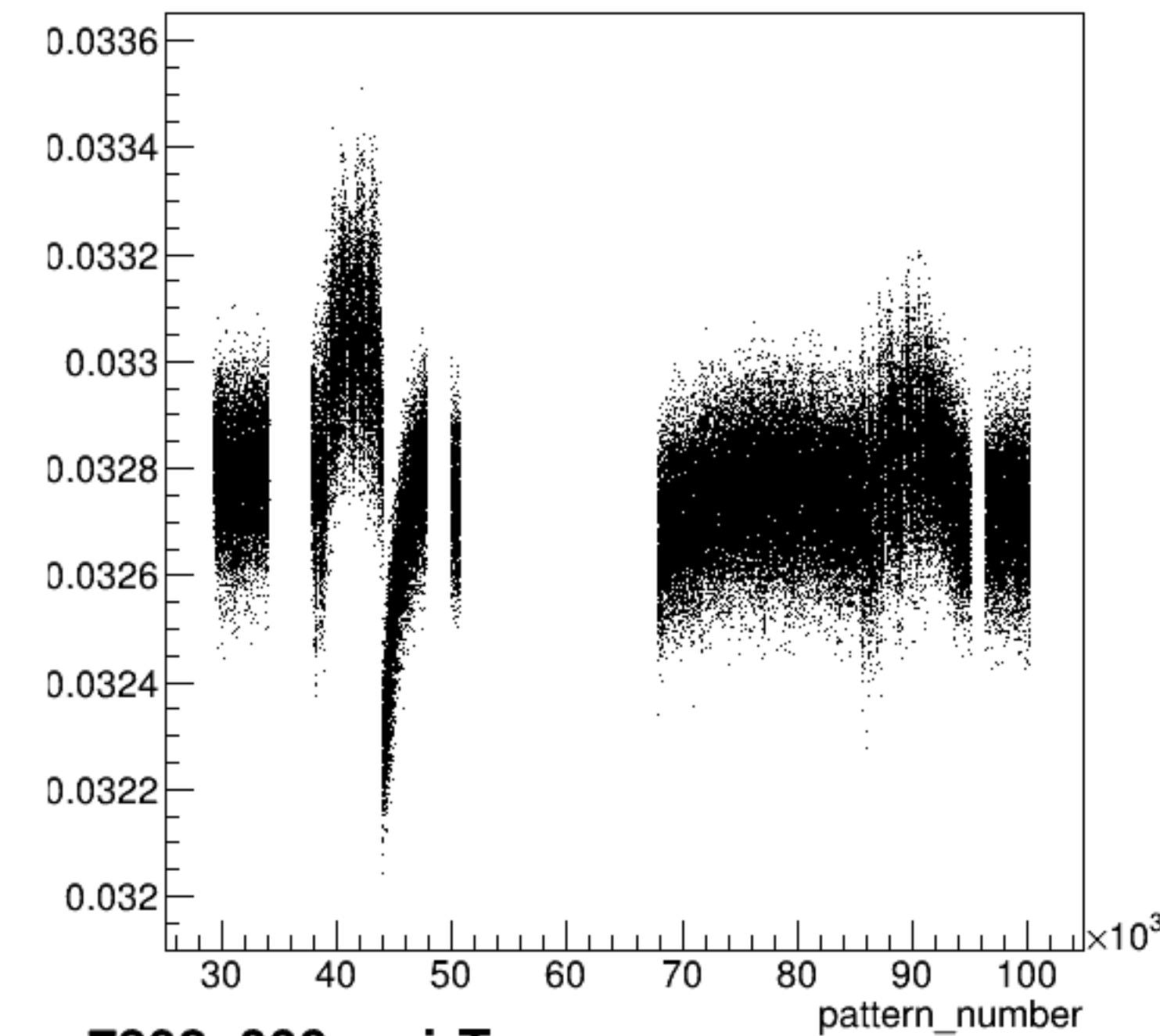
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl/ppm {ErrorFlag==0}

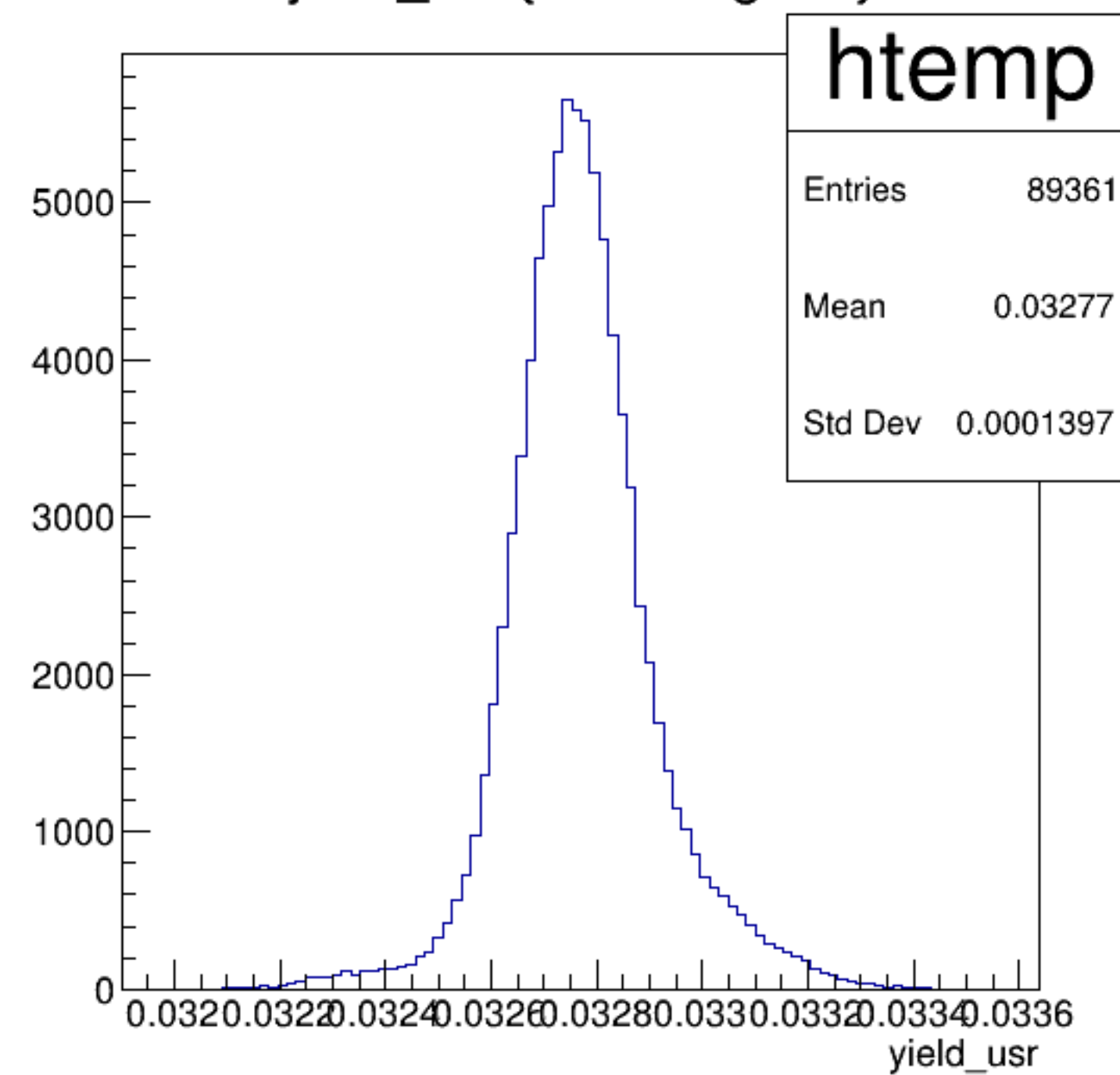


yield_usr:pattern_number {ErrorFlag==0}

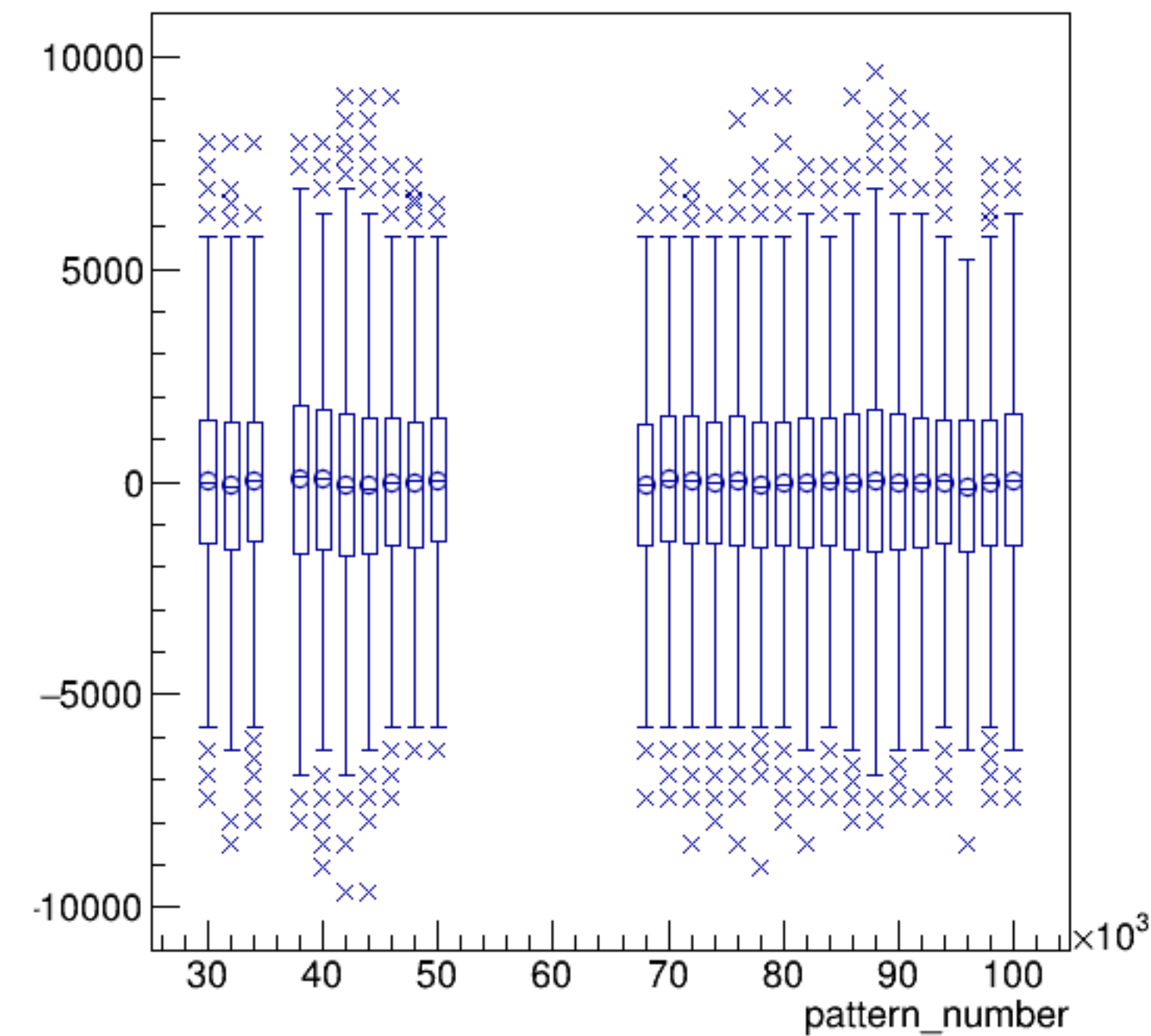


run7392_000_pairTree_usr.png

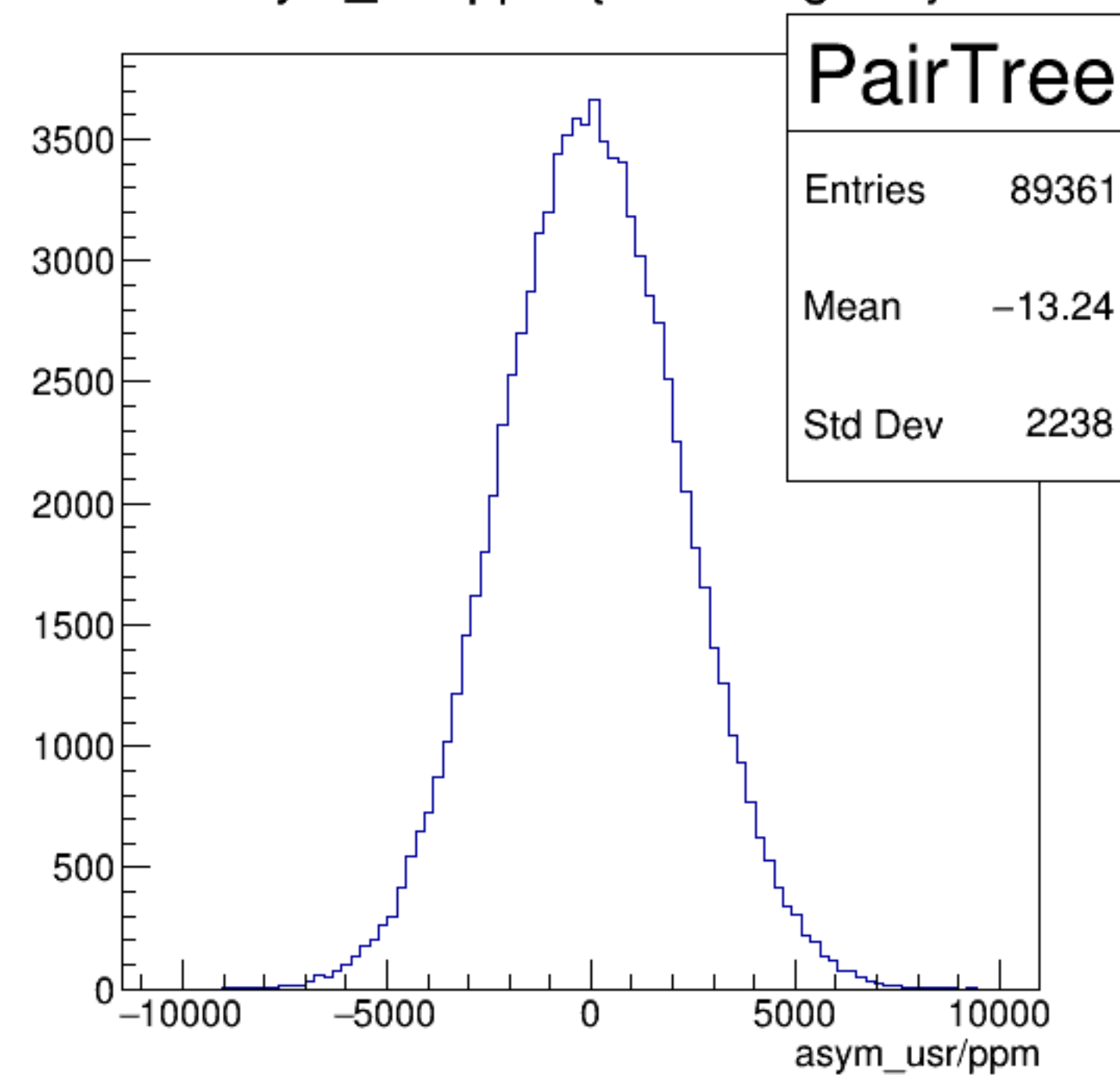
yield_usr {ErrorFlag==0}



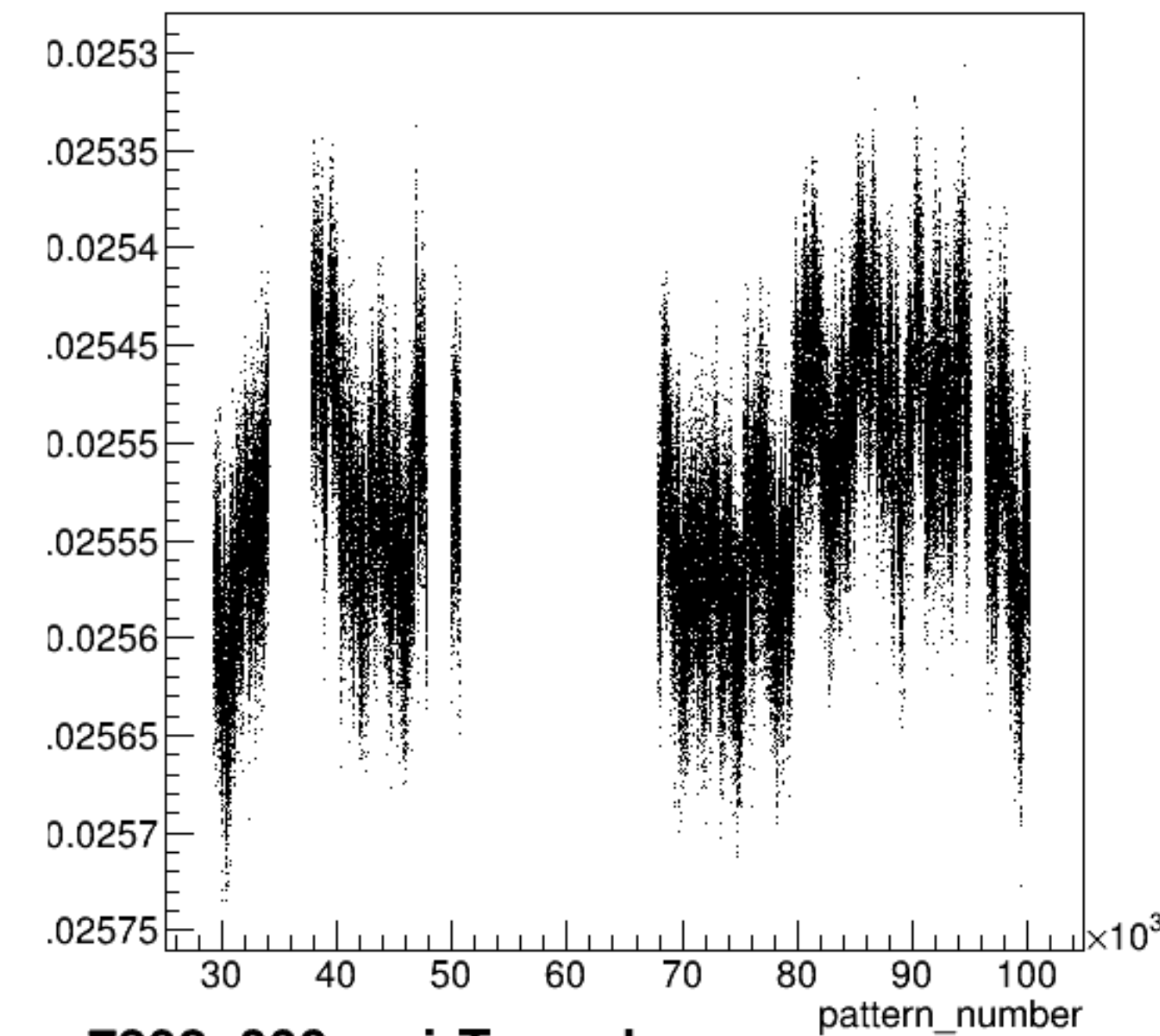
asym_usr/ppm:pattern_number {ErrorFlag==0}



asym_usr/ppm {ErrorFlag==0}

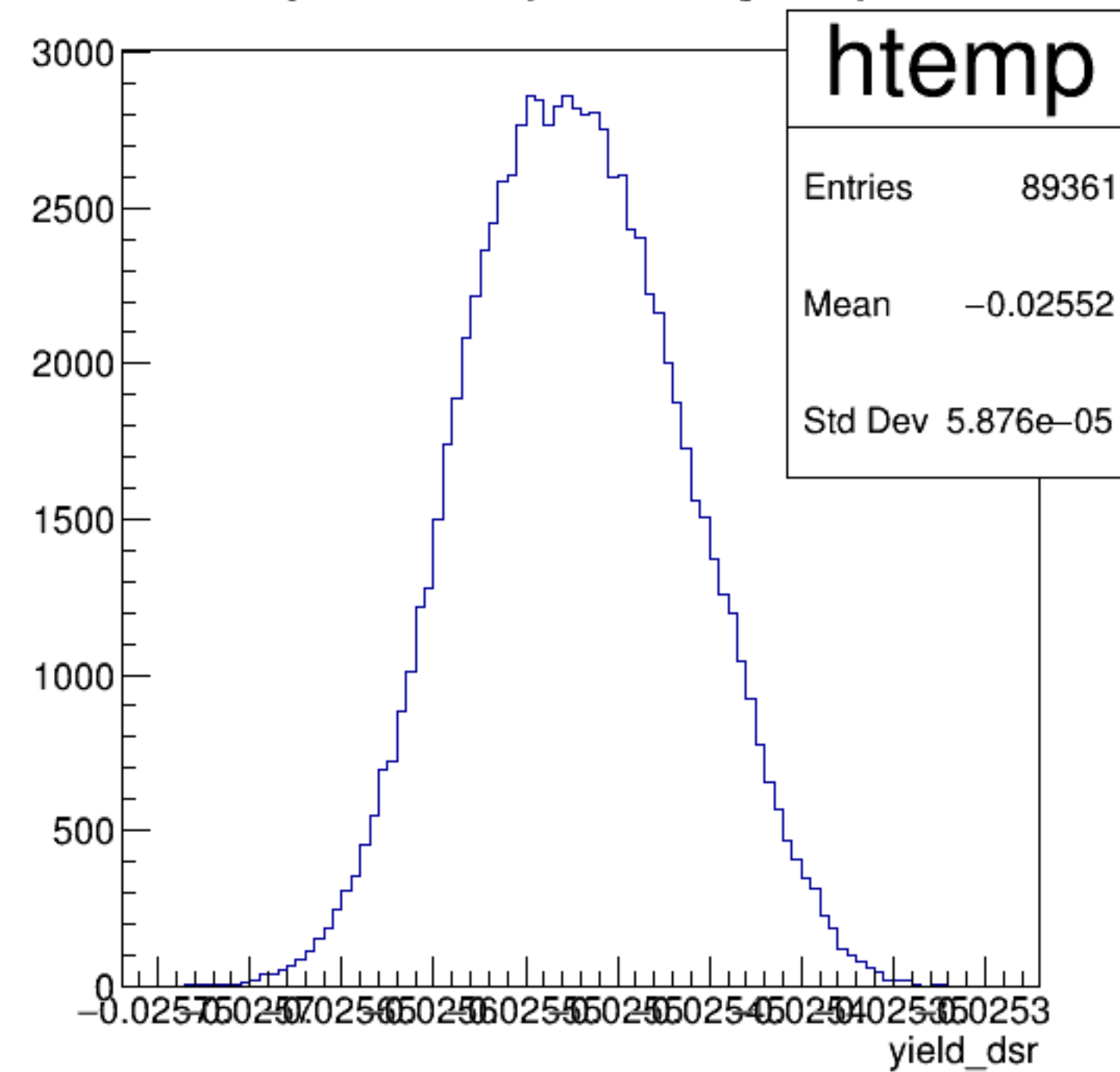


yield_dsr:pattern_number {ErrorFlag==0}

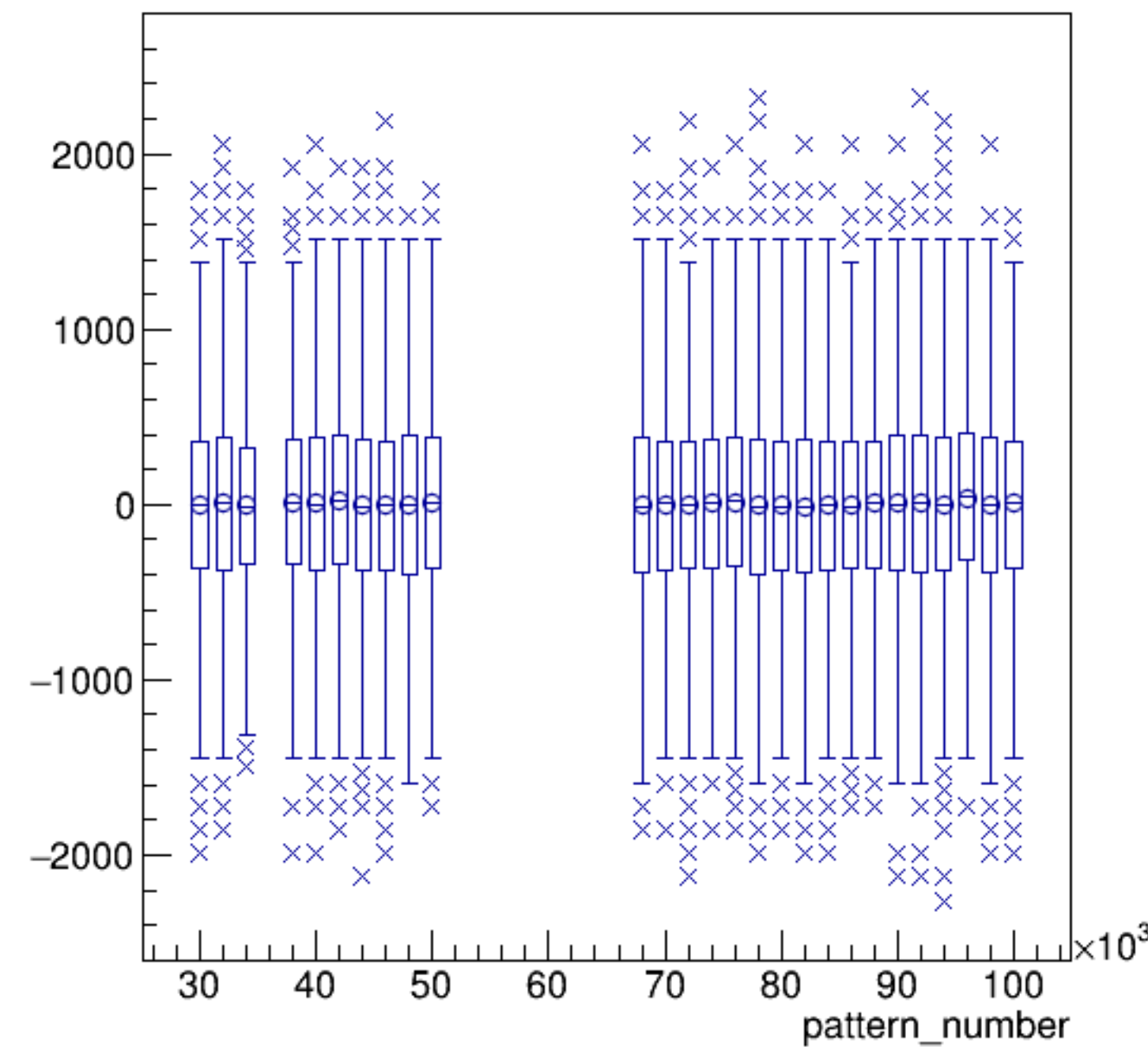


run7392_000_pairTree_dsr.png

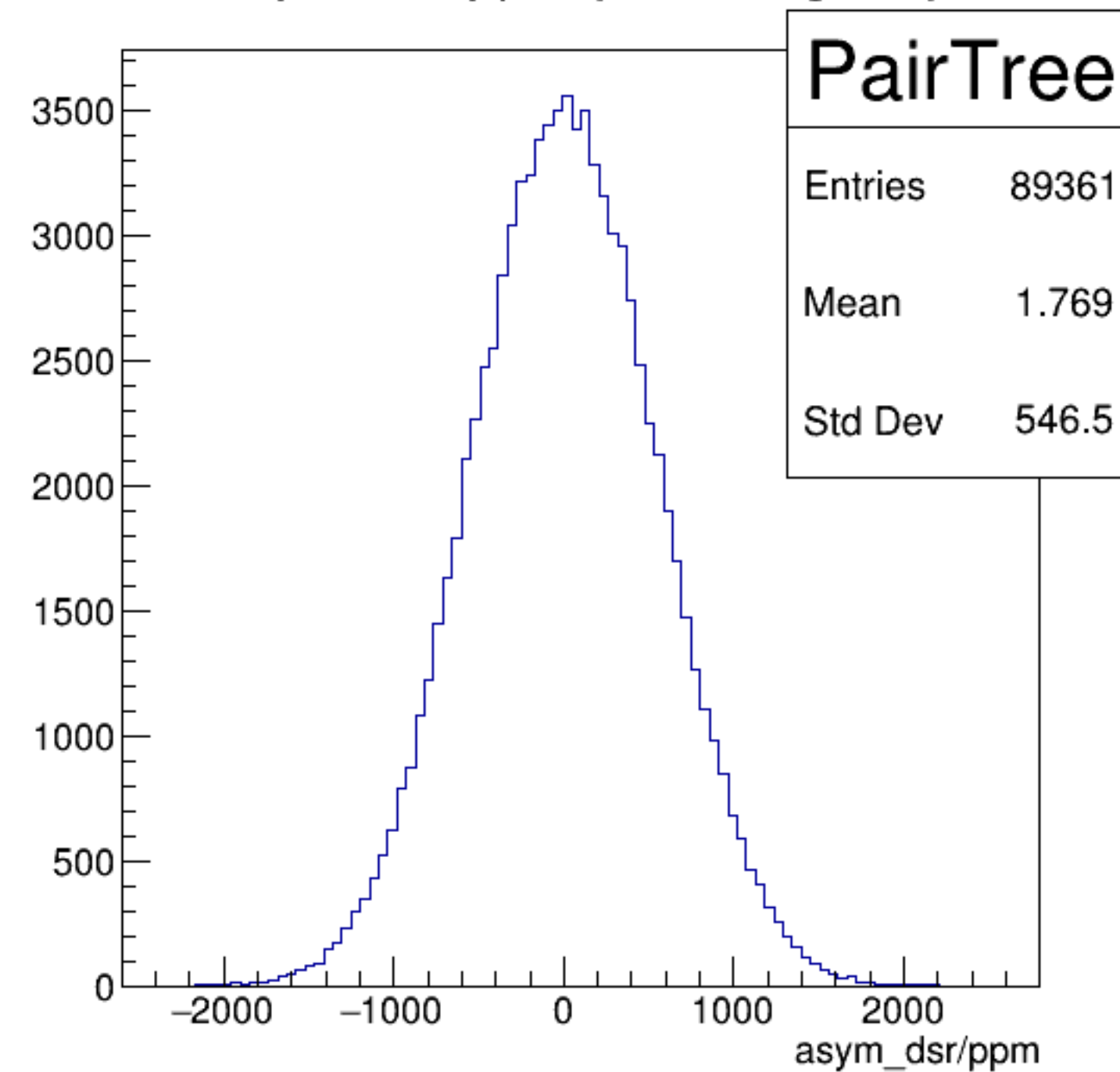
yield_dsr {ErrorFlag==0}



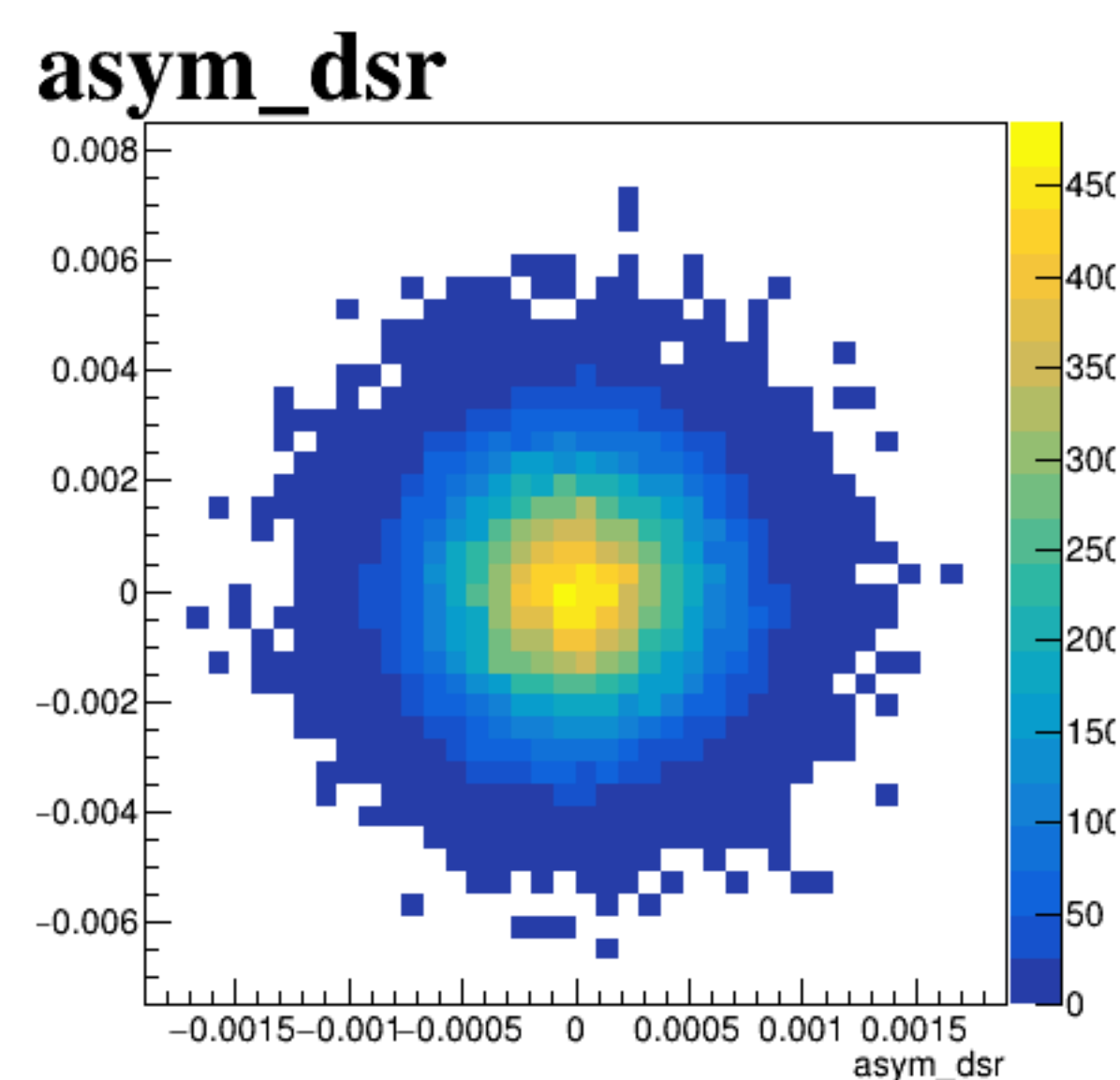
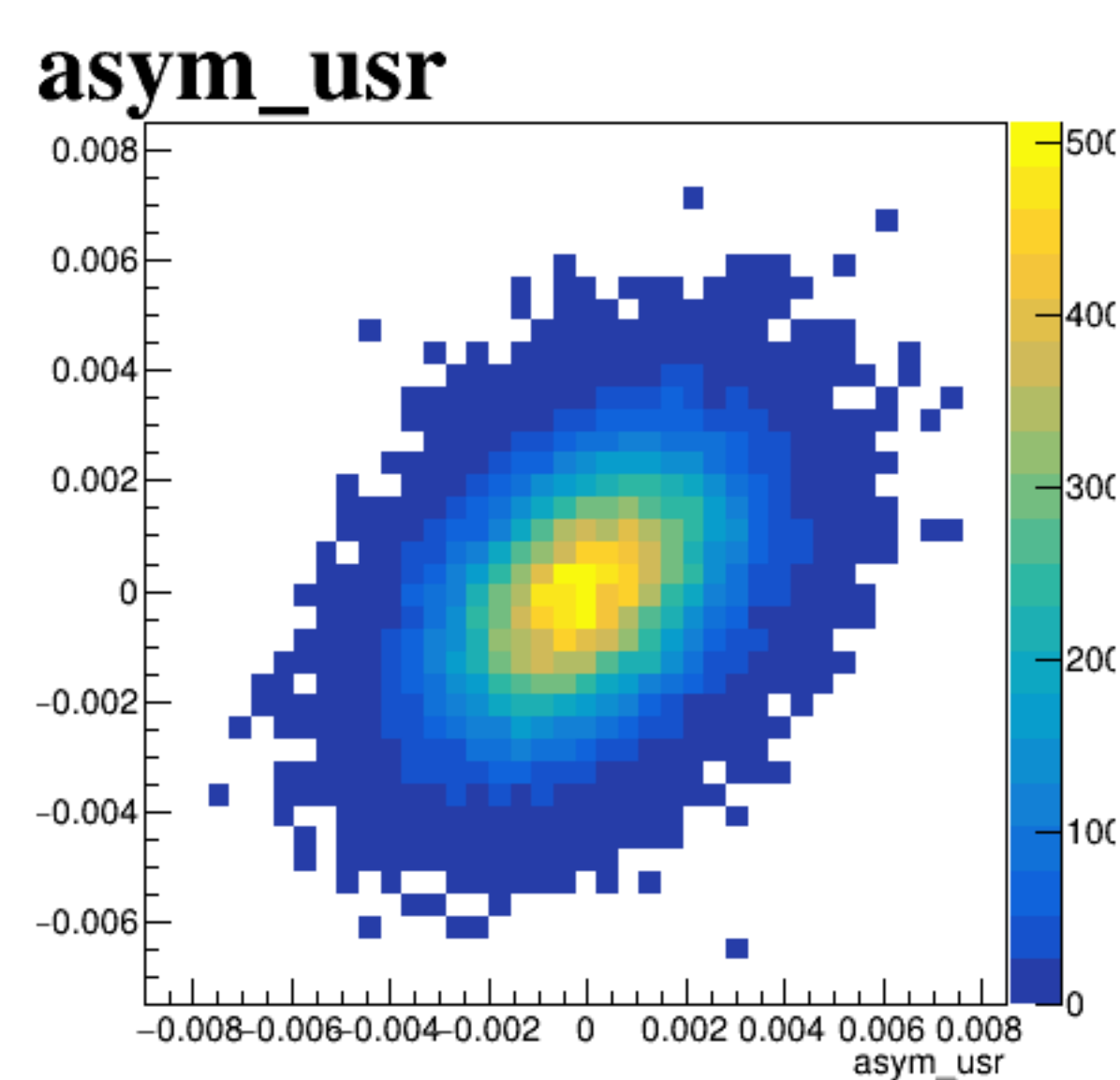
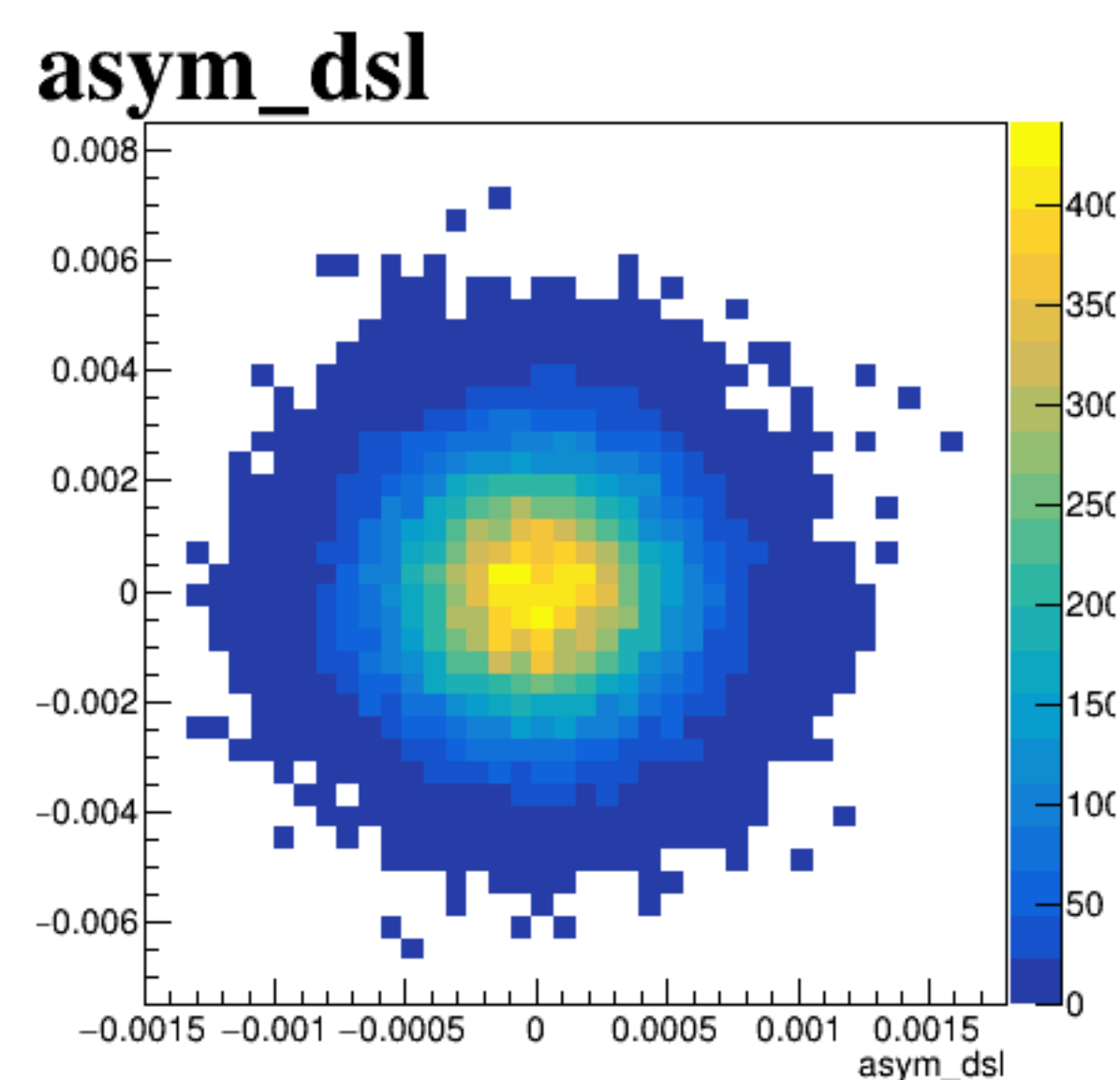
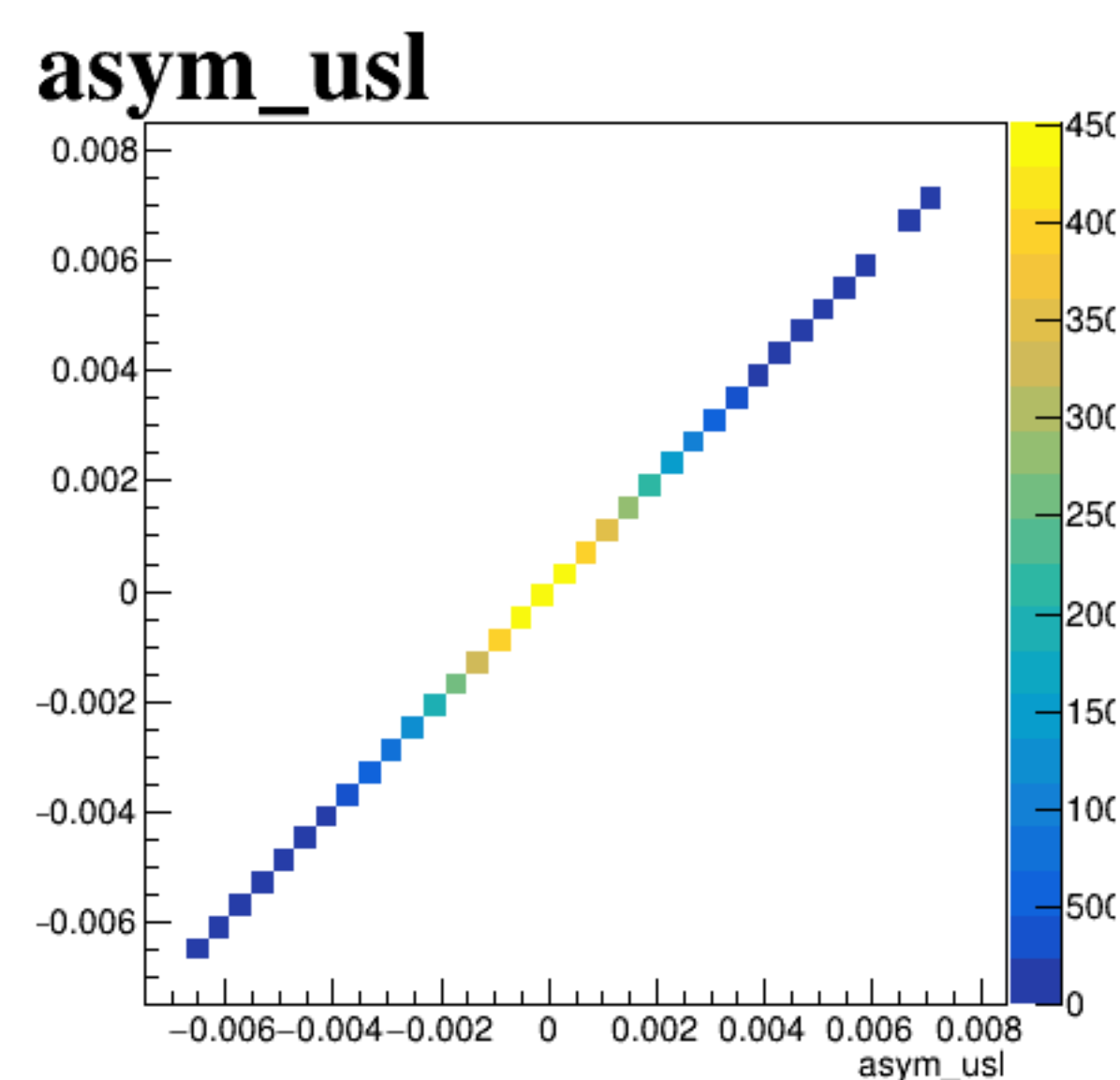
asym_dsr/ppm:pattern_number {ErrorFlag==0}



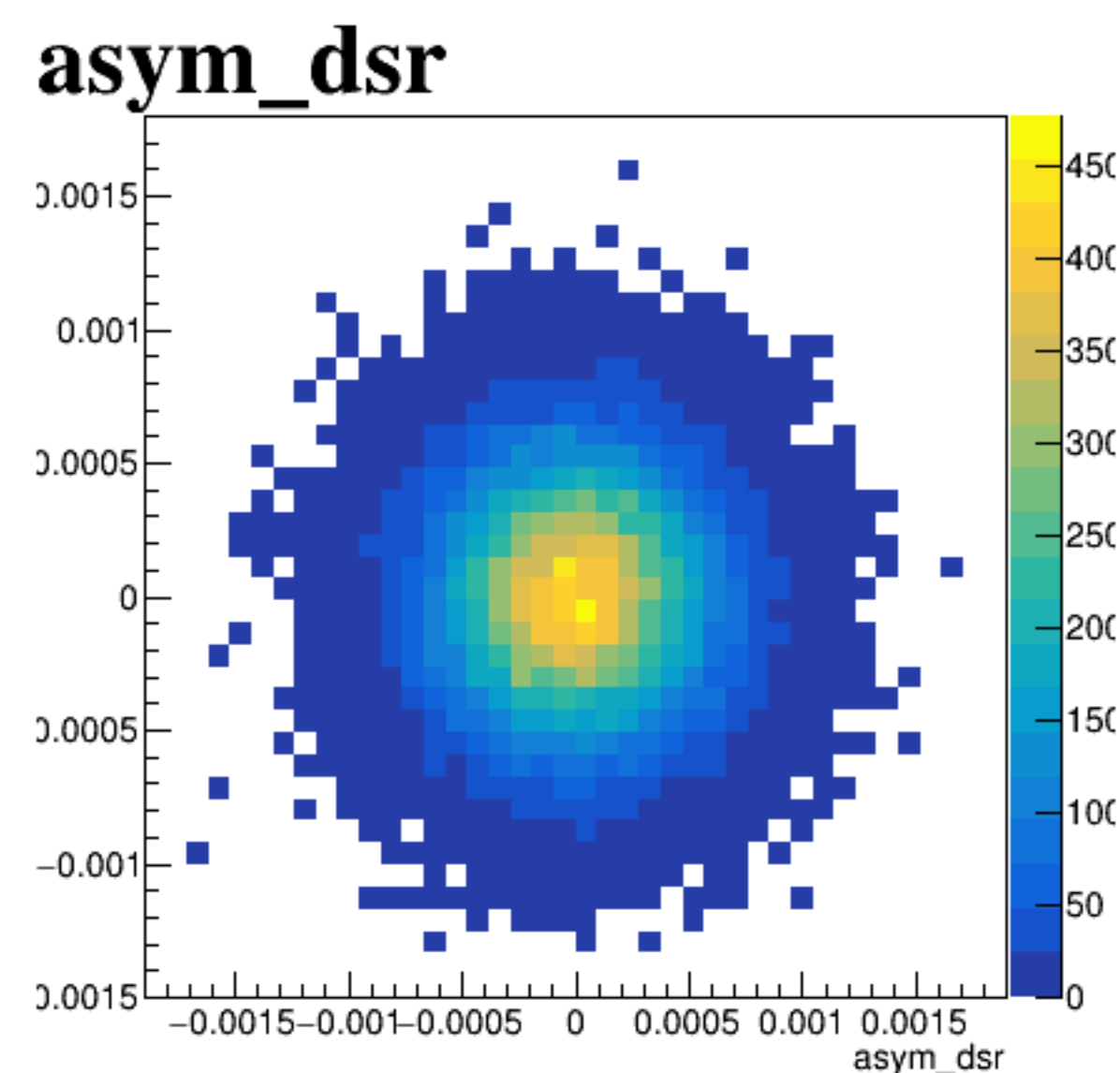
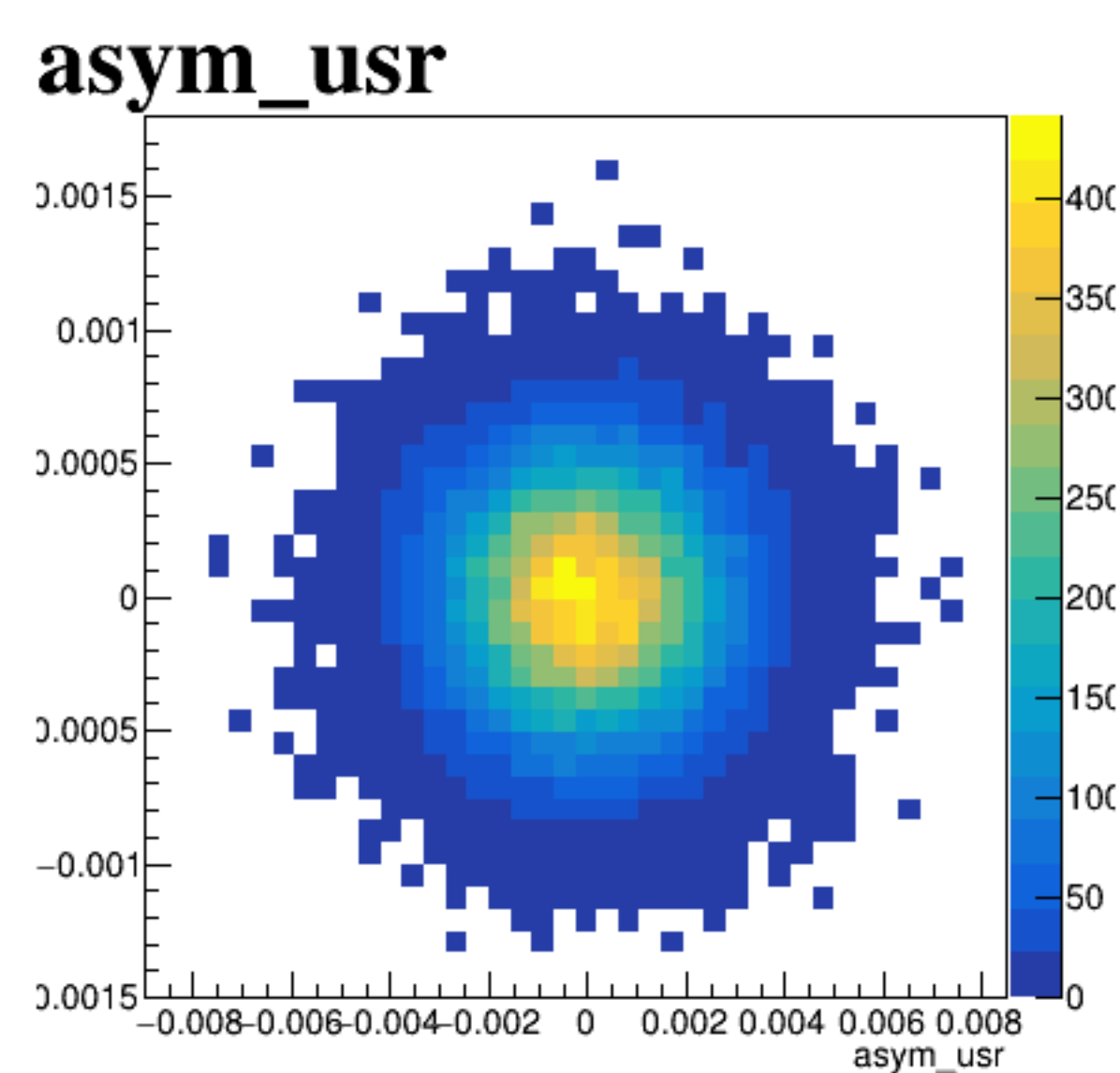
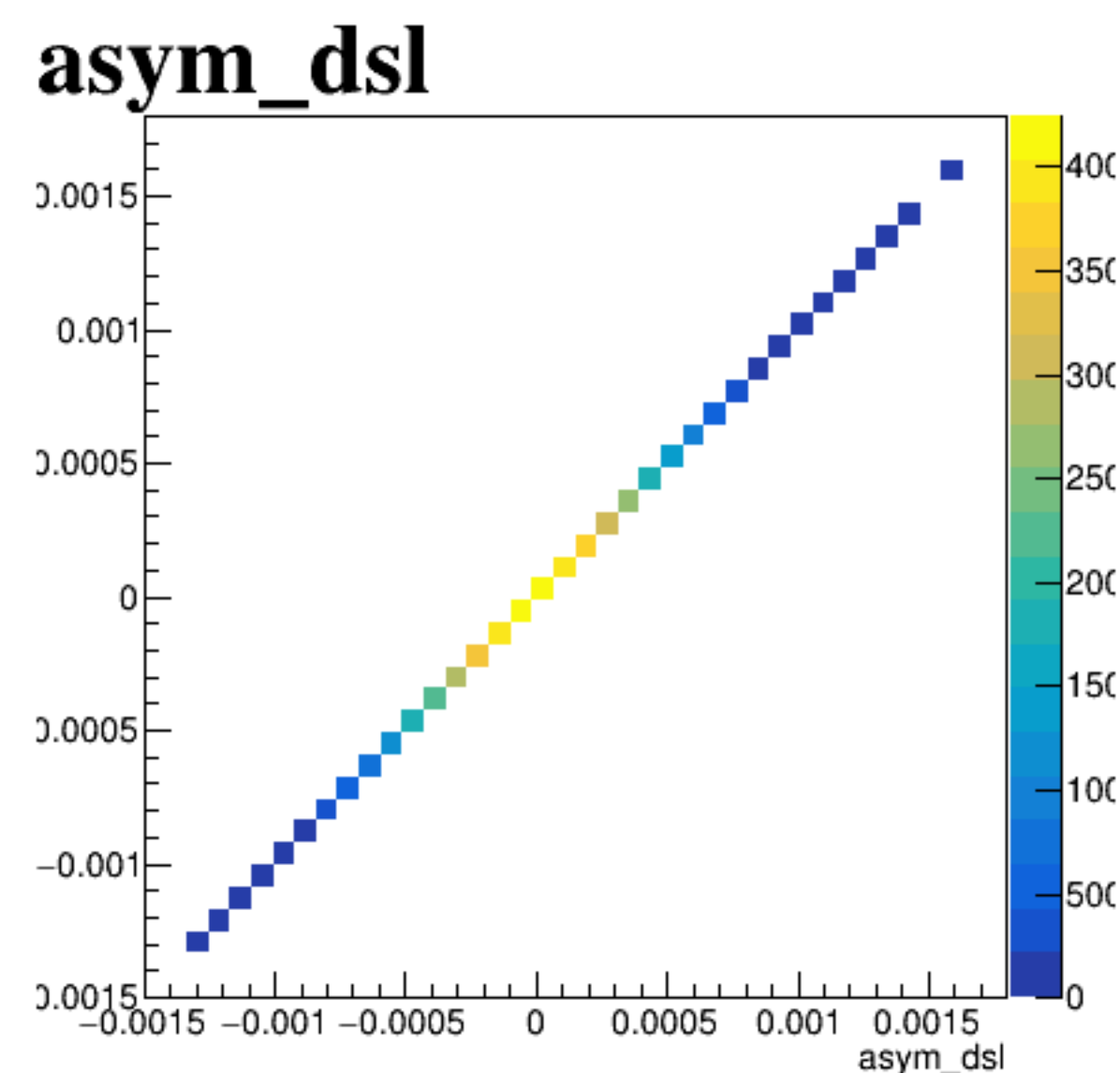
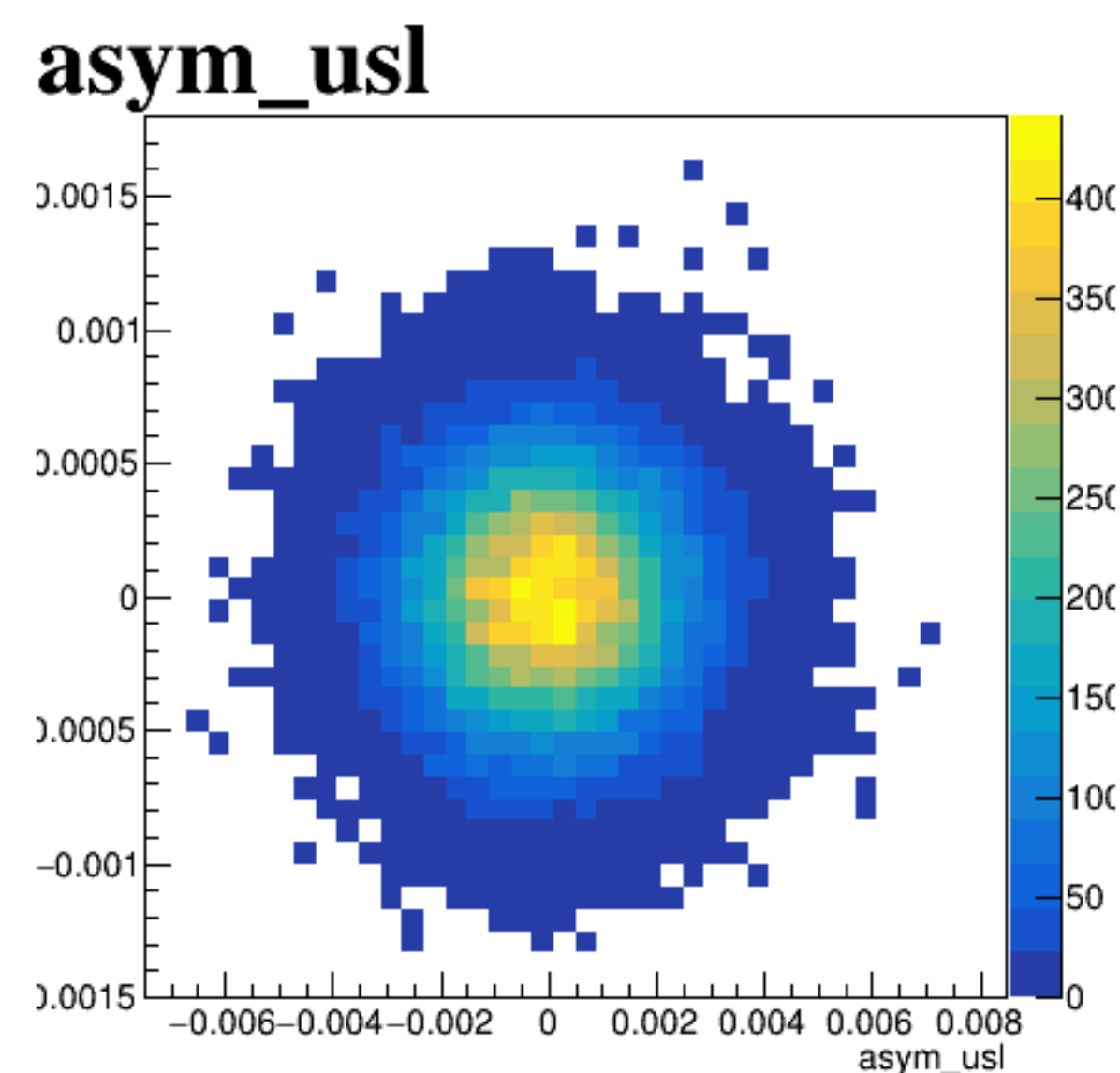
asym_dsr/ppm {ErrorFlag==0}



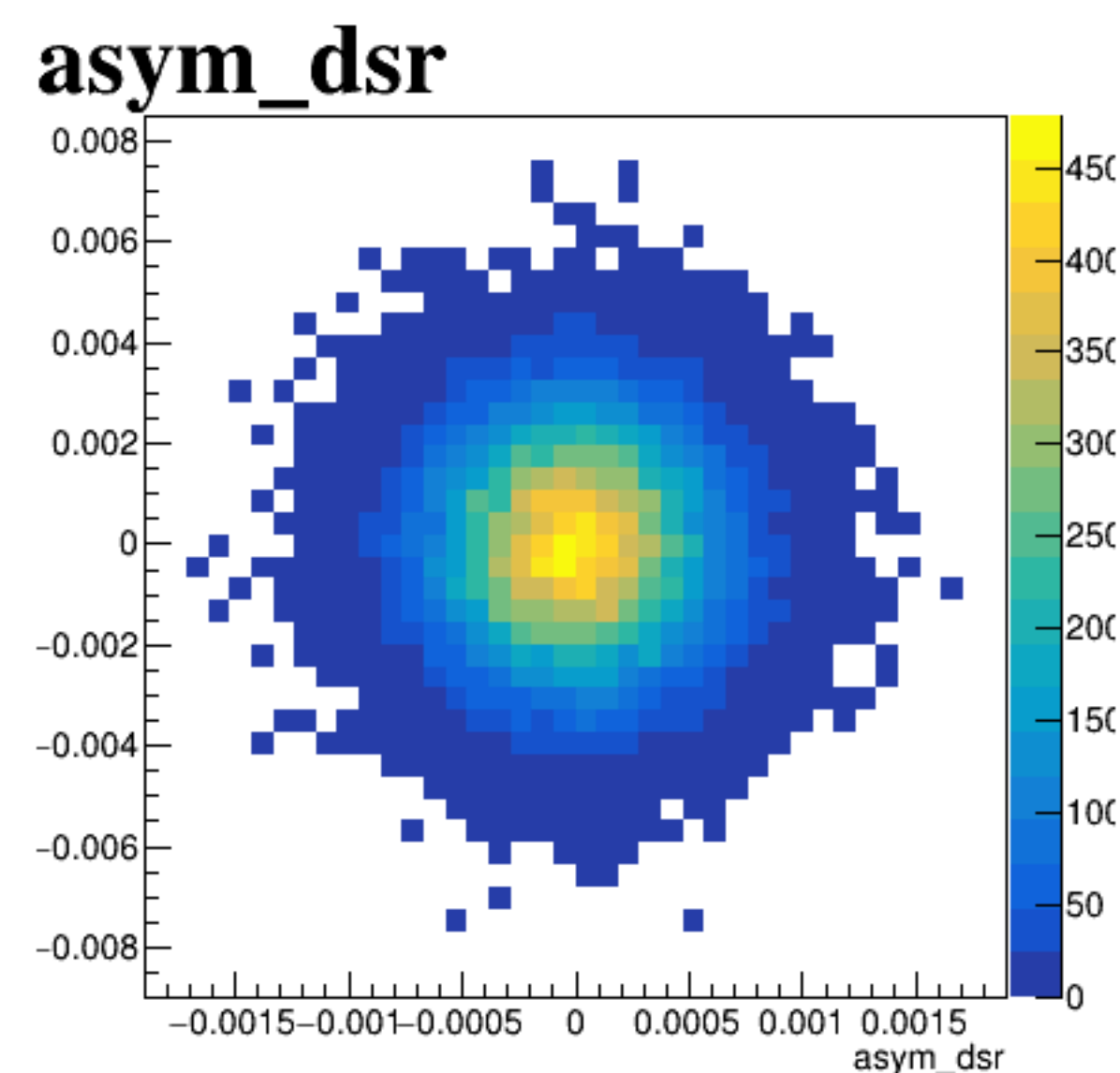
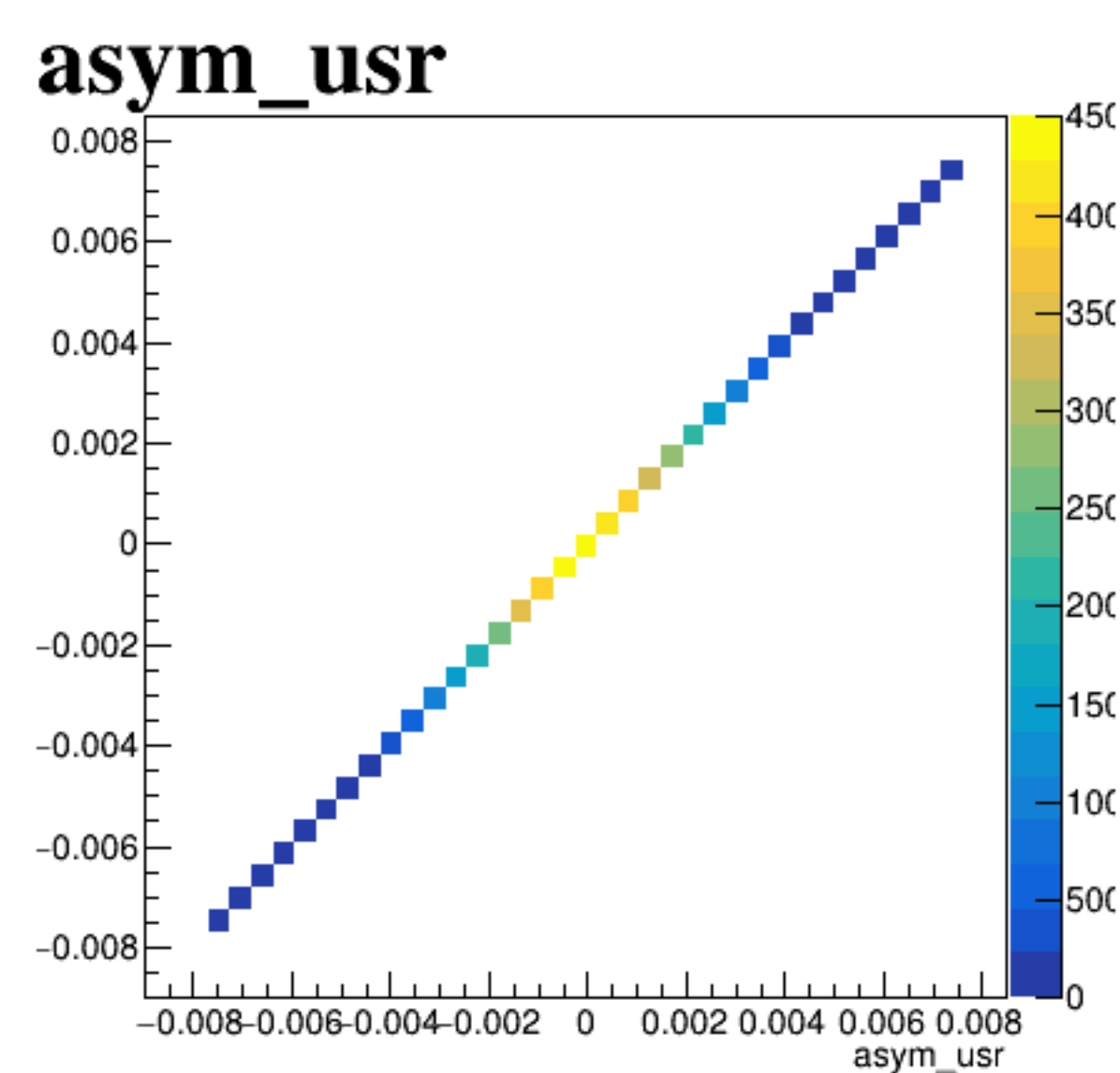
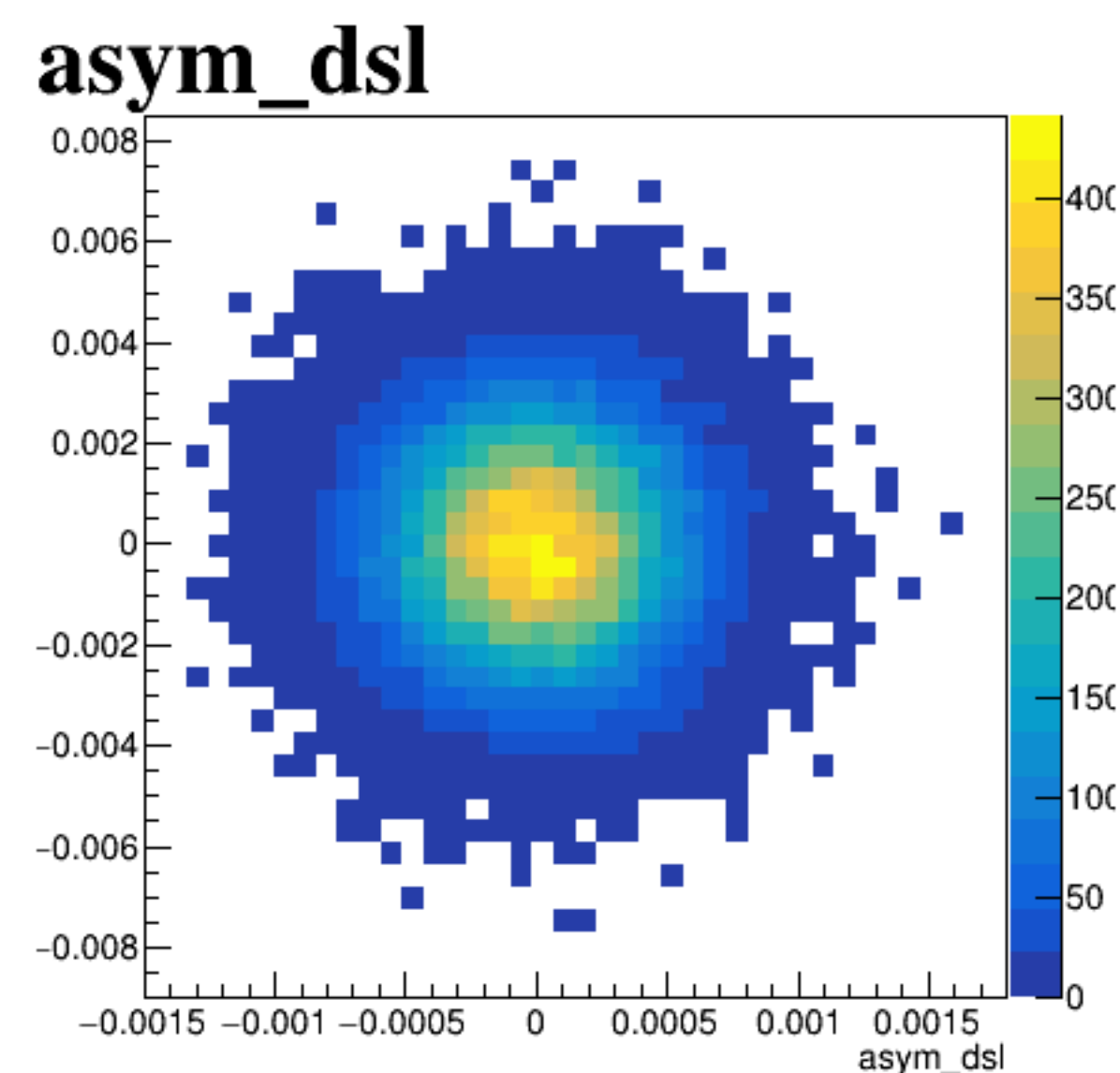
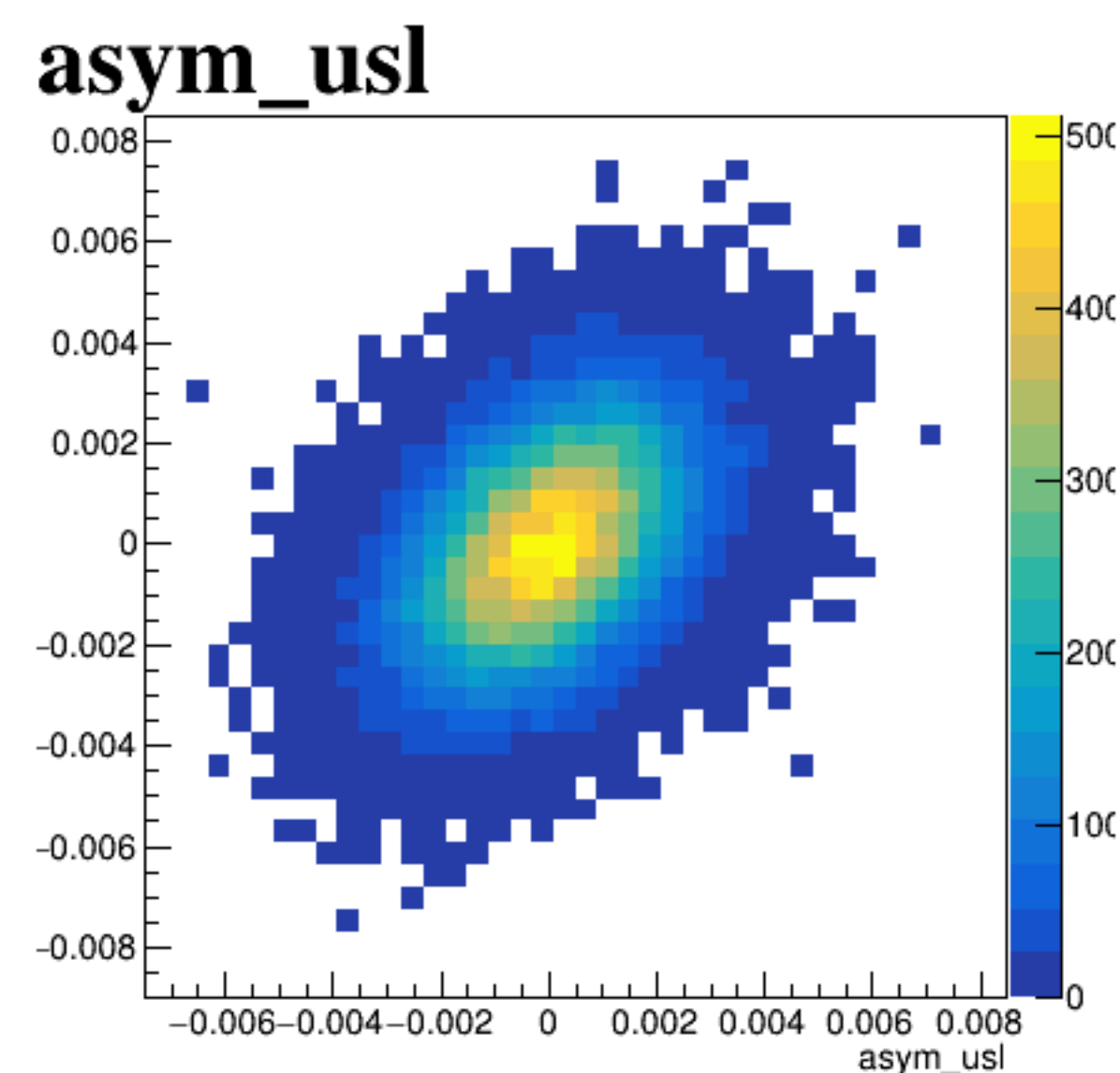
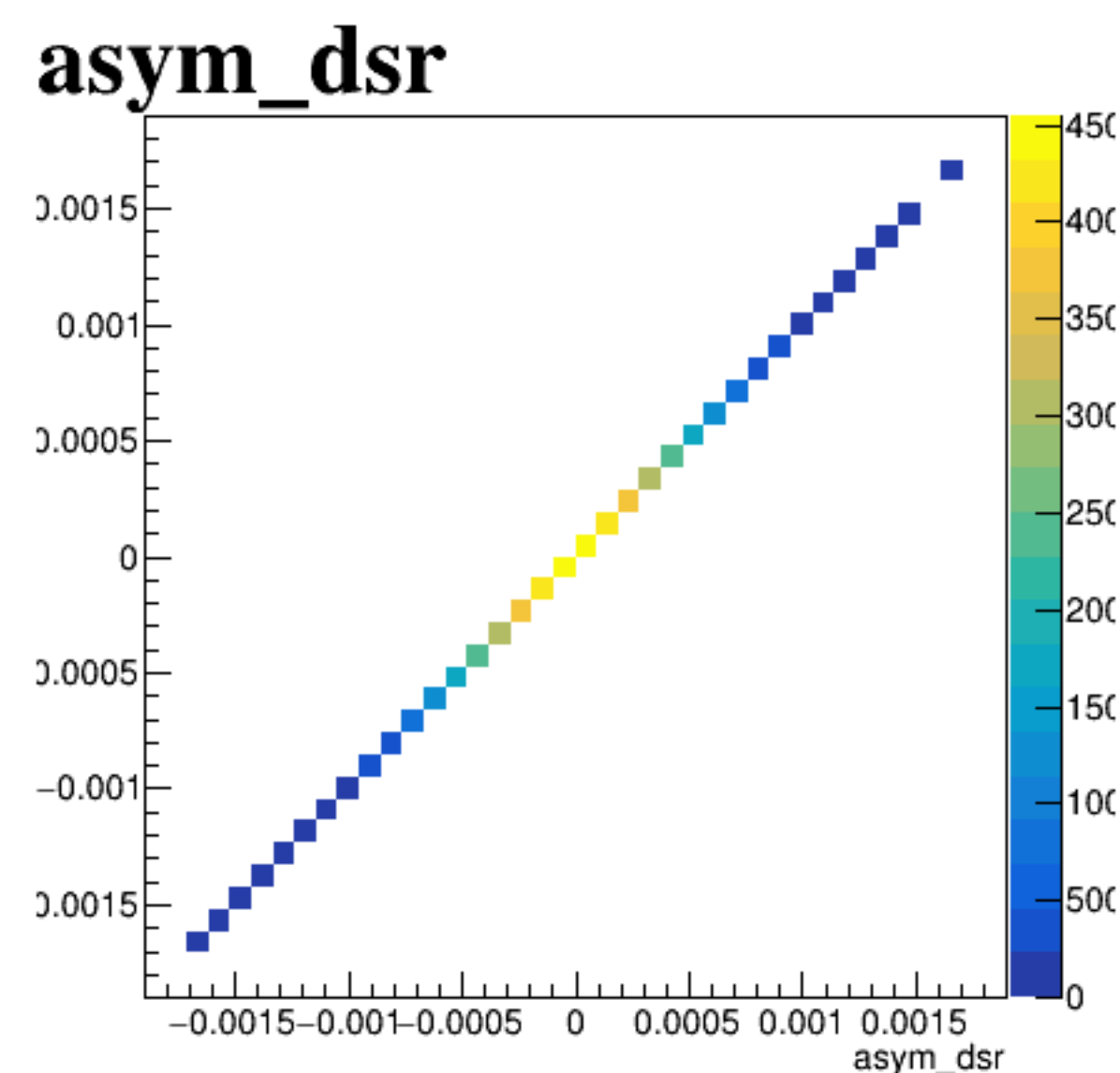
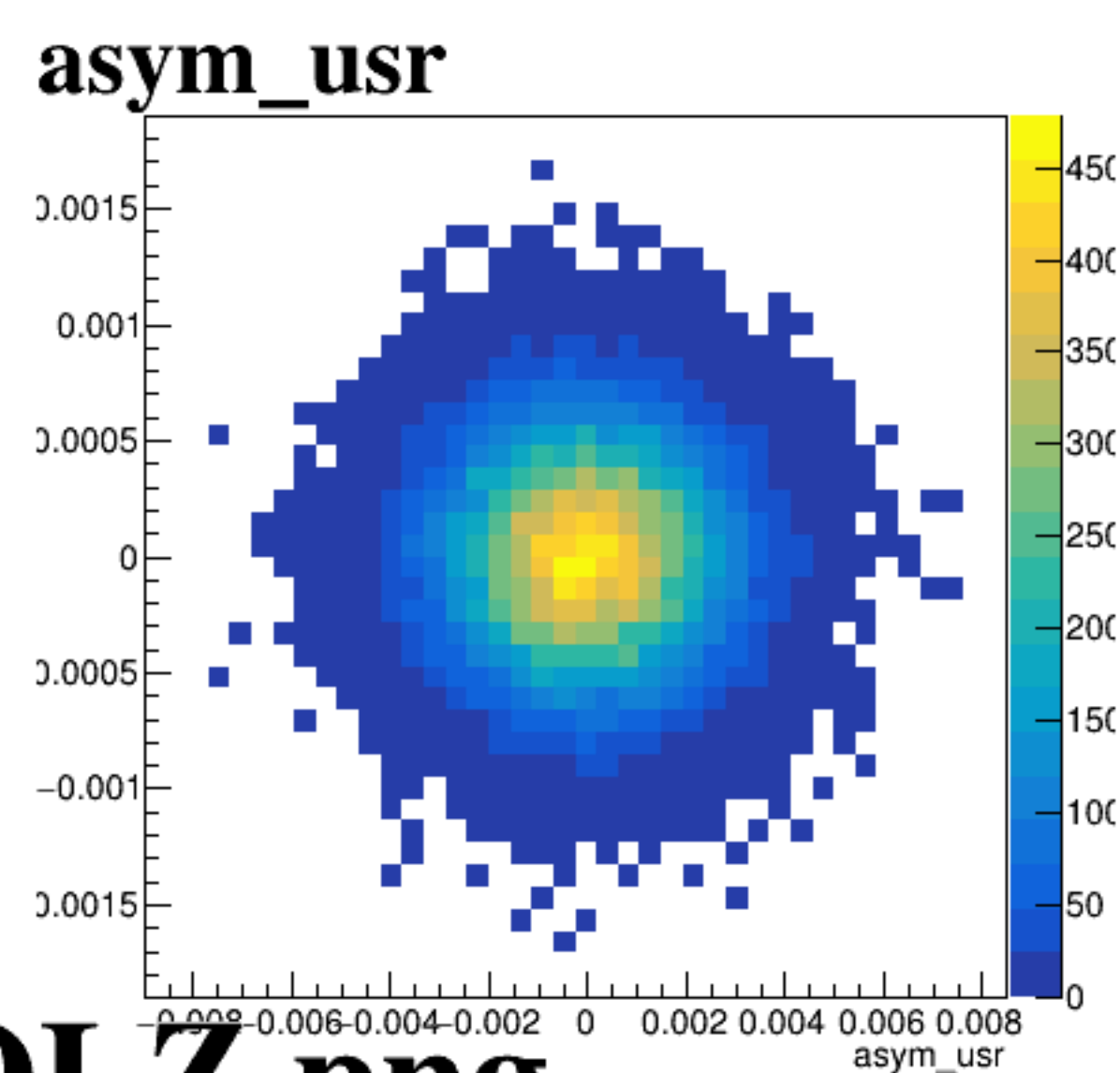
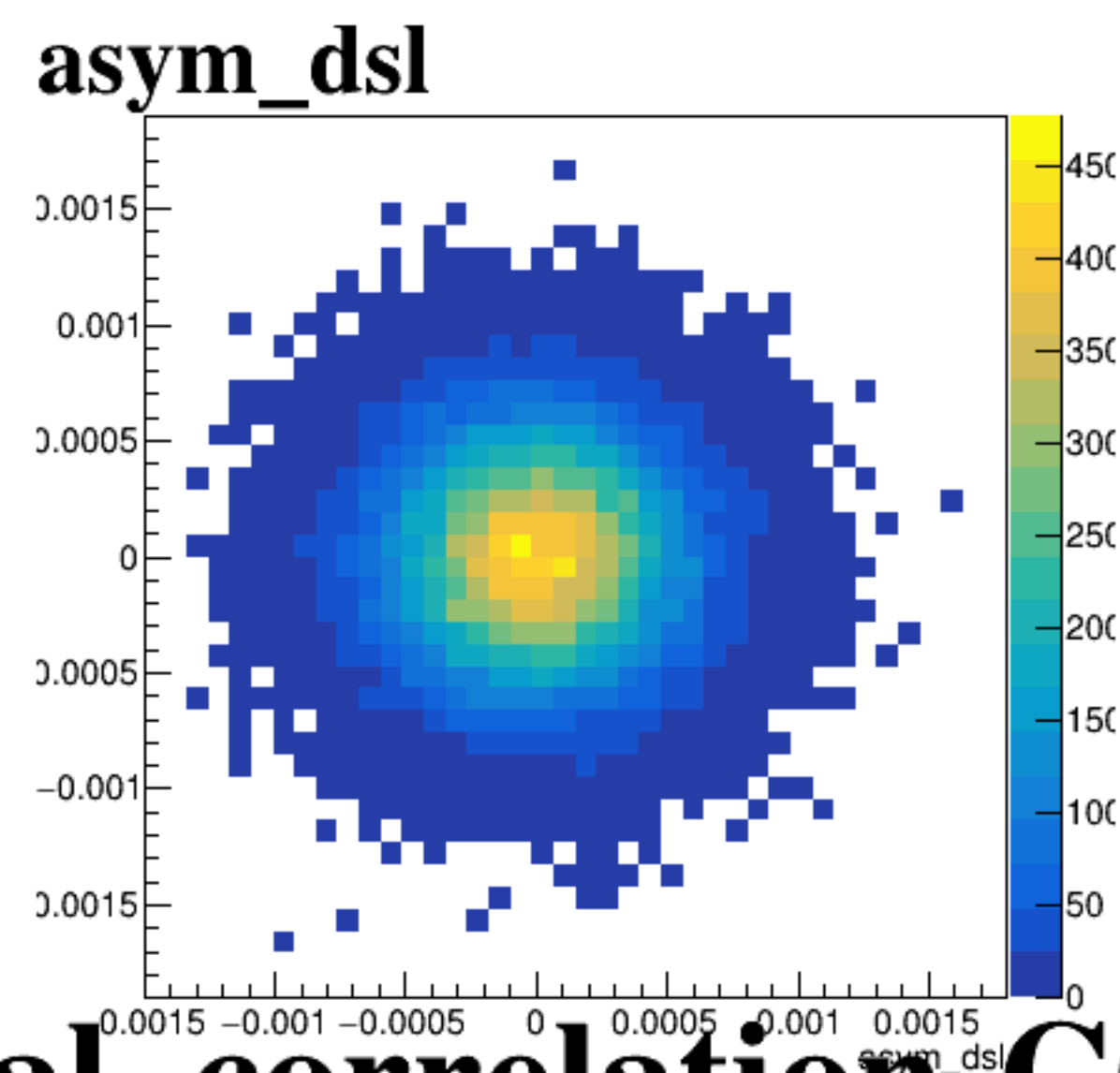
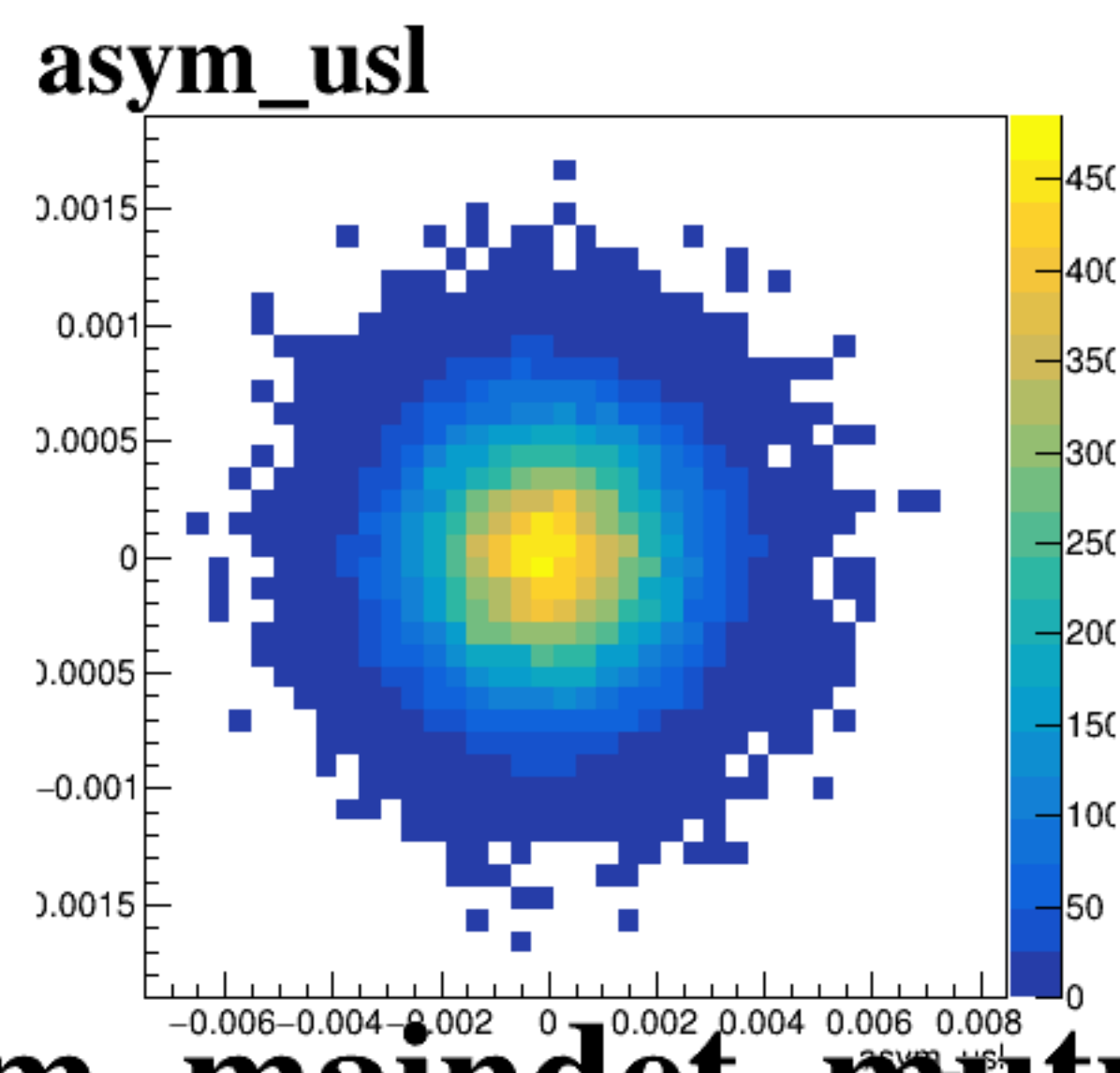
asym_usl



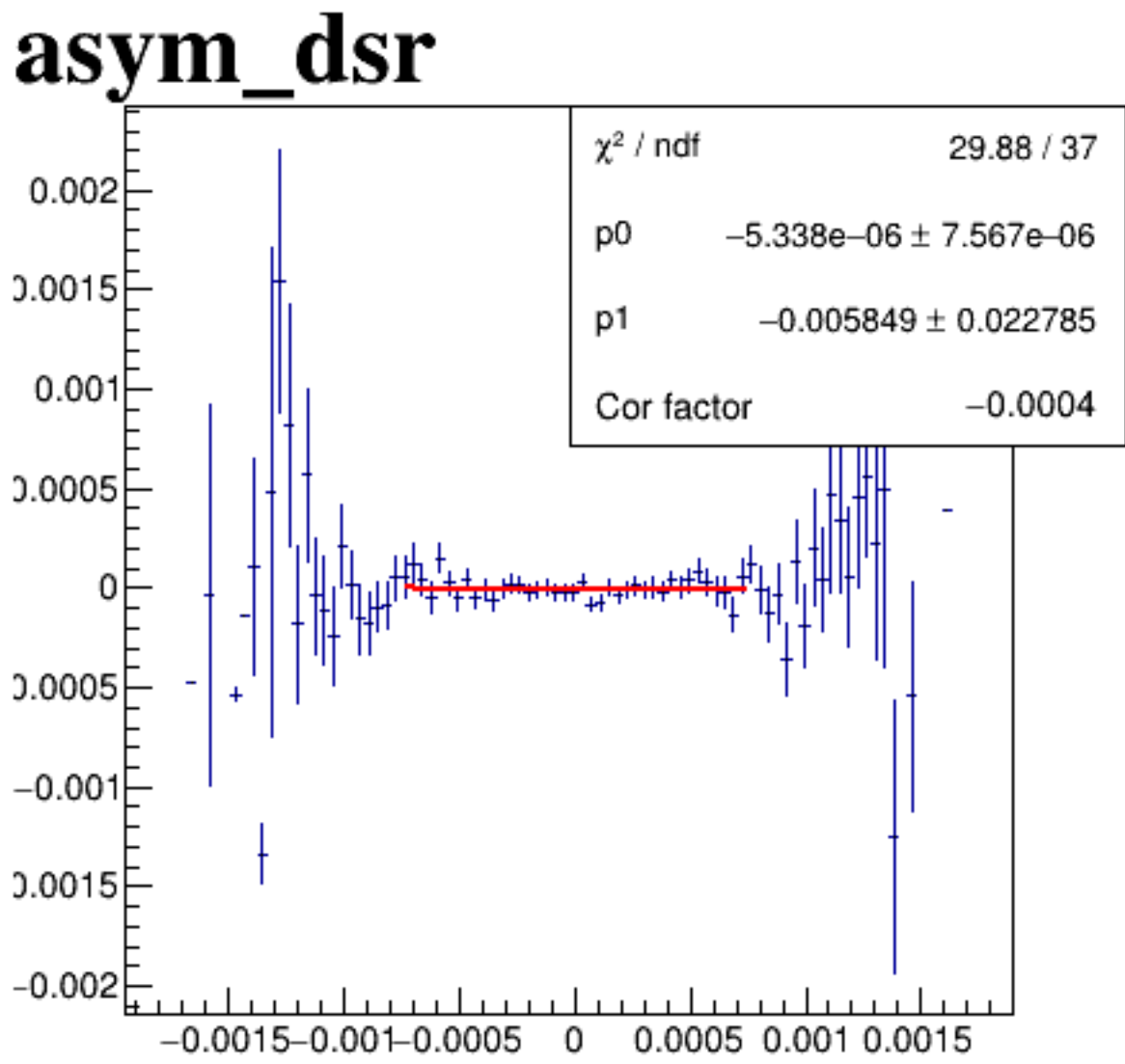
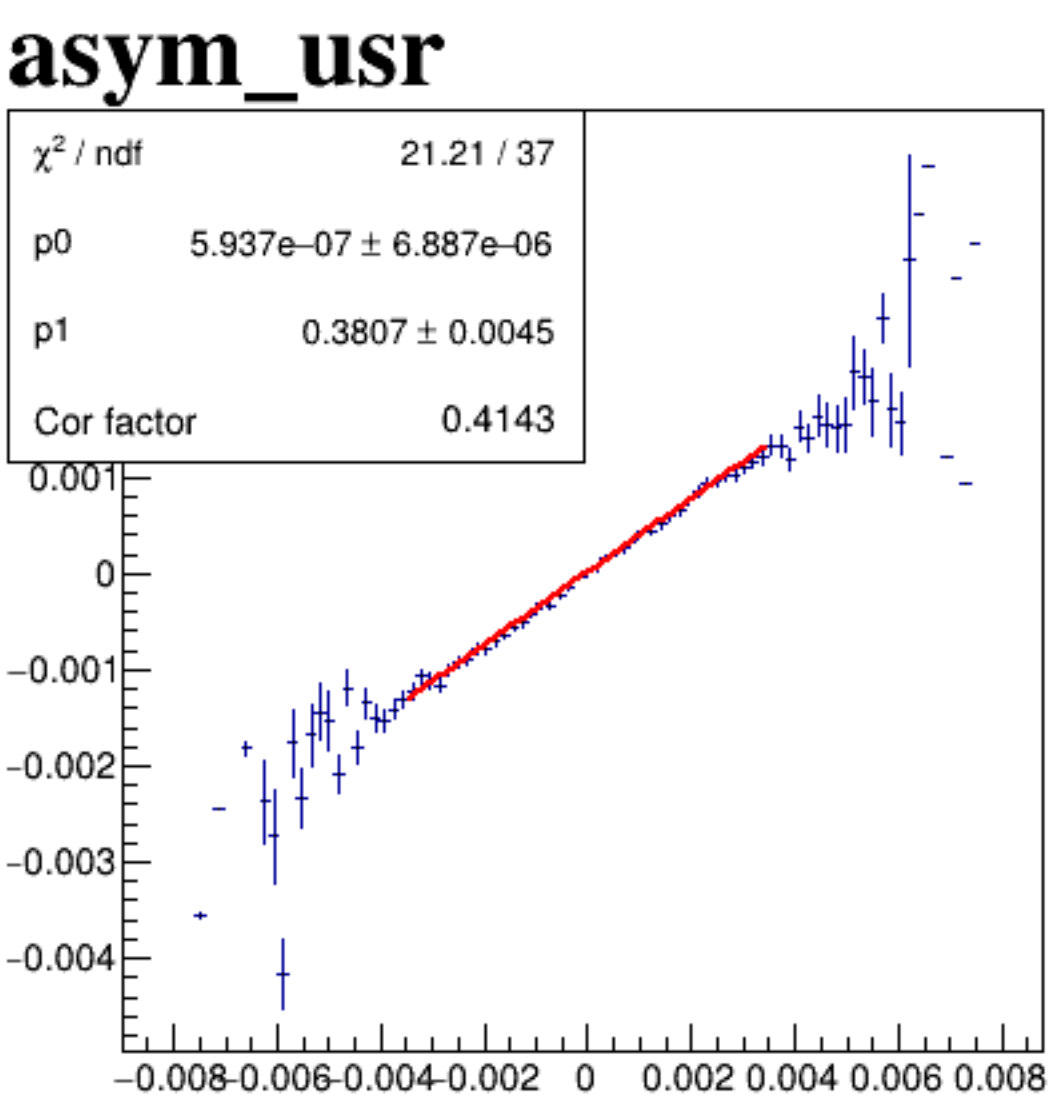
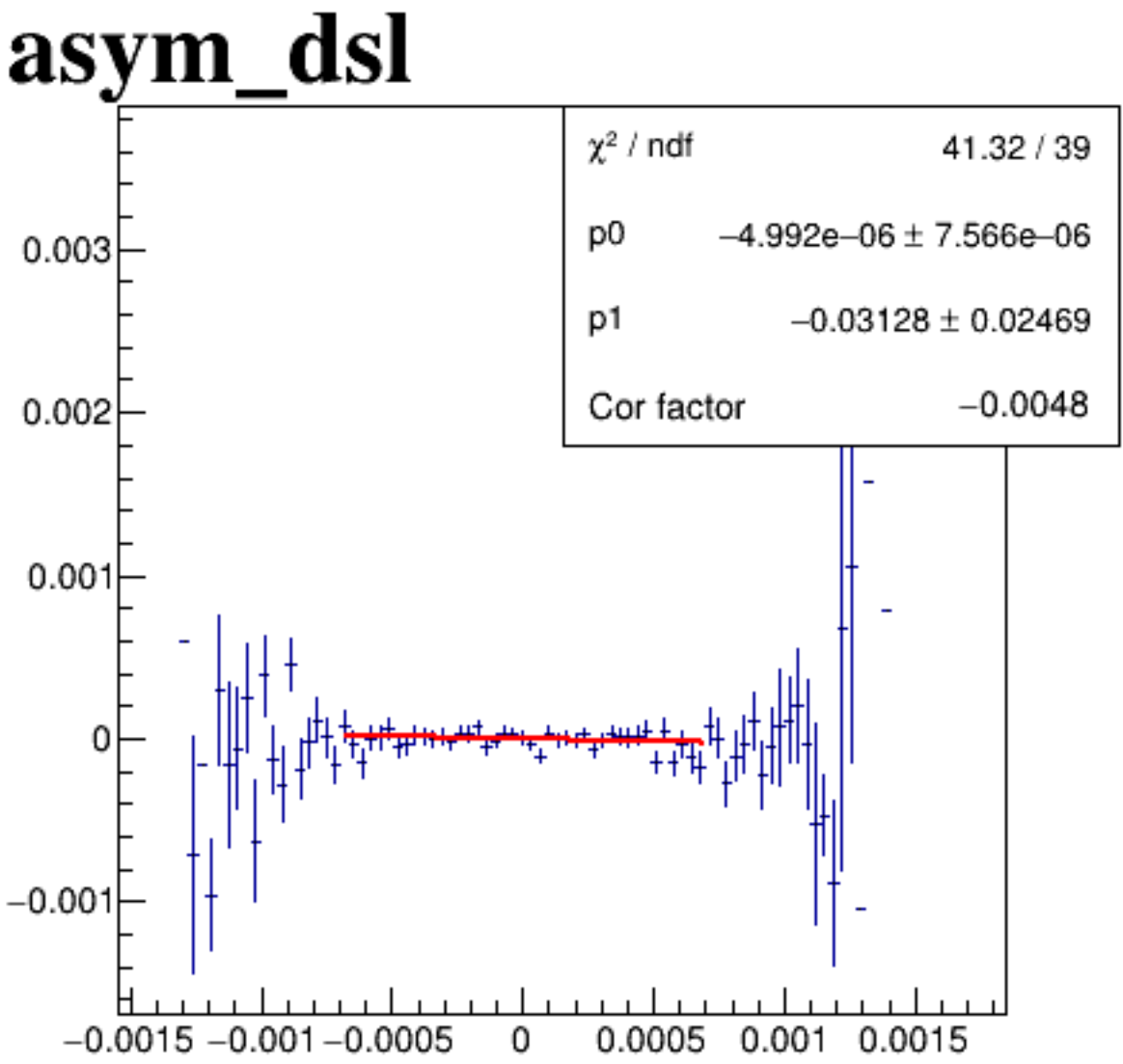
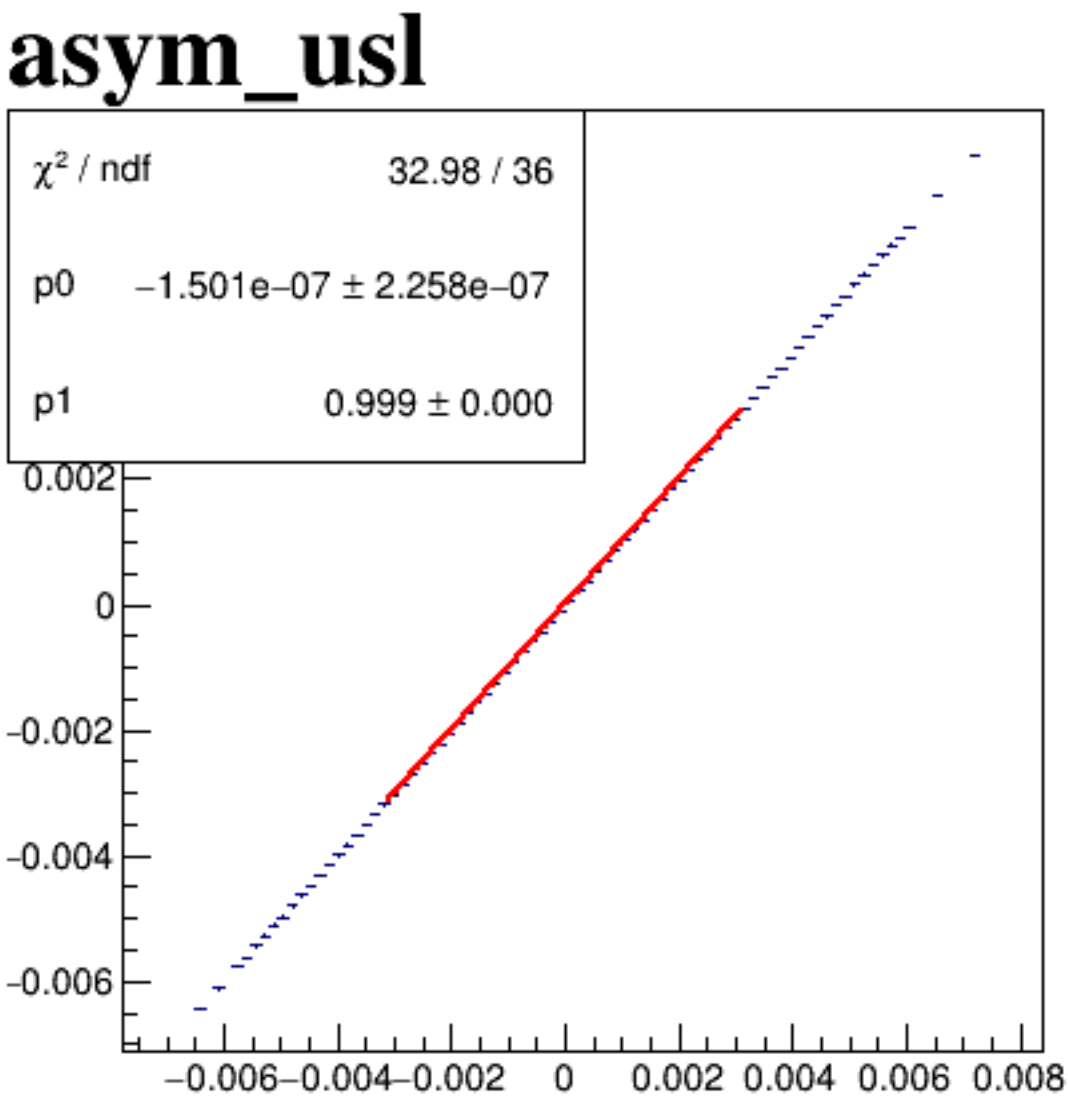
asym_dsl



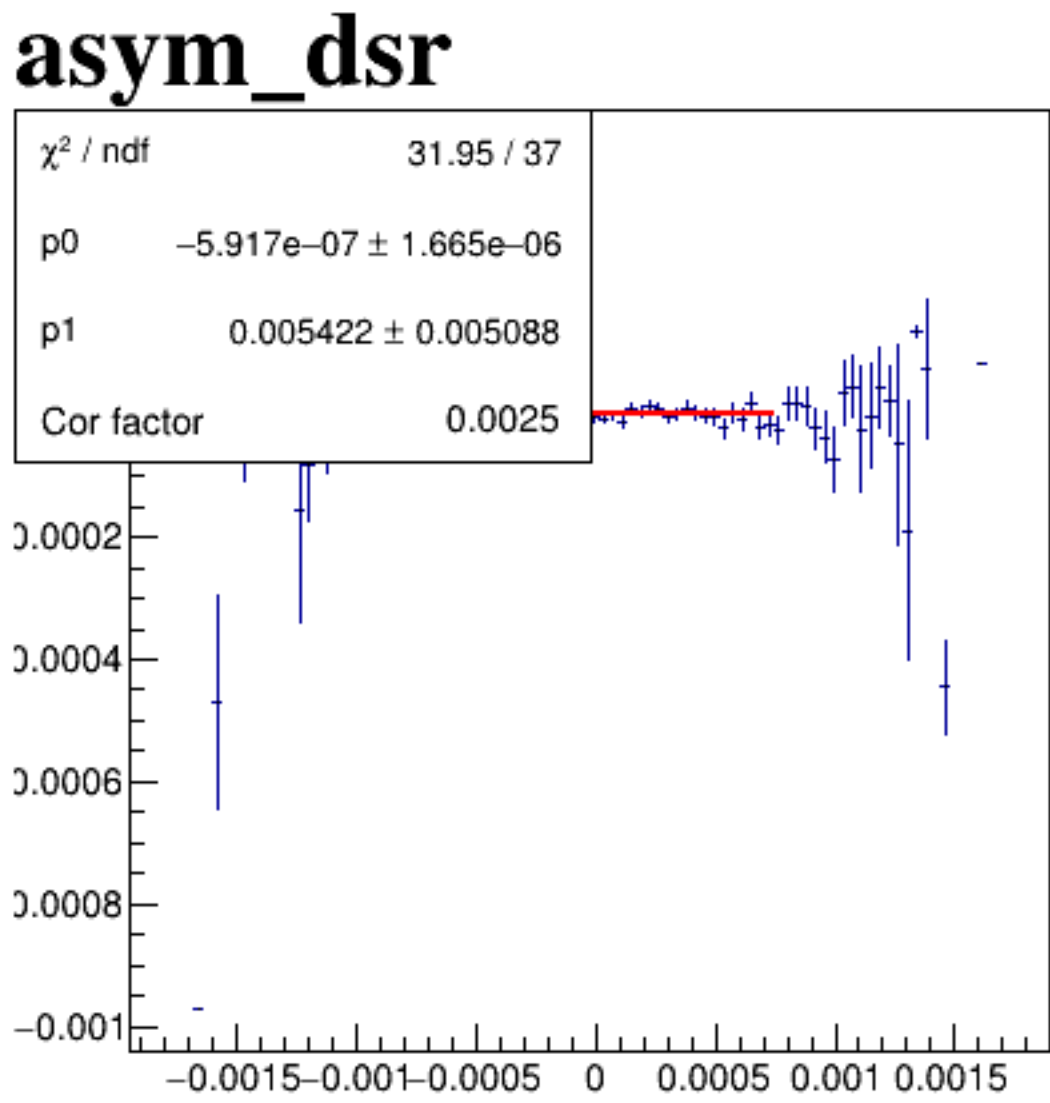
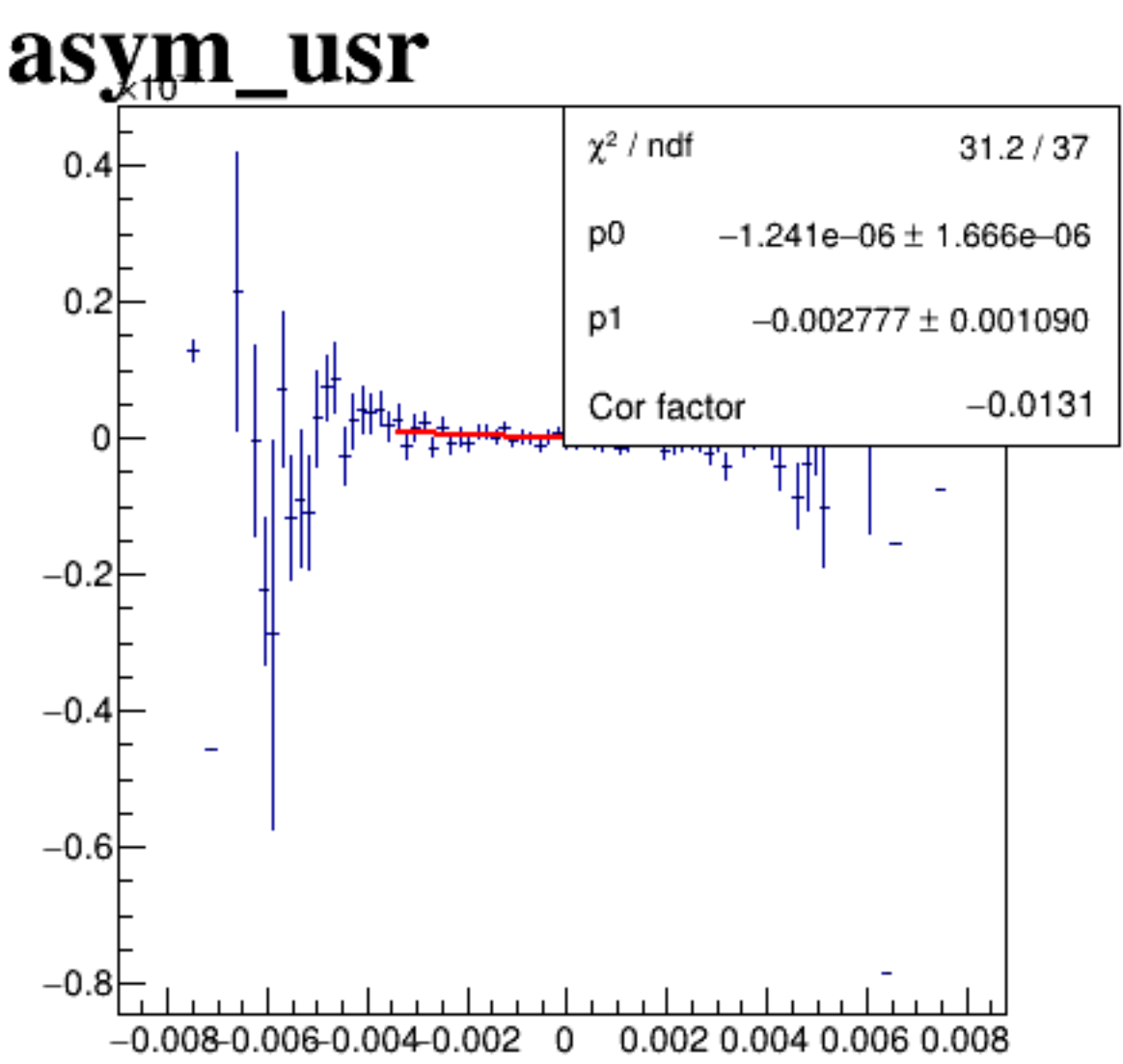
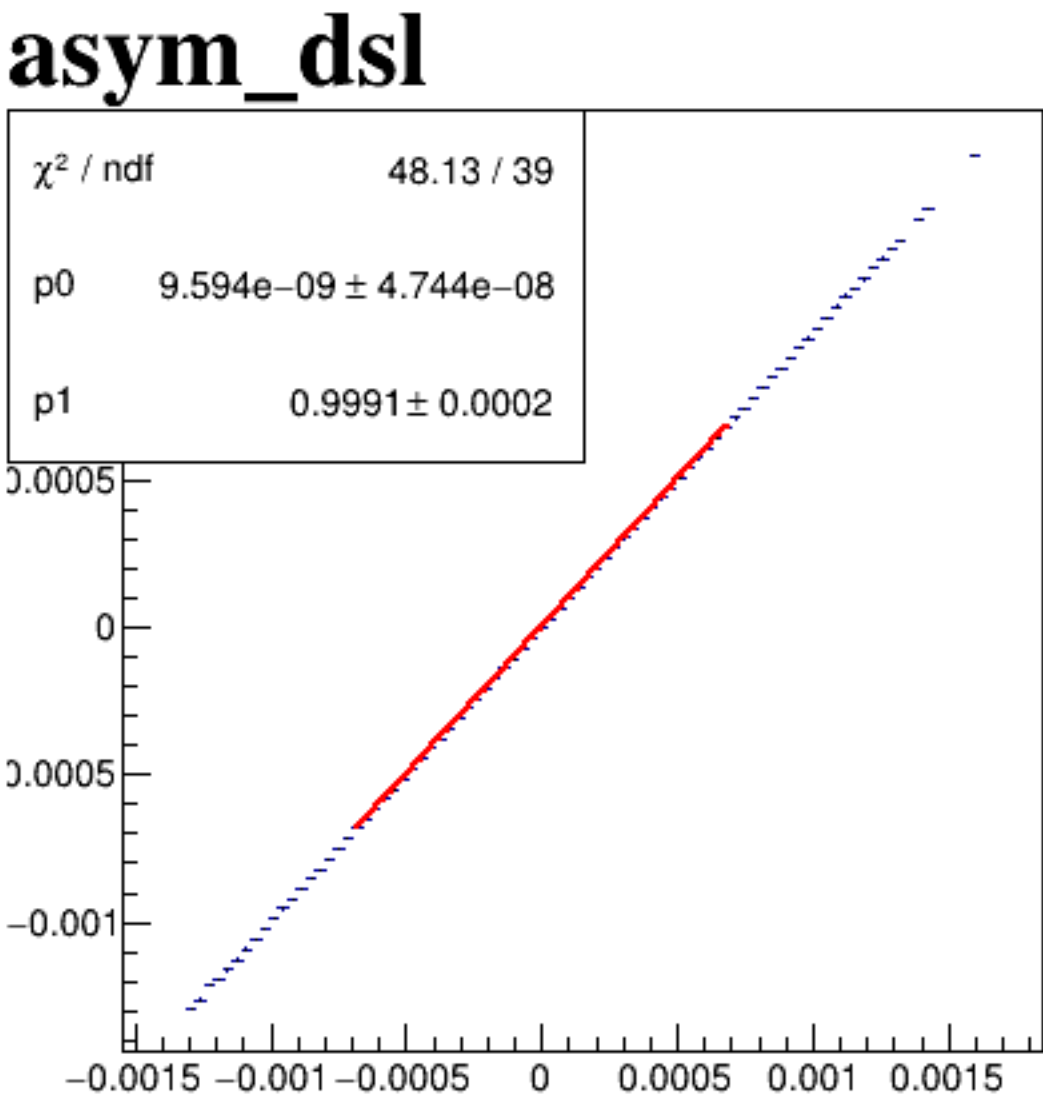
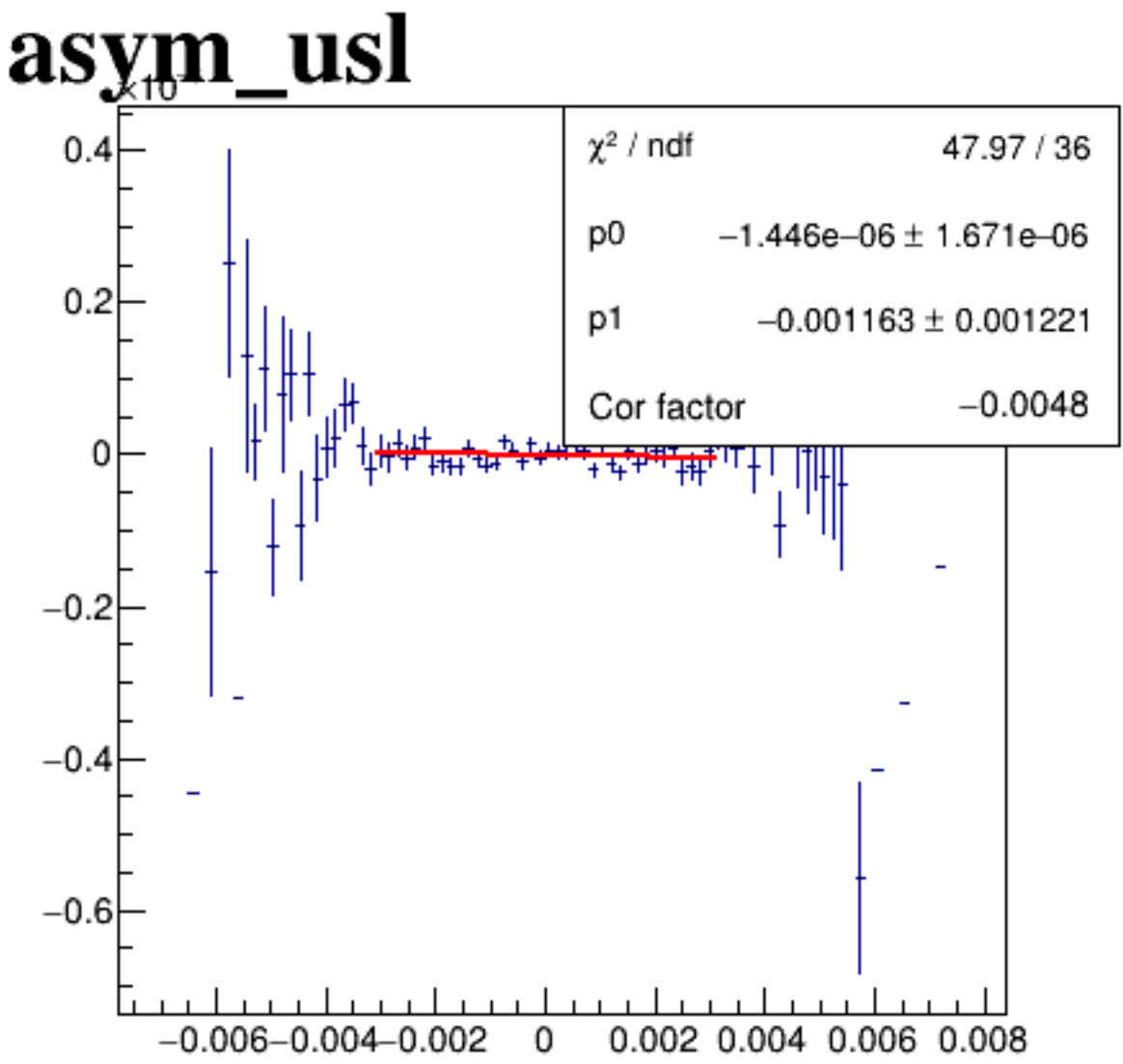
asym_usr

**asym_dsr**

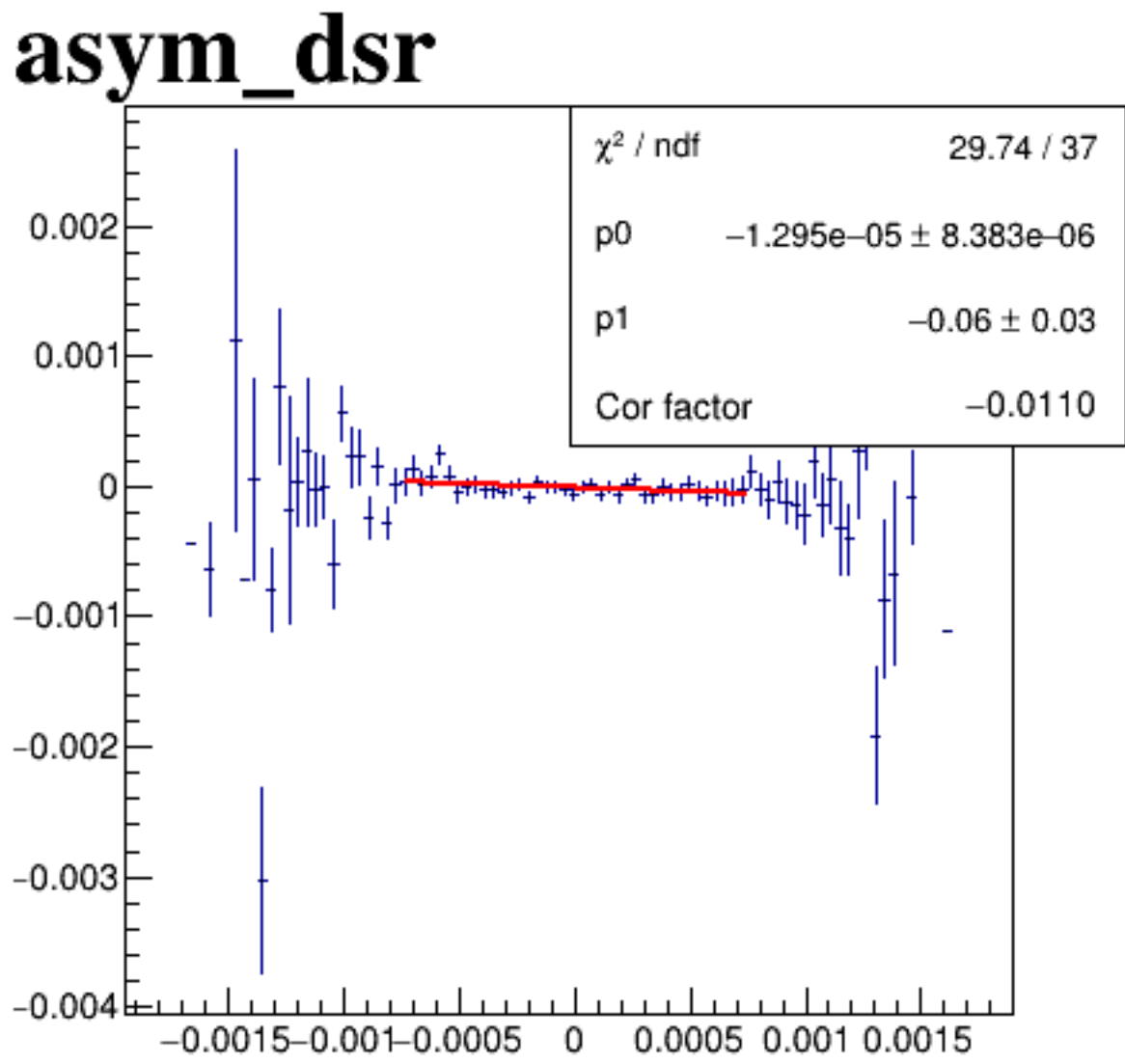
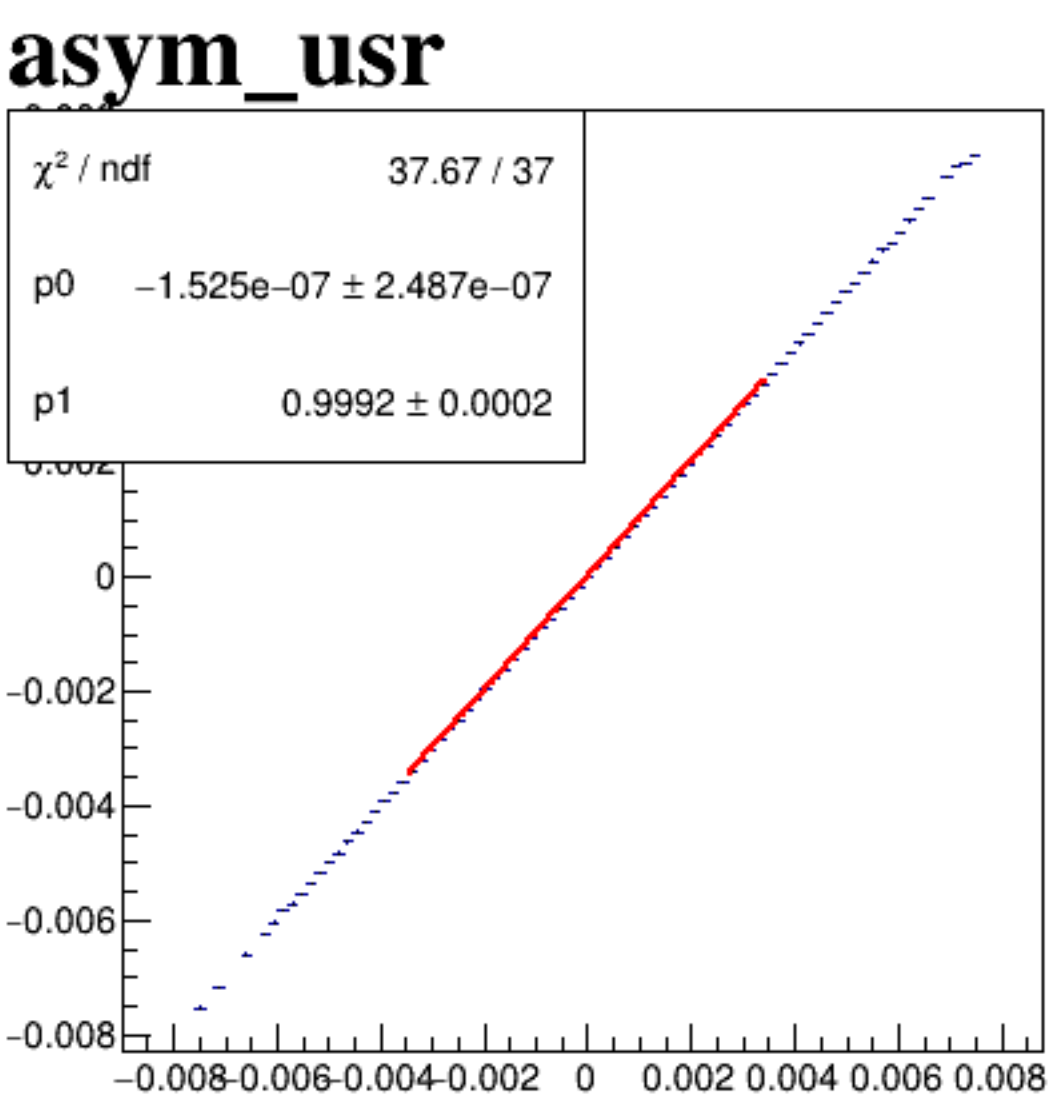
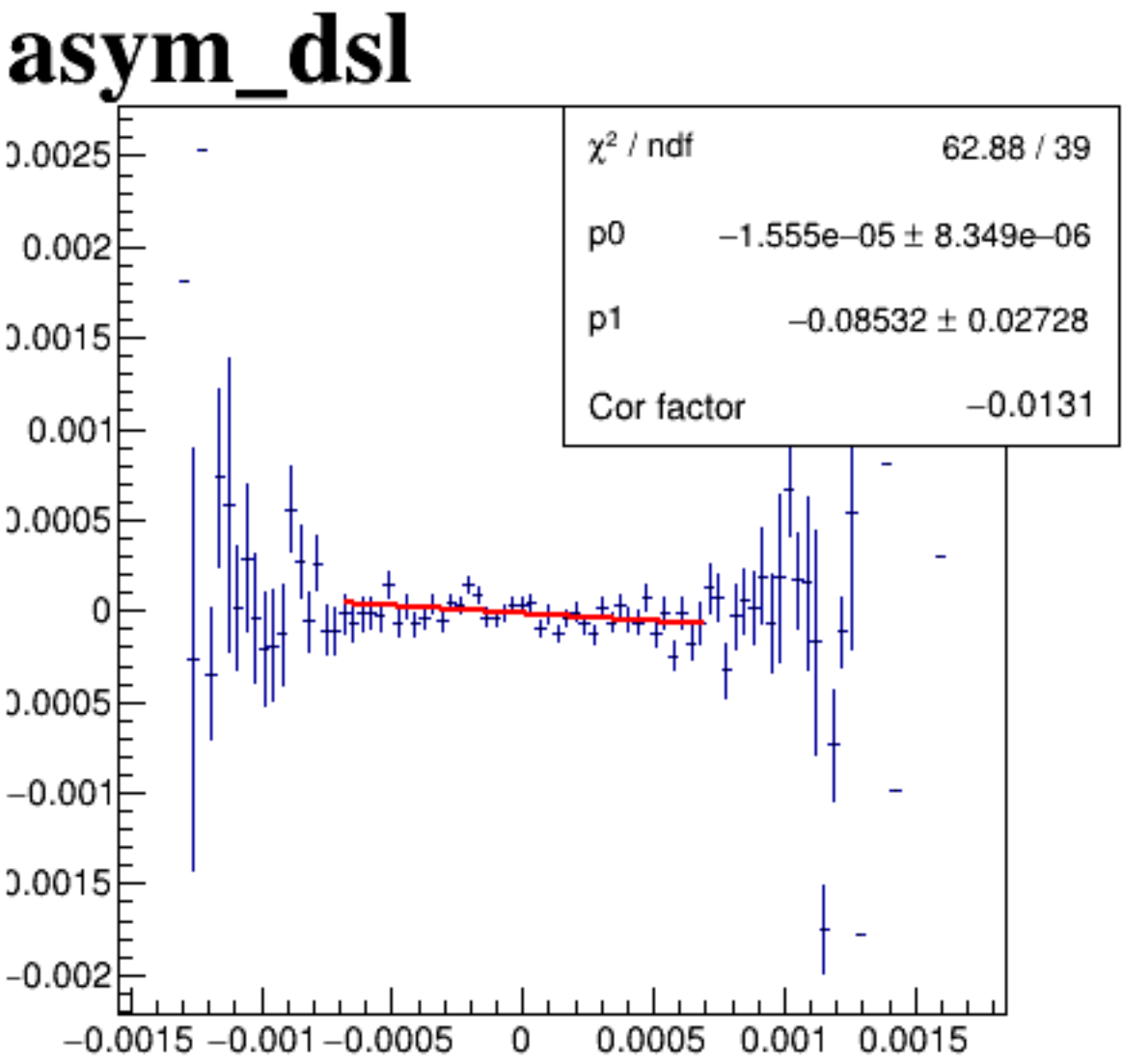
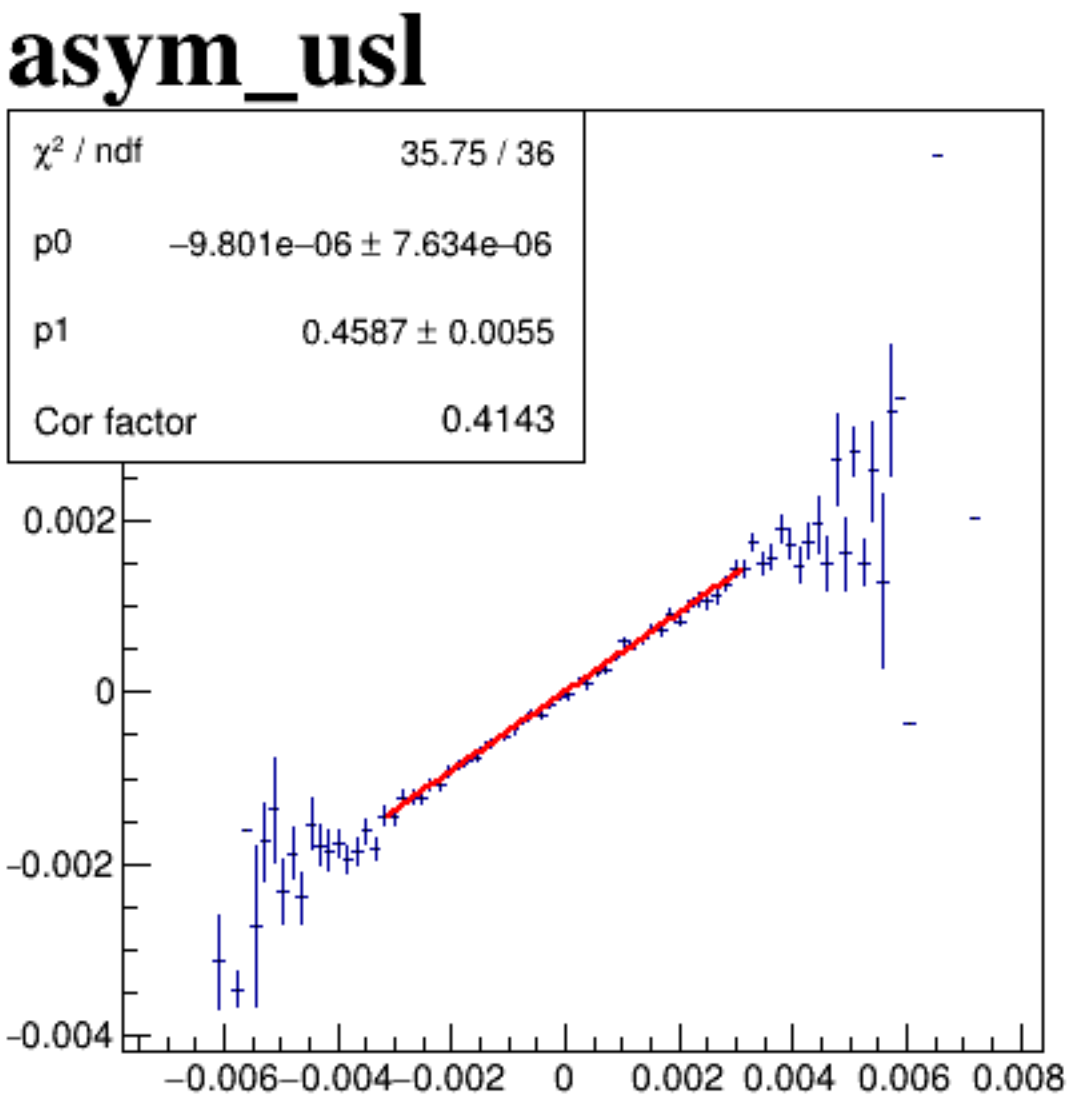
asym_usl



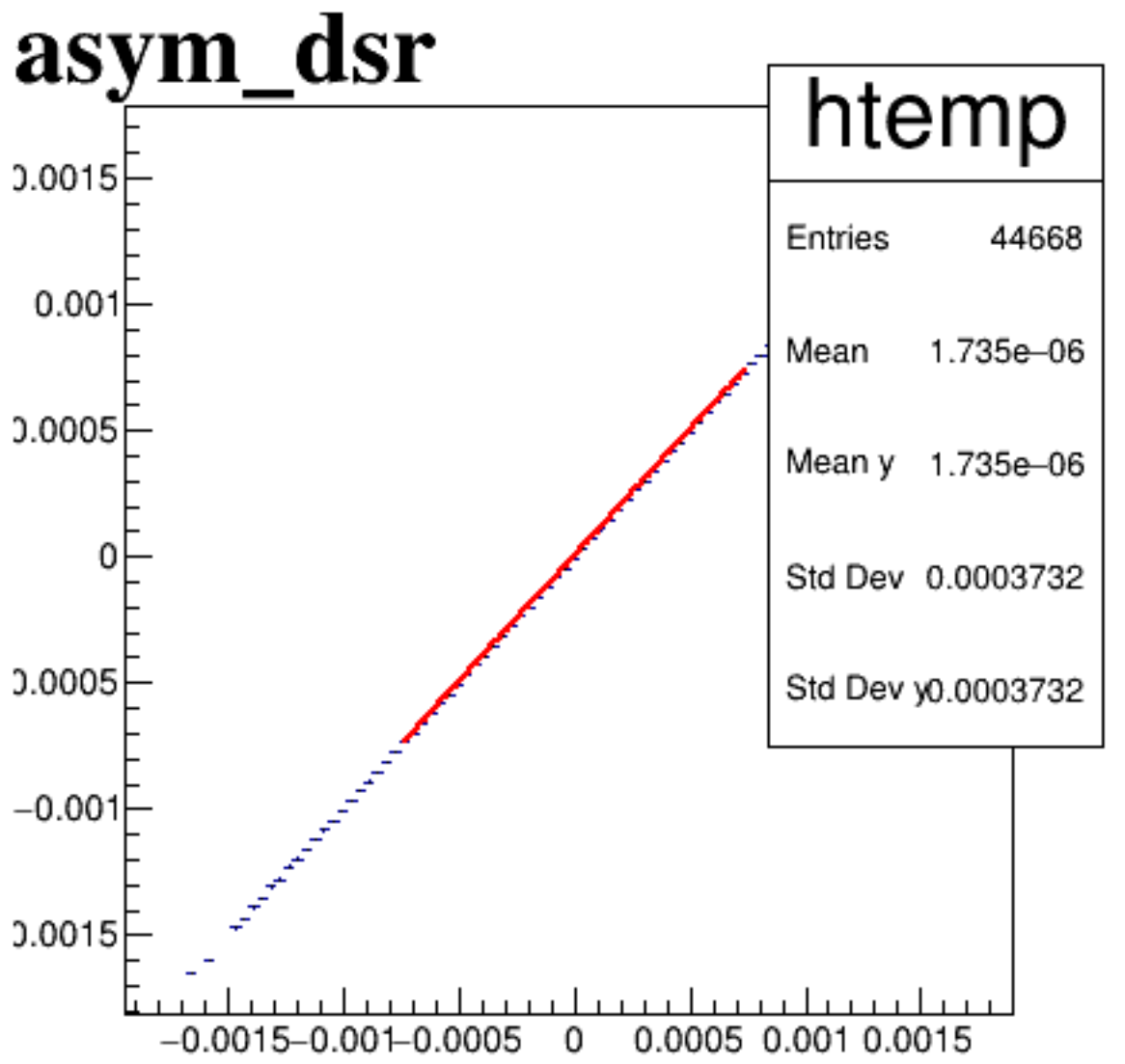
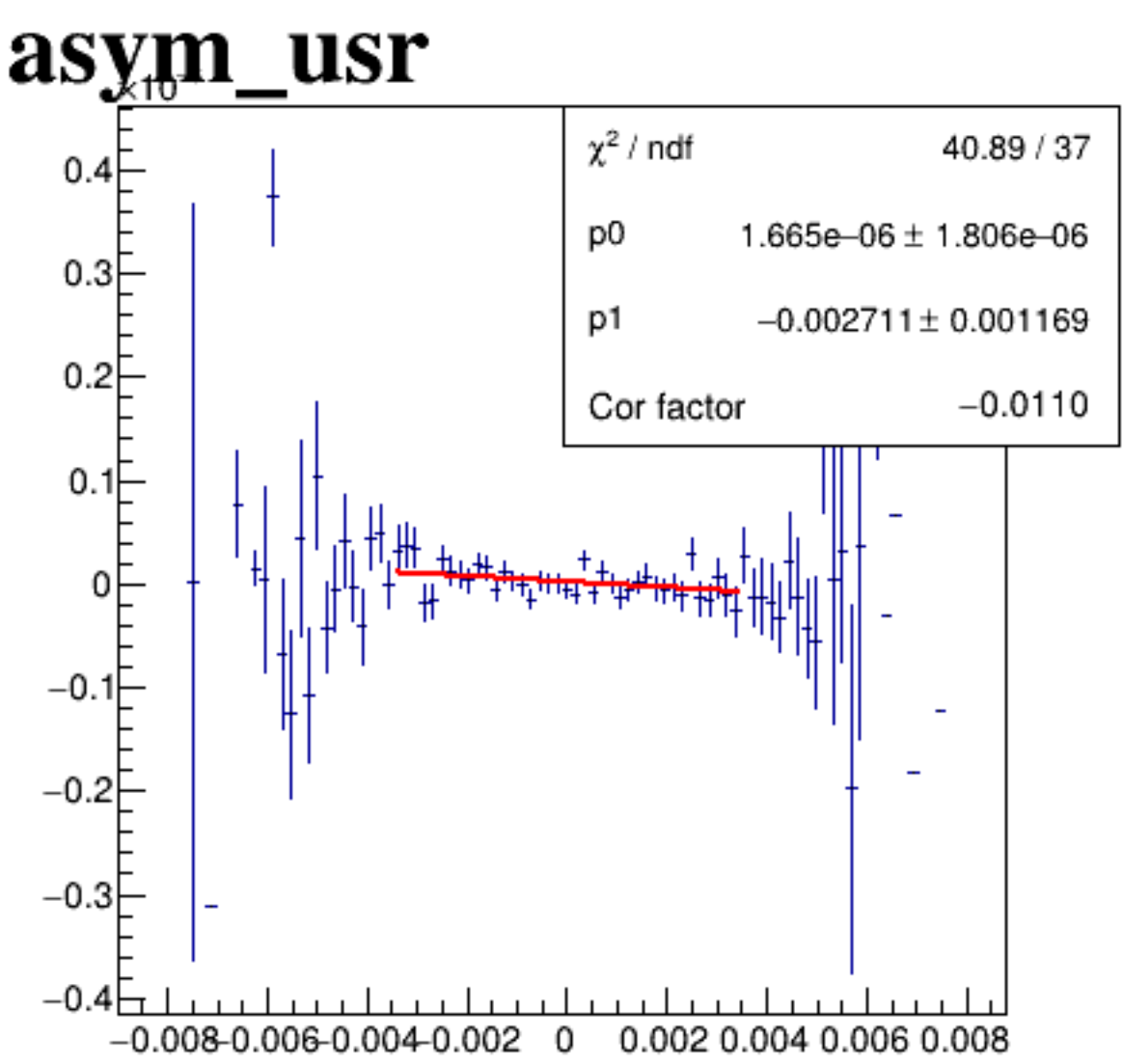
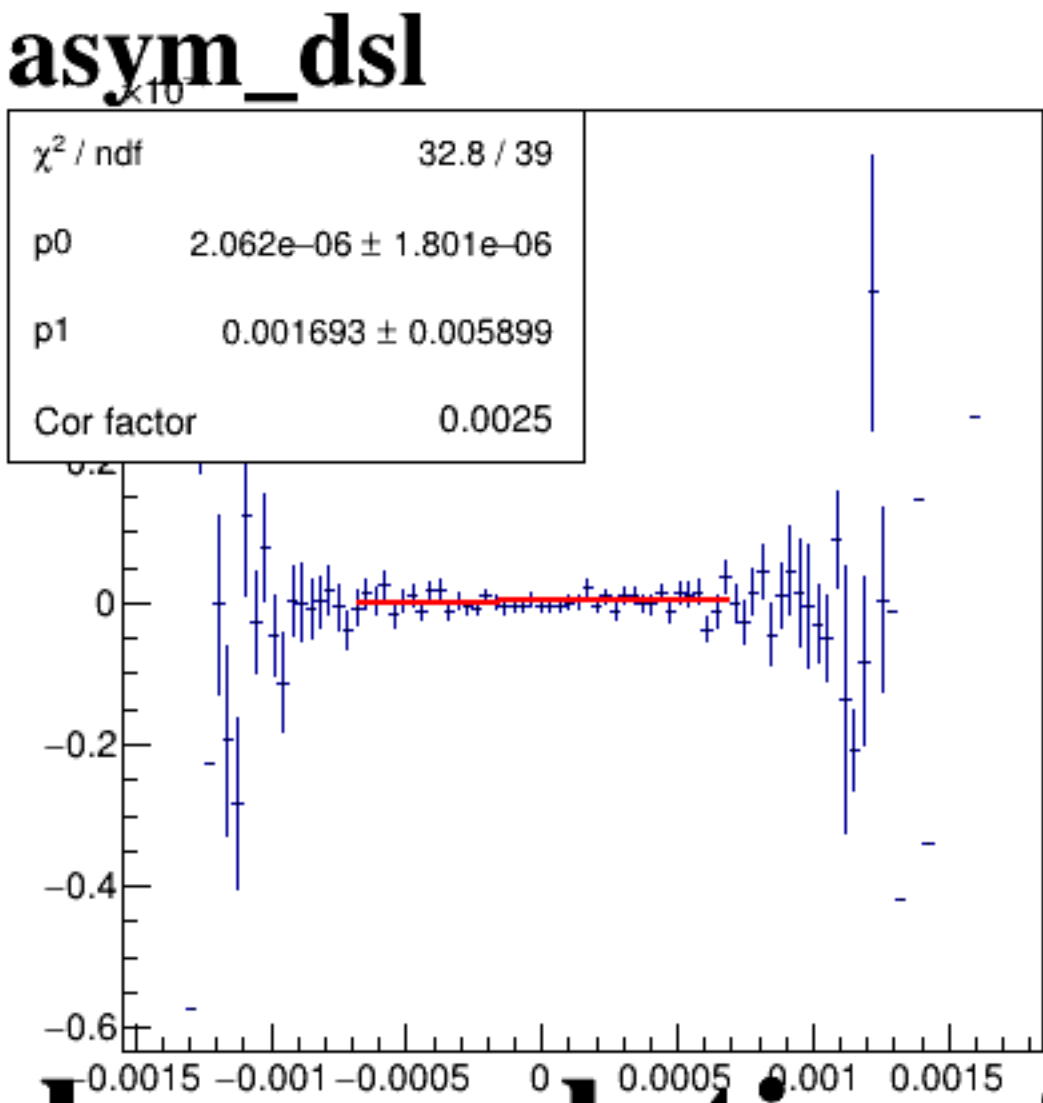
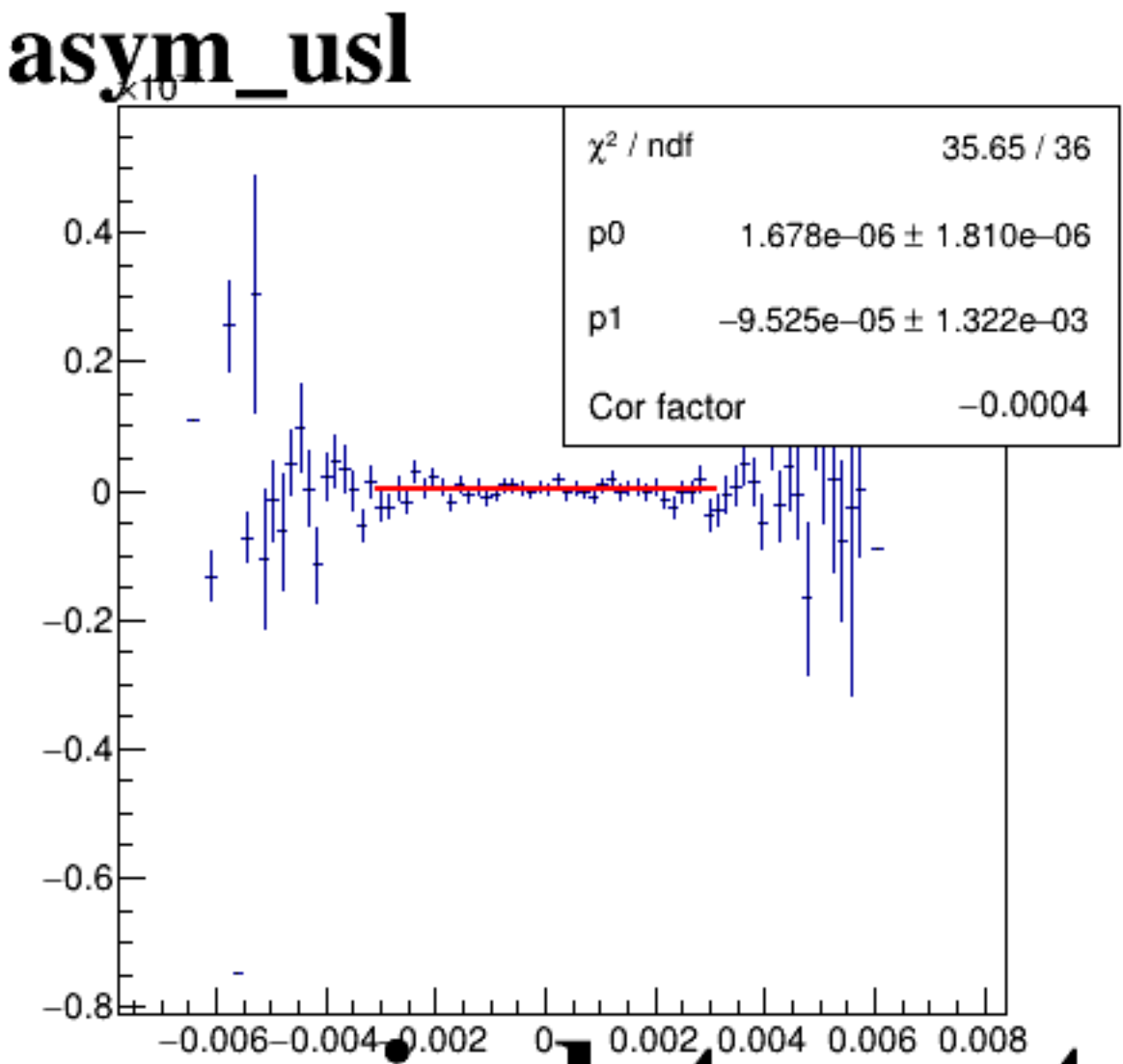
asym_dsl



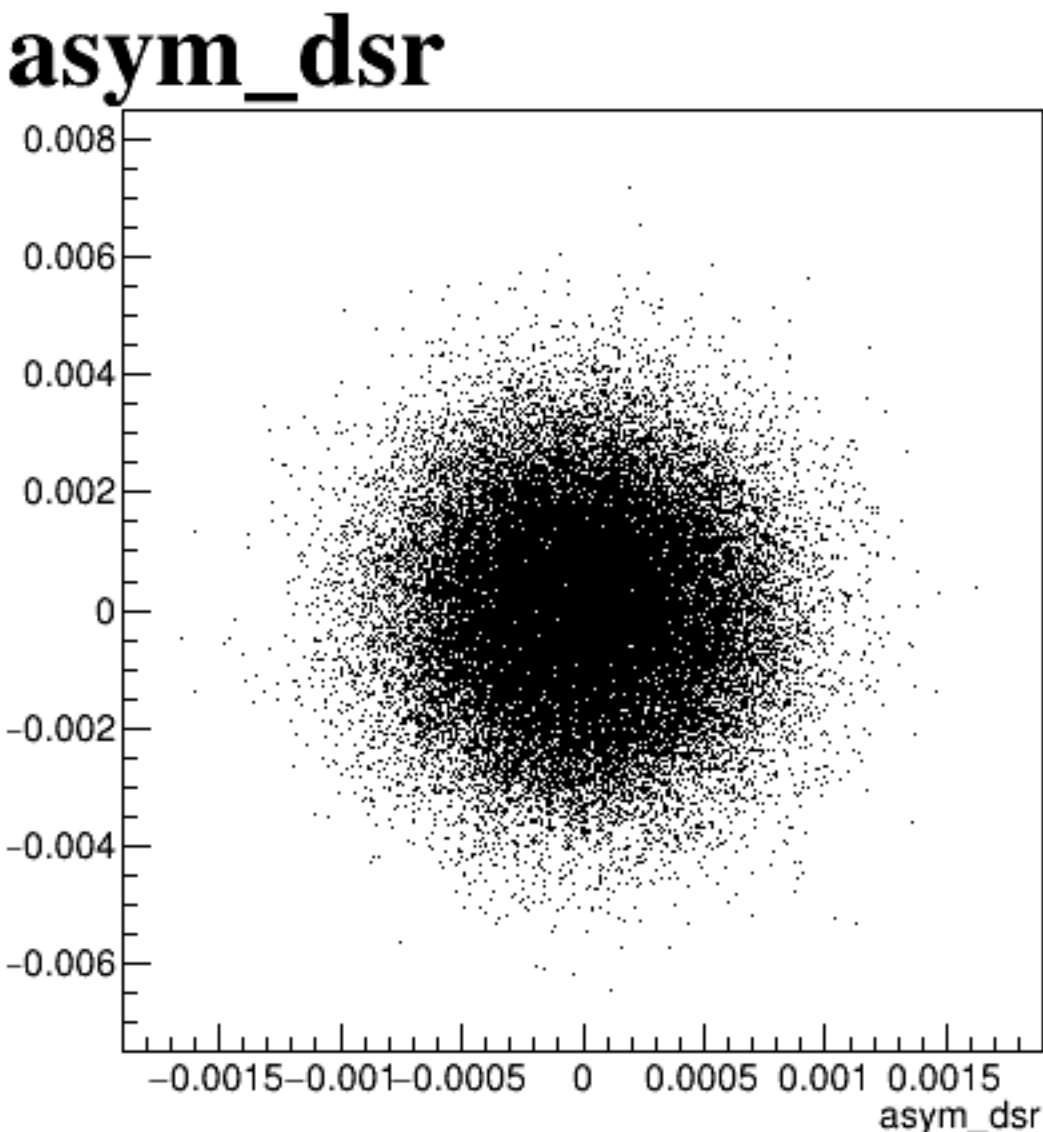
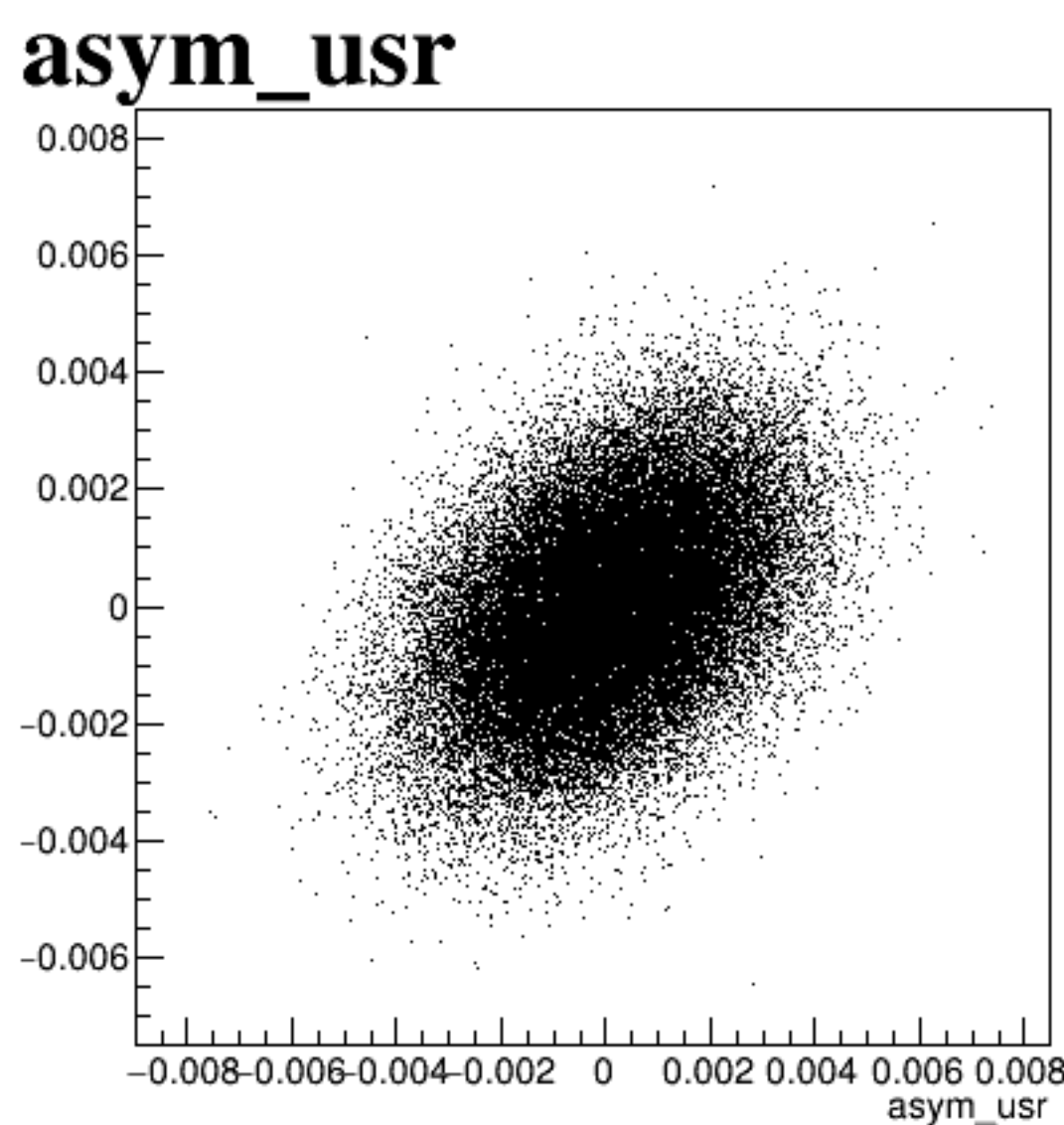
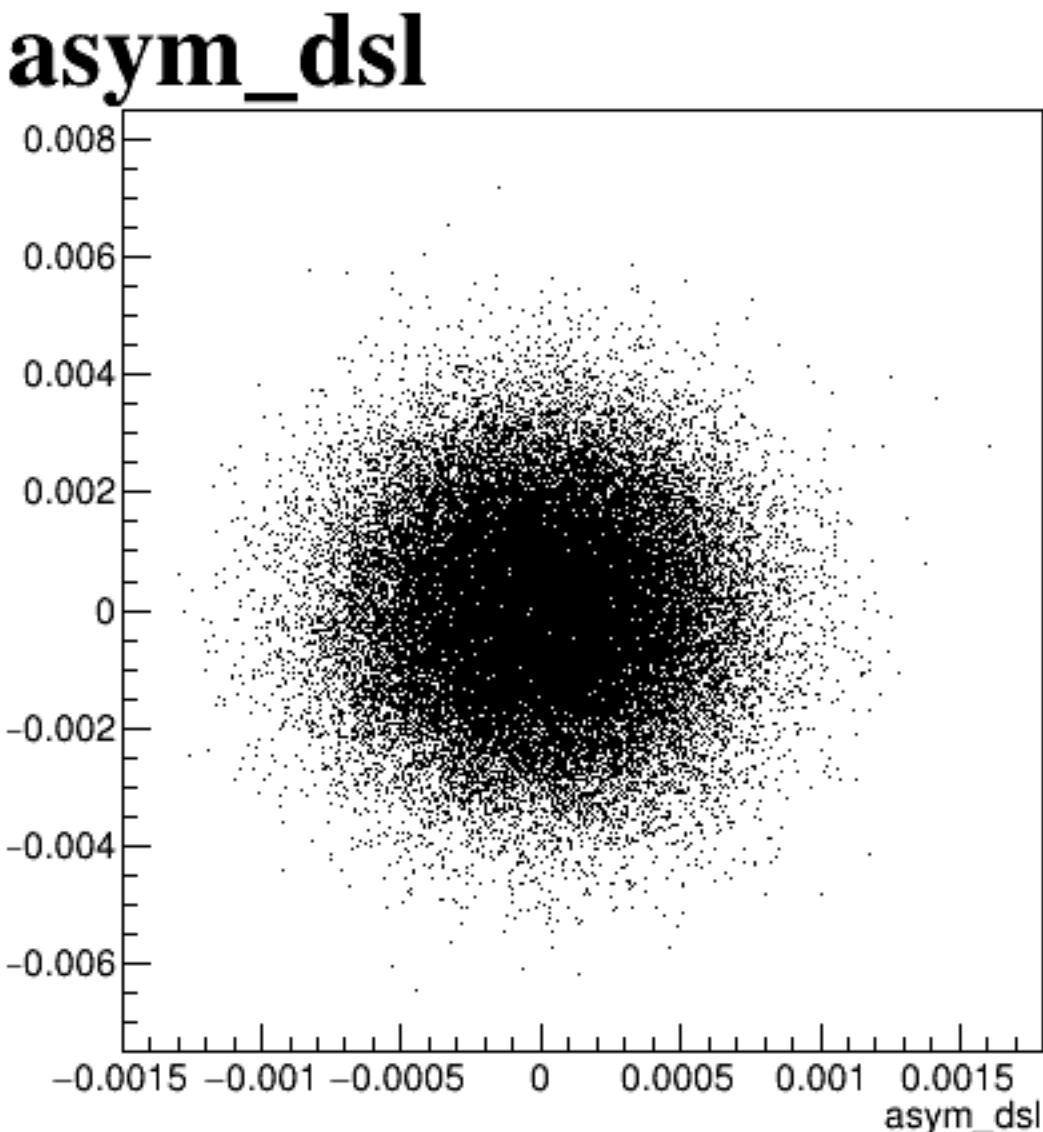
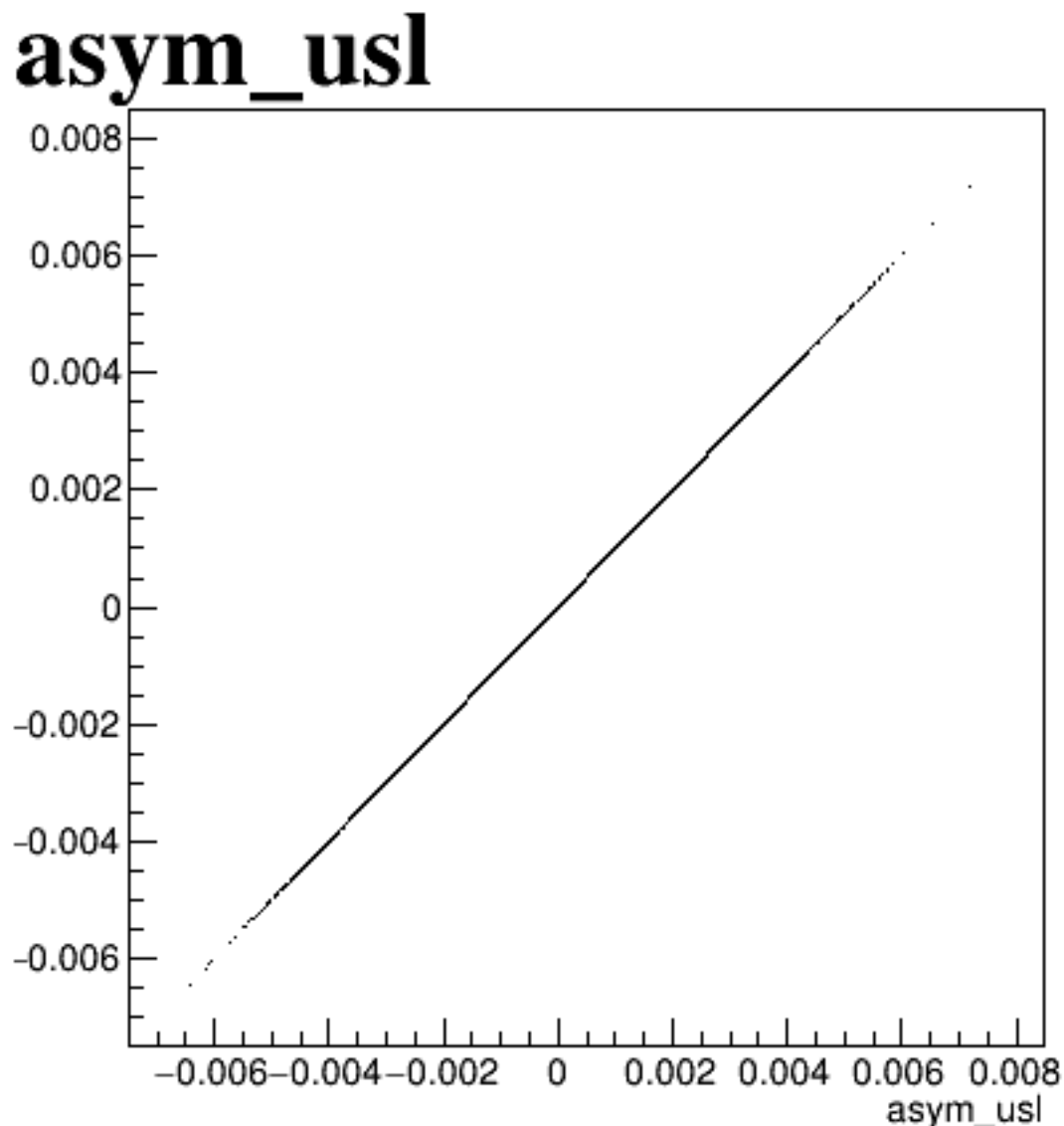
asym_usr



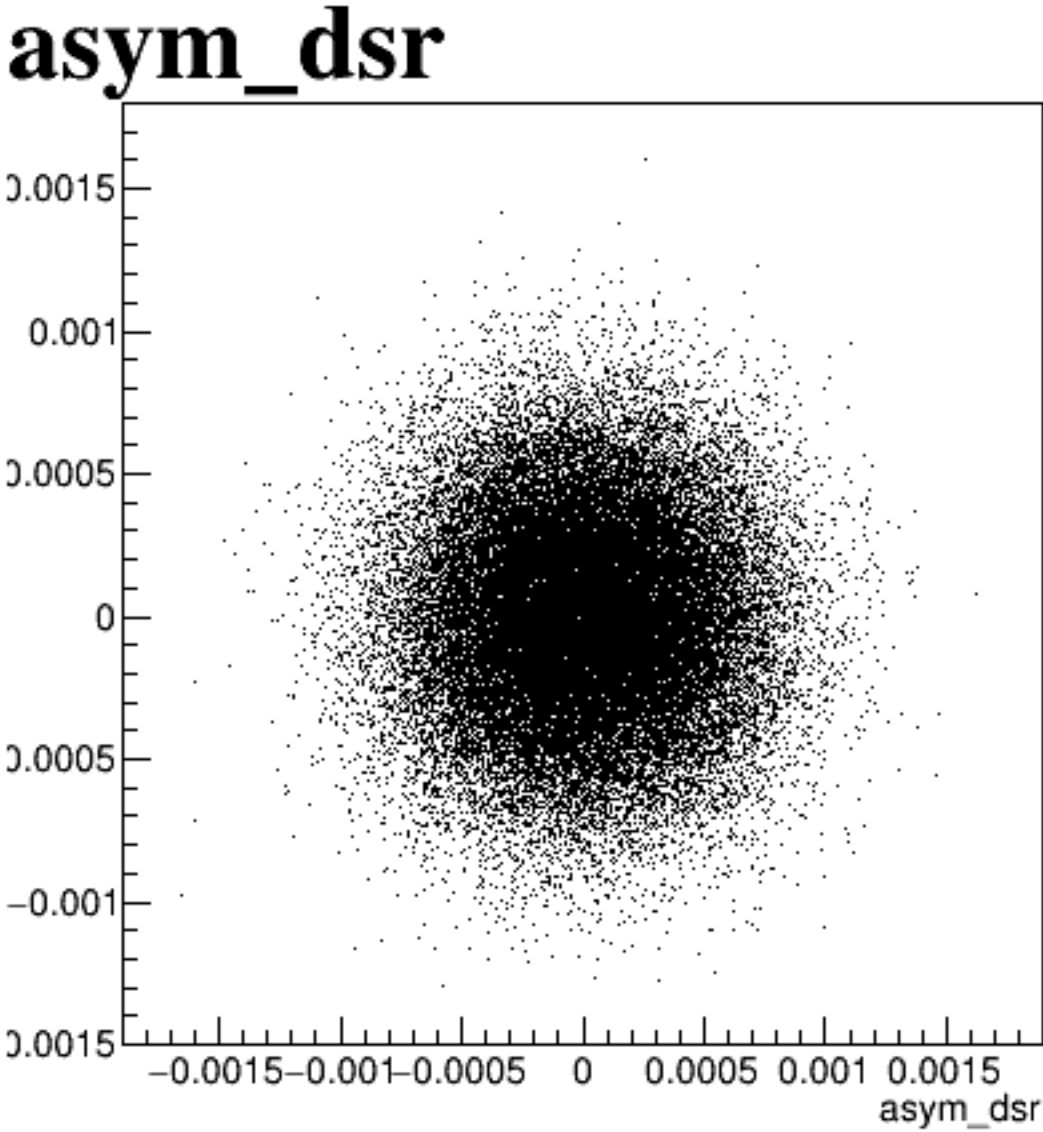
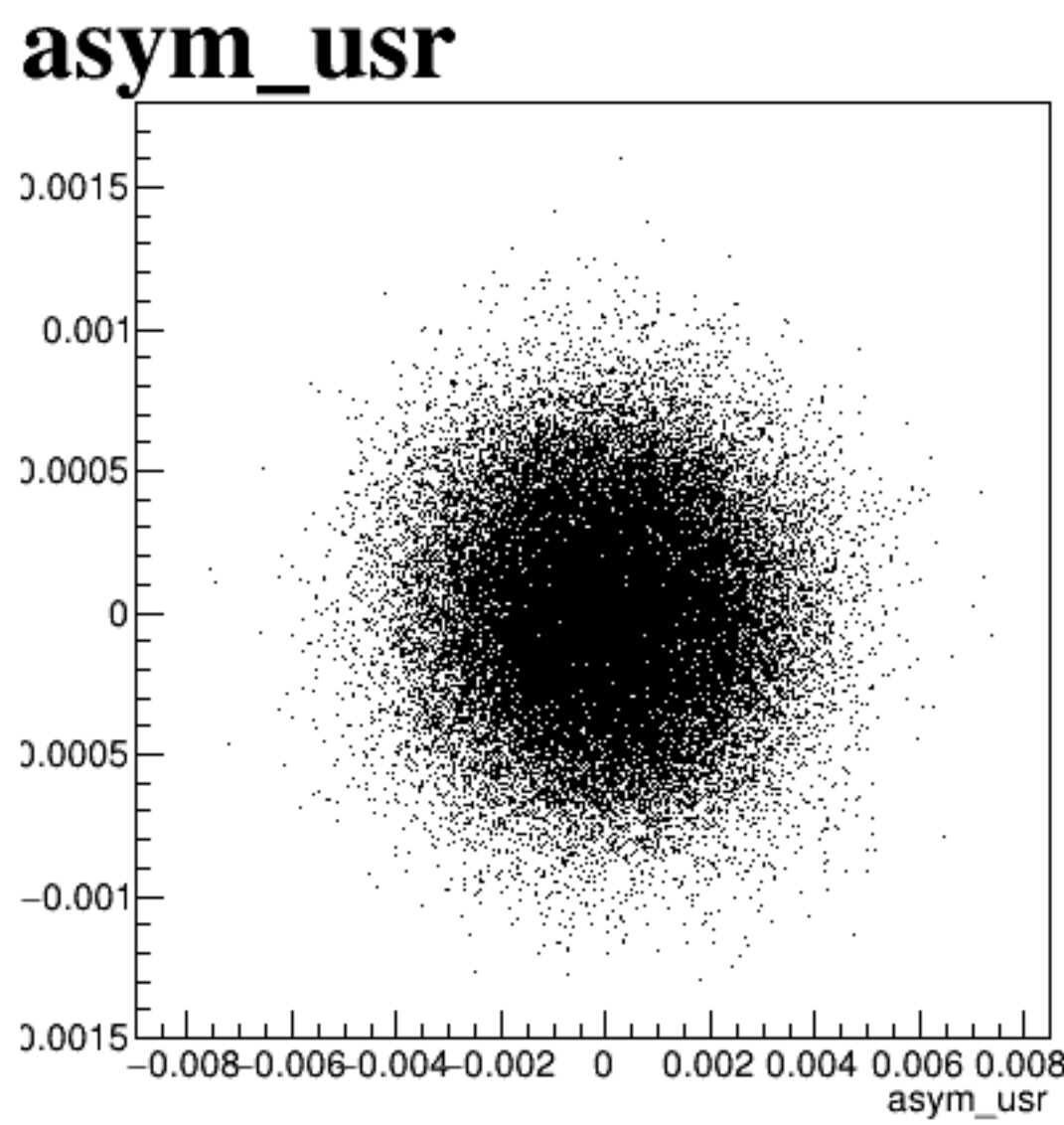
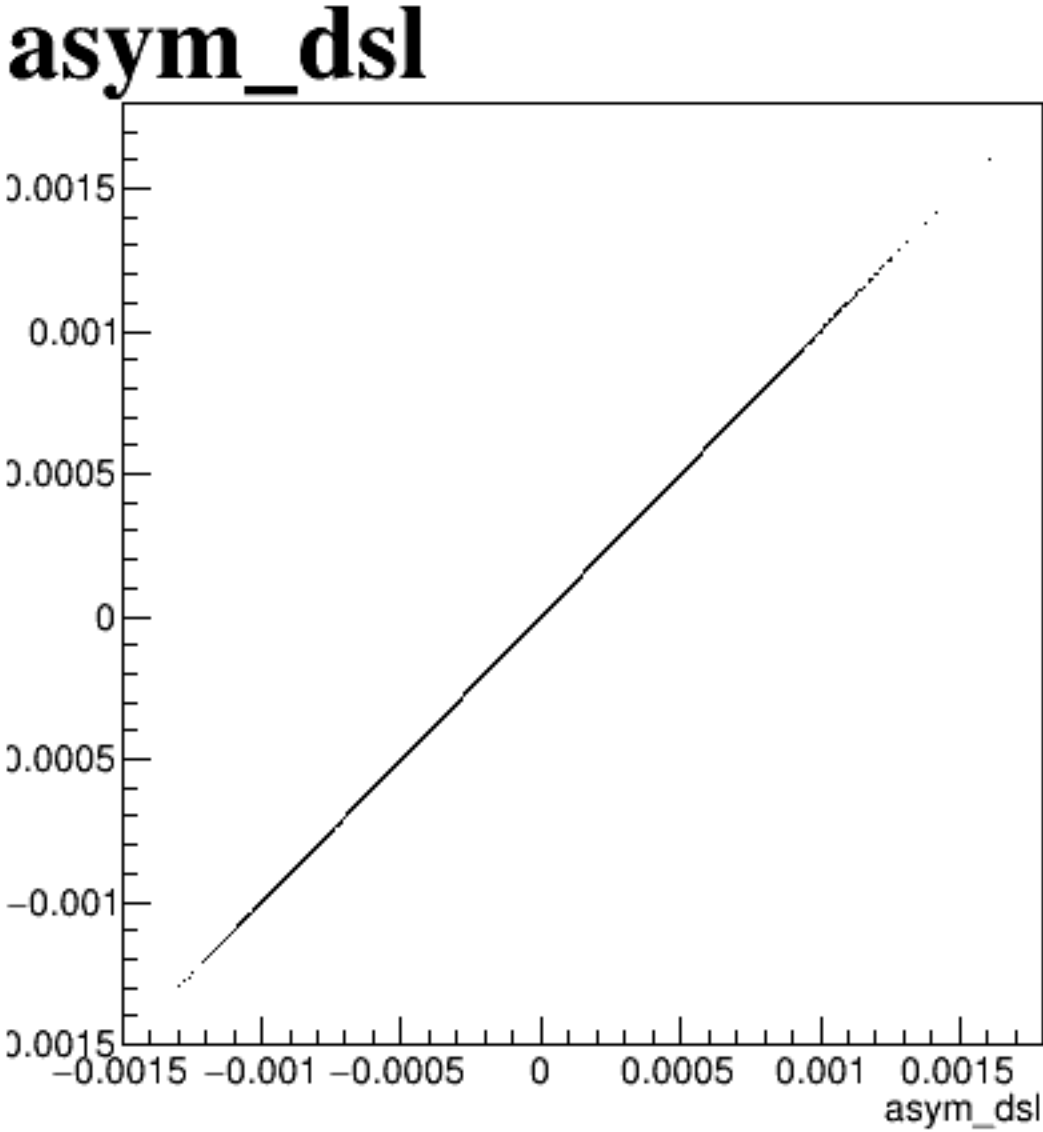
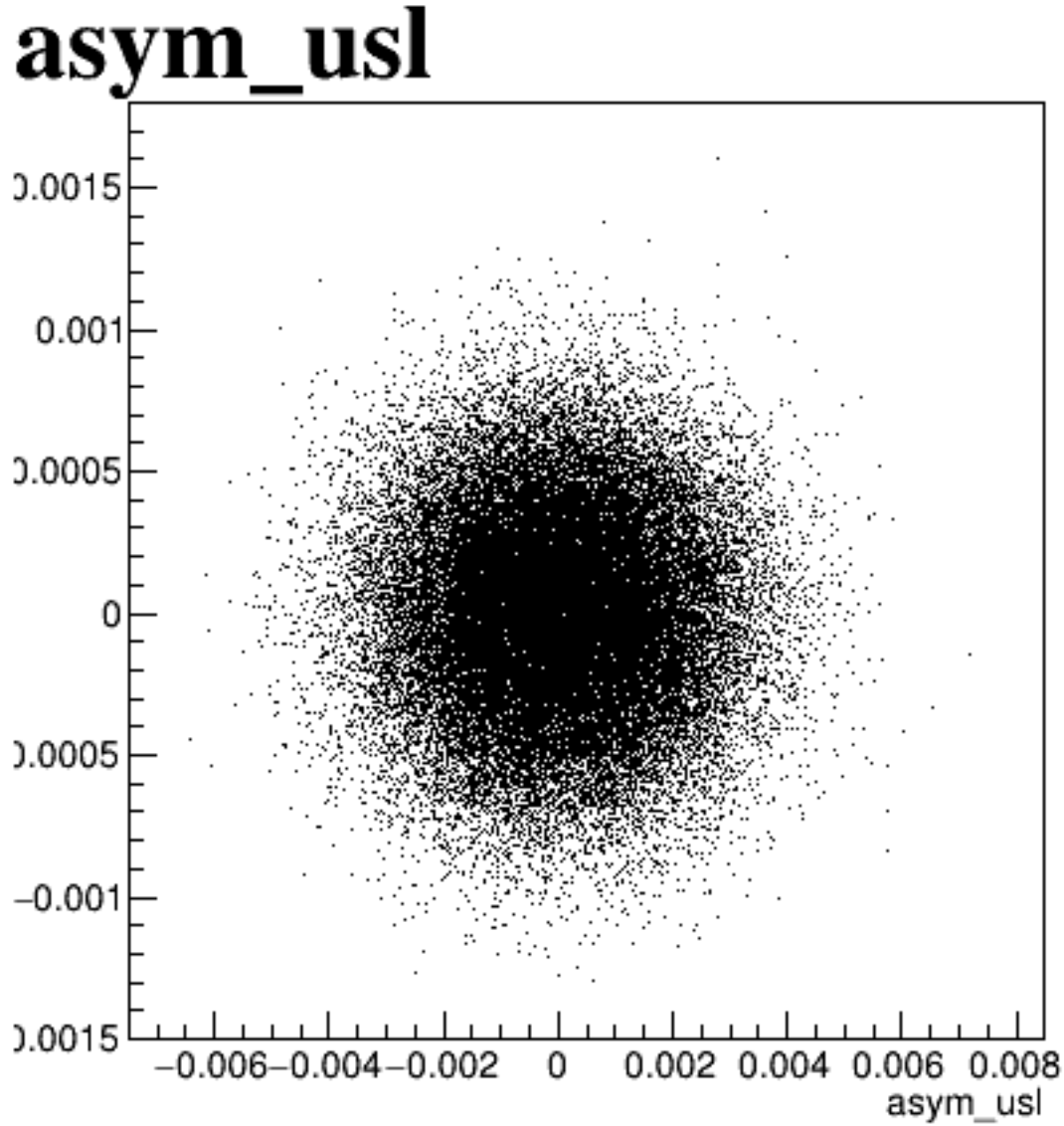
asym_dsr



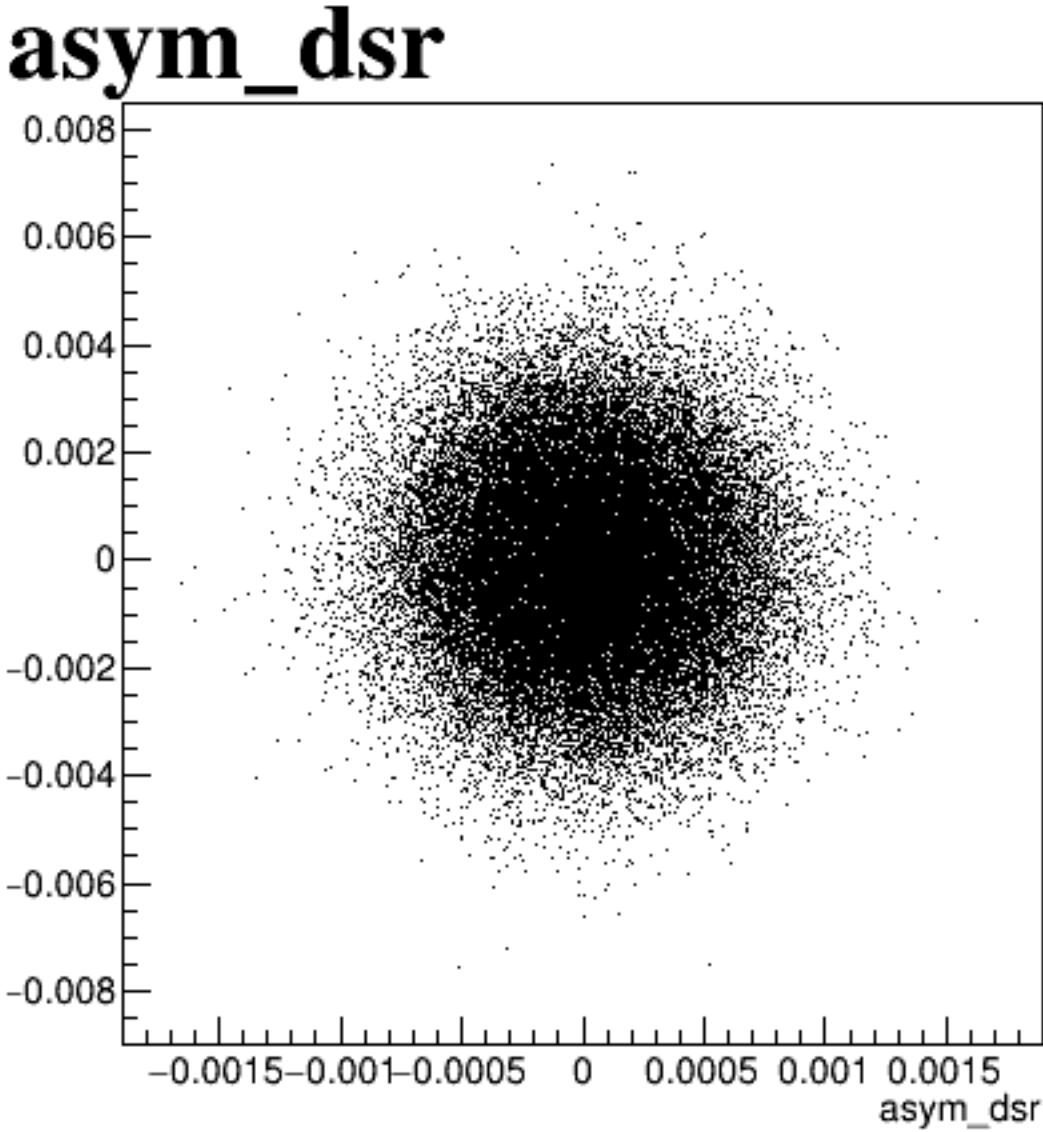
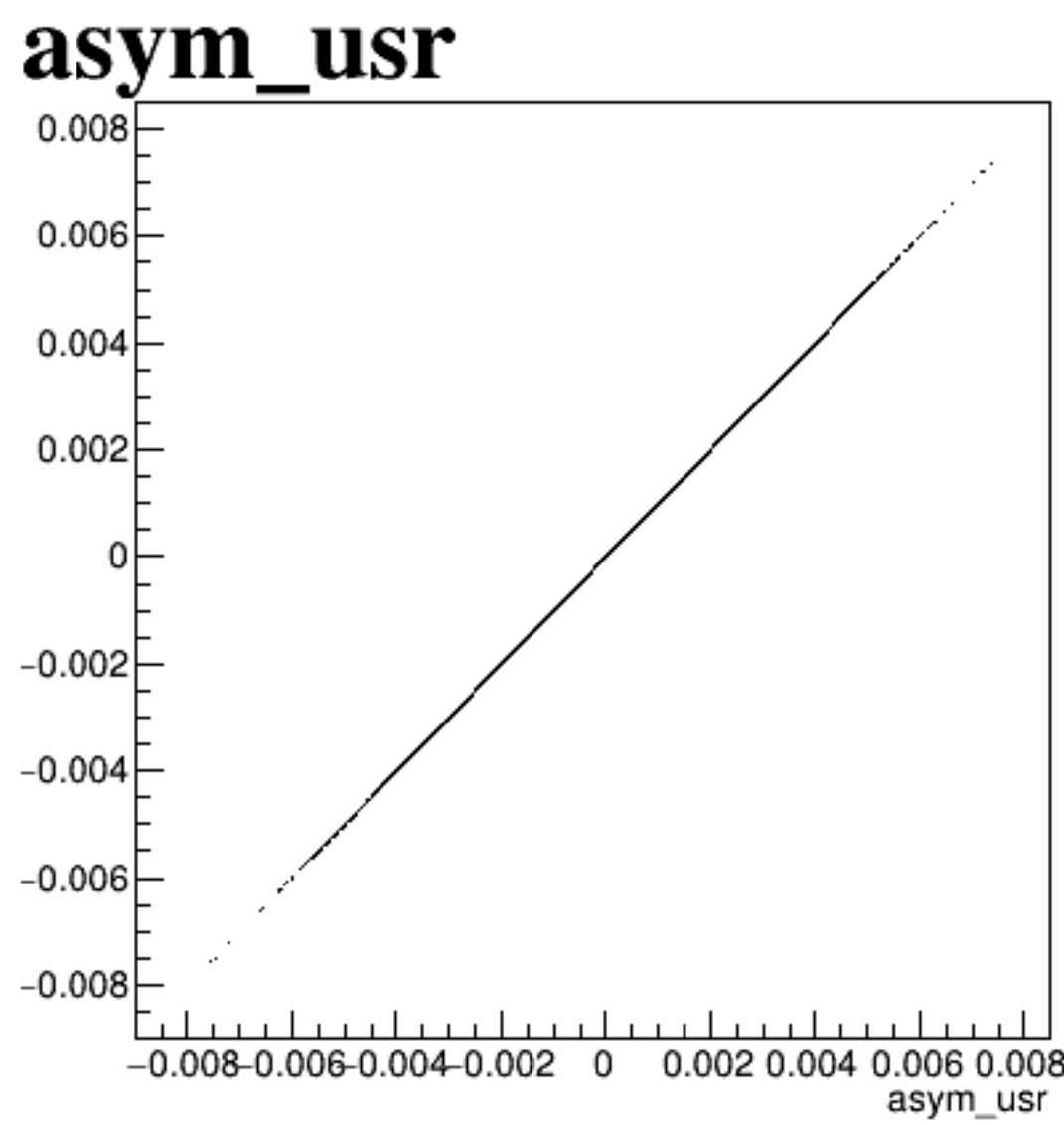
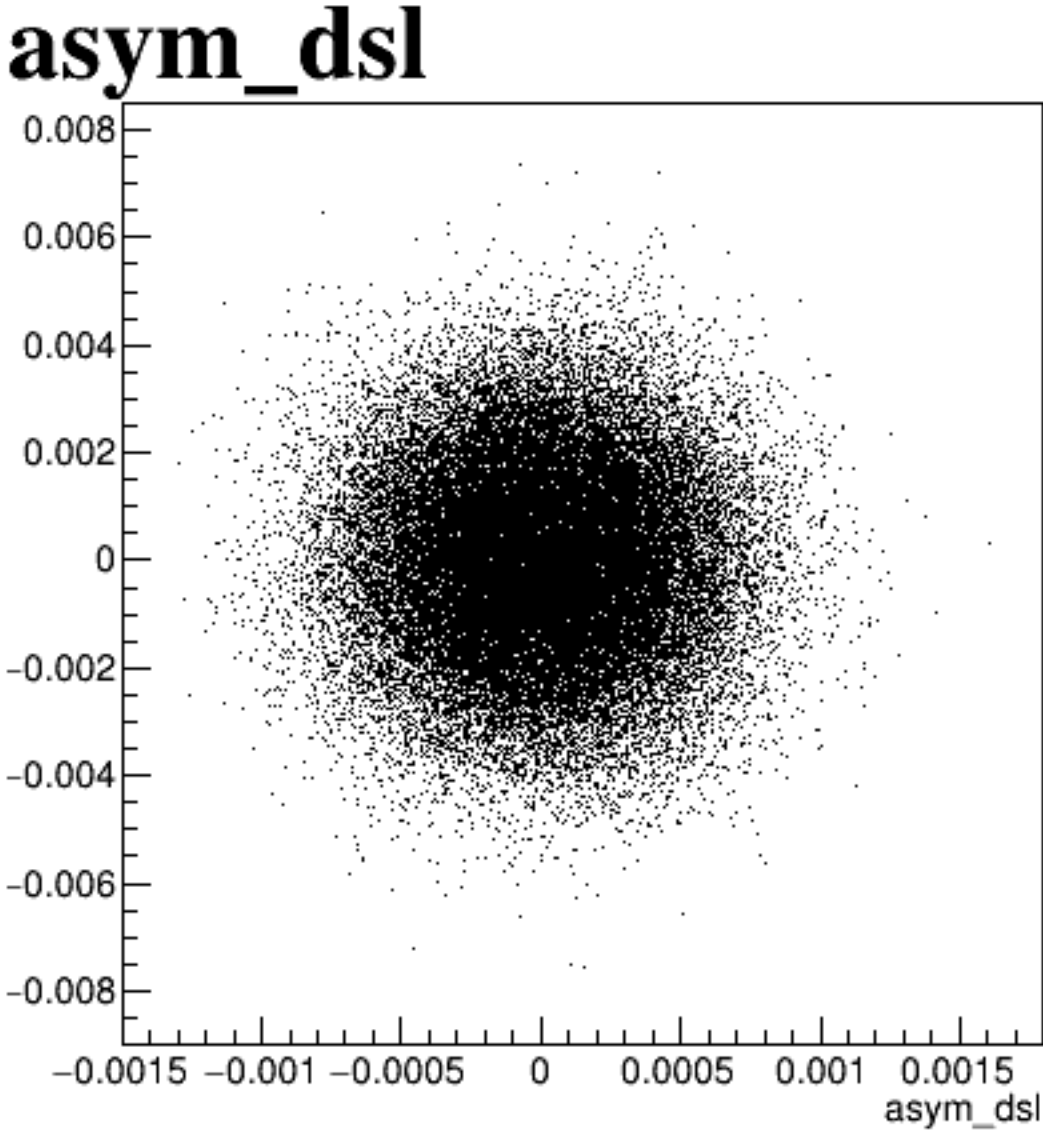
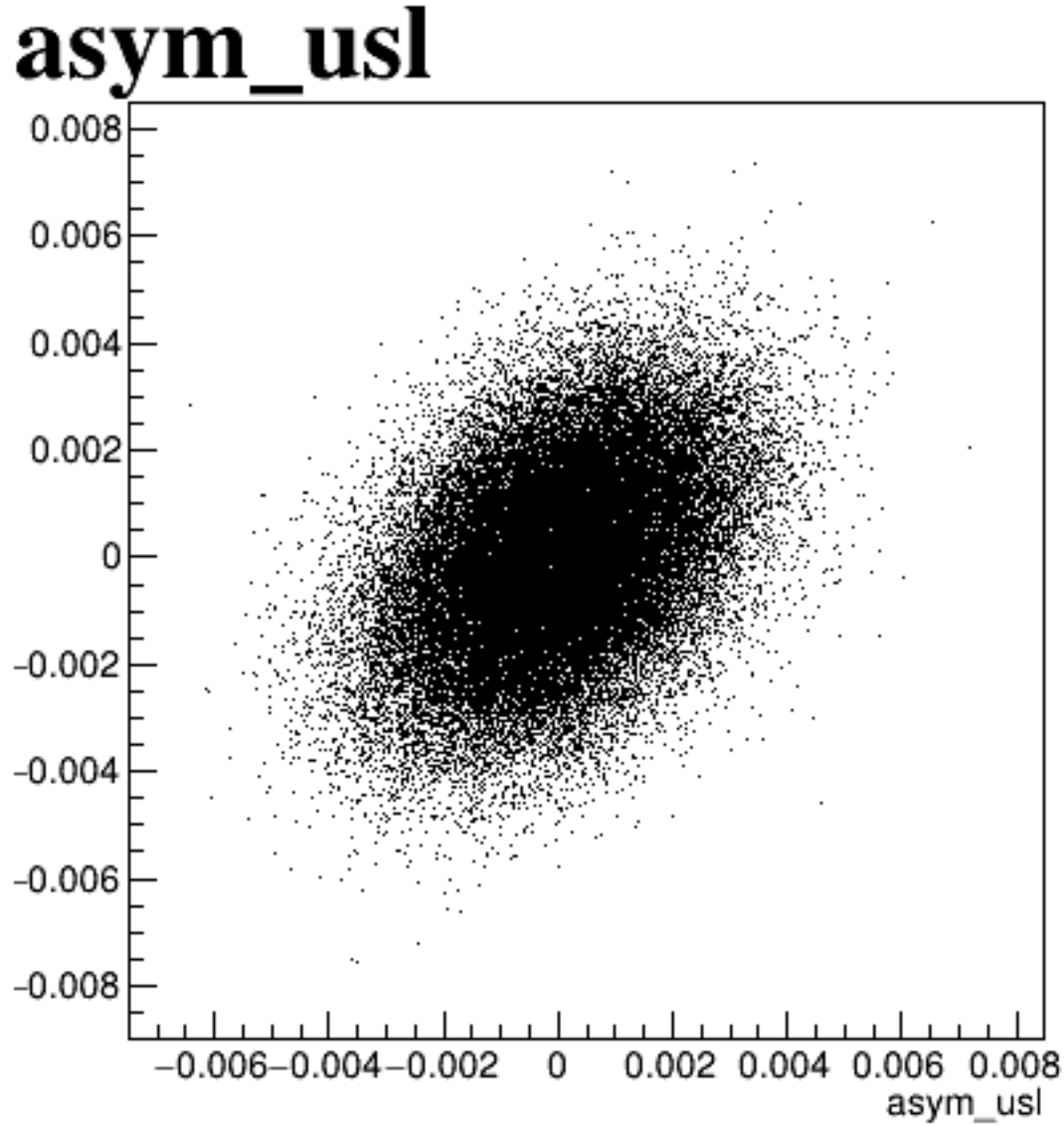
asym_usl



asym_dsl



asym_usr



asym_dsr

