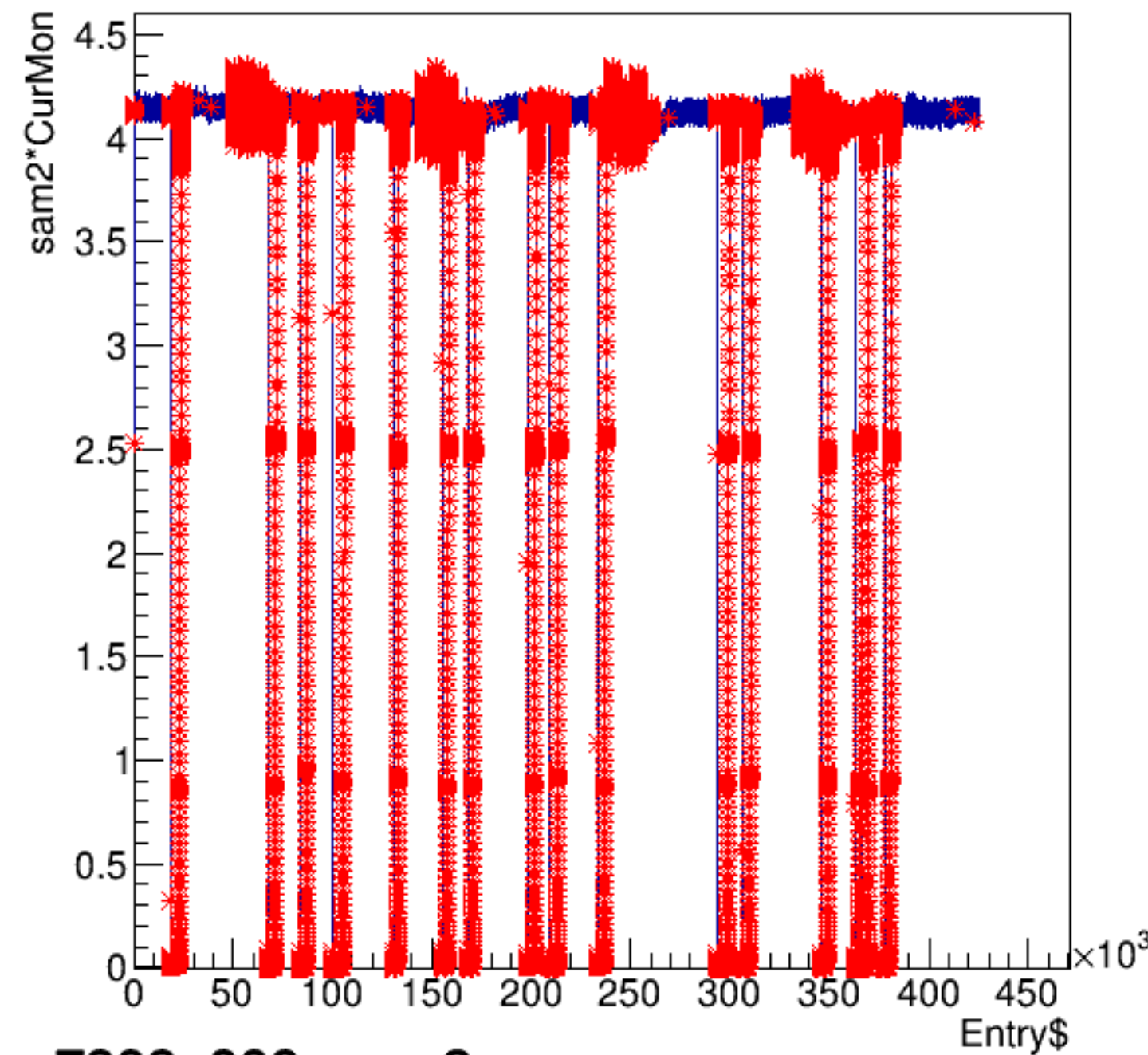
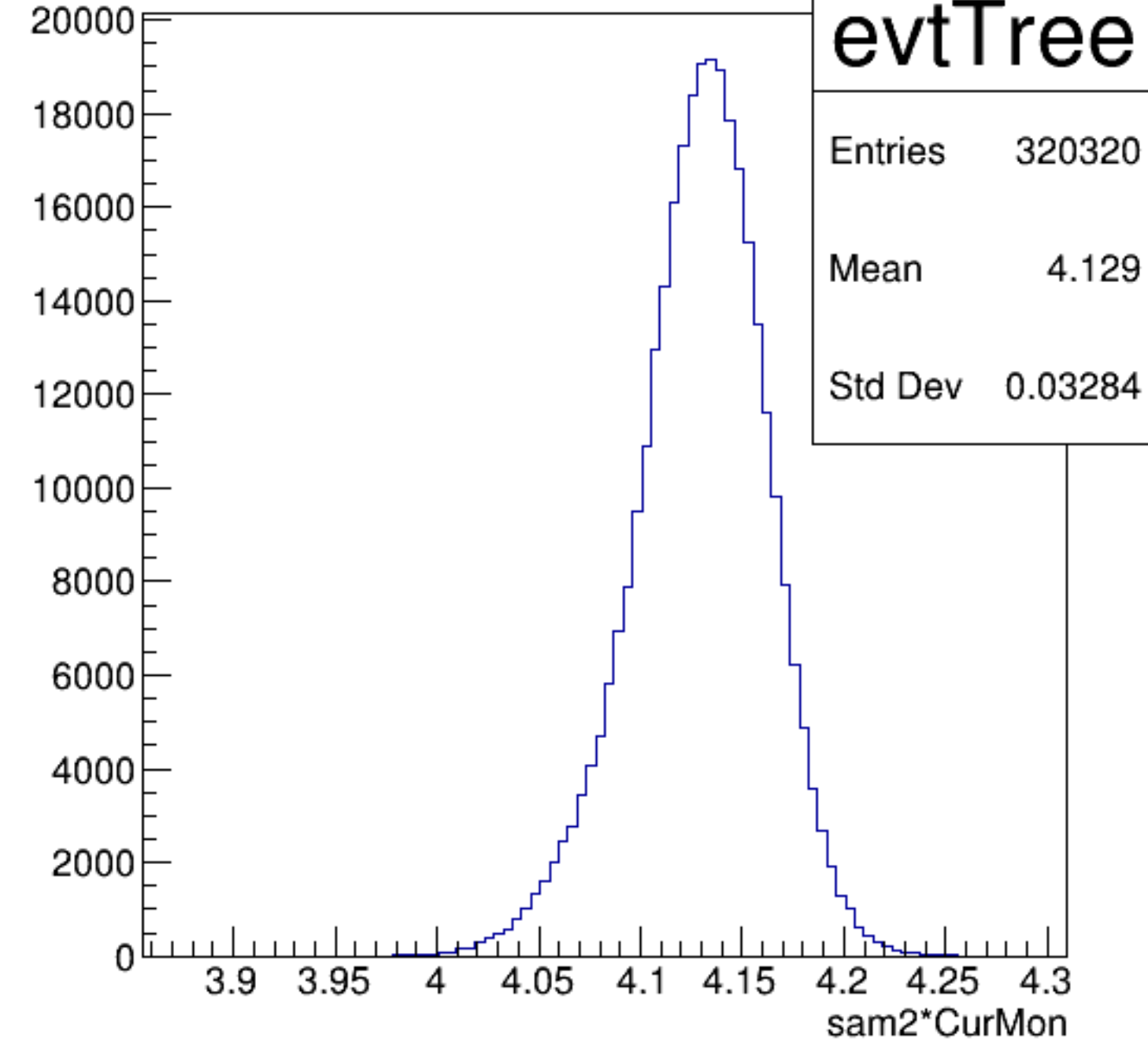


sam2\*CurMon:Entry\$

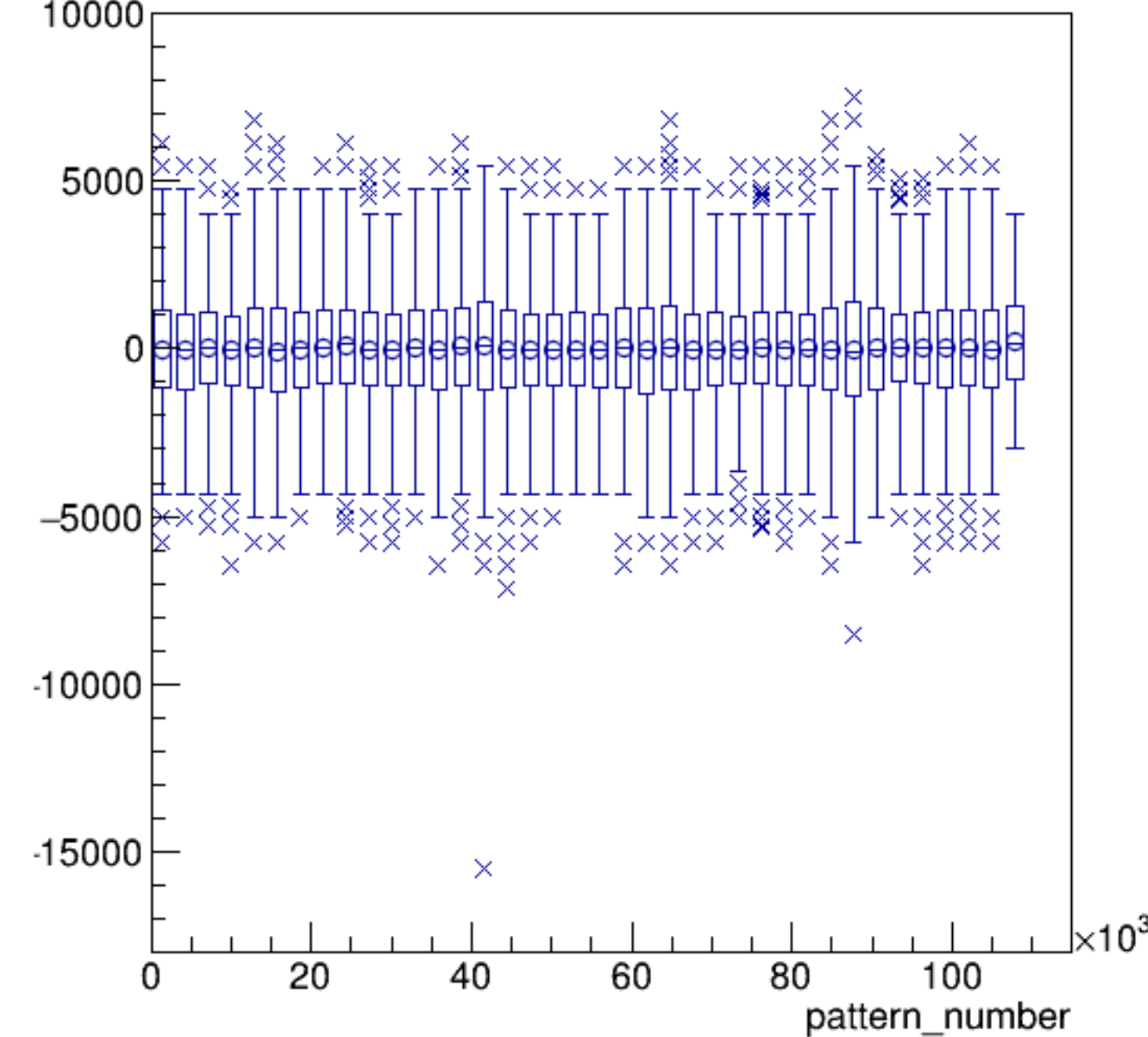


run7398\_000\_sam2.png

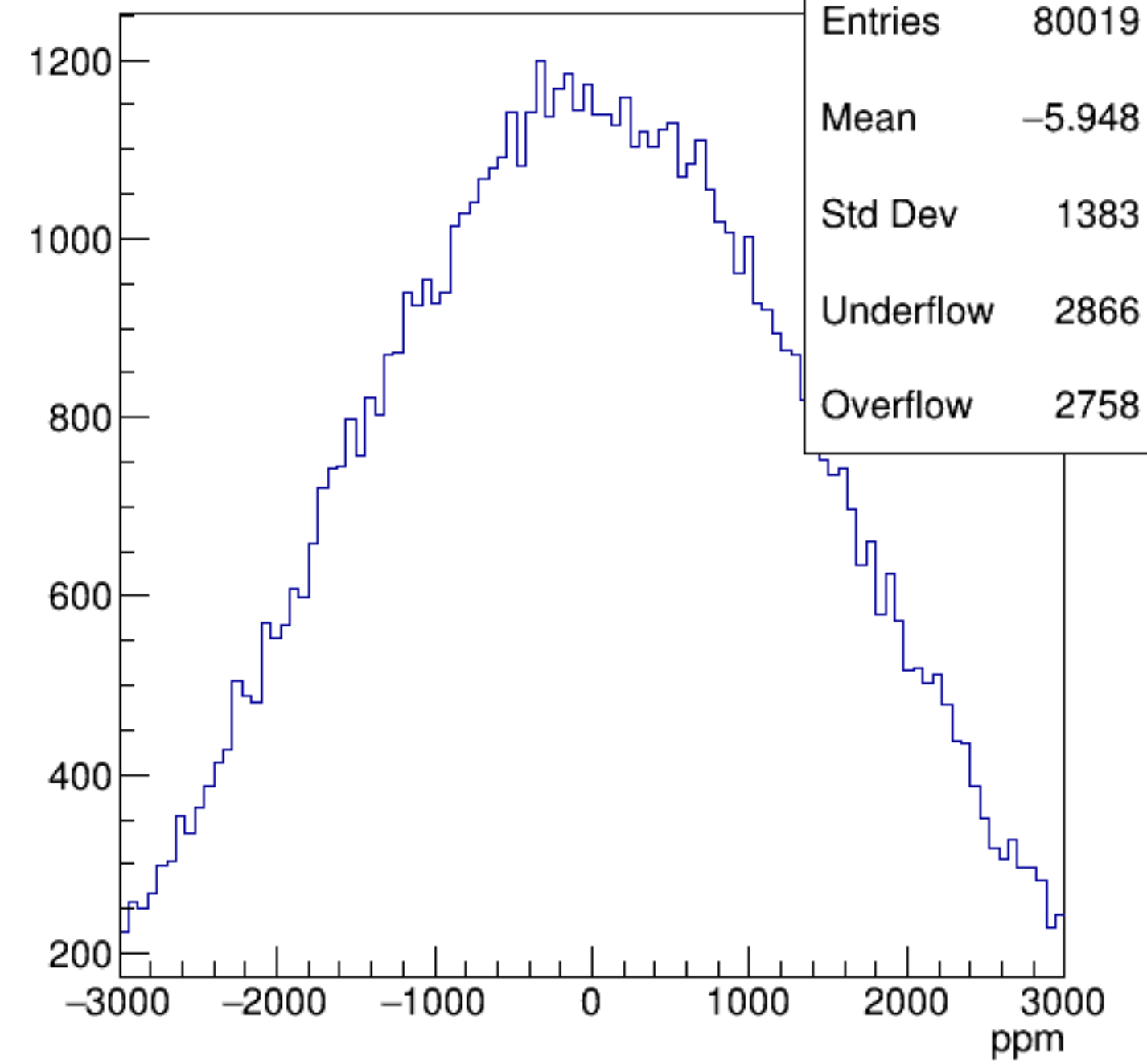
sam2\*CurMon {ErrorFlag==0}



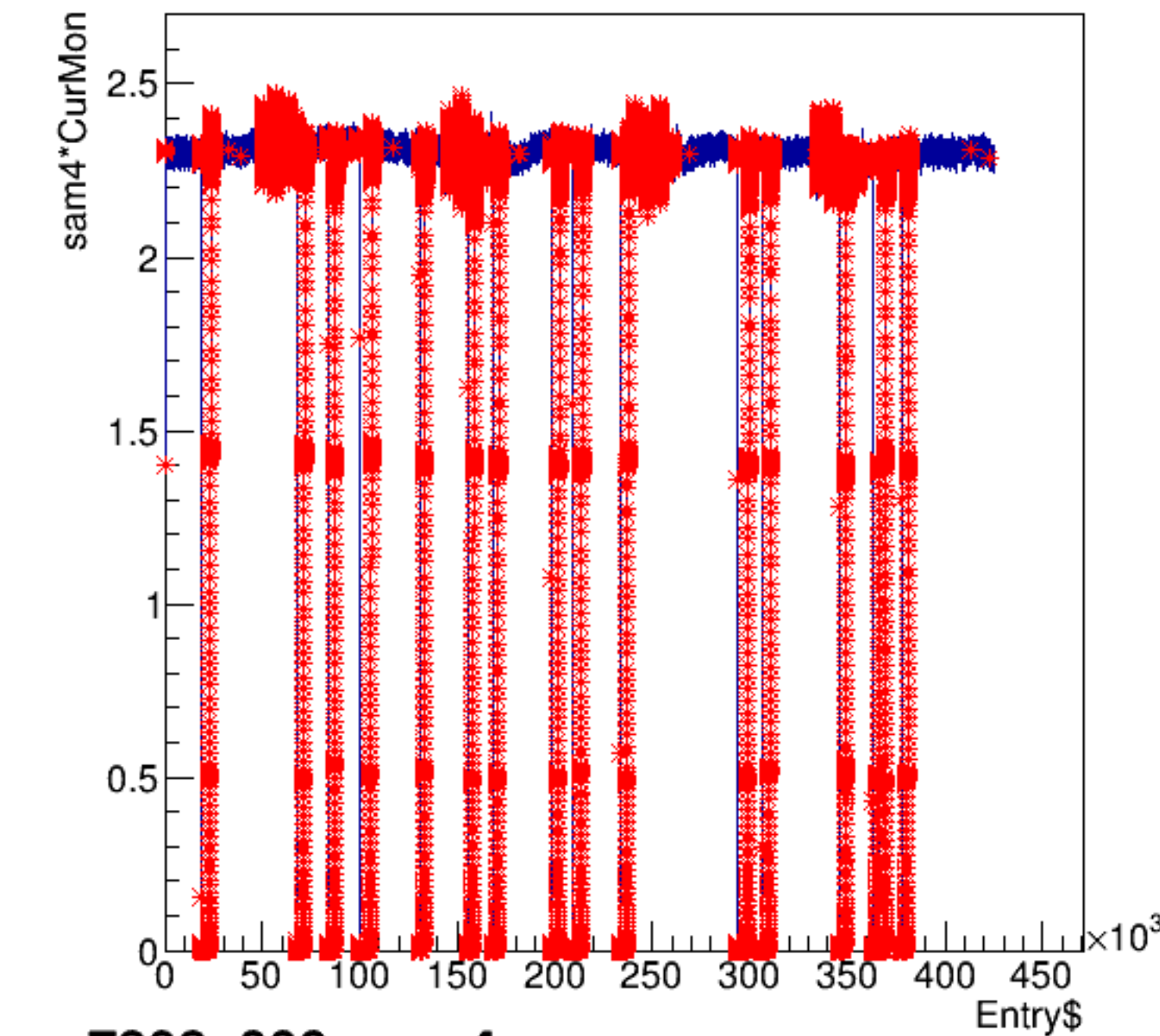
asym\_sam2/ppm:pattern\_number {ErrorFlag==0}



asym\_sam2

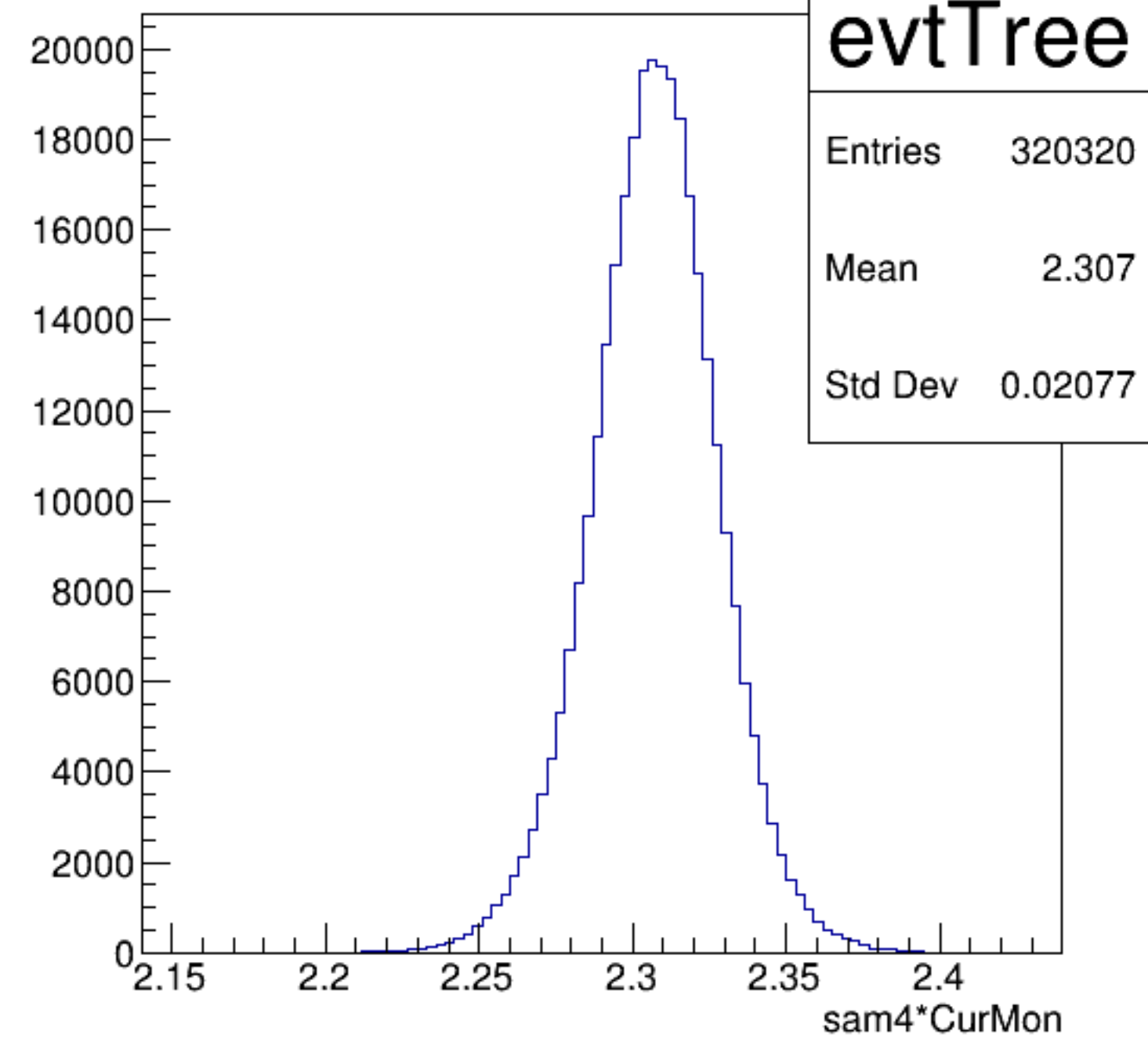


sam4\*CurMon:Entry\$

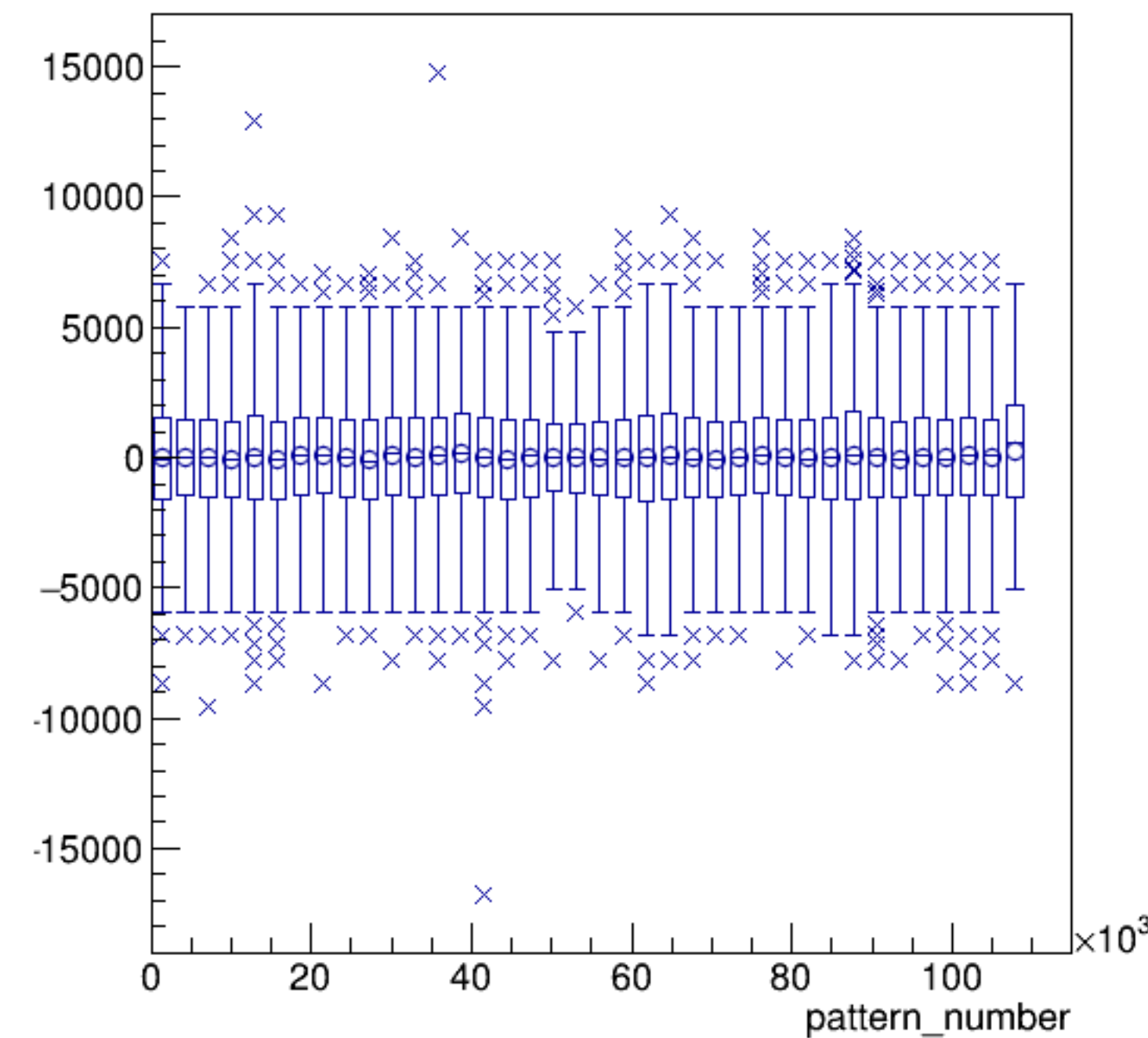


run7398\_000\_sam4.png

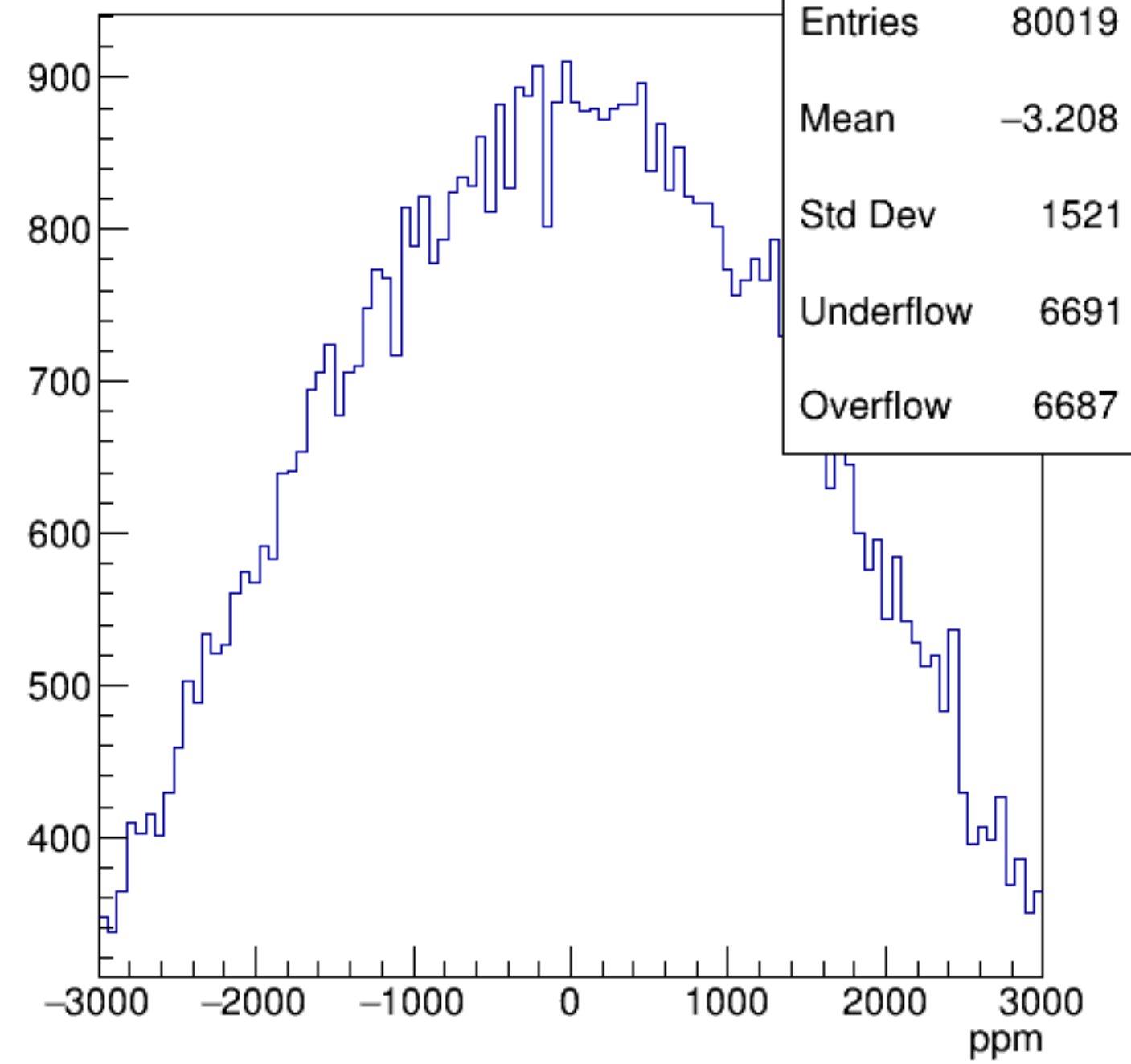
sam4\*CurMon {ErrorFlag==0}



asym\_sam4/ppm:pattern\_number {ErrorFlag==0}

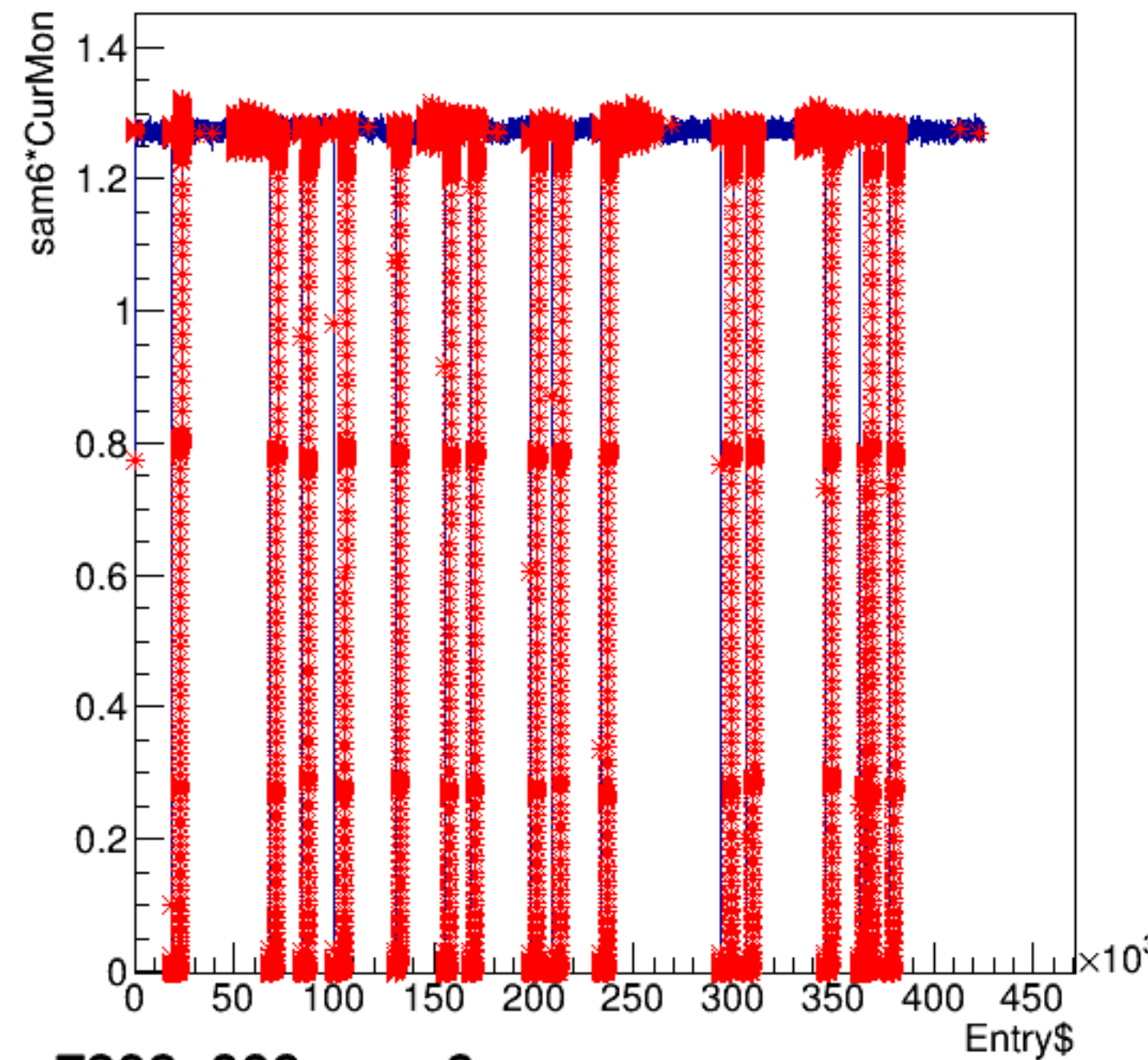


asym\_sam4



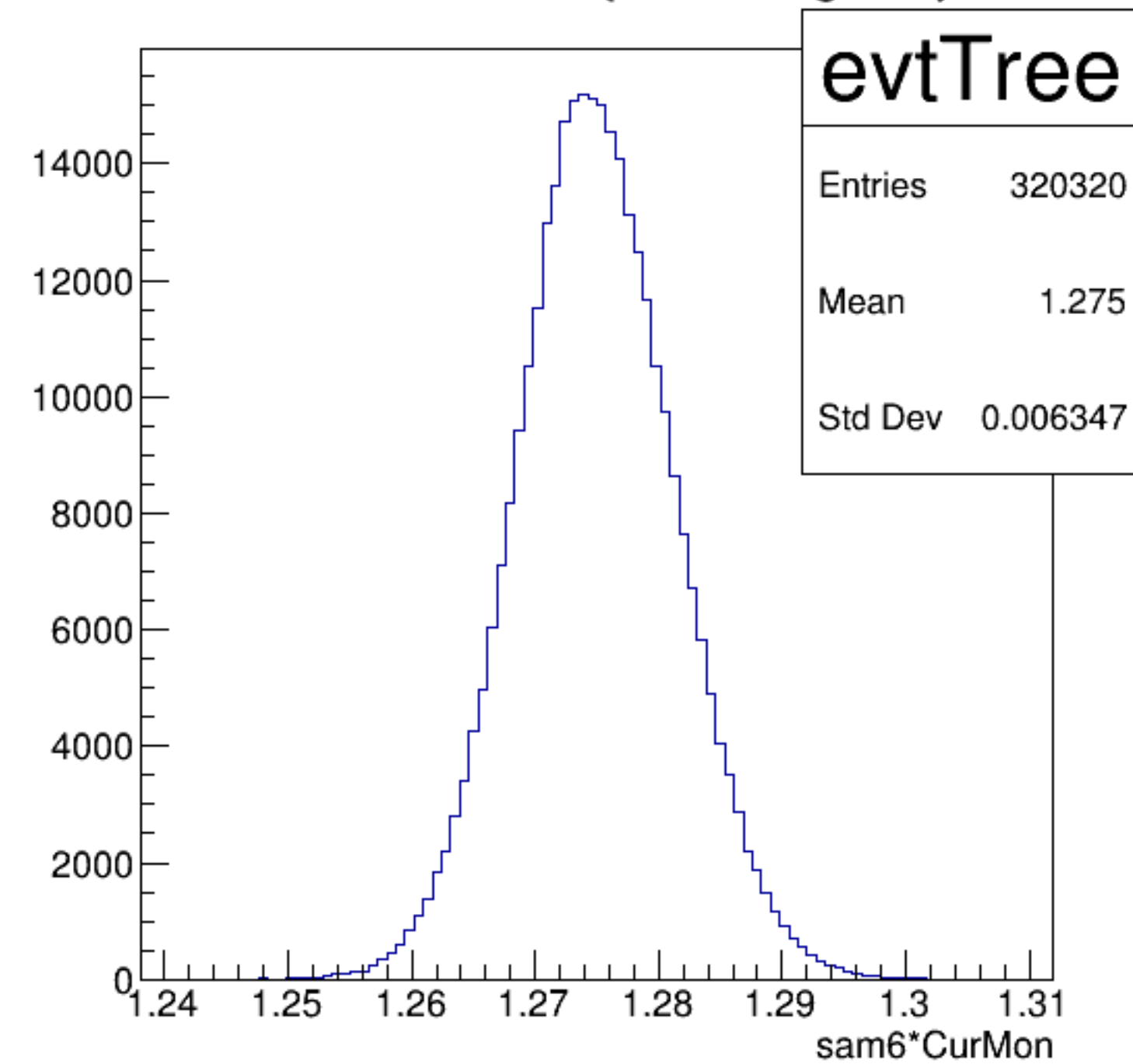


sam6\*CurMon:Entry\$

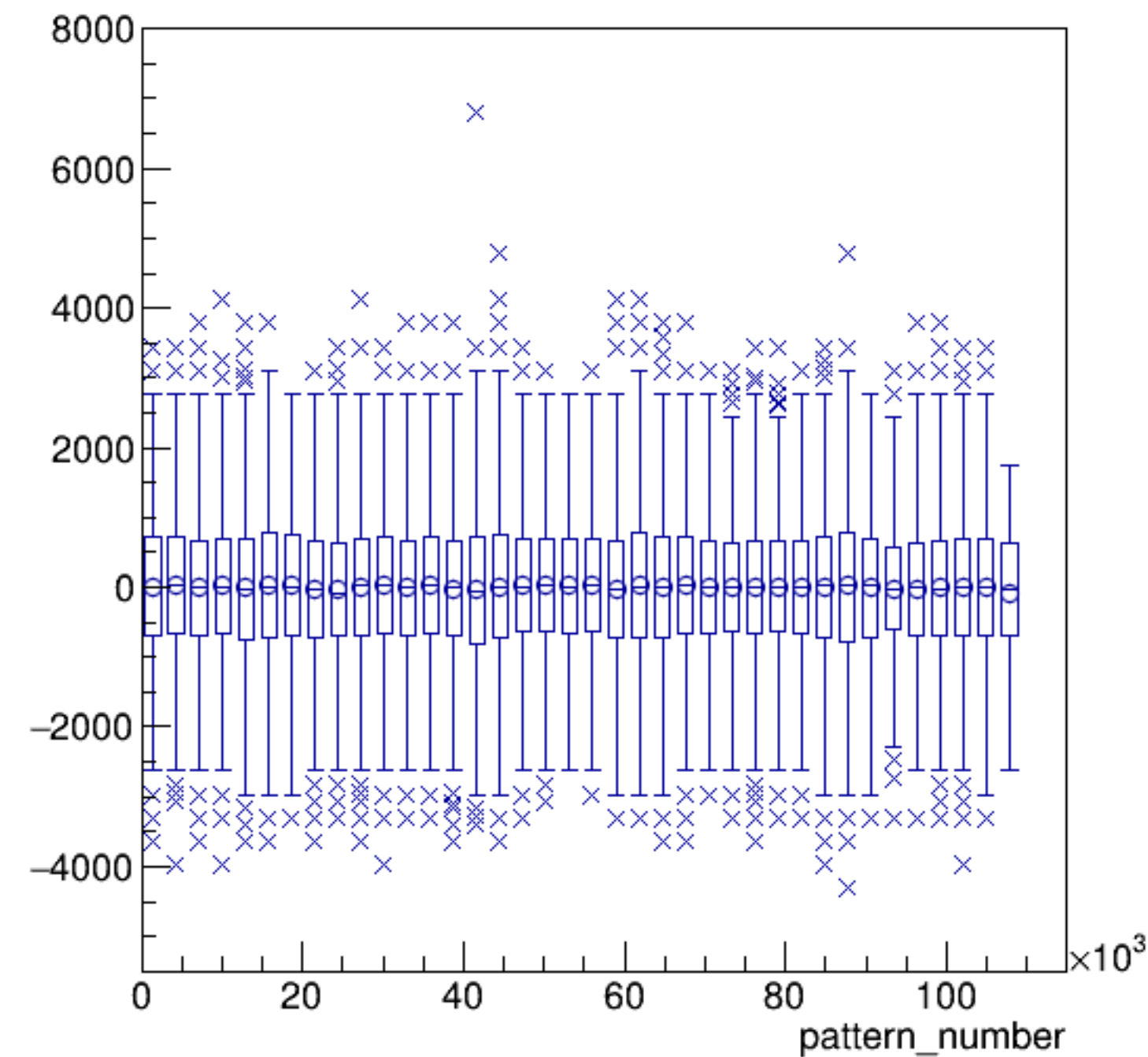


run7398\_000\_sam6.png

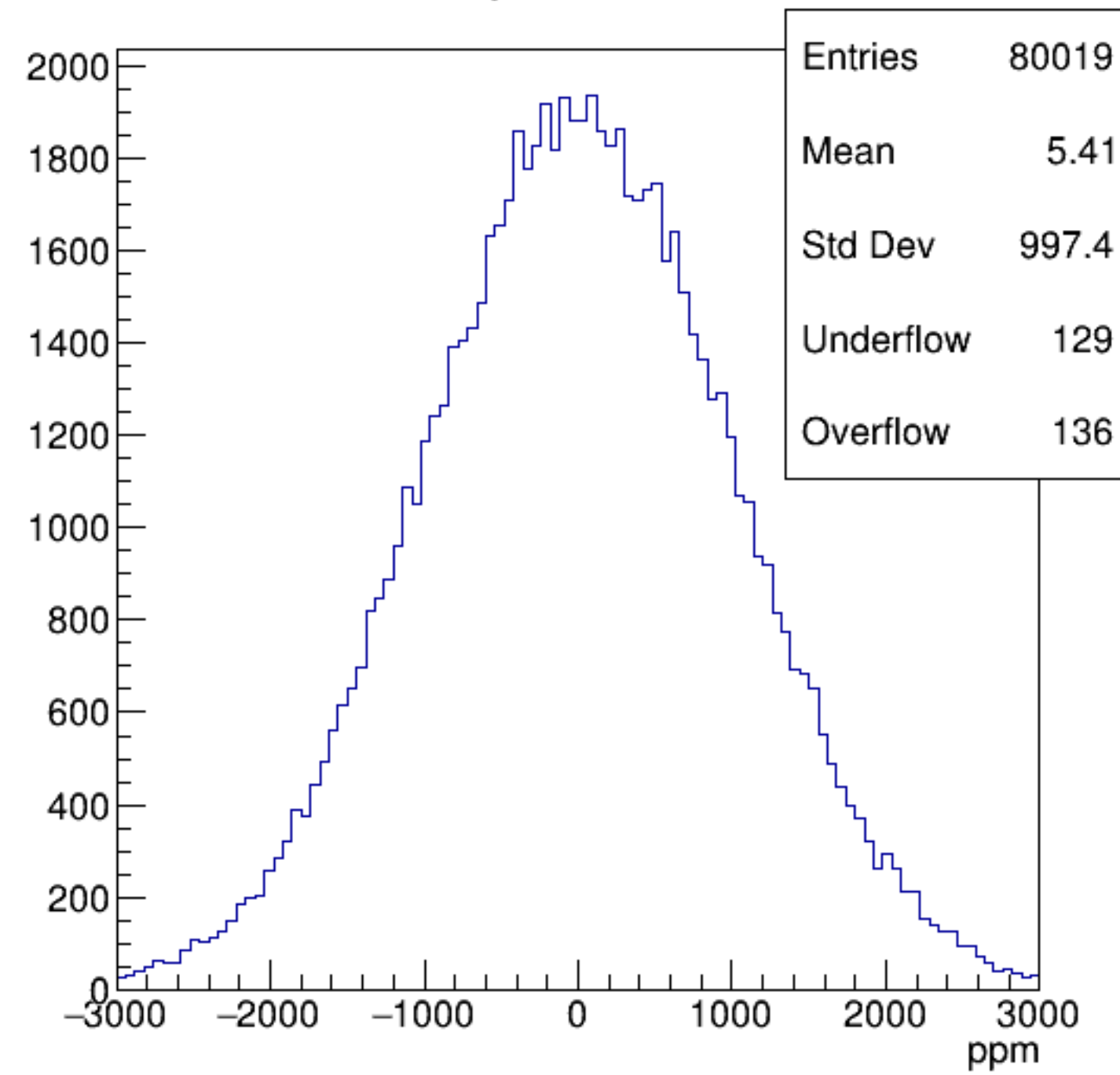
sam6\*CurMon {ErrorFlag==0}



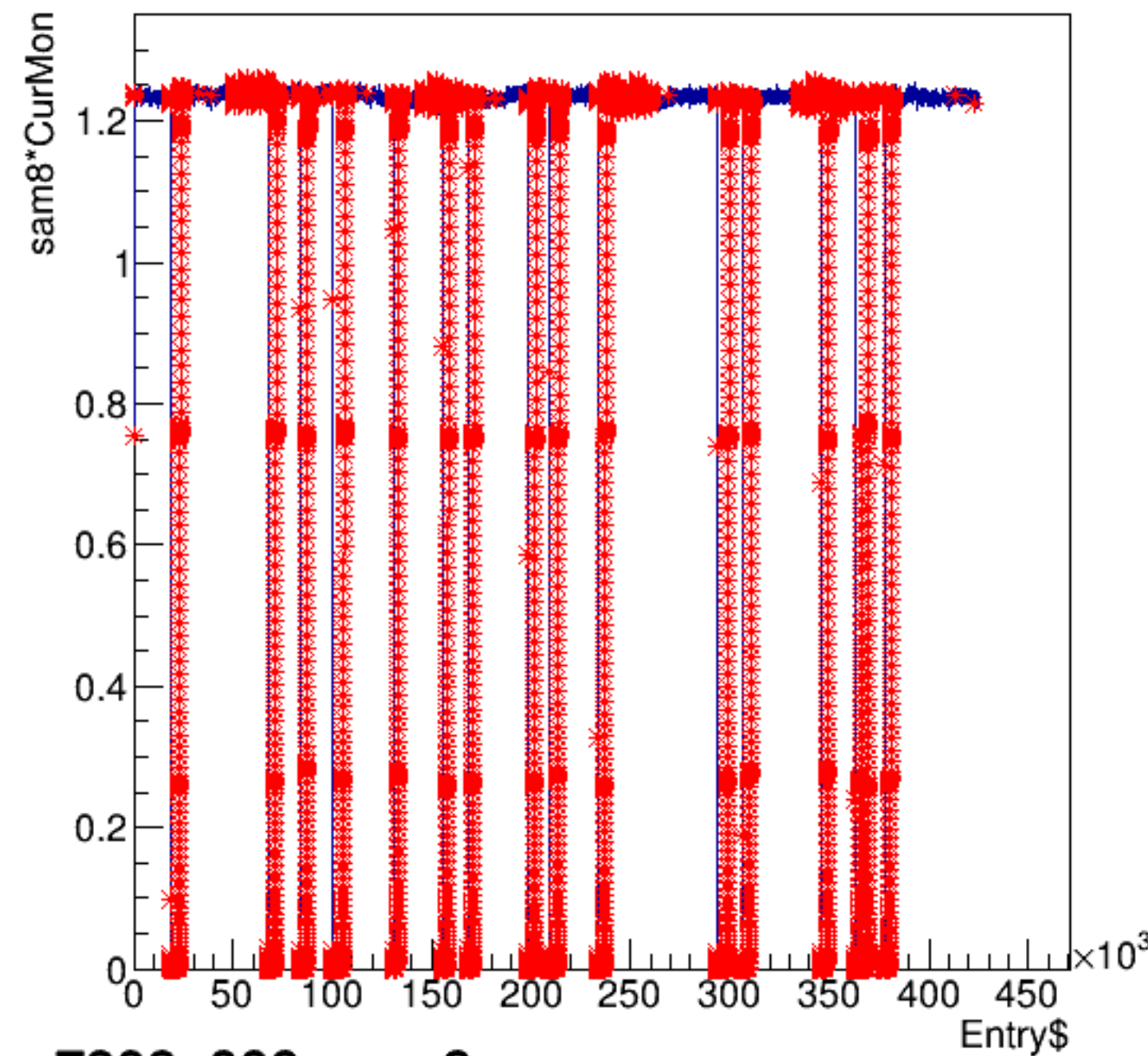
asym\_sam6/ppm:pattern\_number {ErrorFlag==0}



asym\_sam6

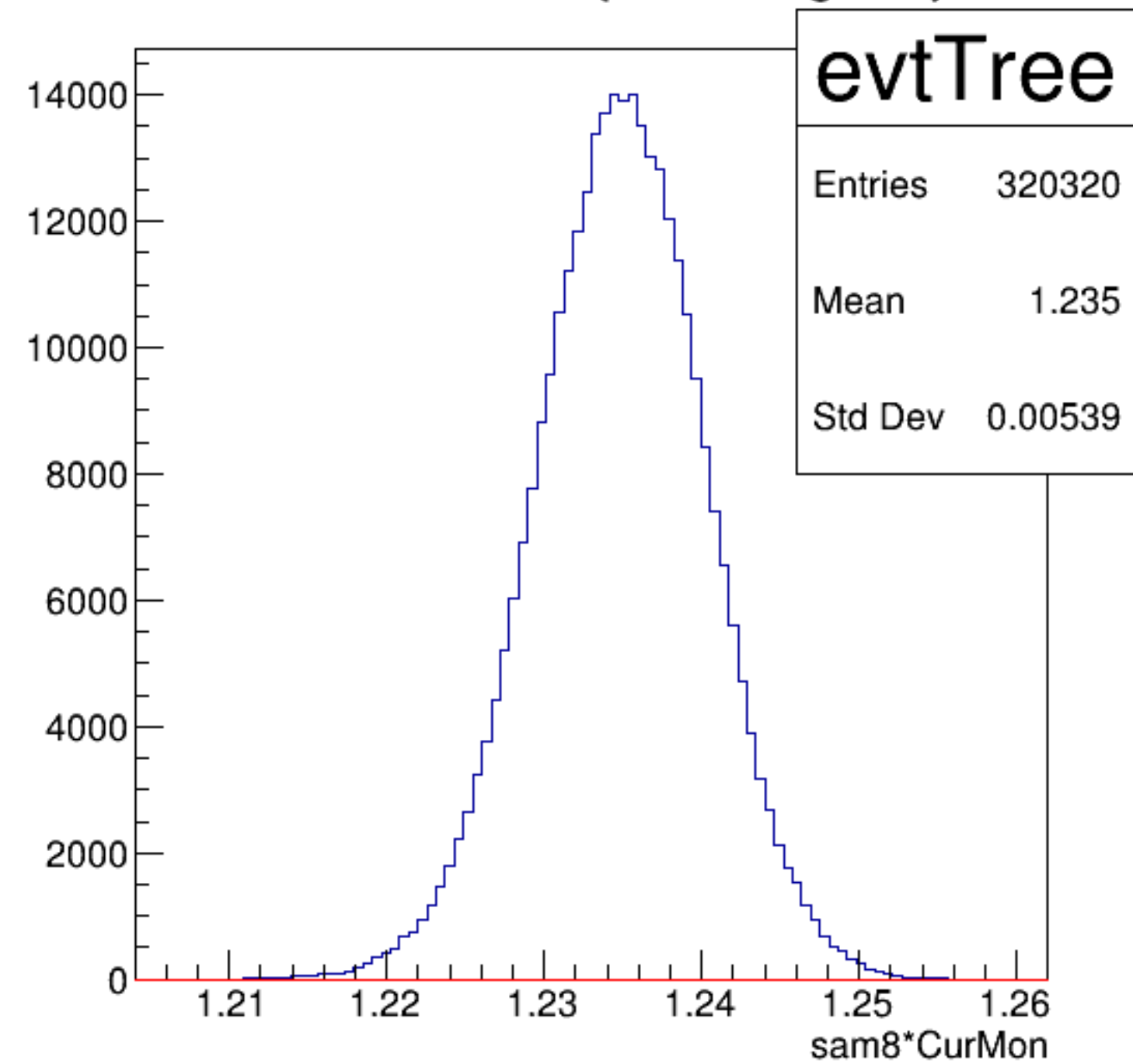


sam8\*CurMon:Entry\$

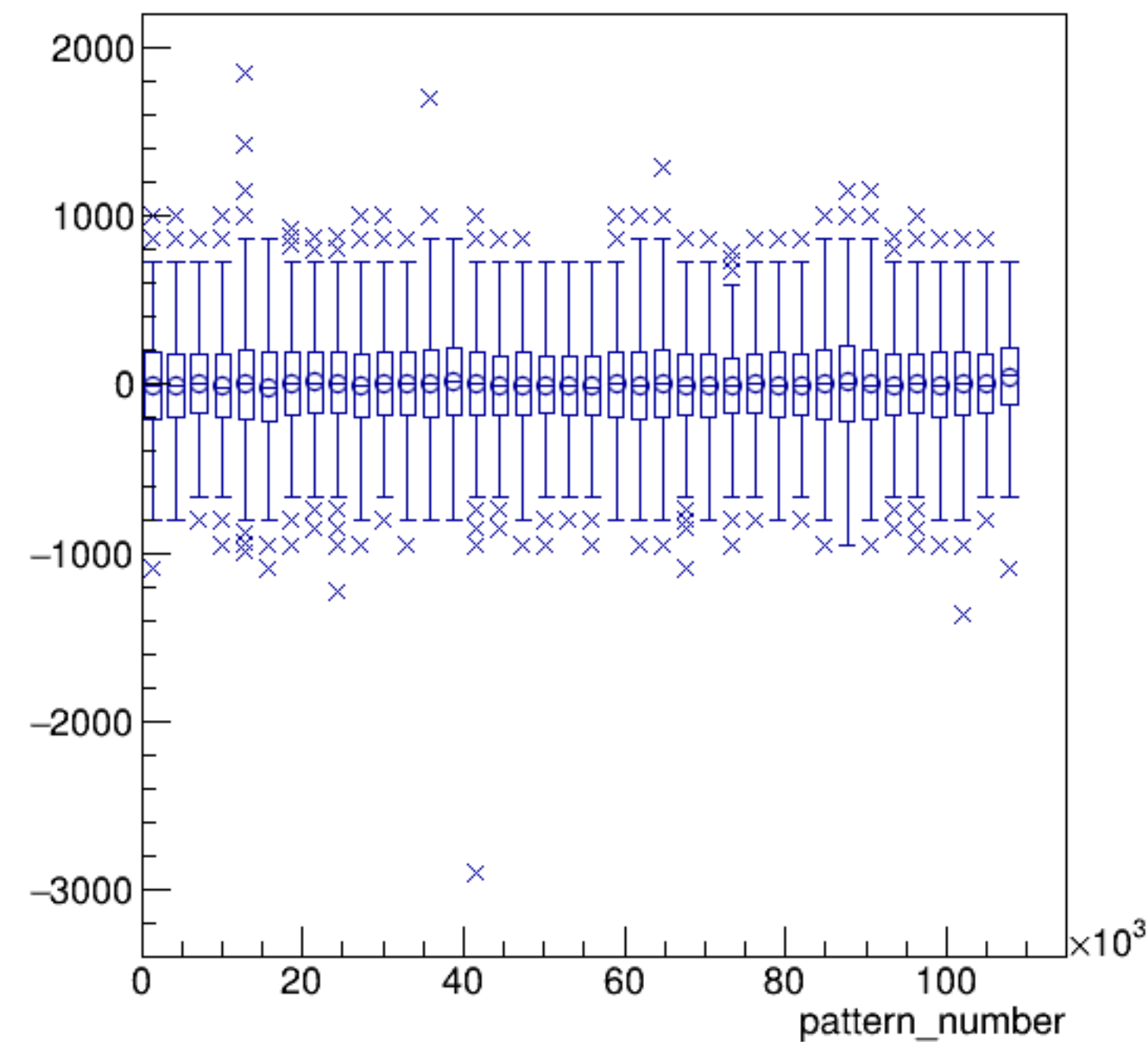


run7398\_000\_sam8.png

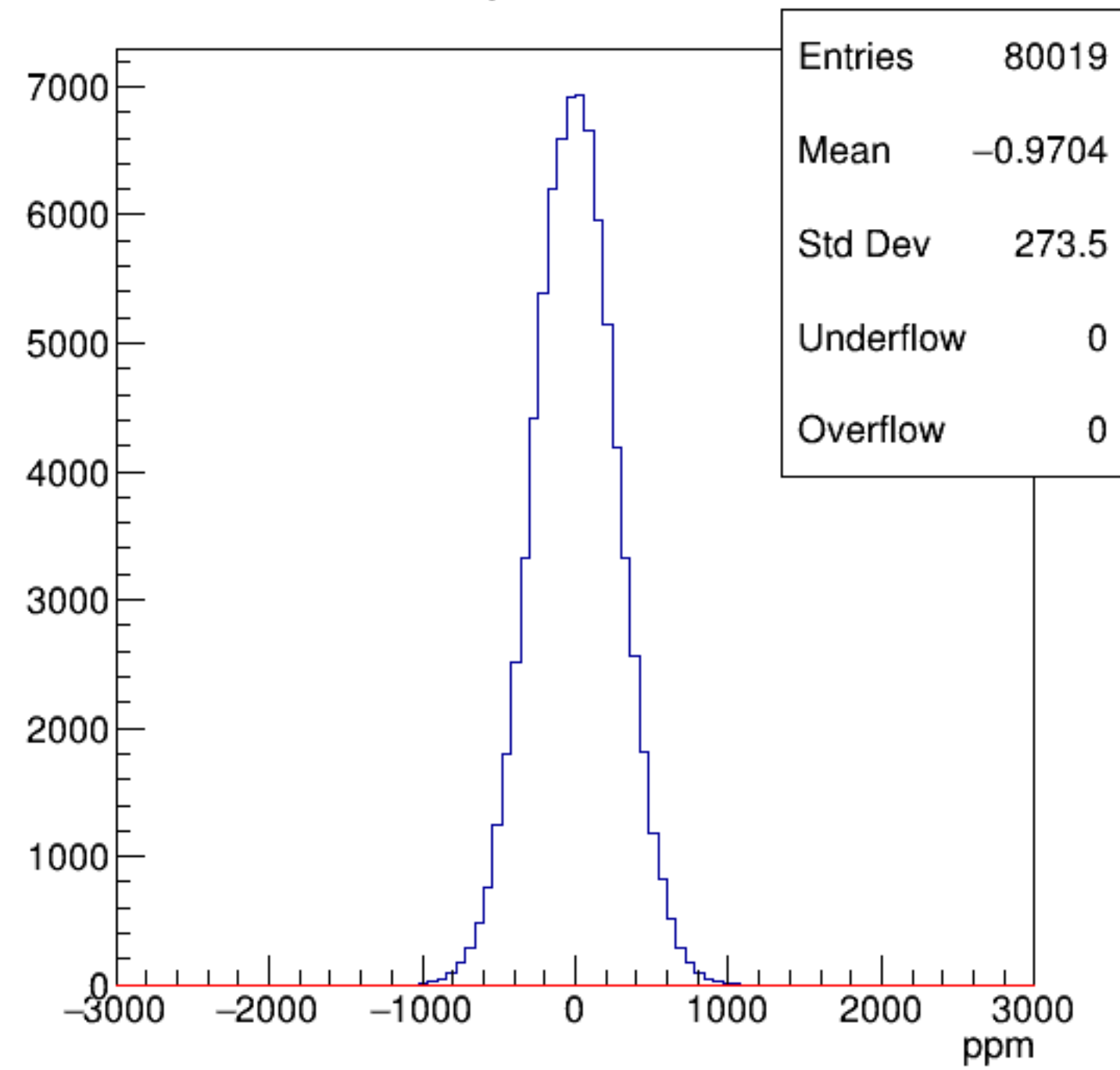
sam8\*CurMon {ErrorFlag==0}



asym\_sam8/ppm:pattern\_number {ErrorFlag==0}

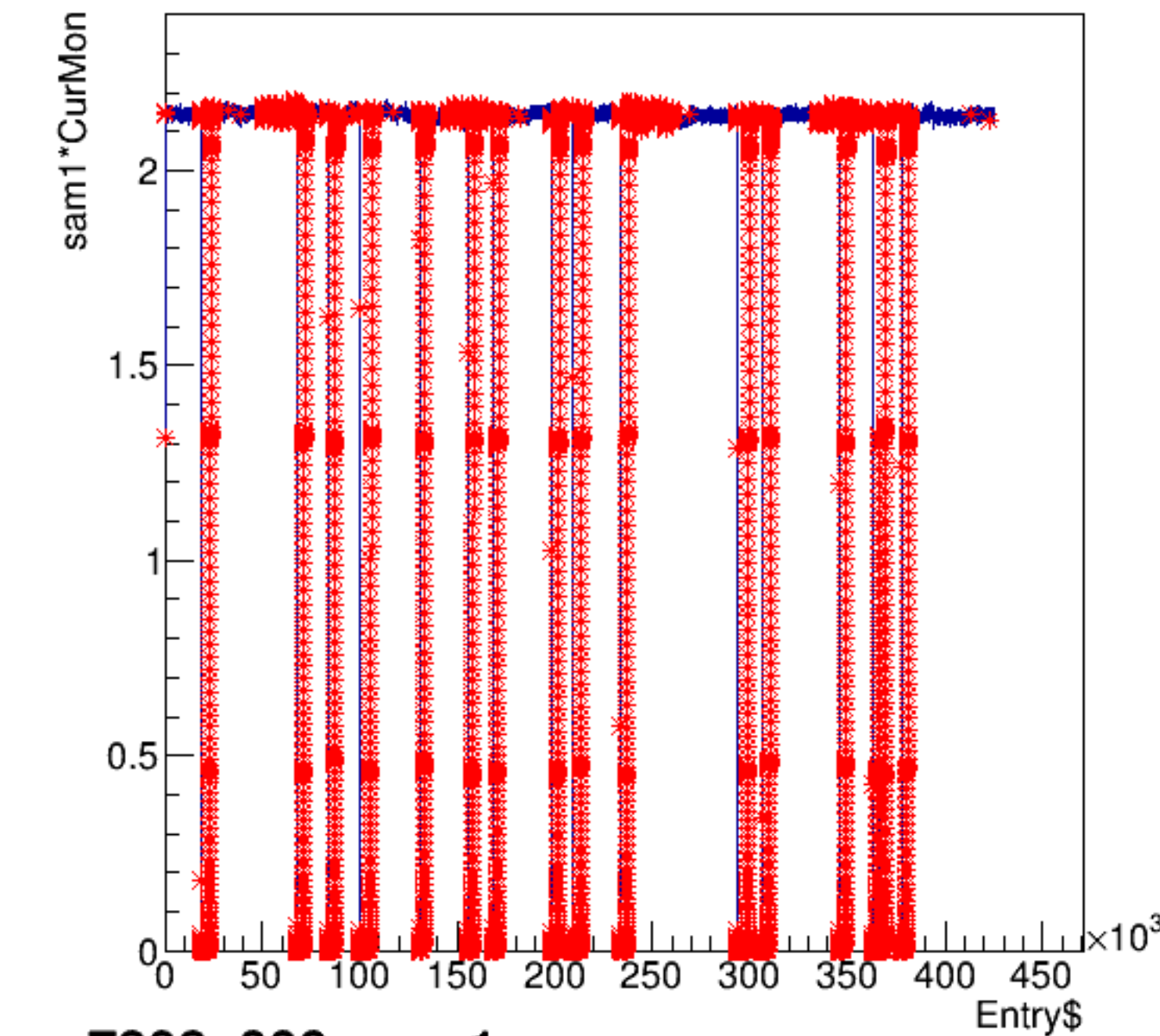


asym\_sam8



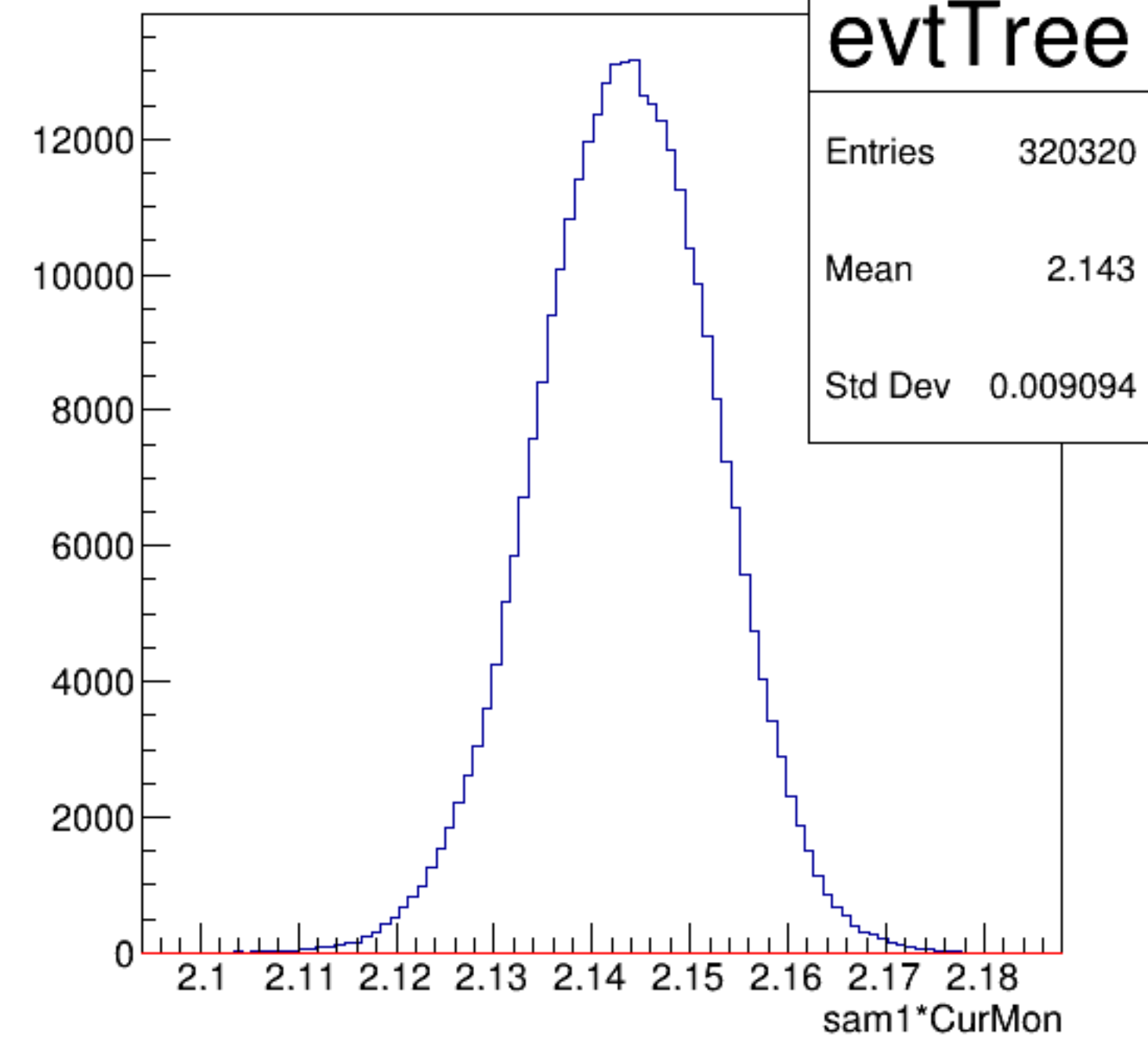


sam1\*CurMon:Entry\$

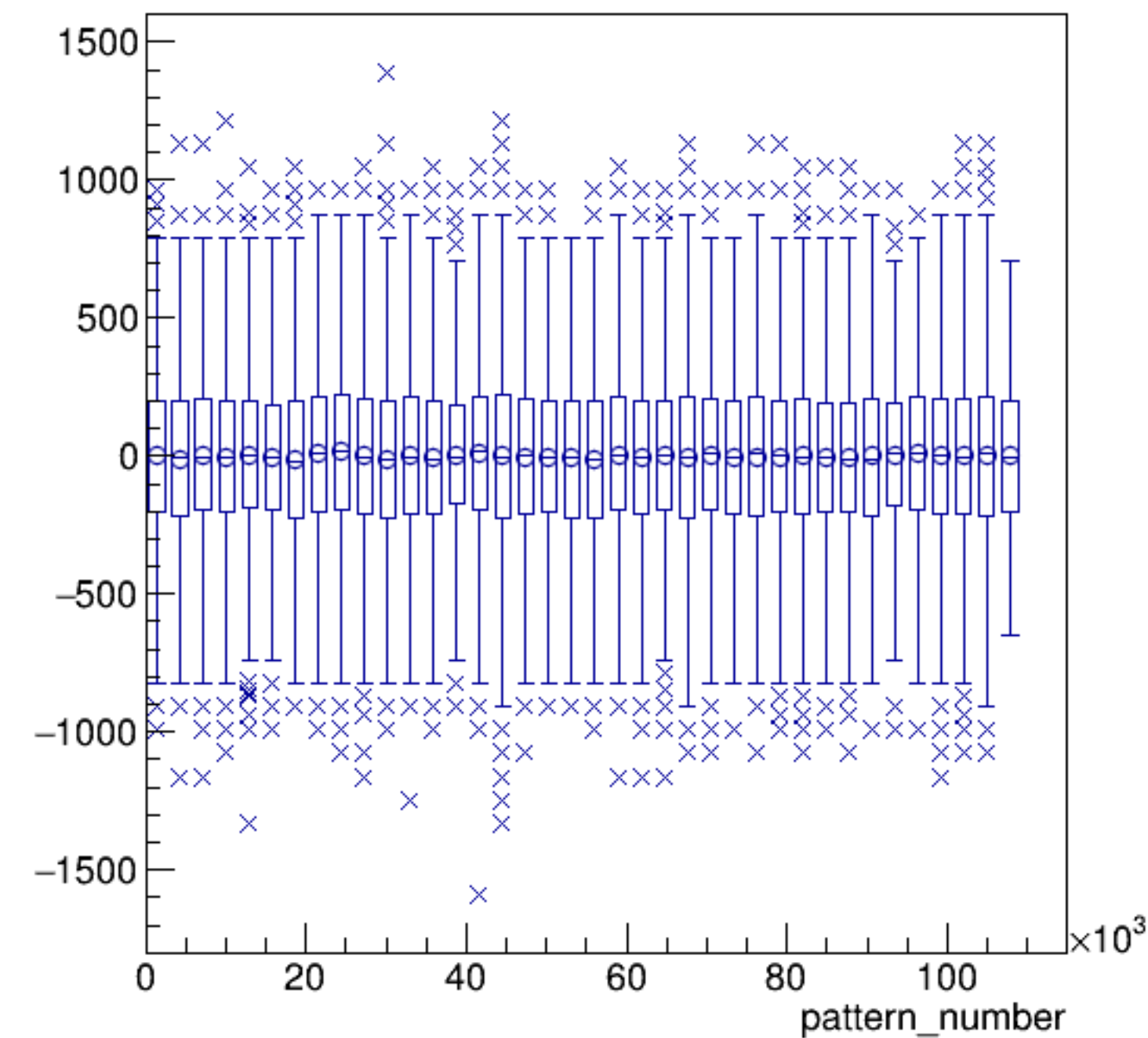


run7398\_000\_sam1.png

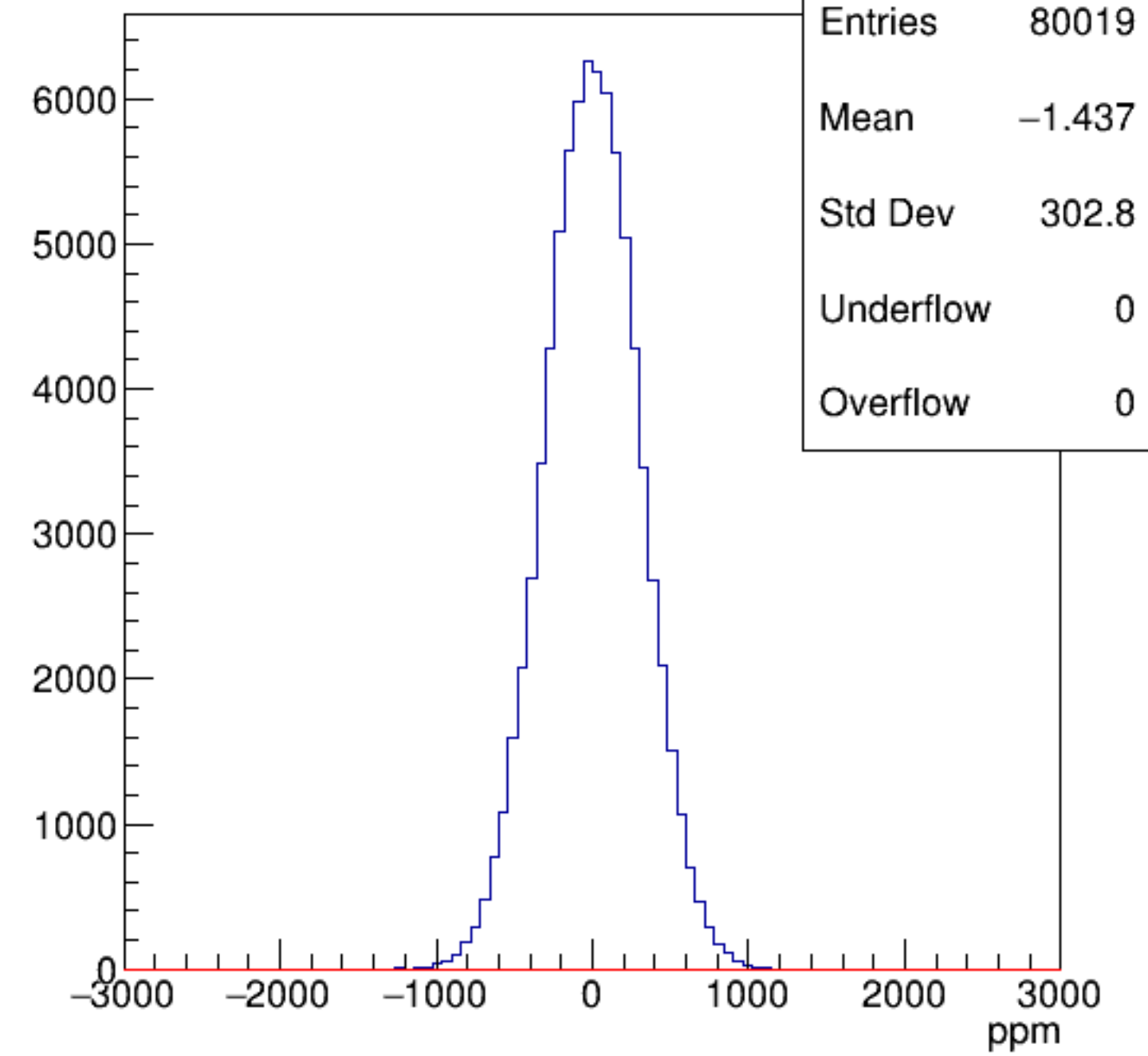
sam1\*CurMon {ErrorFlag==0}



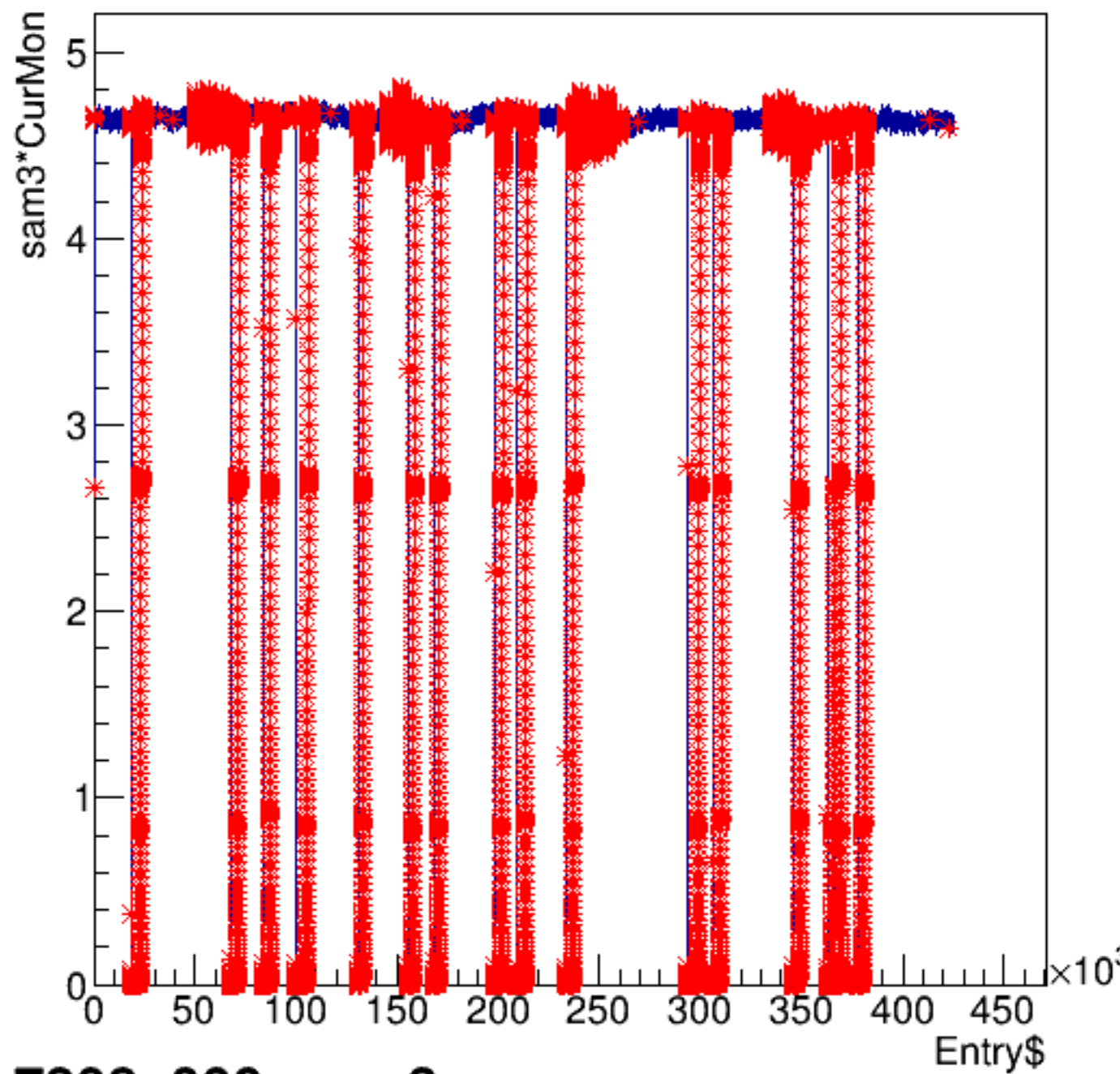
asym\_sam1/ppm:pattern\_number {ErrorFlag==0}



asym\_sam1

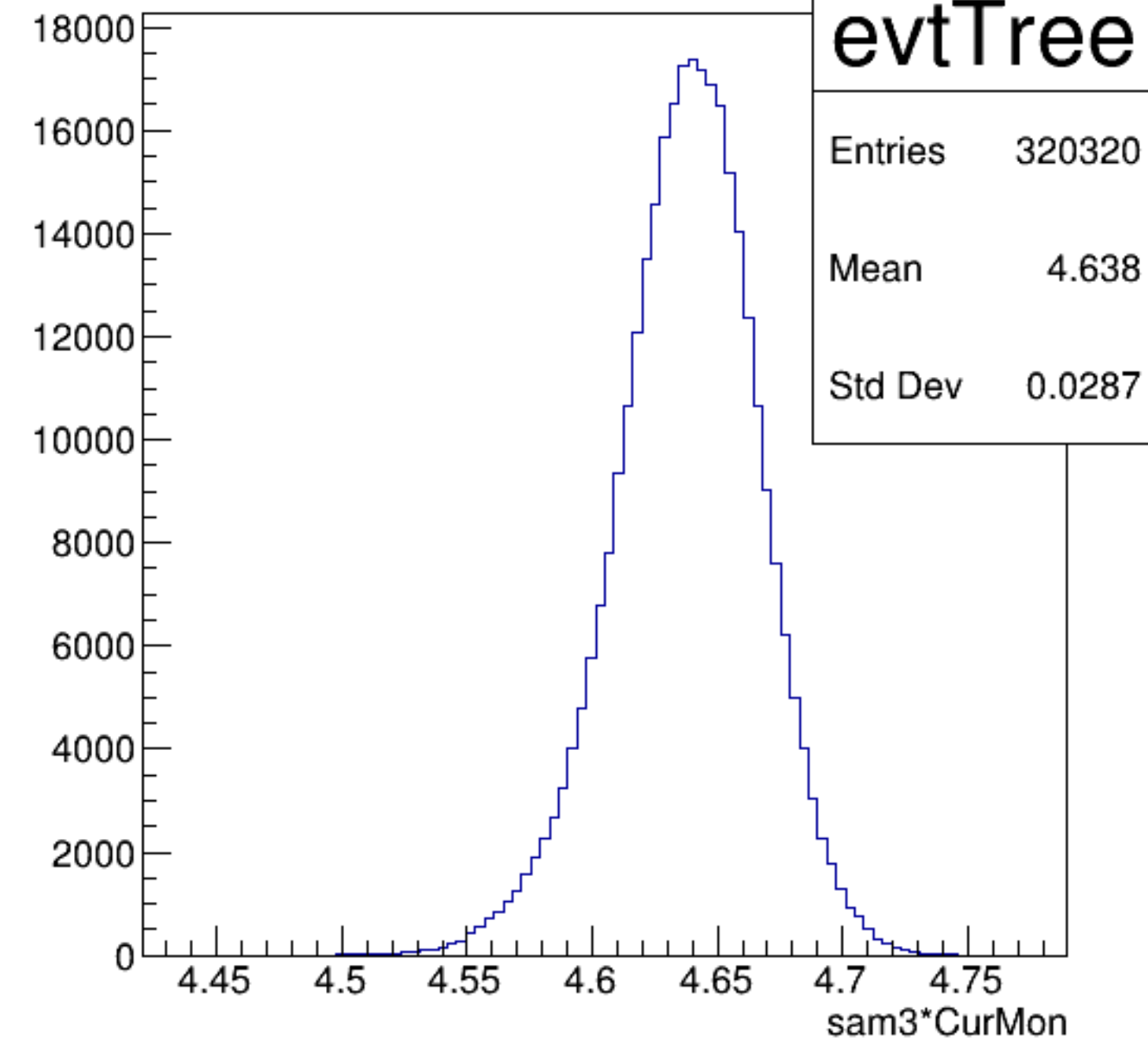


sam3\*CurMon:Entry\$

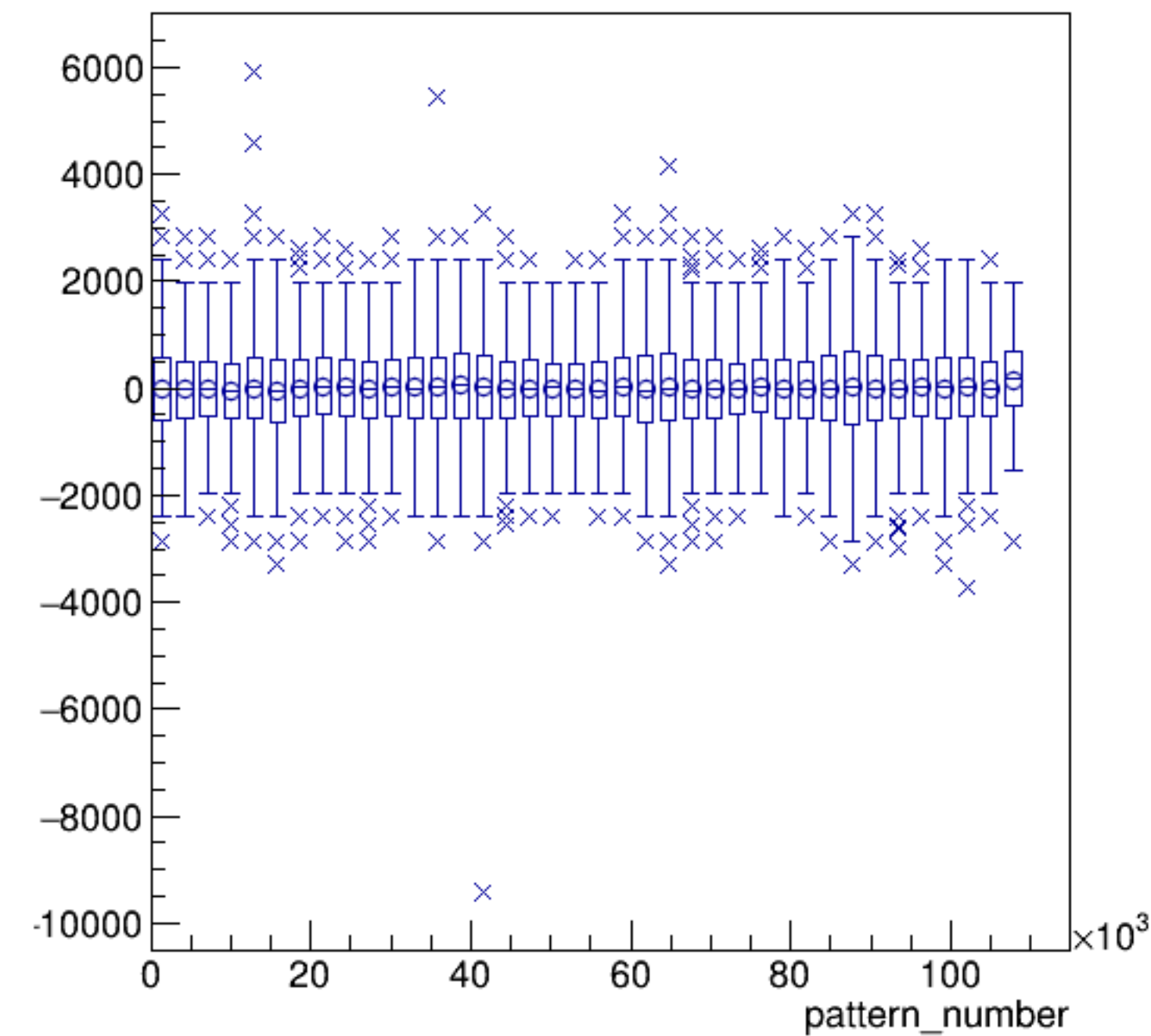


run7398\_000\_sam3.png

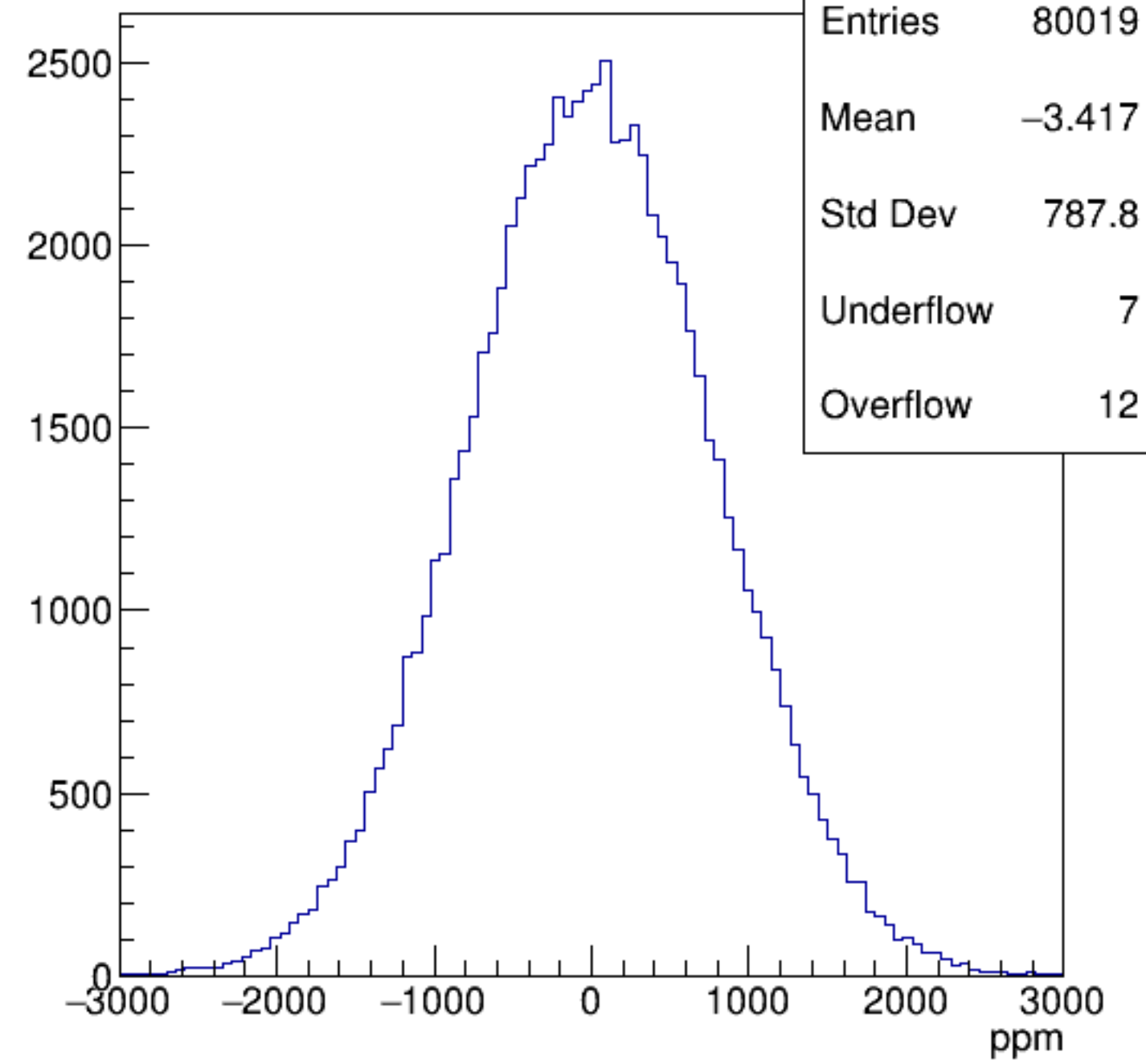
sam3\*CurMon {ErrorFlag==0}



asym\_sam3/ppm:pattern\_number {ErrorFlag==0}

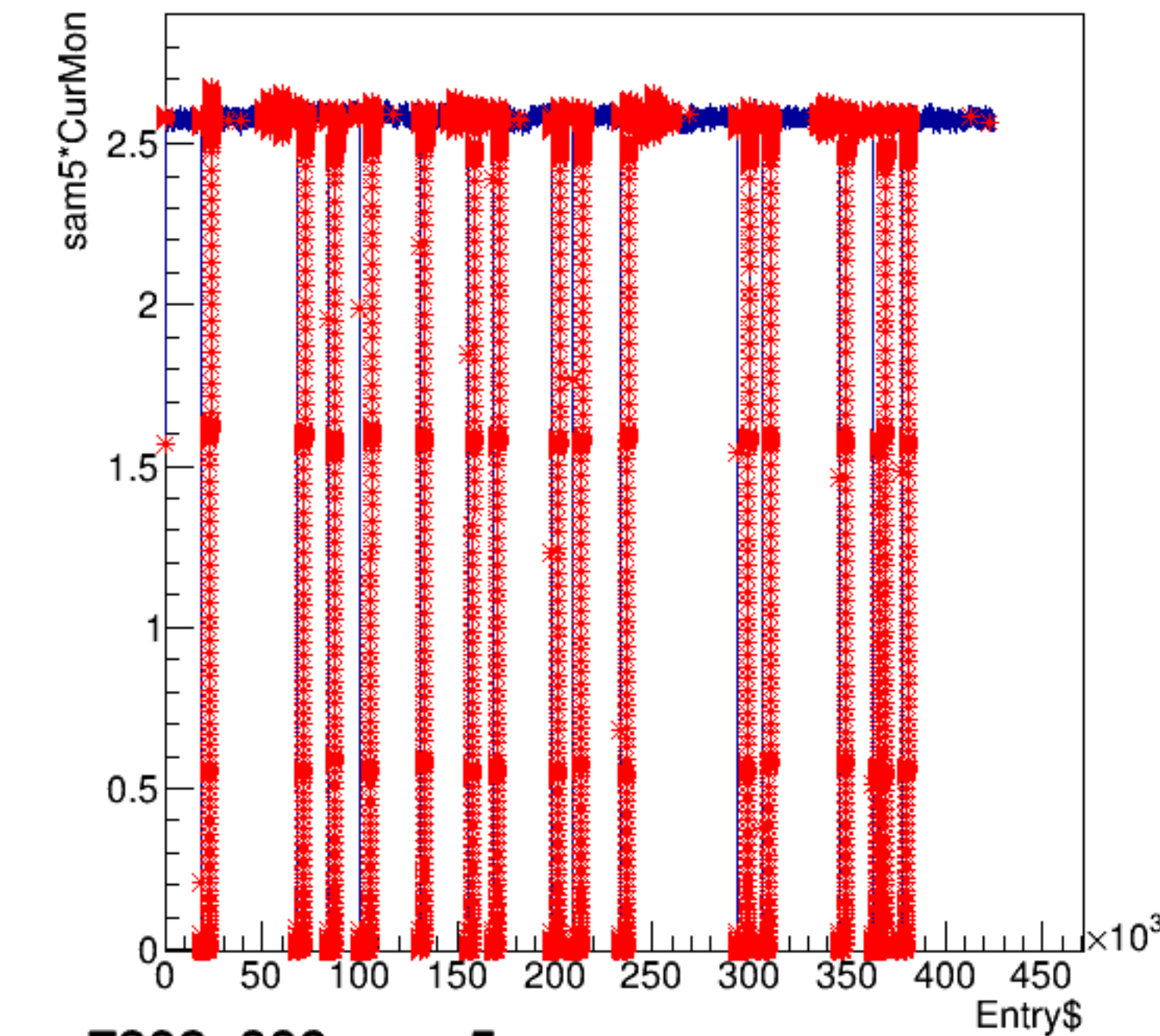


asym\_sam3



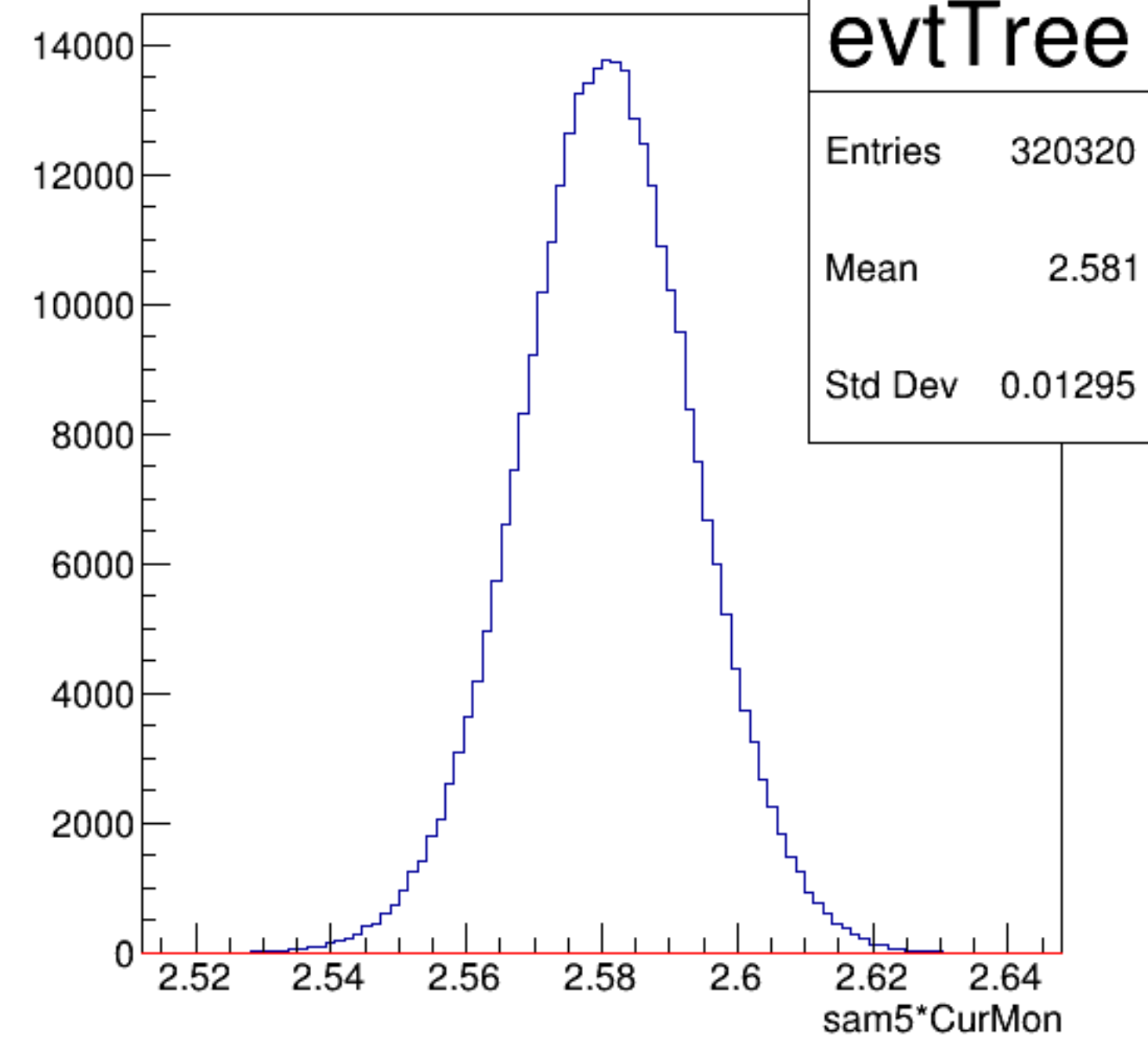


sam5\*CurMon:Entry\$

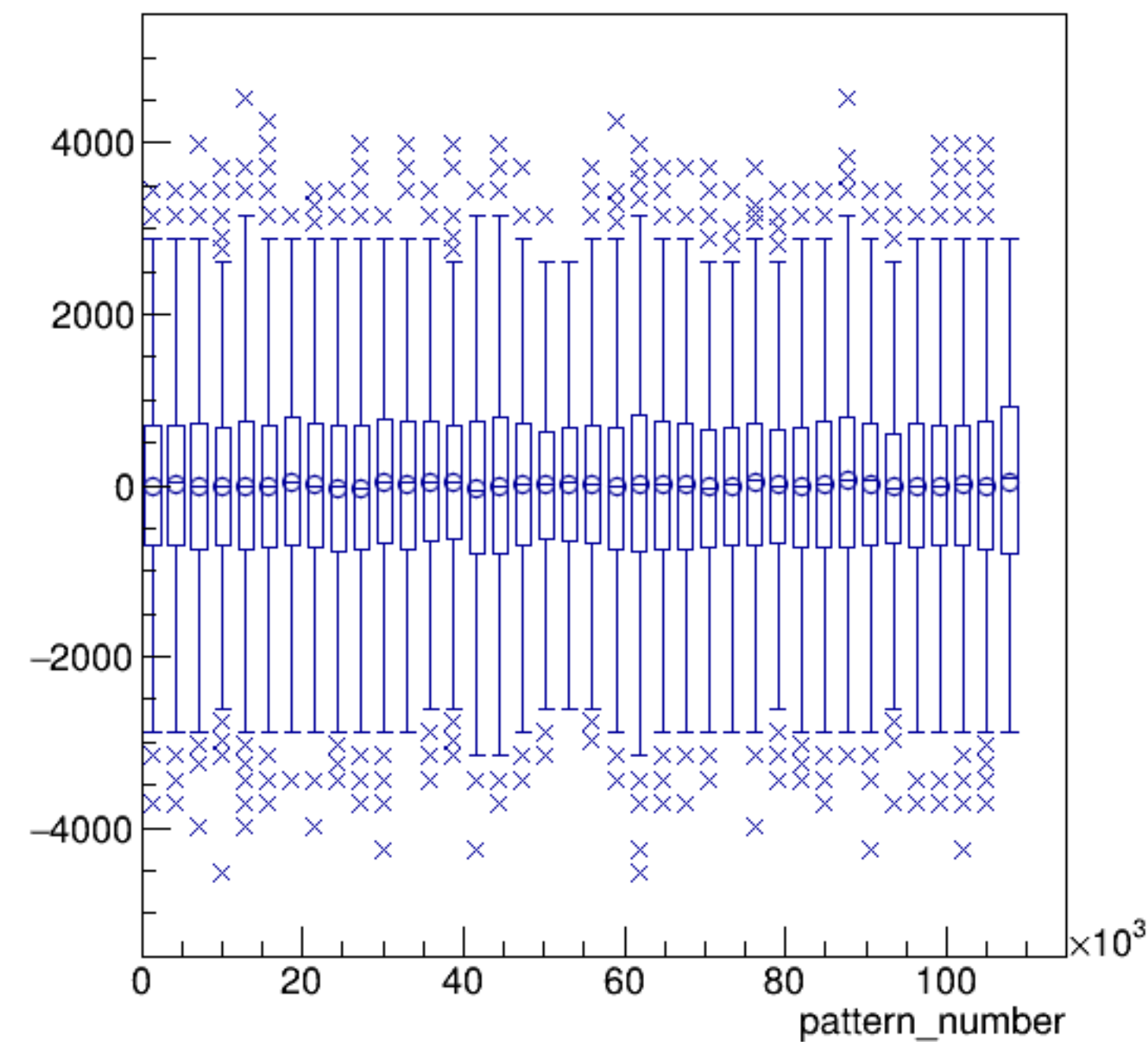


run7398\_000\_sam5.png

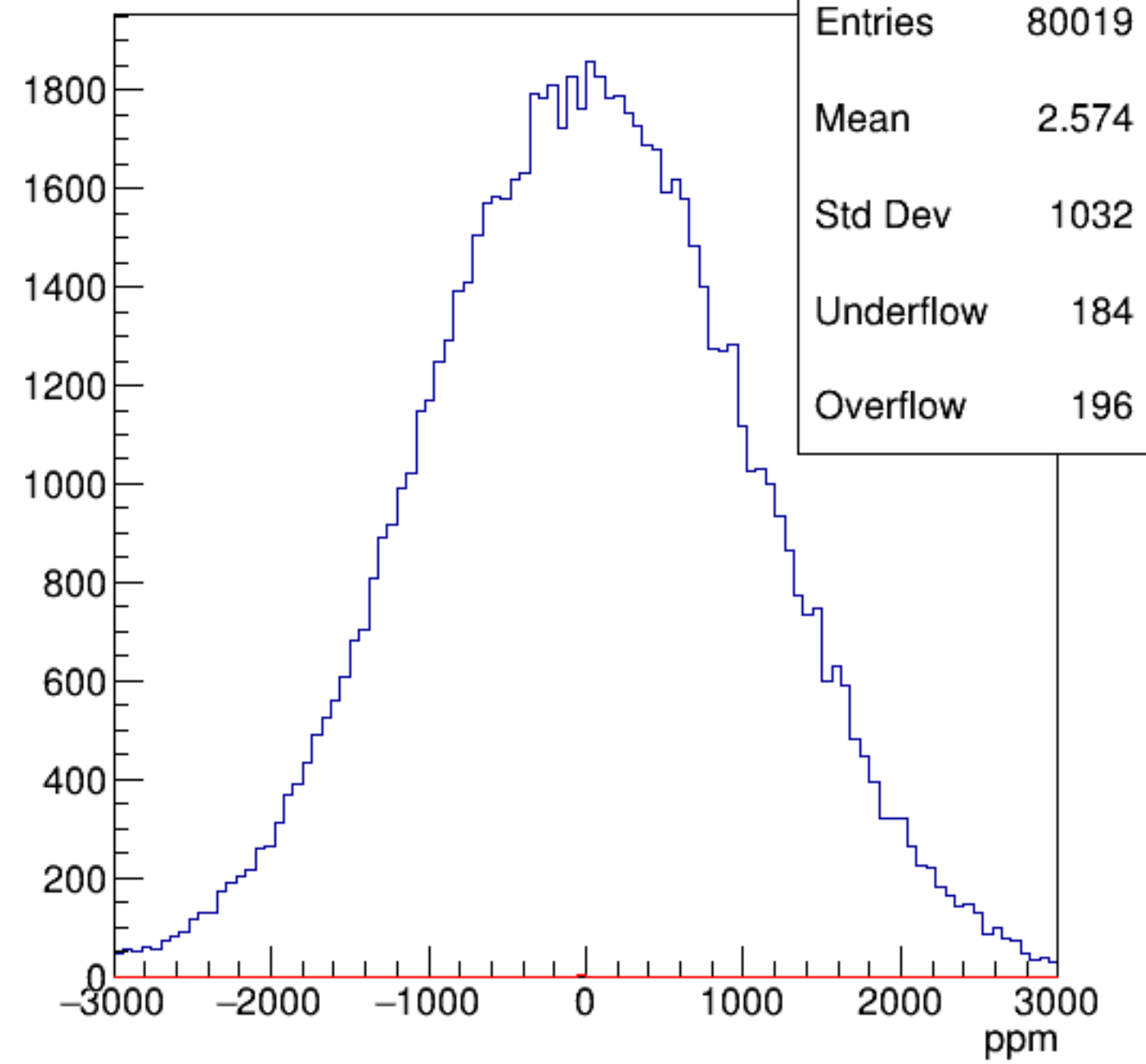
sam5\*CurMon {ErrorFlag==0}



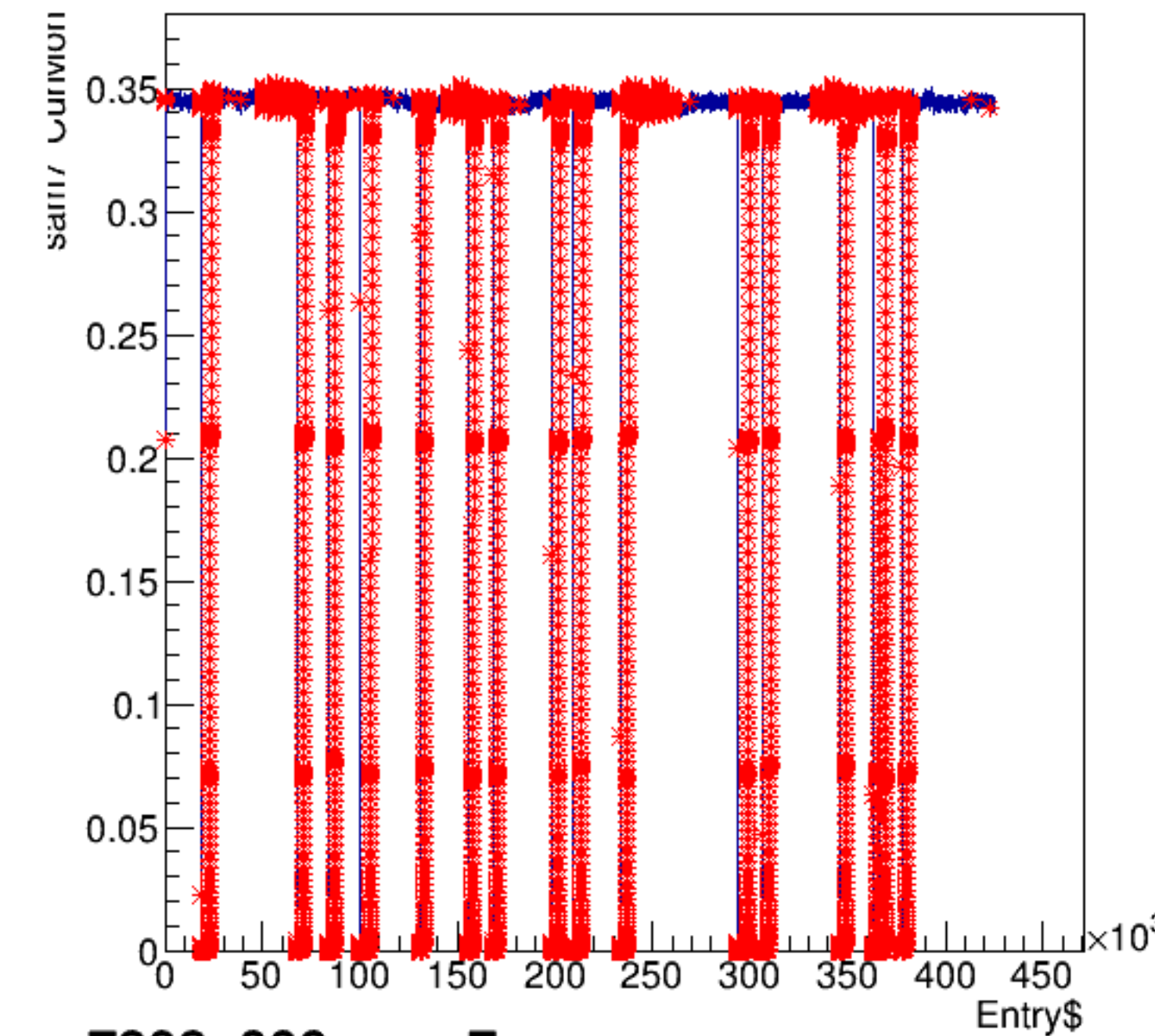
asym\_sam5/ppm:pattern\_number {ErrorFlag==0}



asym\_sam5

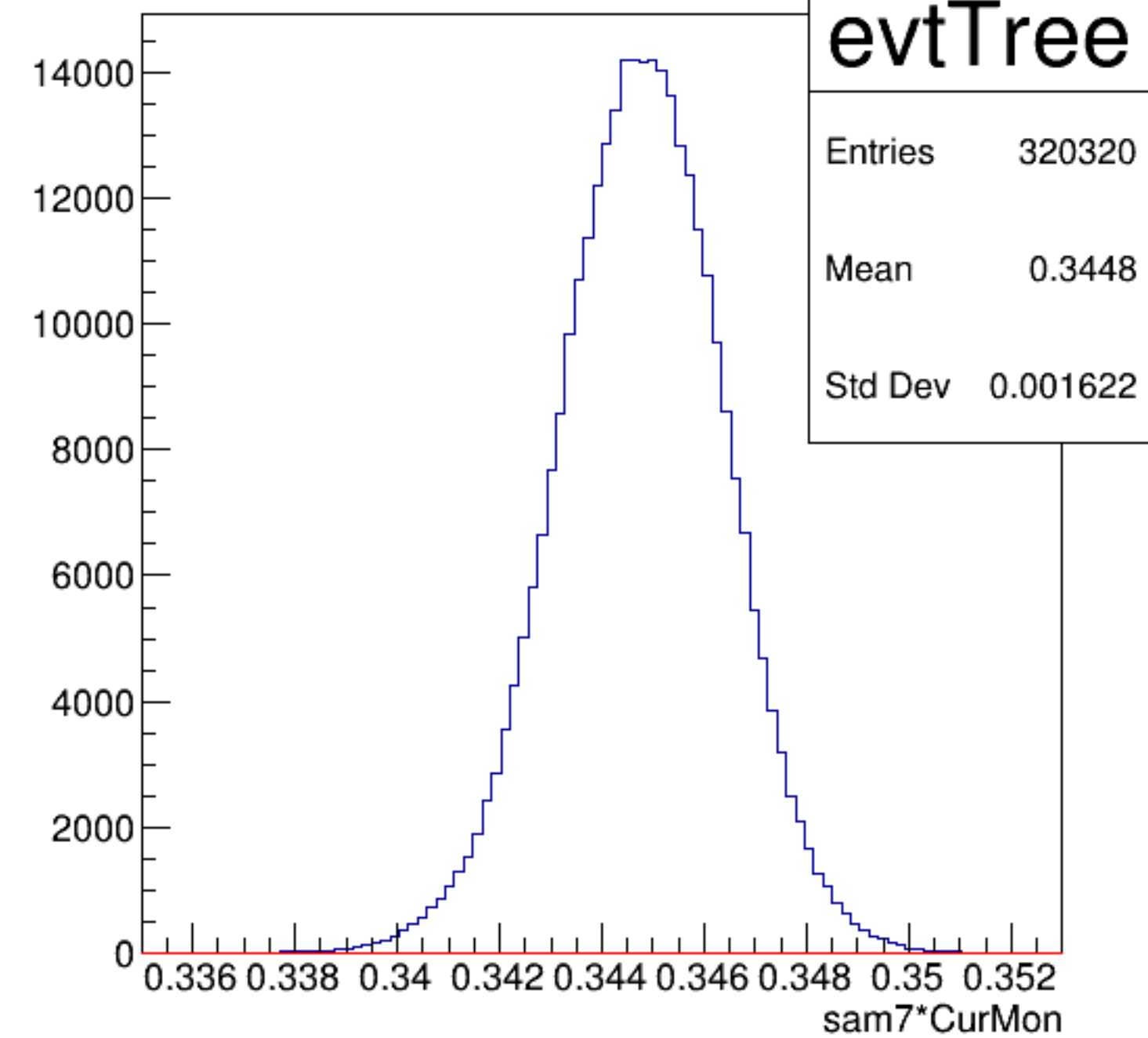


sam7\*CurMon:Entry\$



run7398\_000\_sam7.png

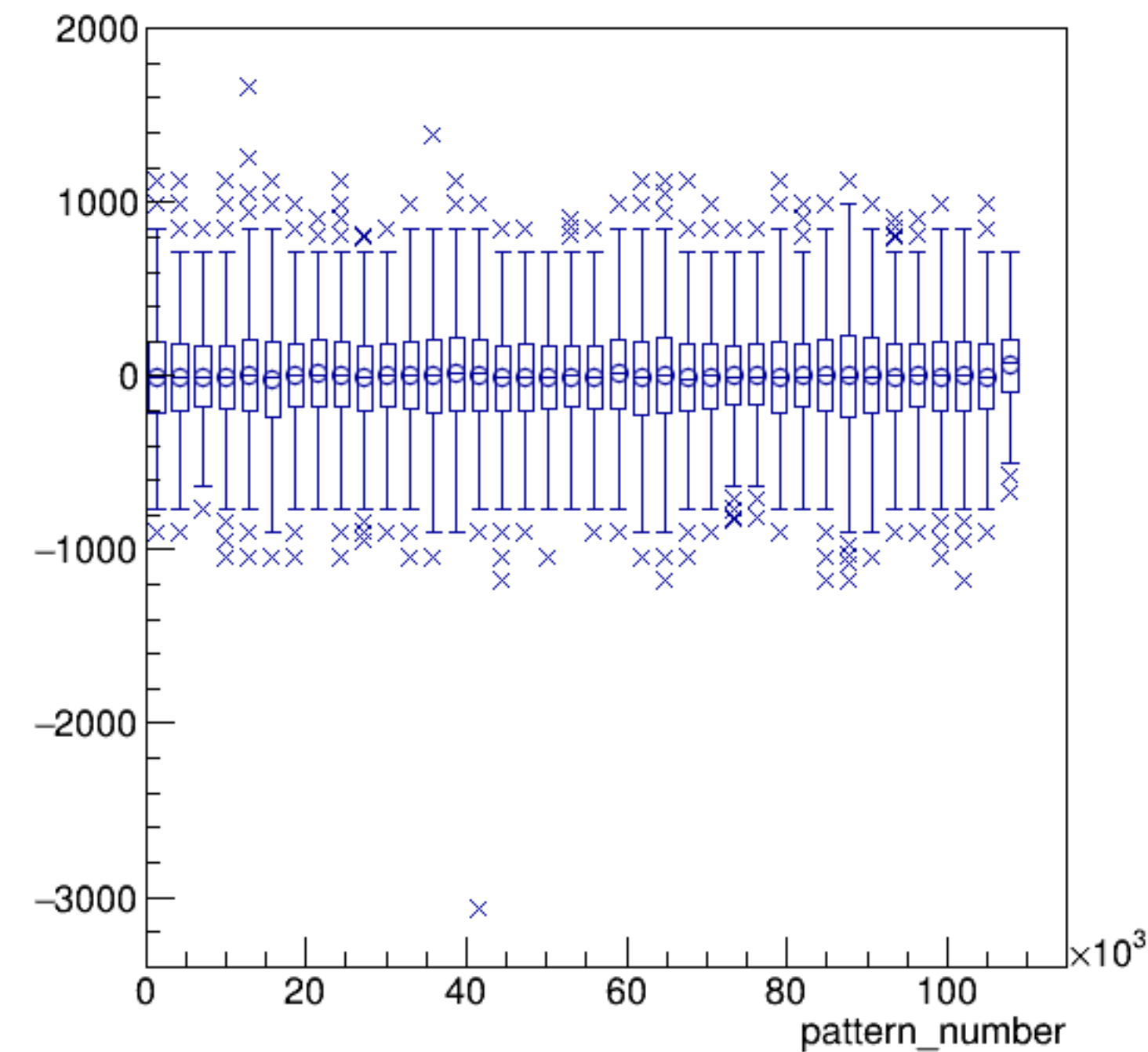
sam7\*CurMon {ErrorFlag==0}



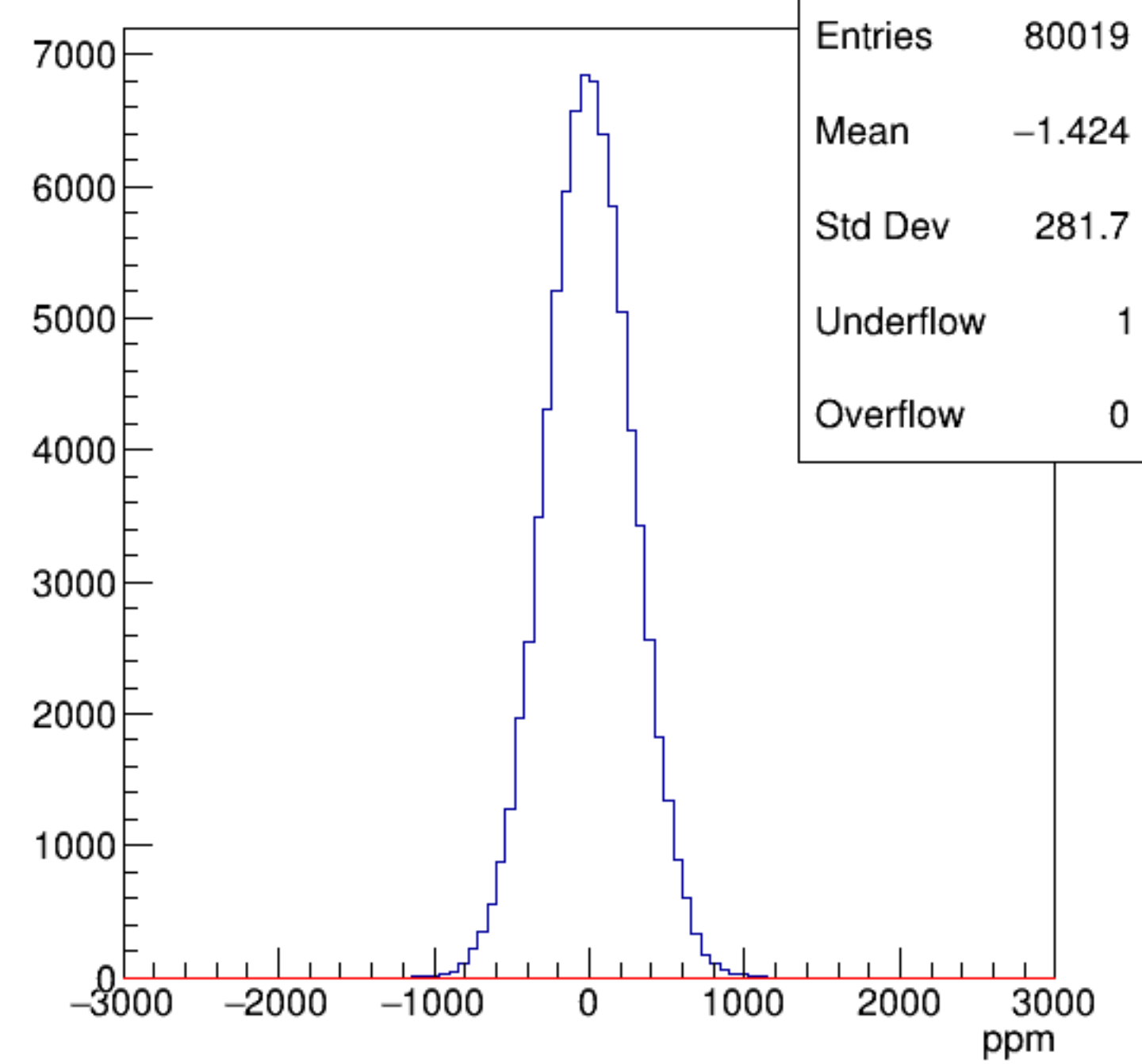
evtTree

|         |          |
|---------|----------|
| Entries | 320320   |
| Mean    | 0.3448   |
| Std Dev | 0.001622 |

asym\_sam7/ppm:pattern\_number {ErrorFlag==0}



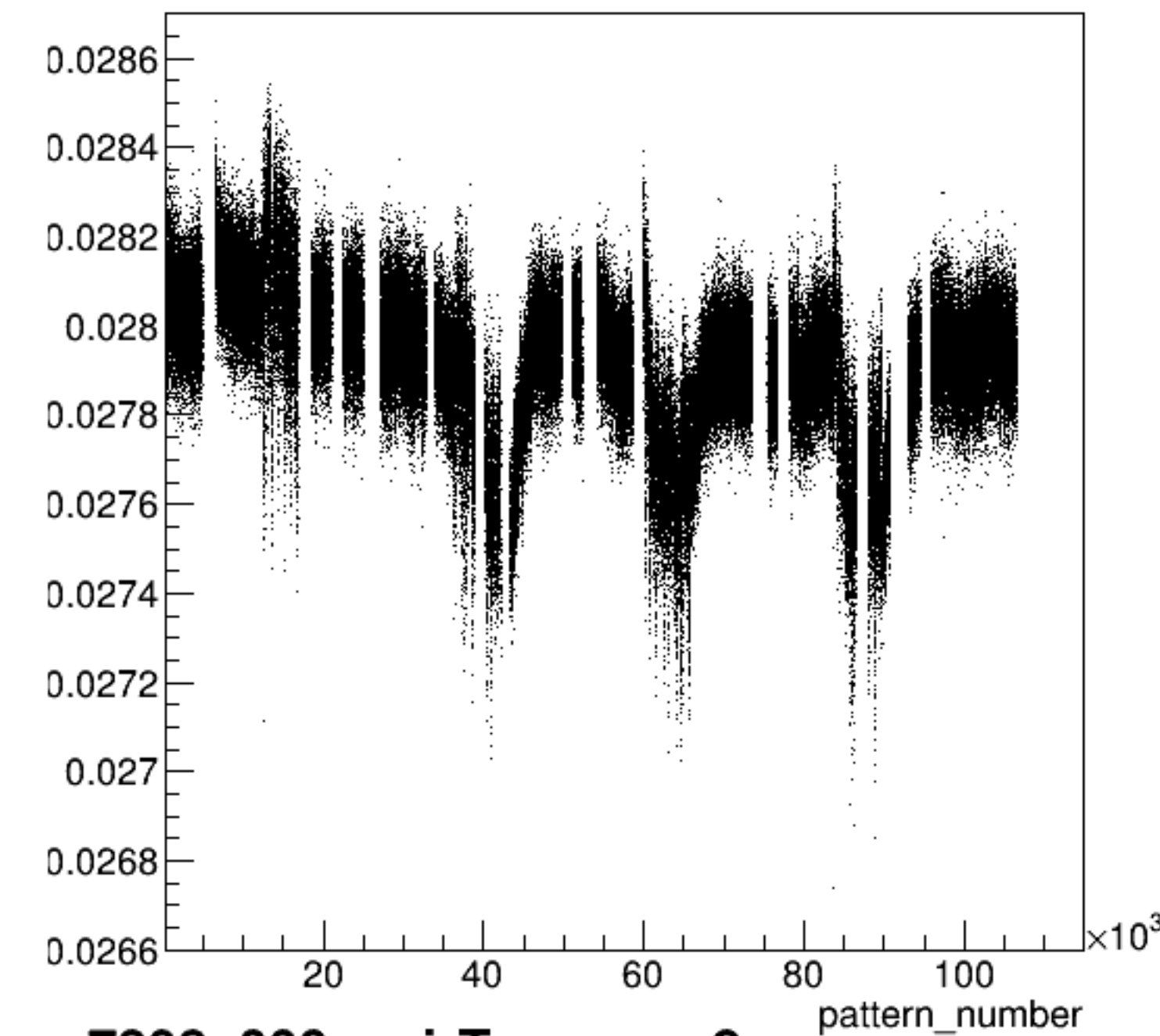
asym\_sam7



|           |        |
|-----------|--------|
| Entries   | 80019  |
| Mean      | -1.424 |
| Std Dev   | 281.7  |
| Underflow | 1      |
| Overflow  | 0      |

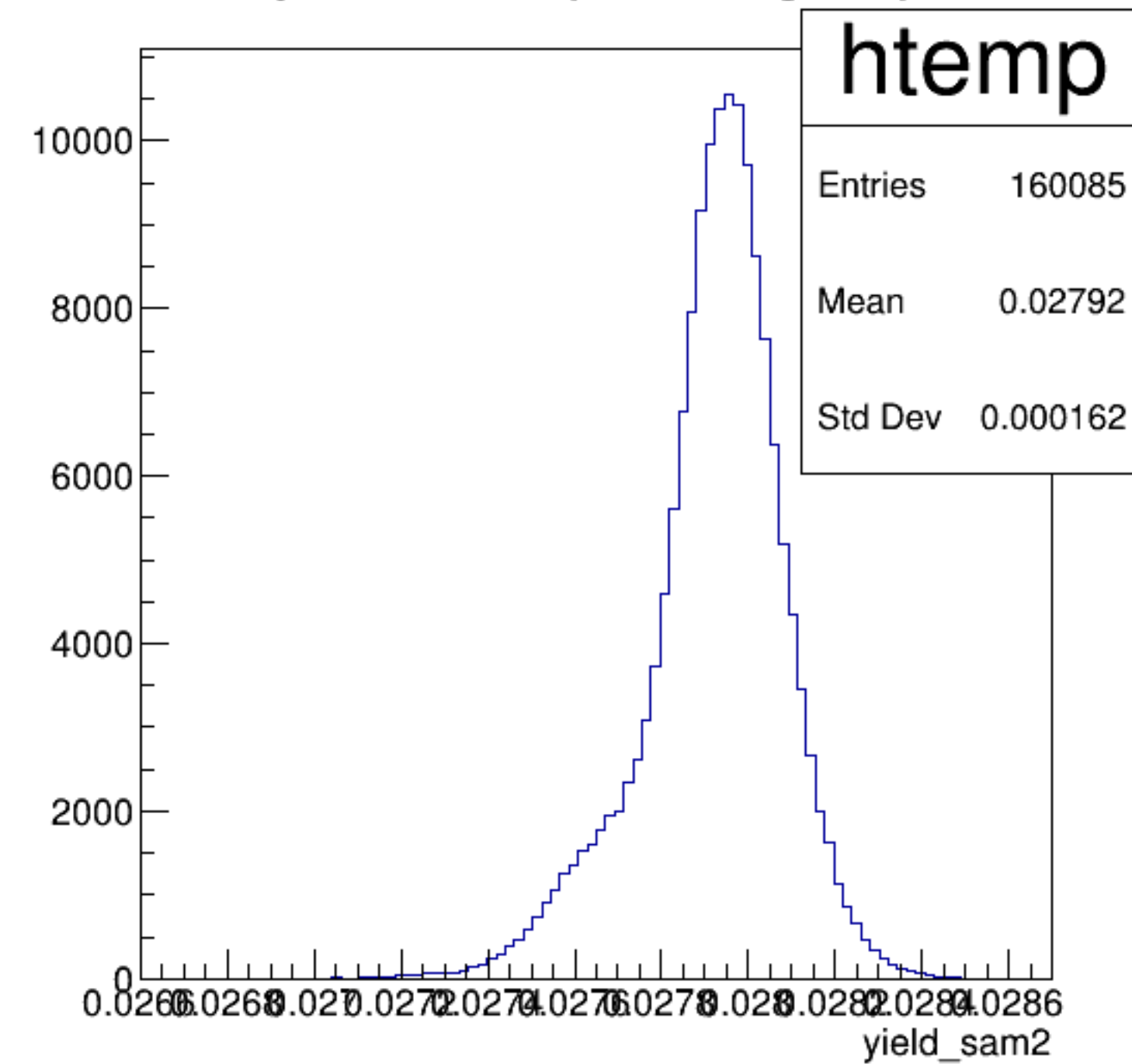


yield\_sam2:pattern\_number {ErrorFlag==0}

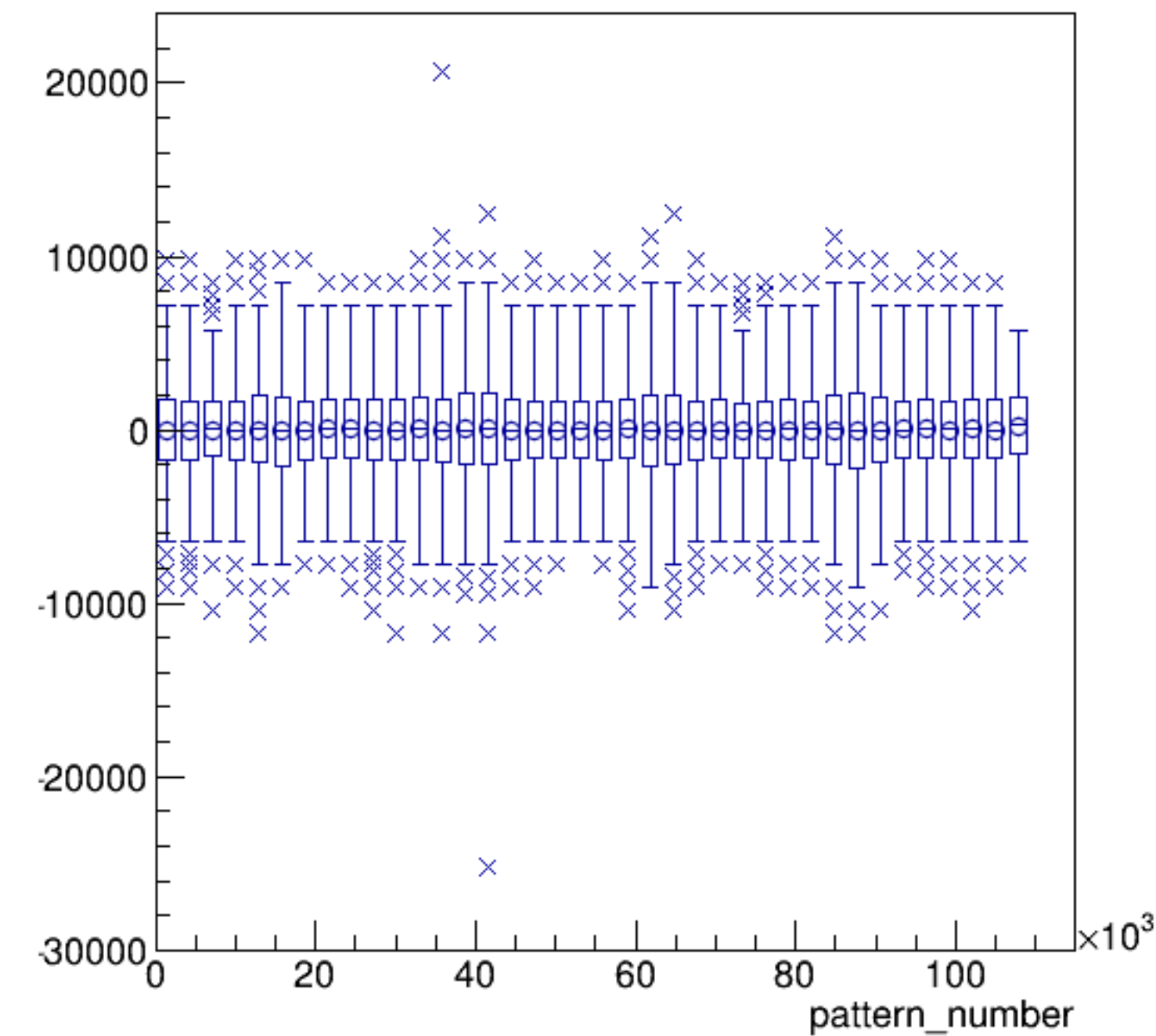


run7398\_000\_pairTree\_sam2.png

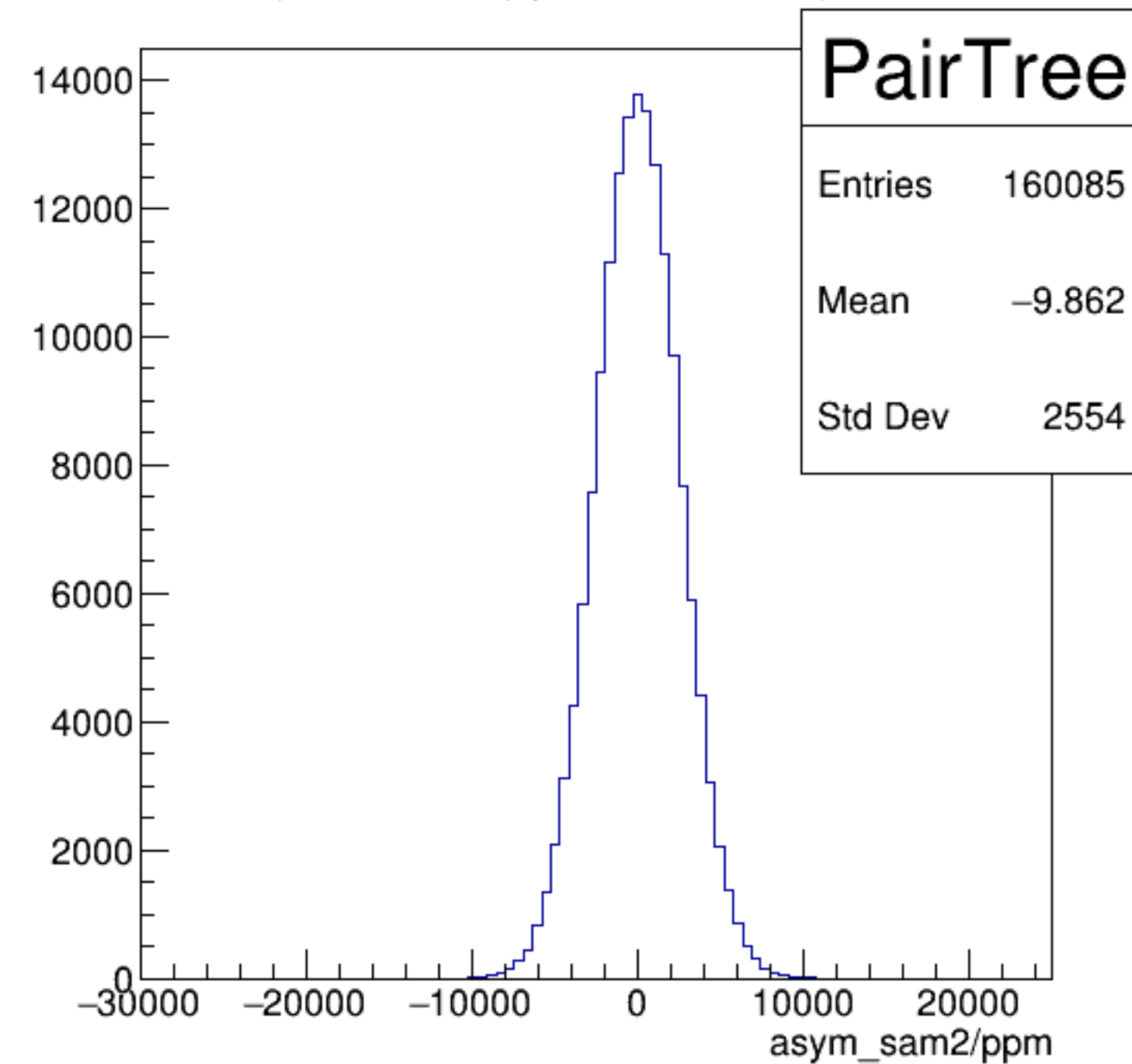
yield\_sam2 {ErrorFlag==0}



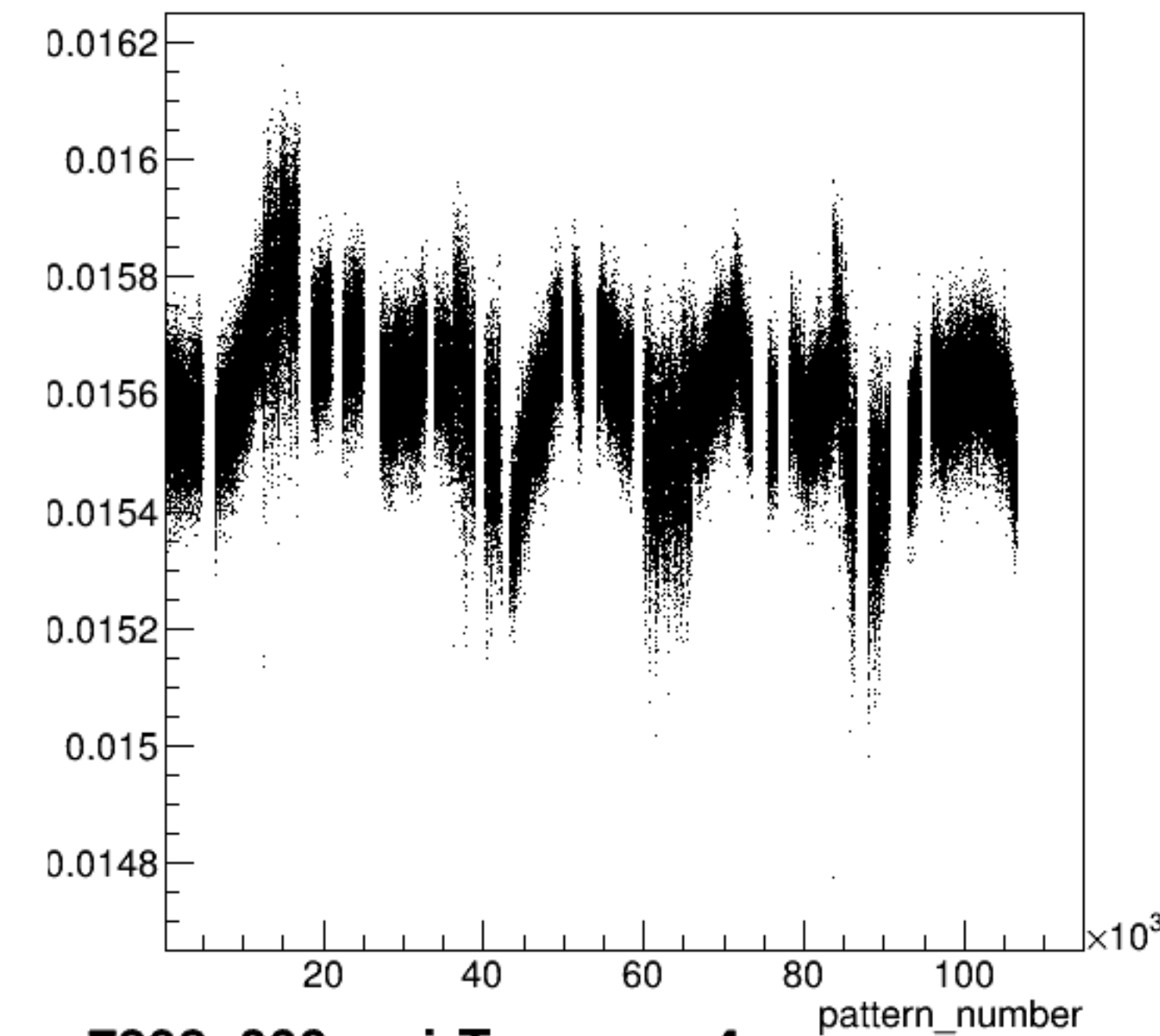
asym\_sam2/ppm:pattern\_number {ErrorFlag==0}



asym\_sam2/ppm {ErrorFlag==0}

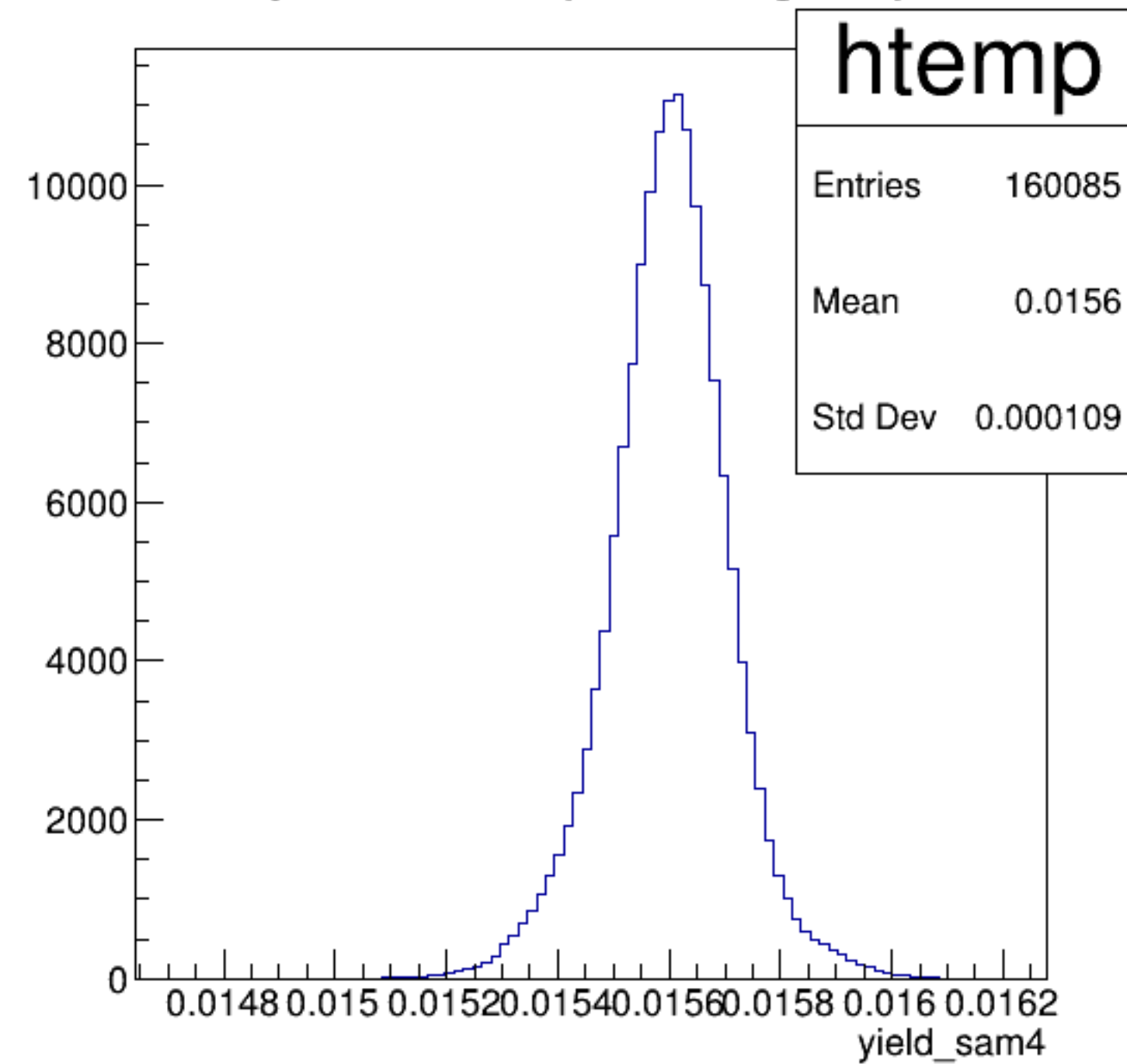


yield\_sam4:pattern\_number {ErrorFlag==0}

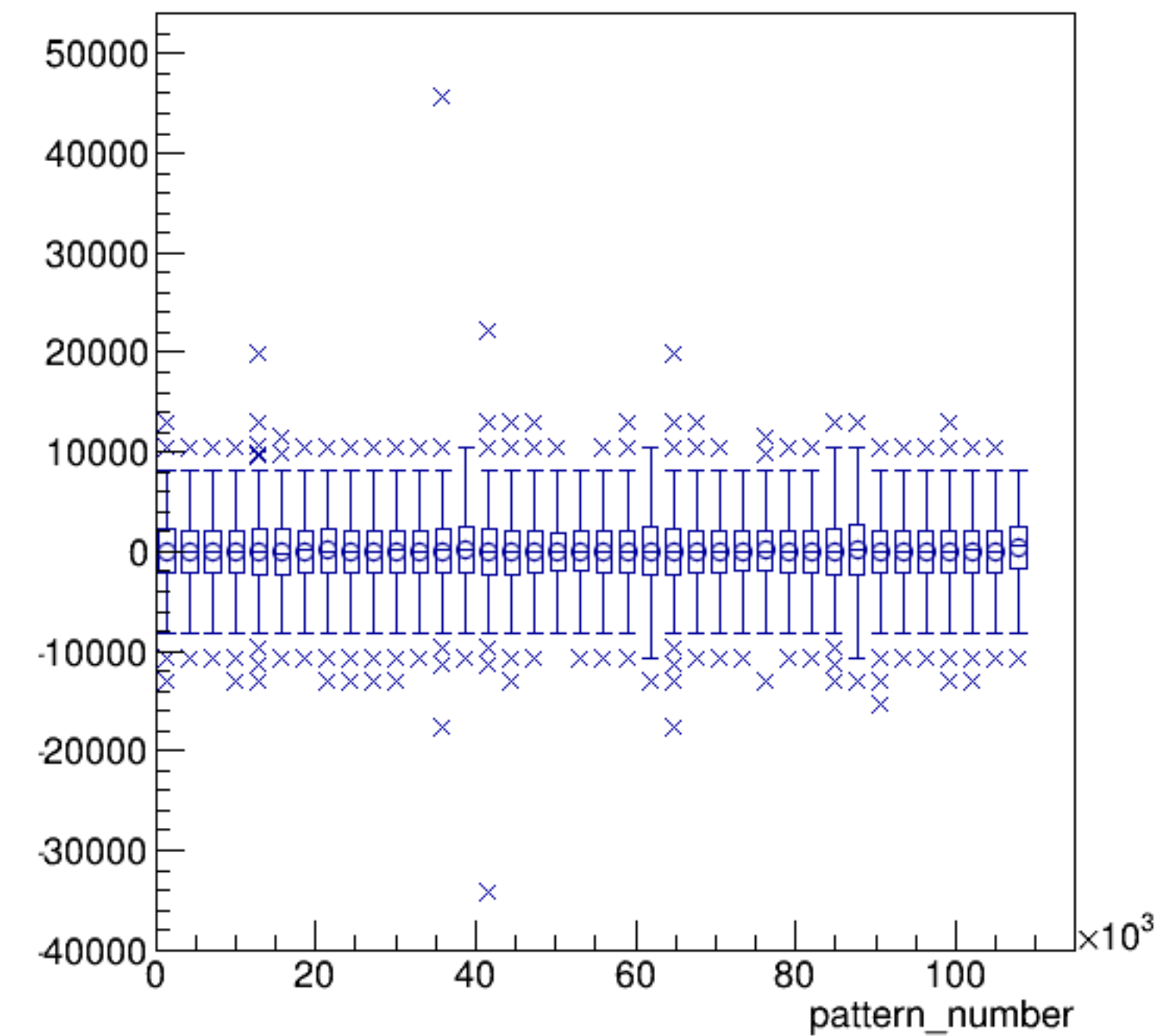


run7398\_000\_pairTree\_sam4.png

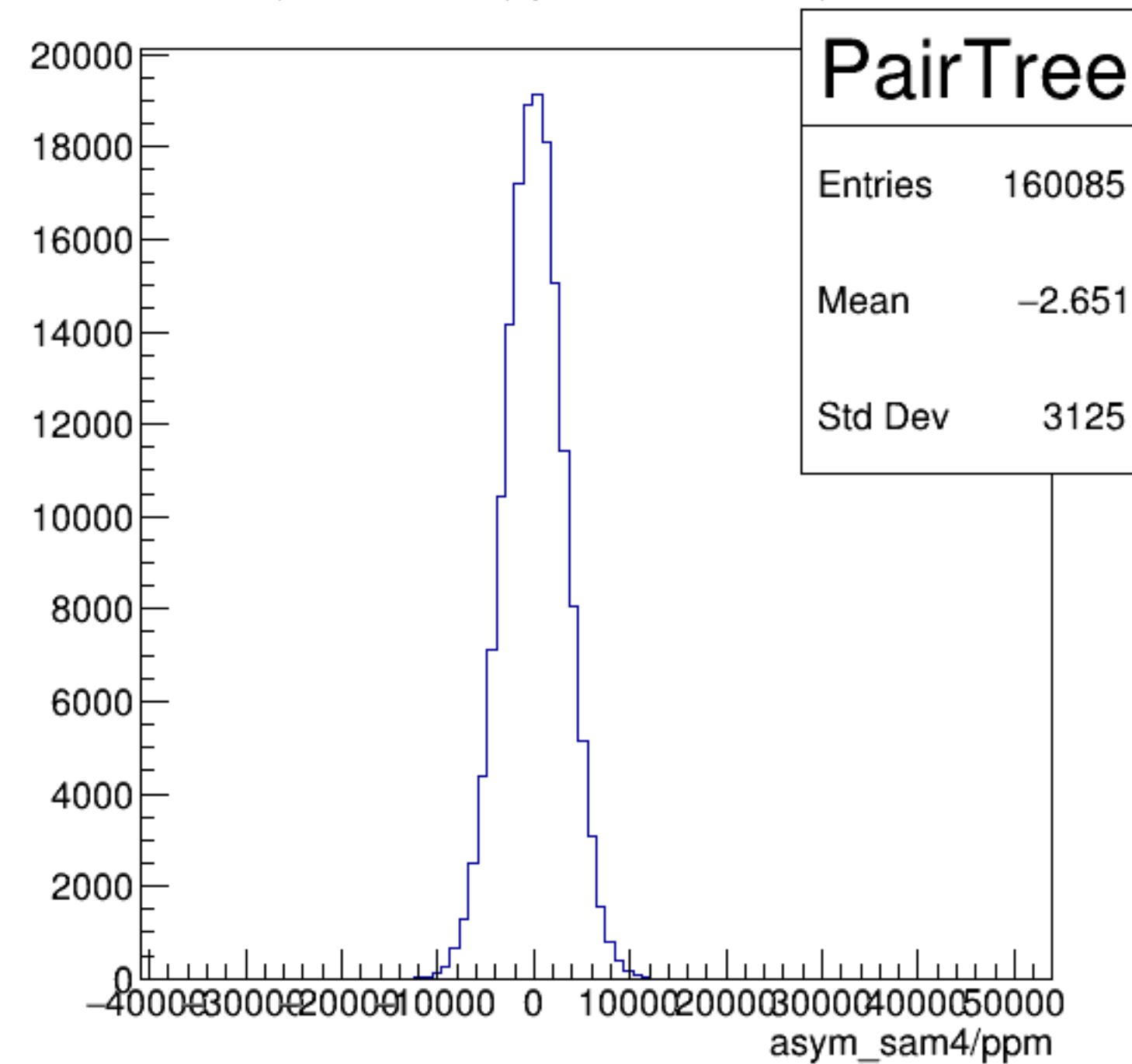
yield\_sam4 {ErrorFlag==0}



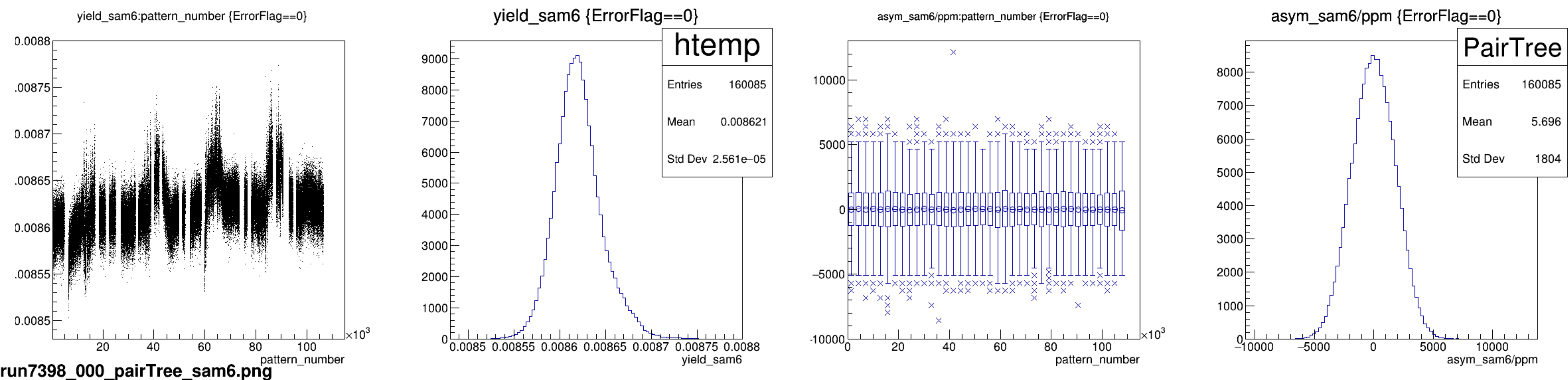
asym\_sam4/ppm:pattern\_number {ErrorFlag==0}



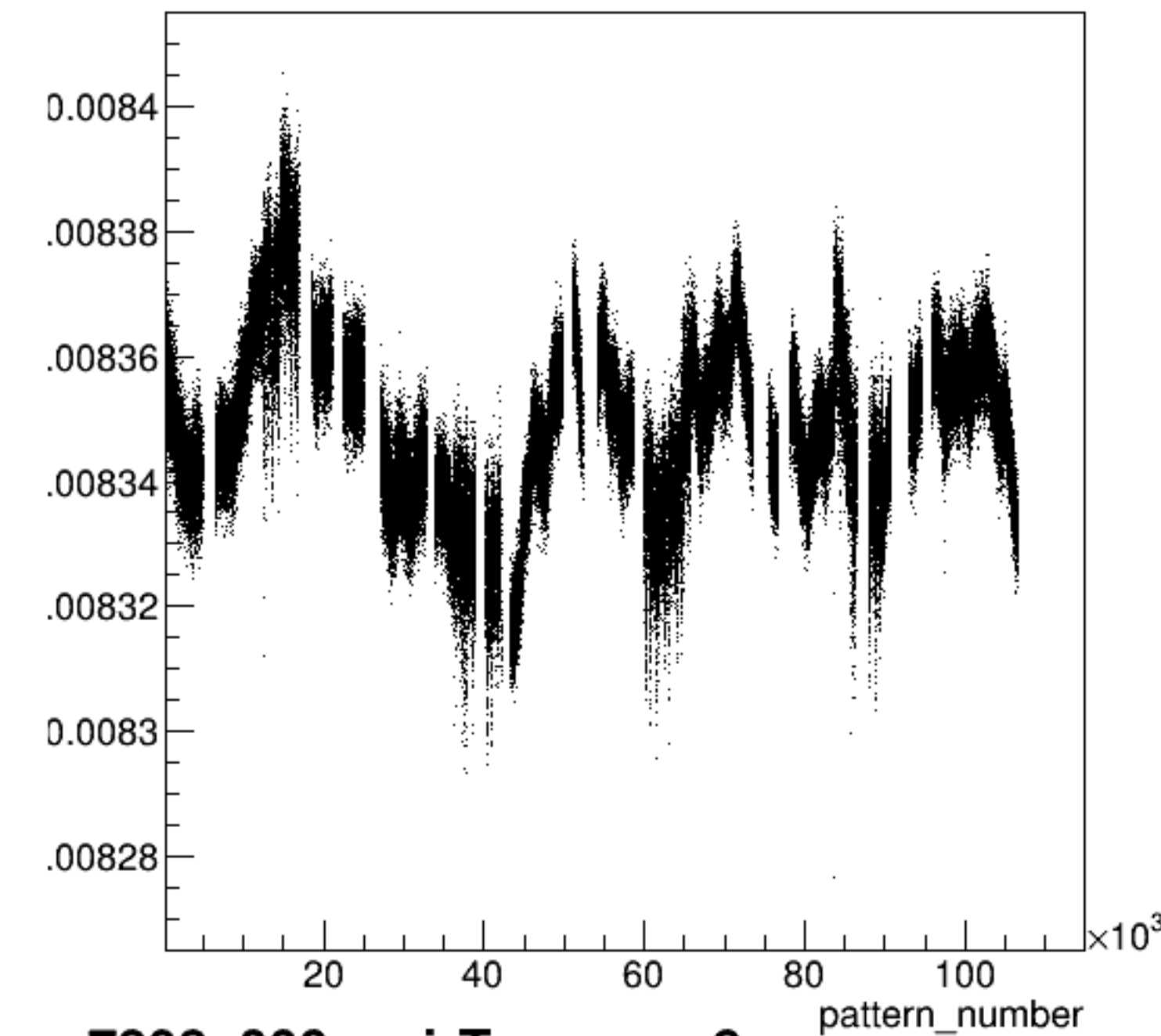
asym\_sam4/ppm {ErrorFlag==0}





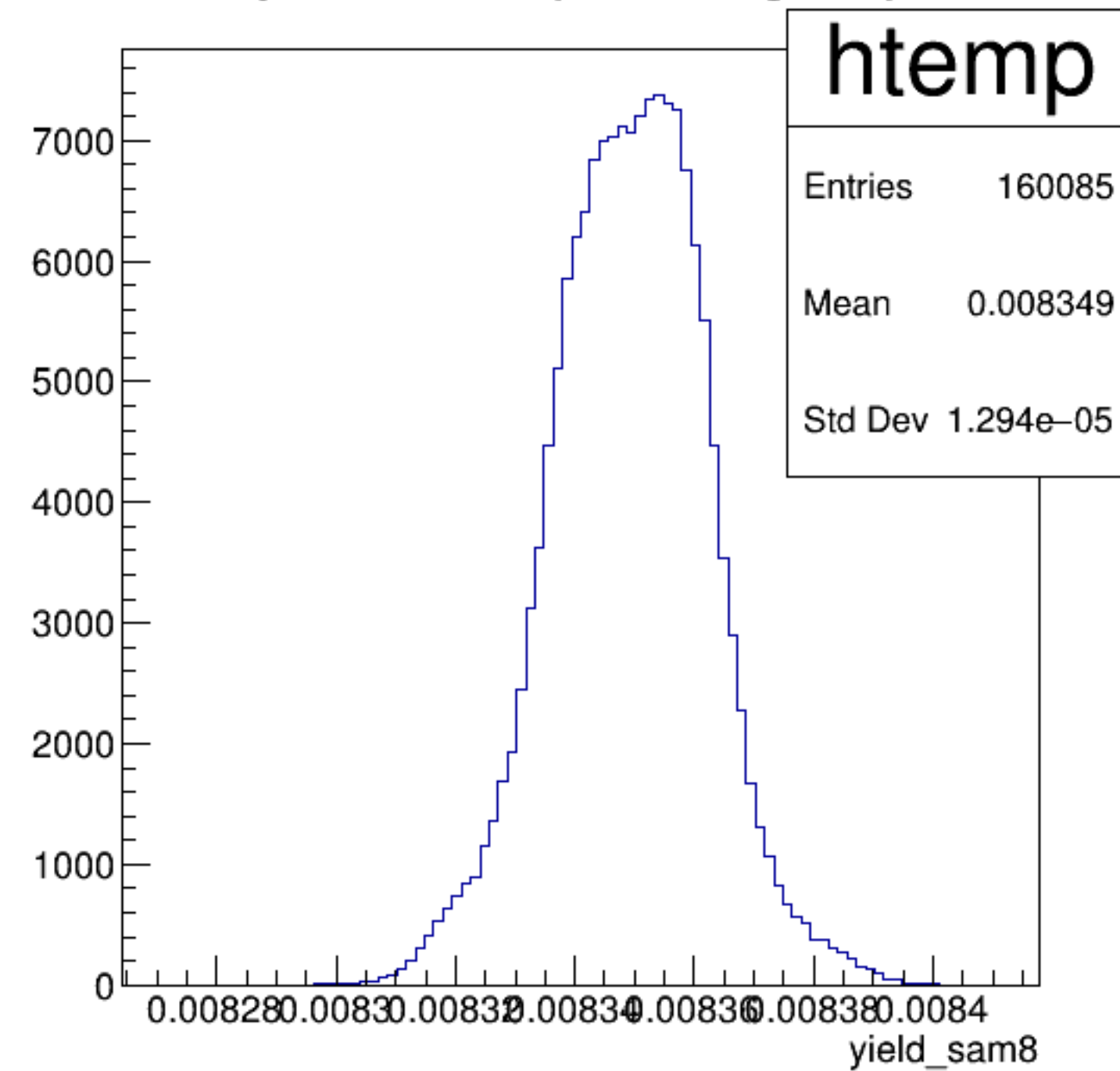


yield\_sam8:pattern\_number {ErrorFlag==0}

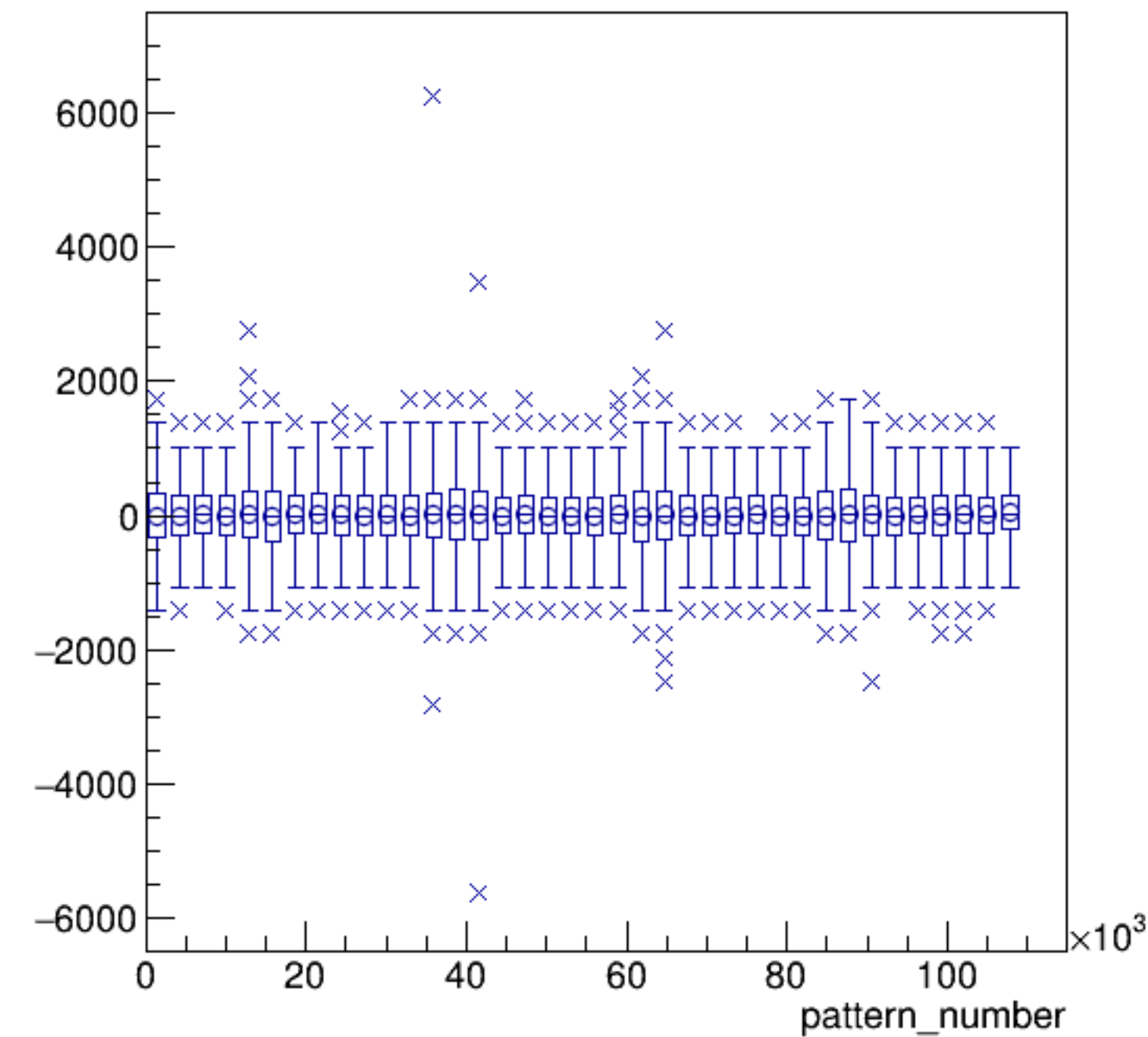


run7398\_000\_pairTree\_sam8.png

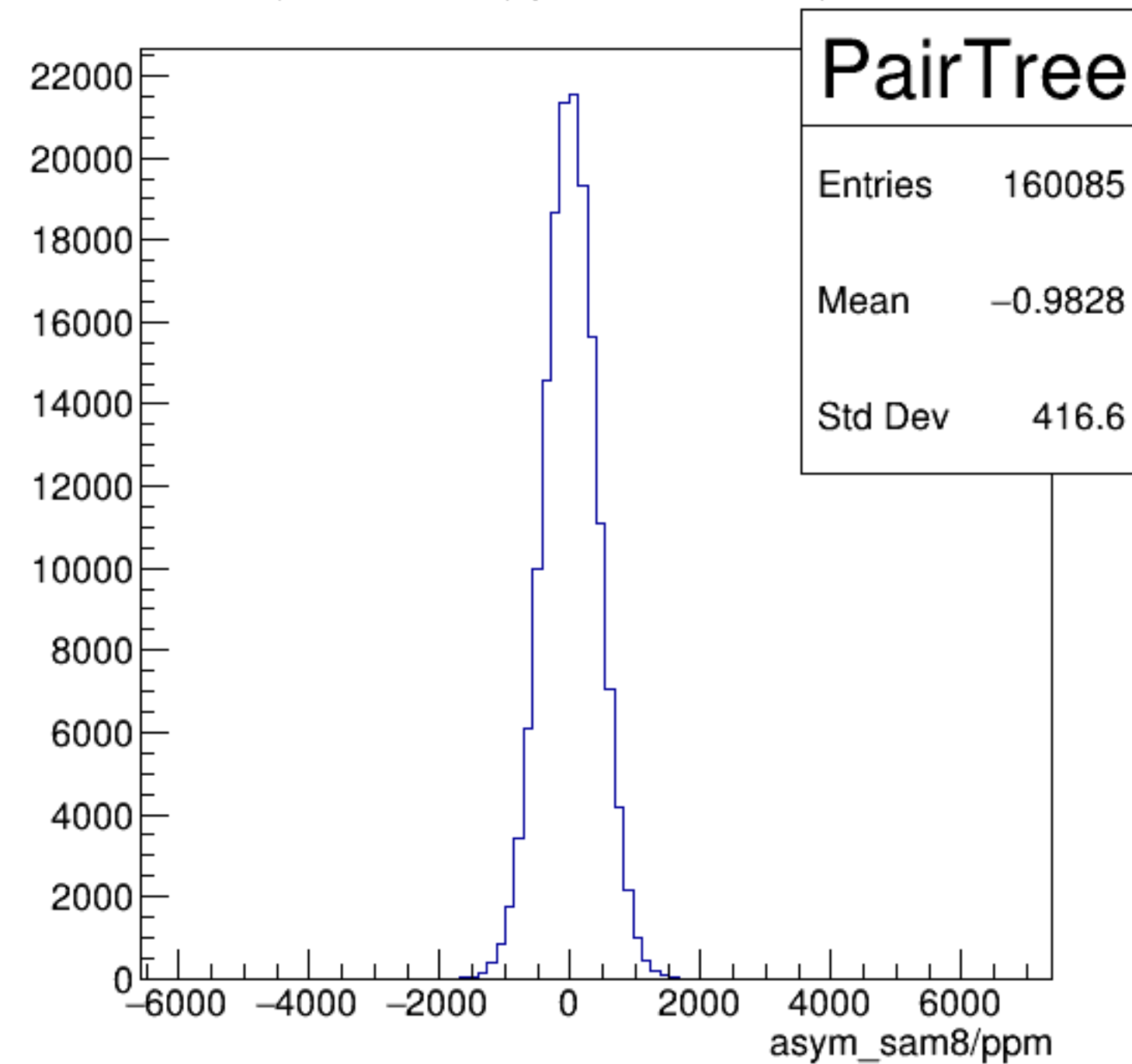
yield\_sam8 {ErrorFlag==0}



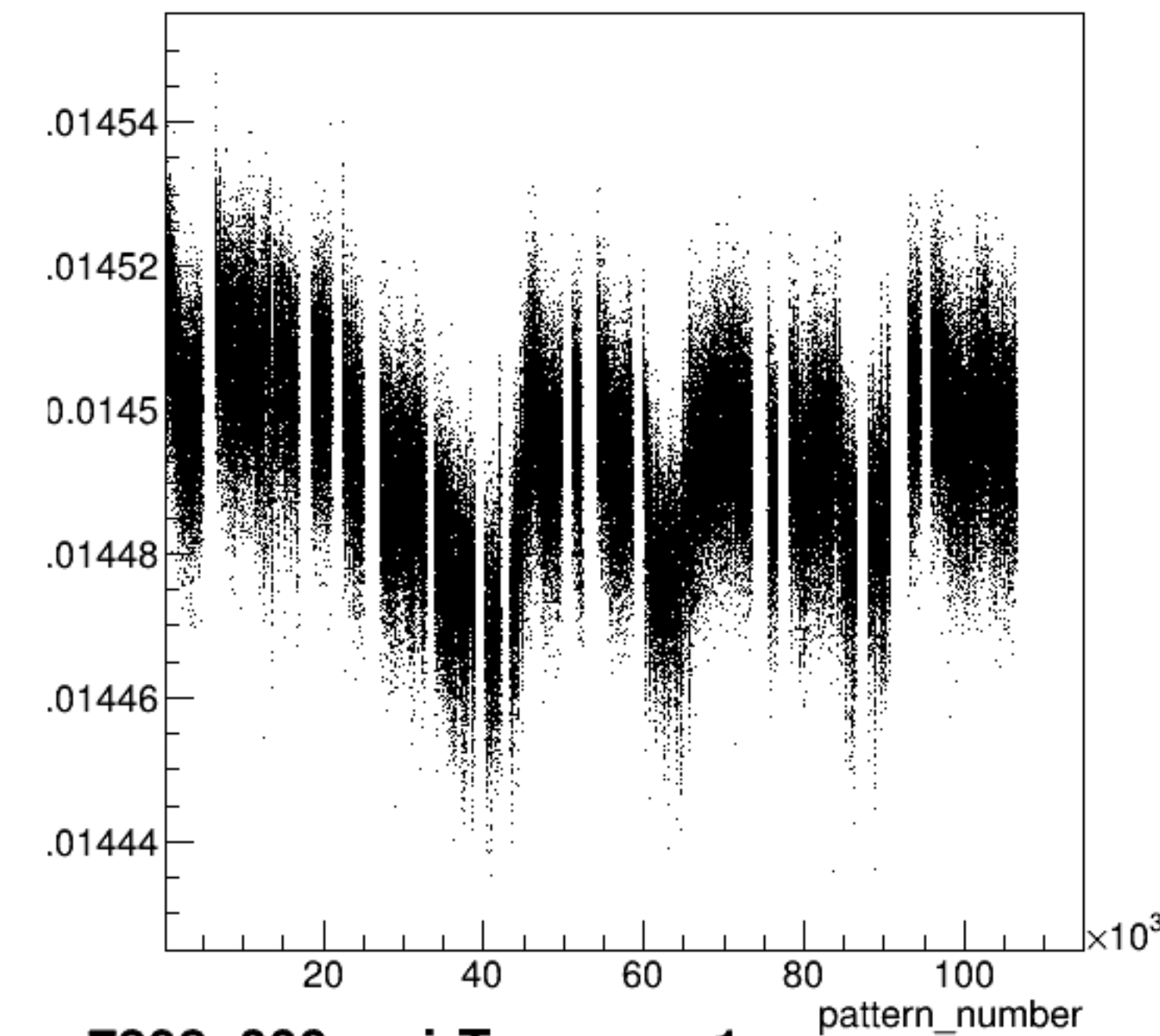
asym\_sam8/ppm:pattern\_number {ErrorFlag==0}



asym\_sam8/ppm {ErrorFlag==0}

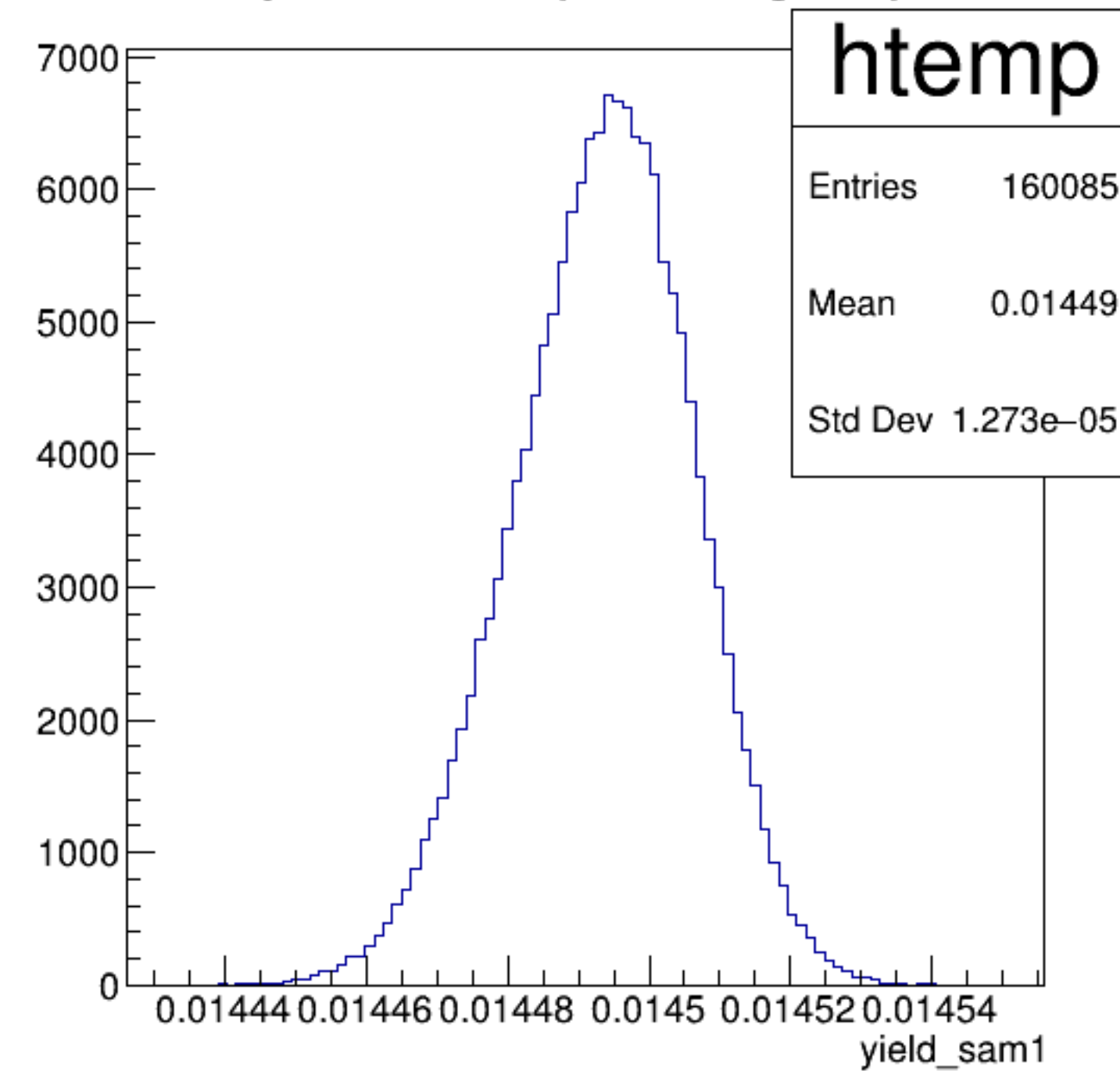


yield\_sam1:pattern\_number {ErrorFlag==0}

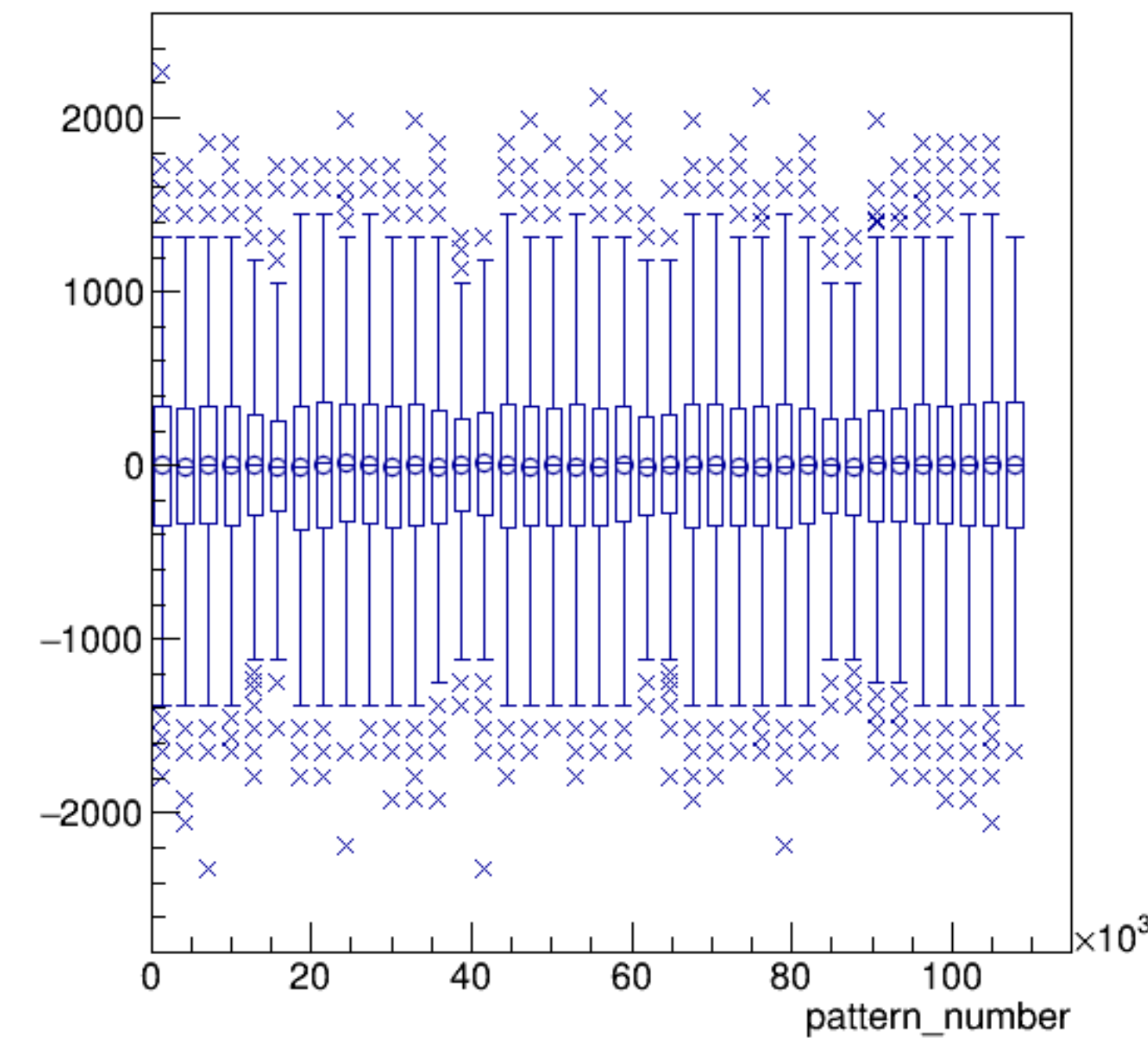


run7398\_000\_pairTree\_sam1.png

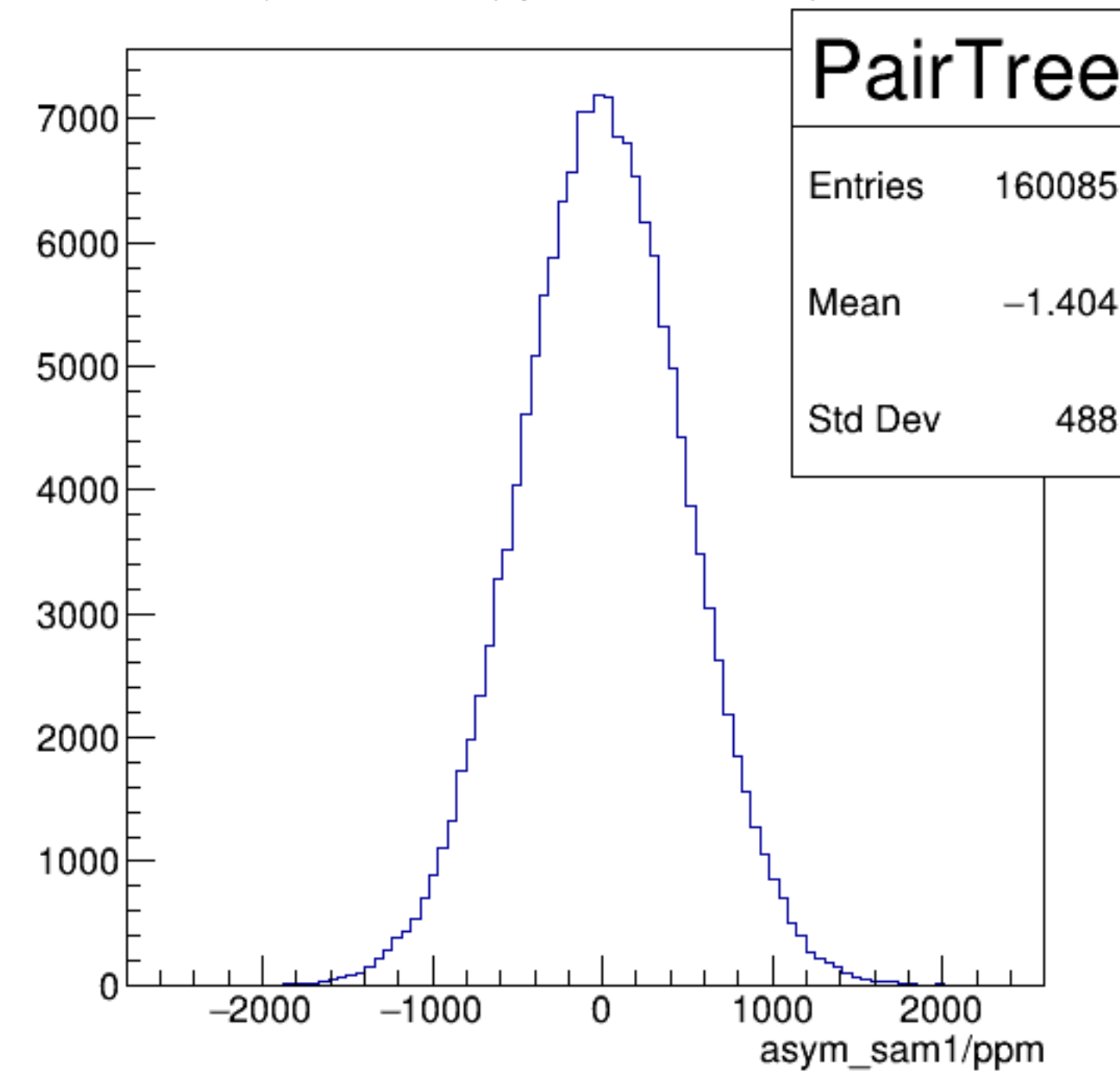
yield\_sam1 {ErrorFlag==0}



asym\_sam1/ppm:pattern\_number {ErrorFlag==0}

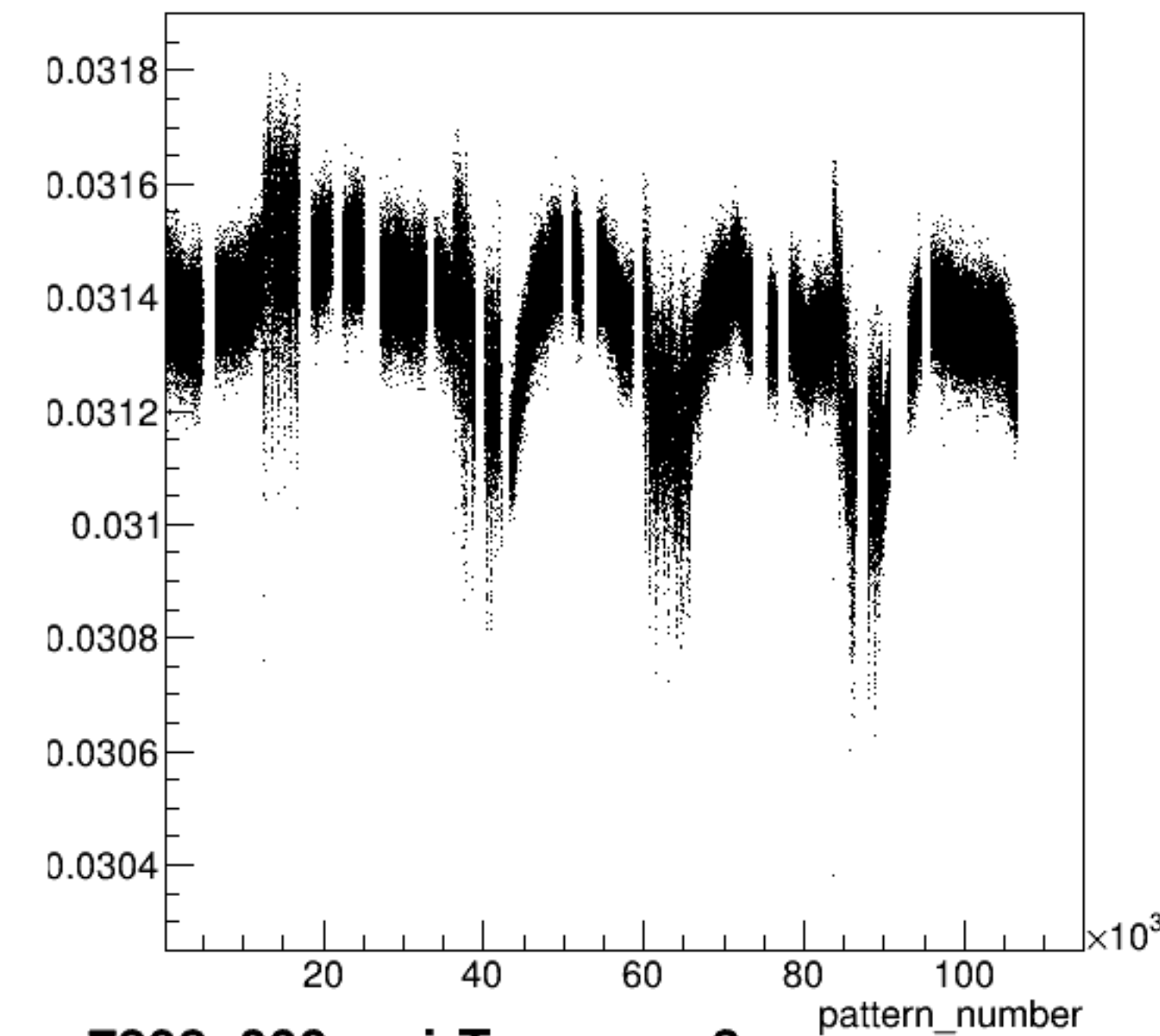


asym\_sam1/ppm {ErrorFlag==0}



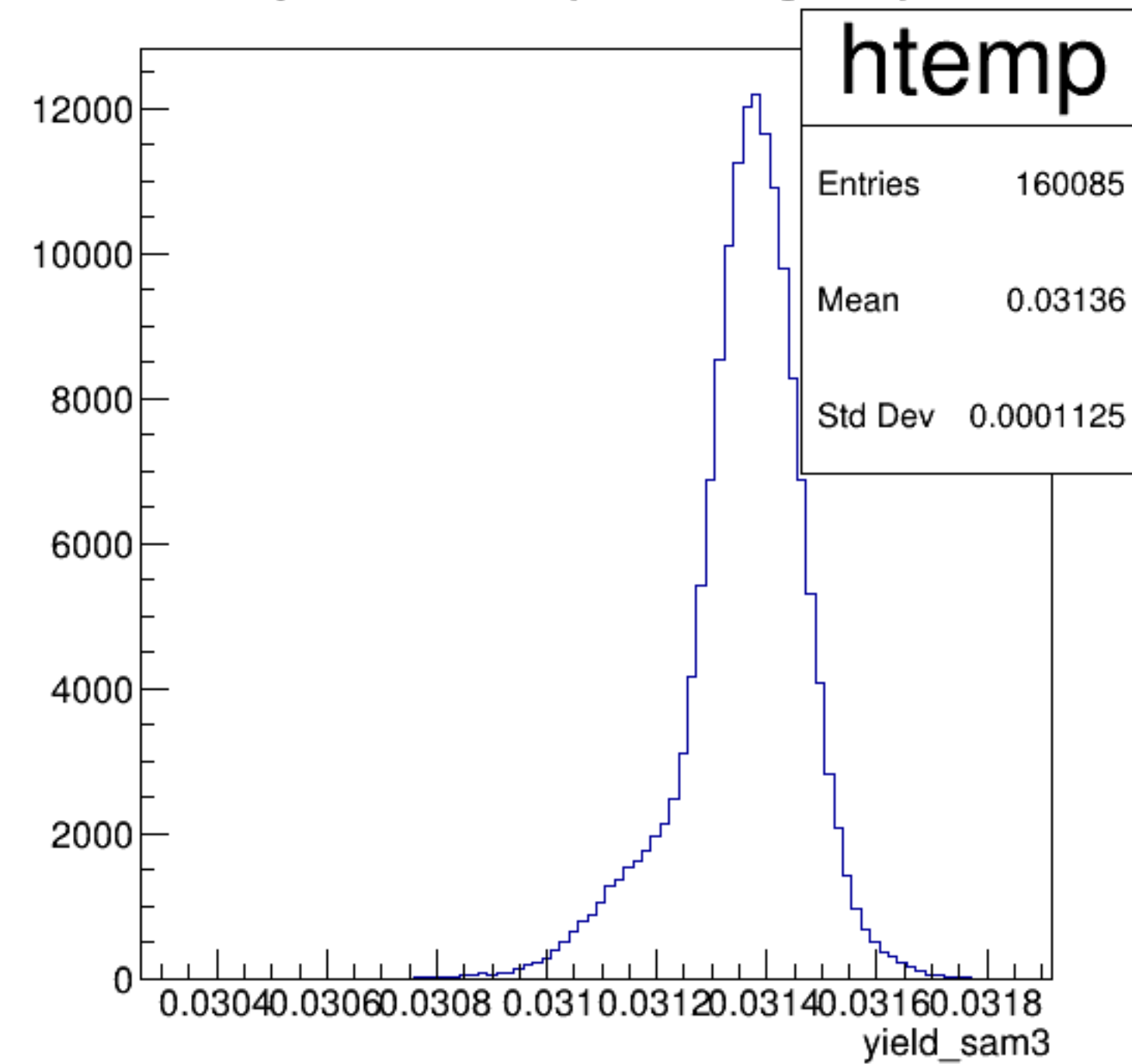


yield\_sam3:pattern\_number {ErrorFlag==0}

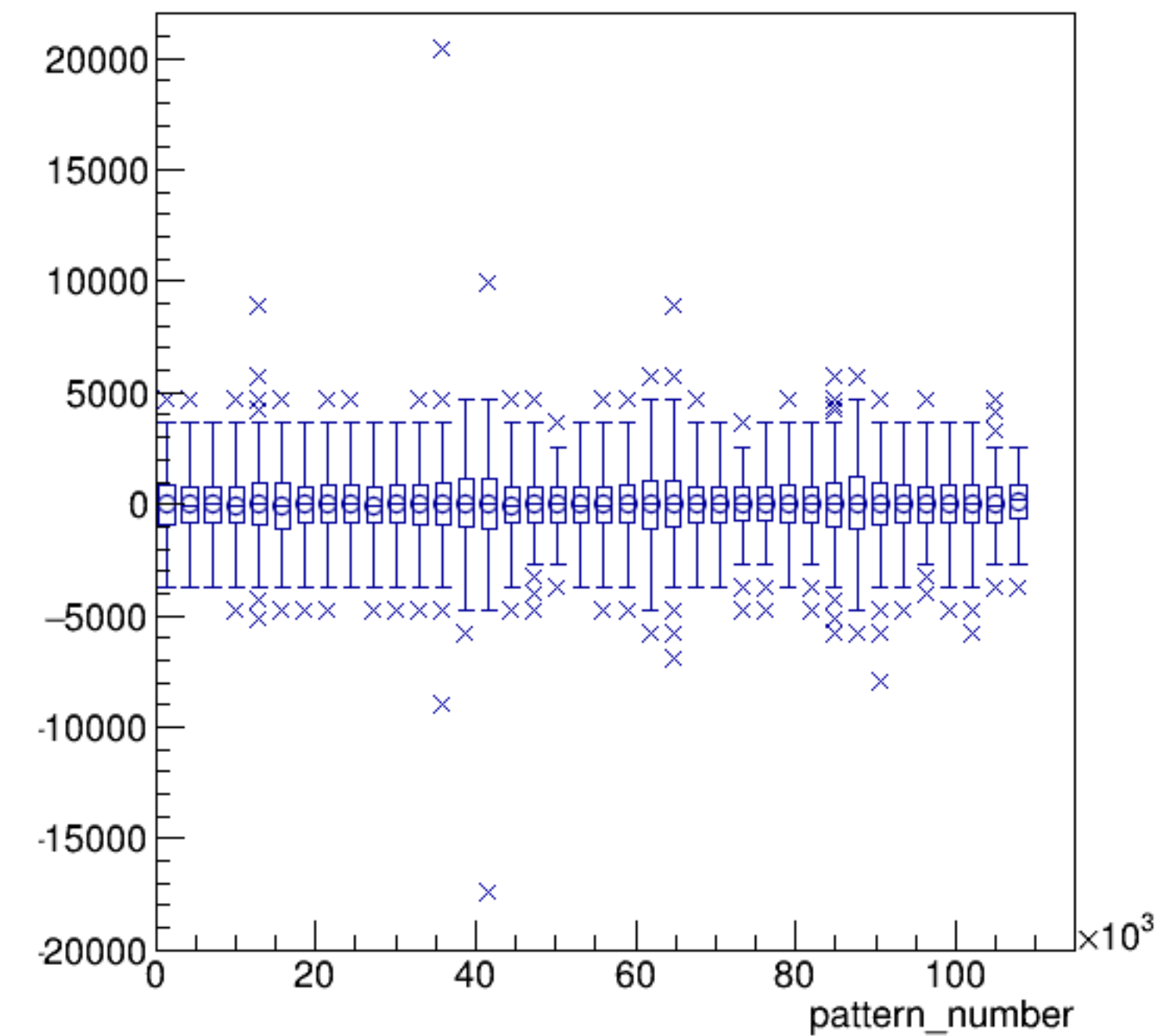


run7398\_000\_pairTree\_sam3.png

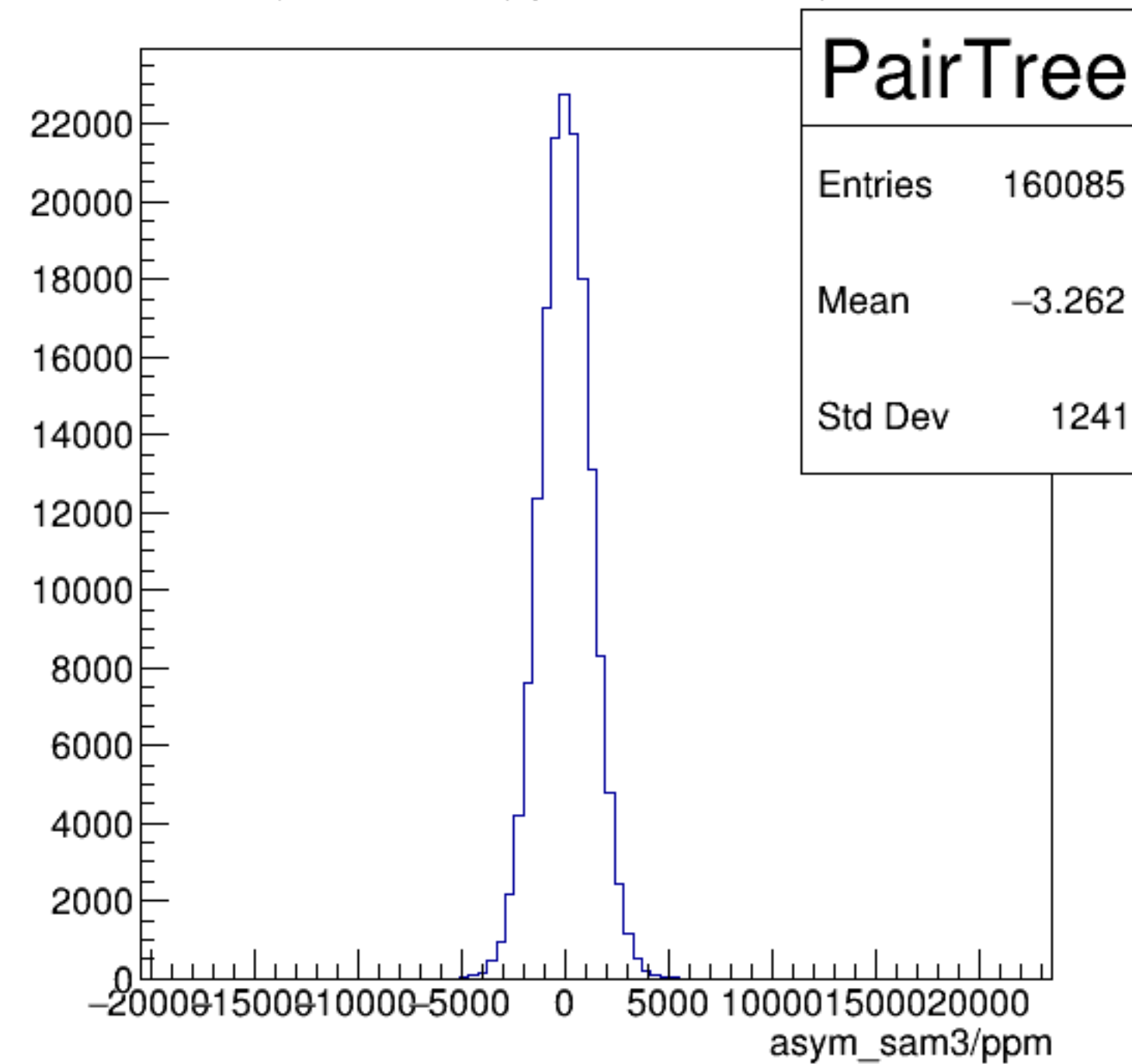
yield\_sam3 {ErrorFlag==0}



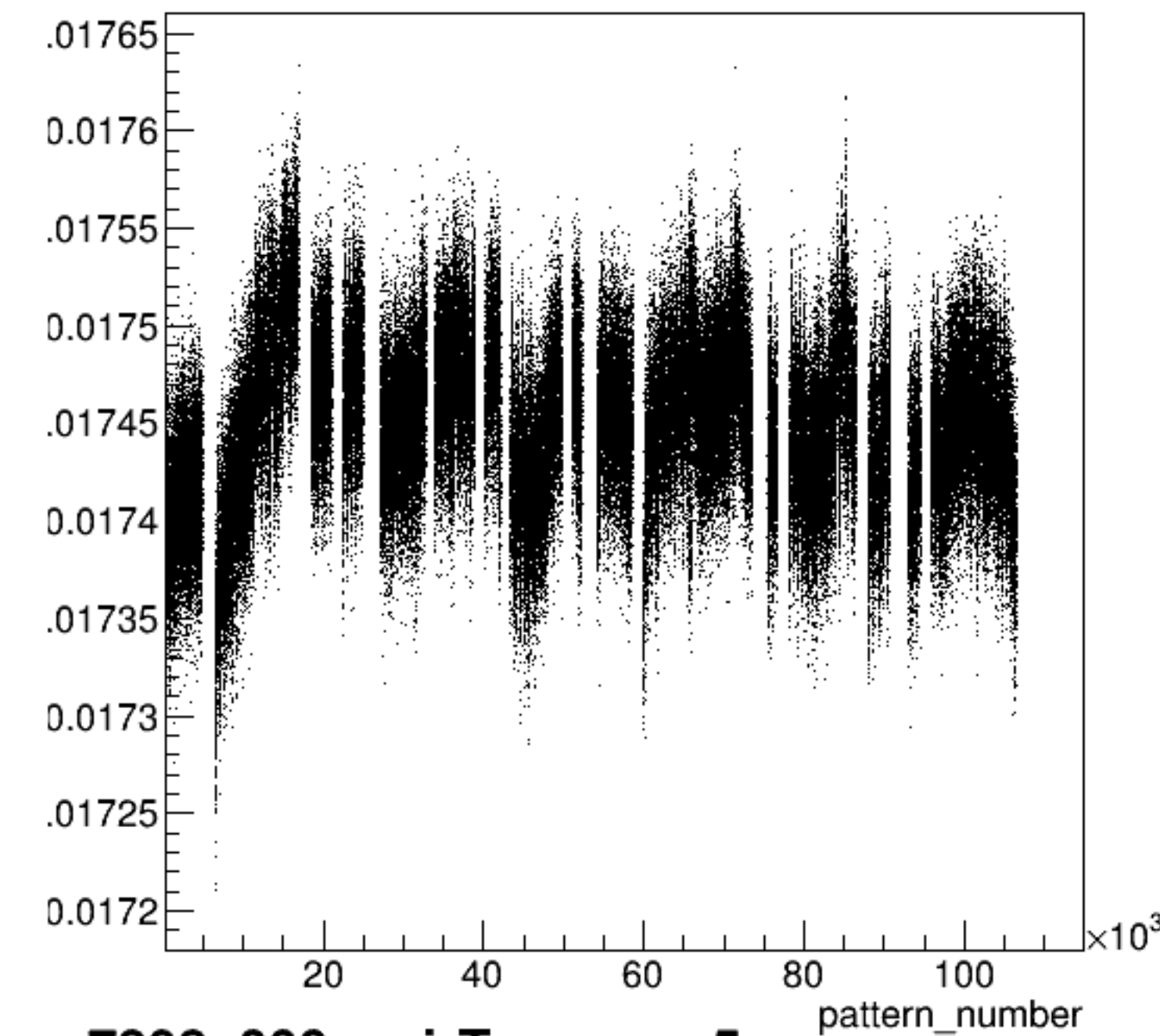
asym\_sam3/ppm:pattern\_number {ErrorFlag==0}



asym\_sam3/ppm {ErrorFlag==0}

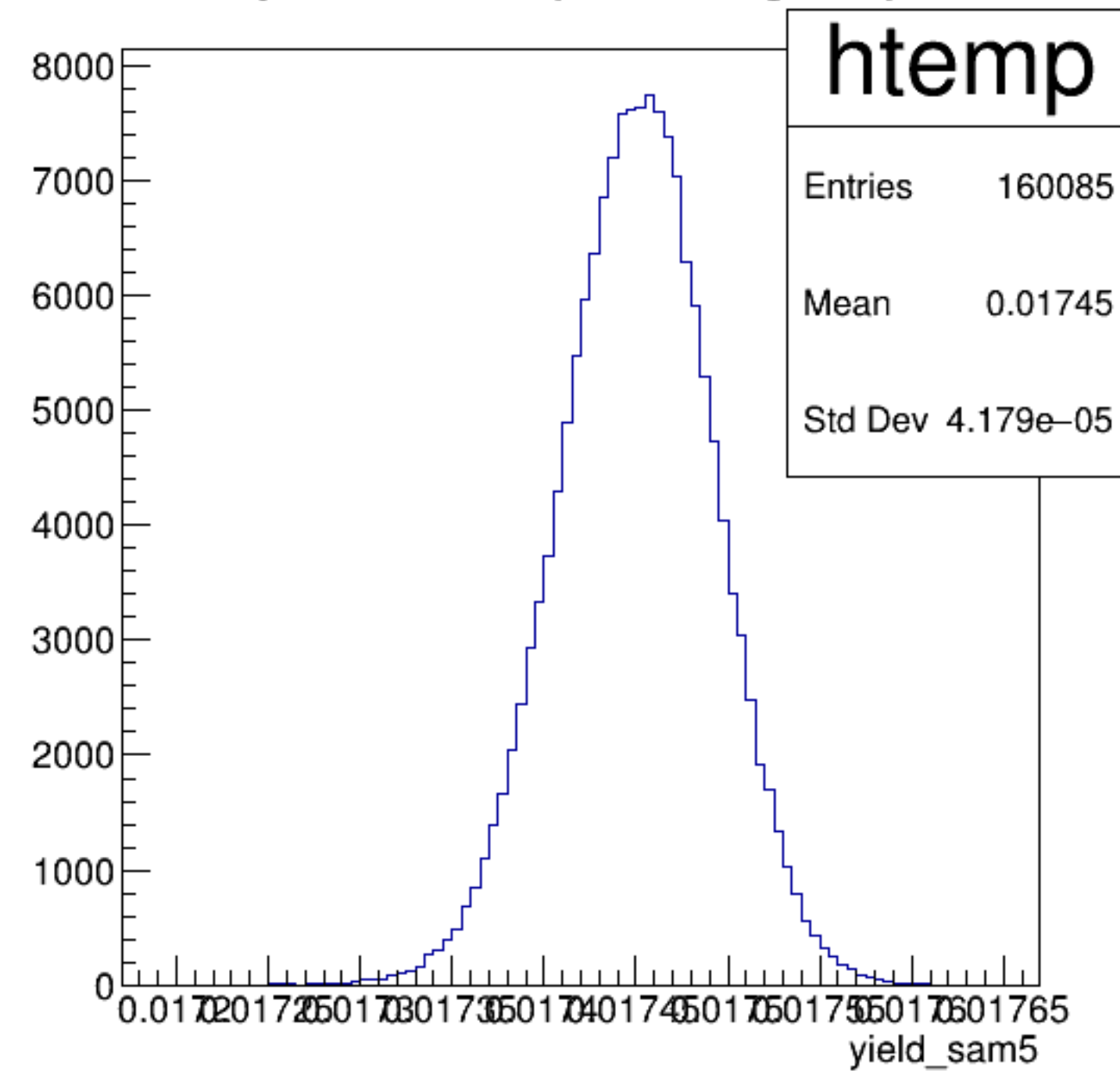


yield\_sam5:pattern\_number {ErrorFlag==0}

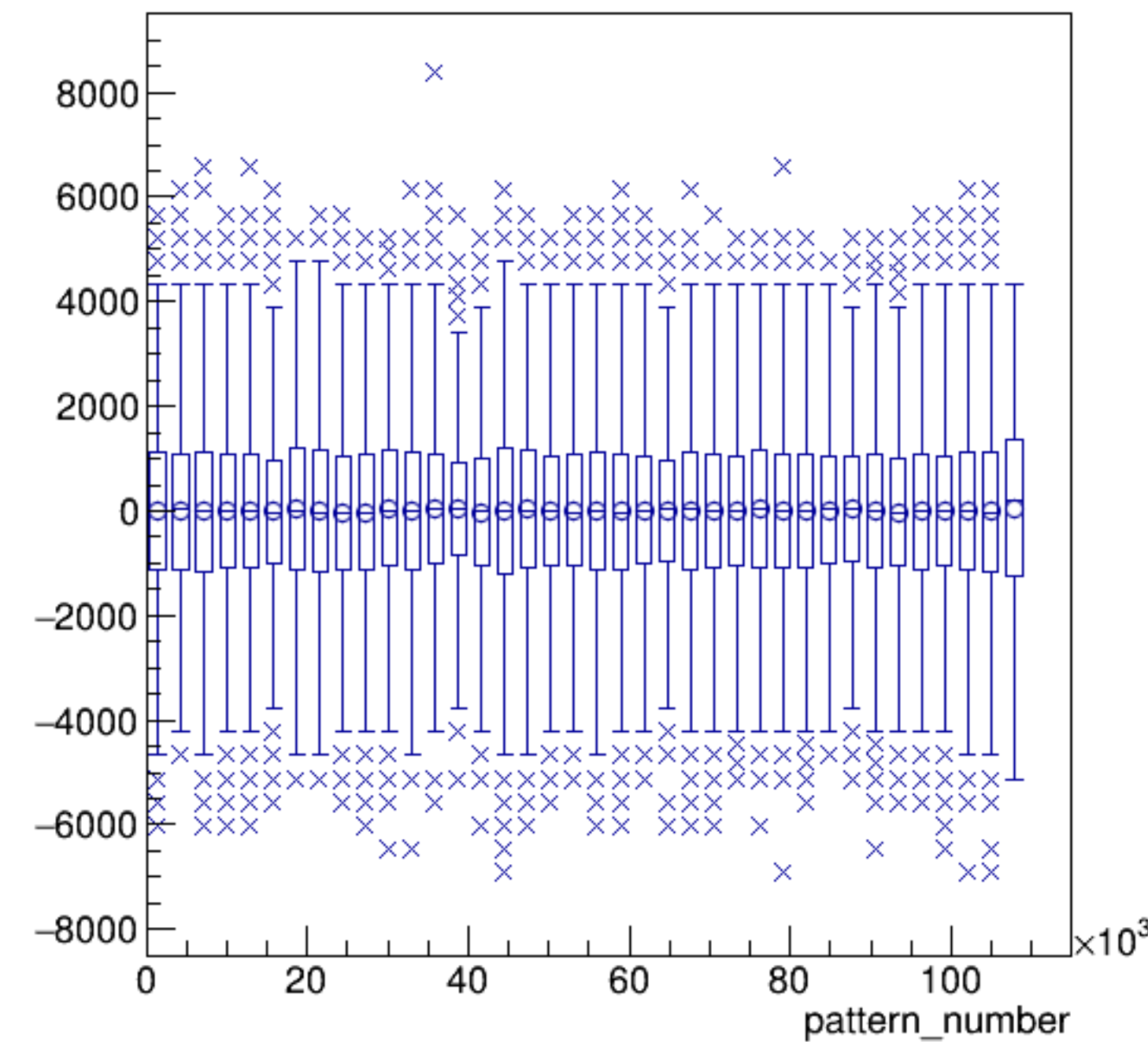


run7398\_000\_pairTree\_sam5.png

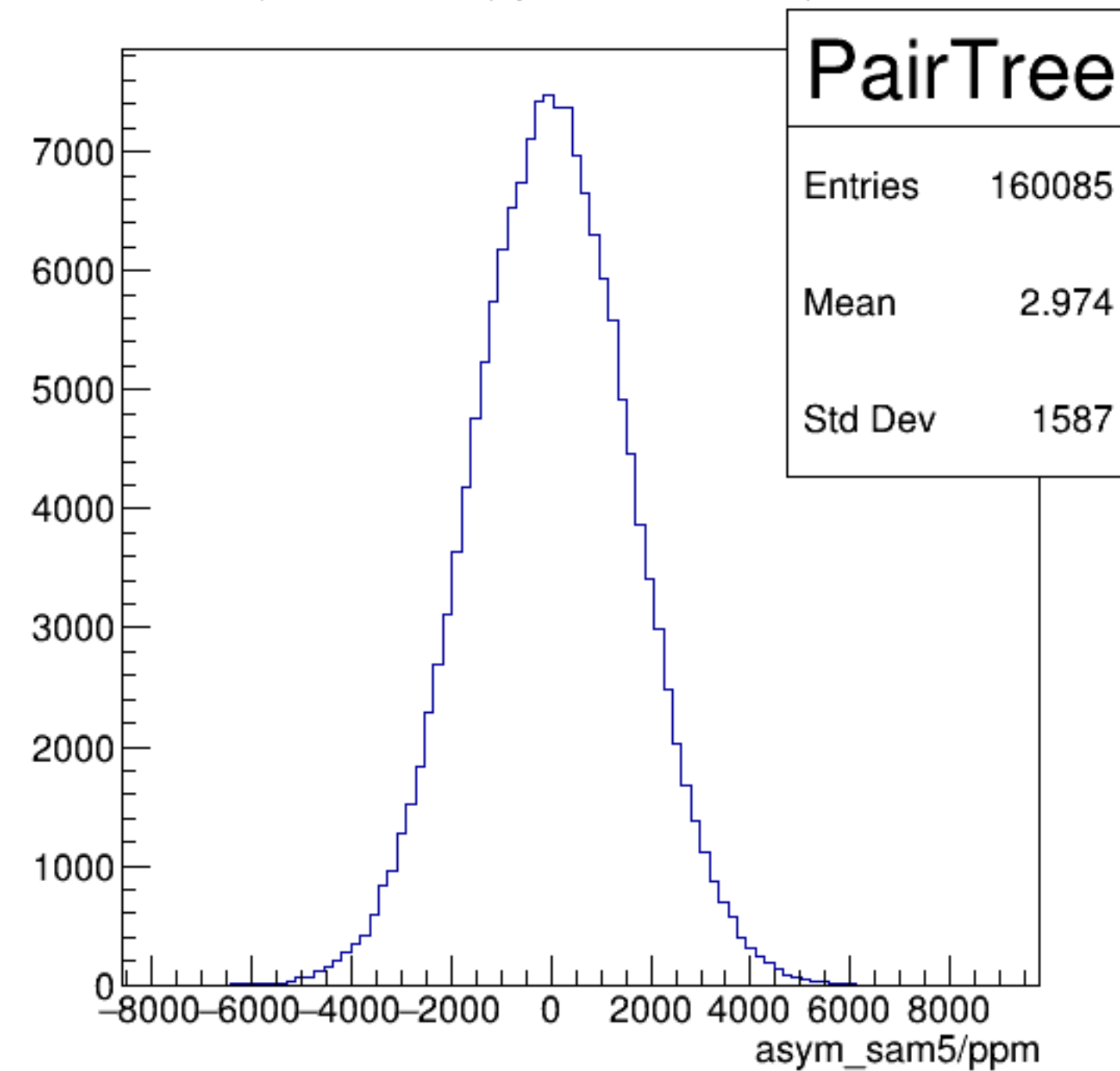
yield\_sam5 {ErrorFlag==0}

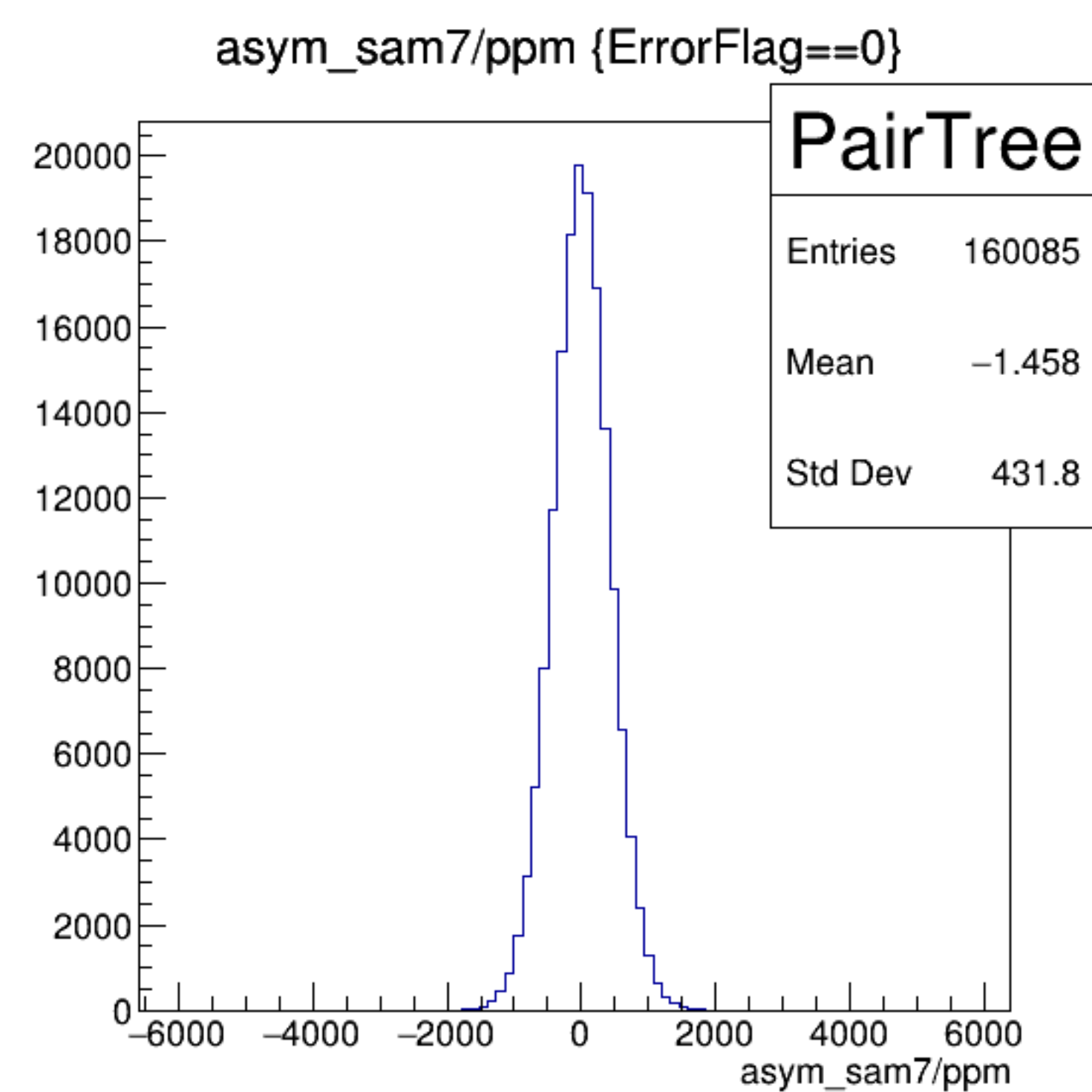
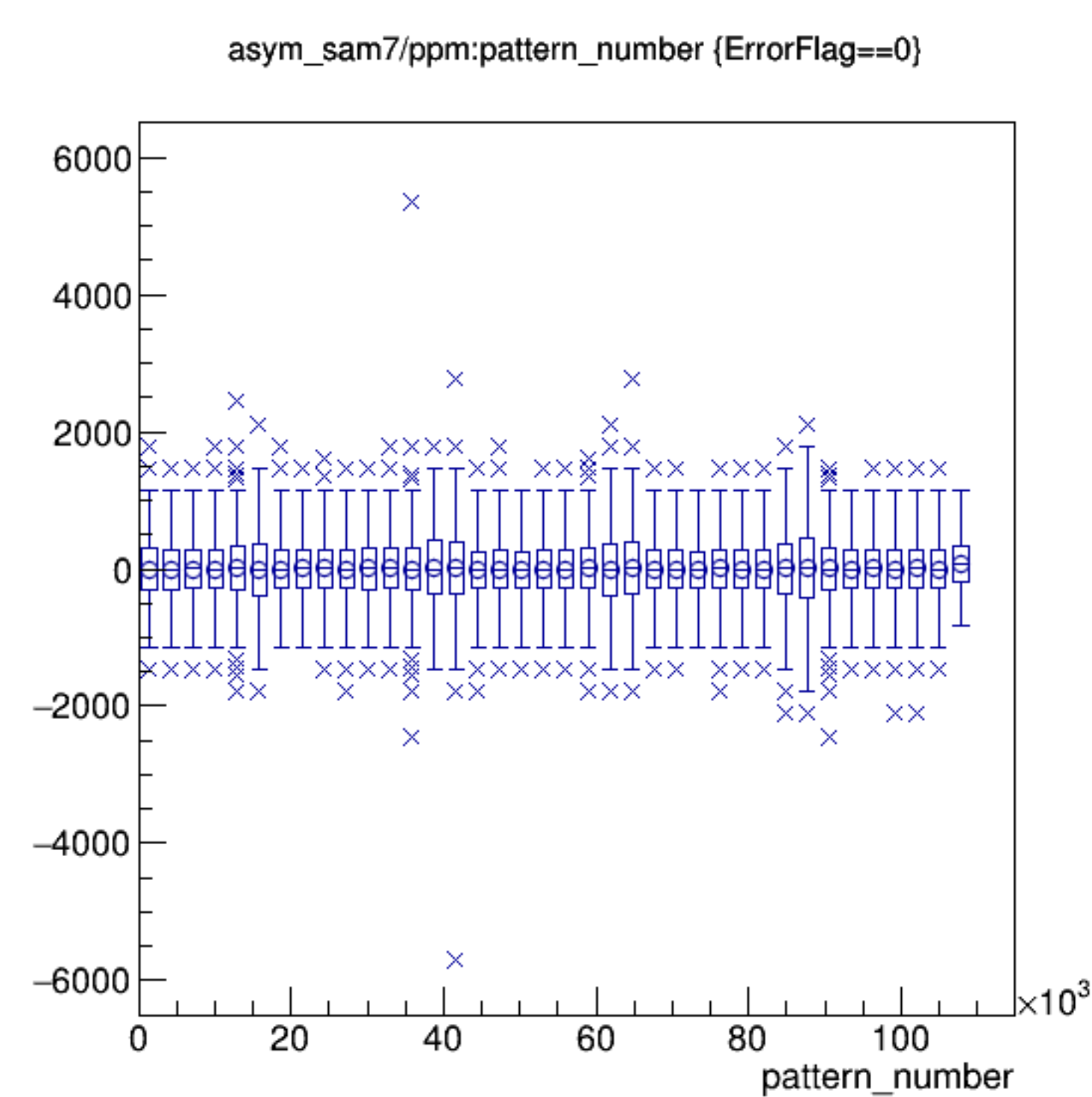
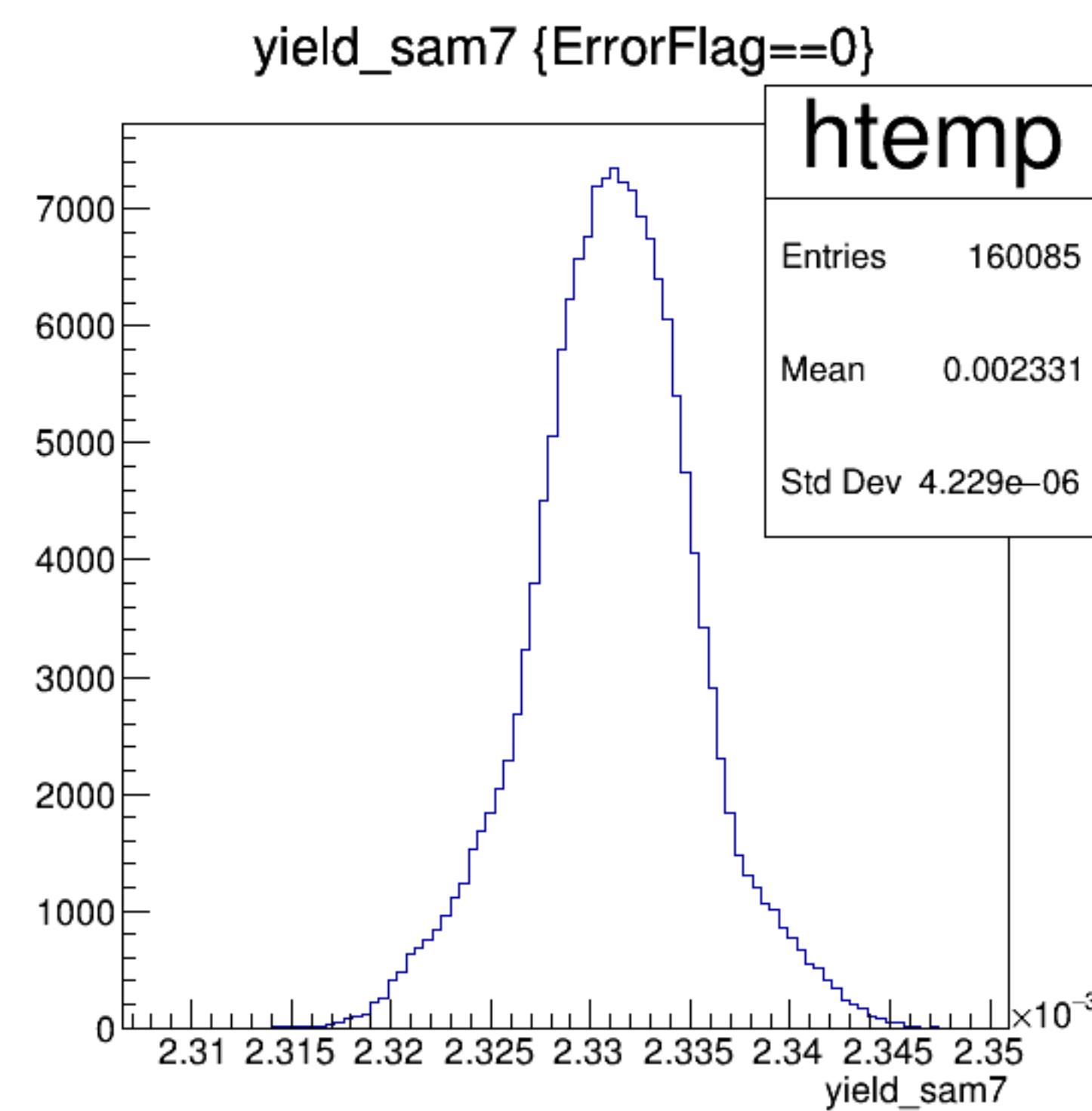
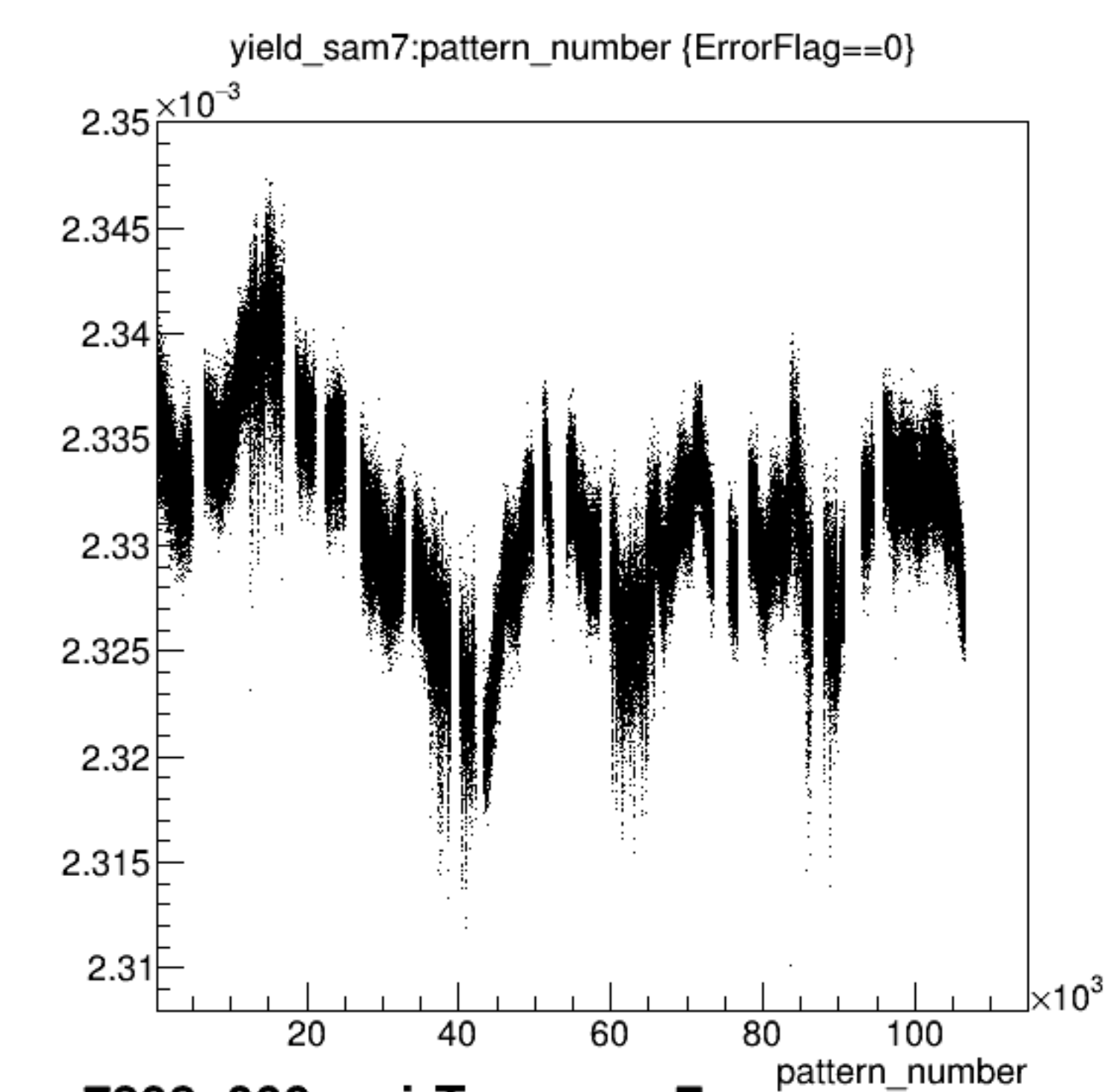


asym\_sam5/ppm:pattern\_number {ErrorFlag==0}



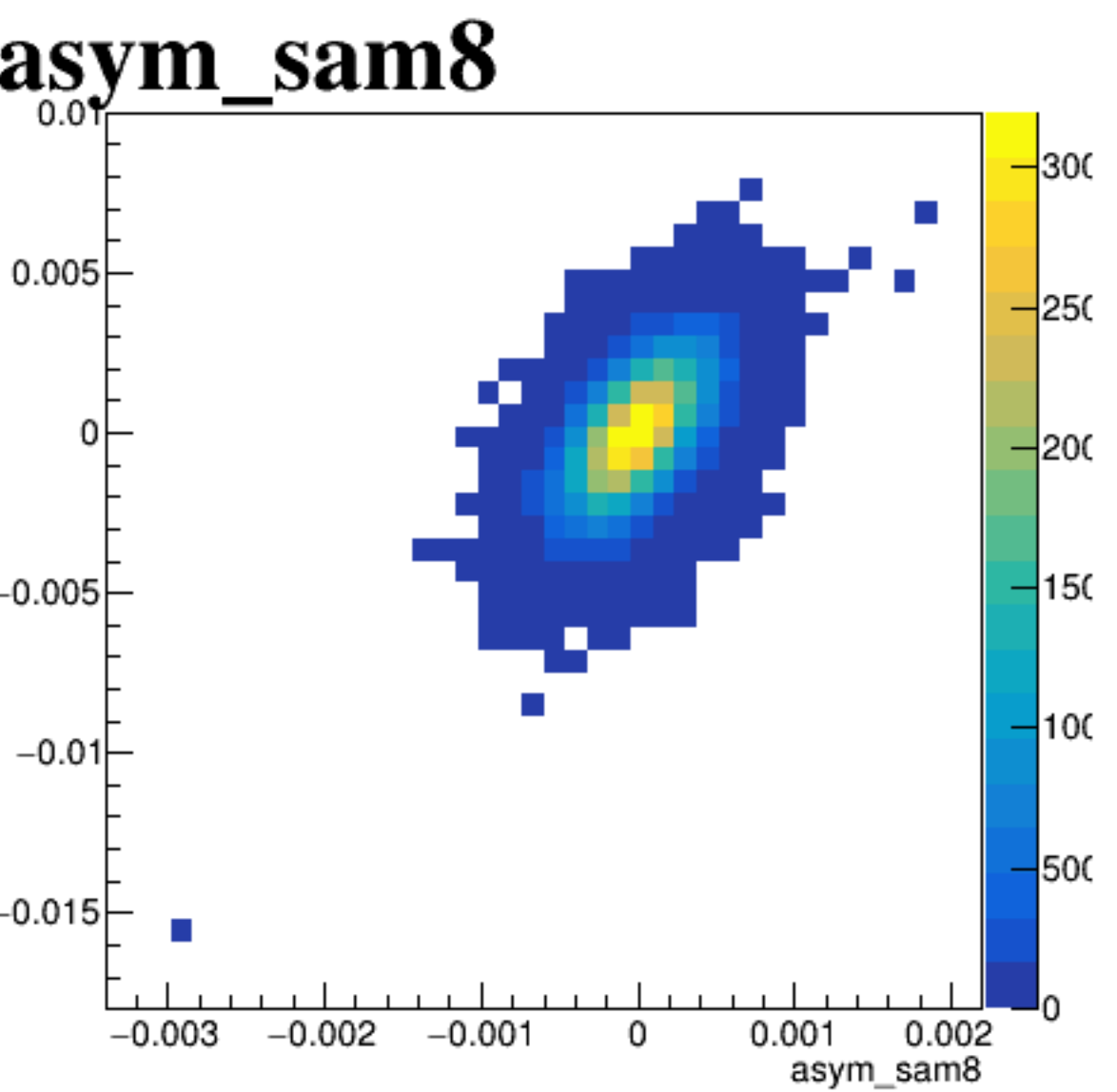
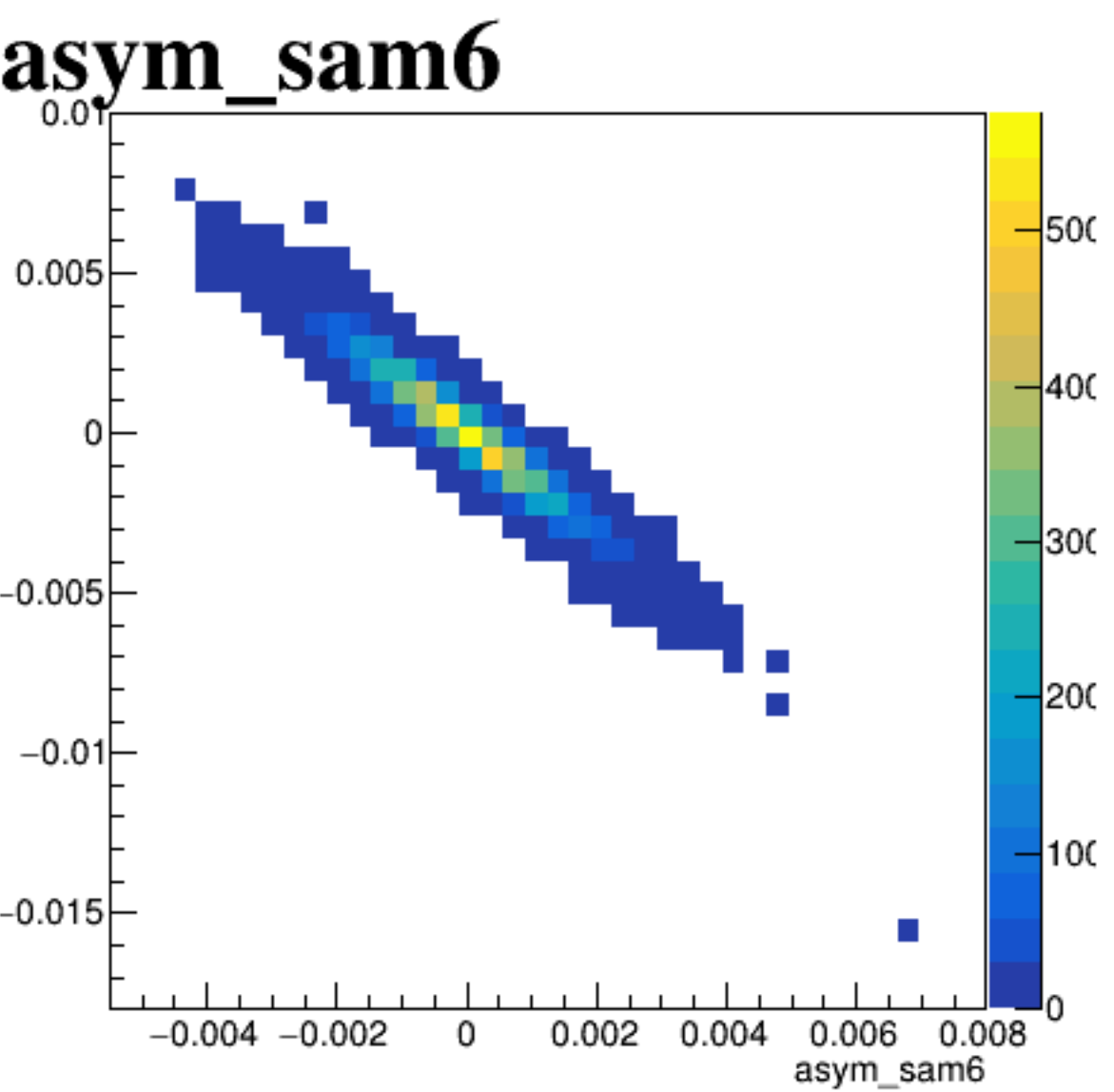
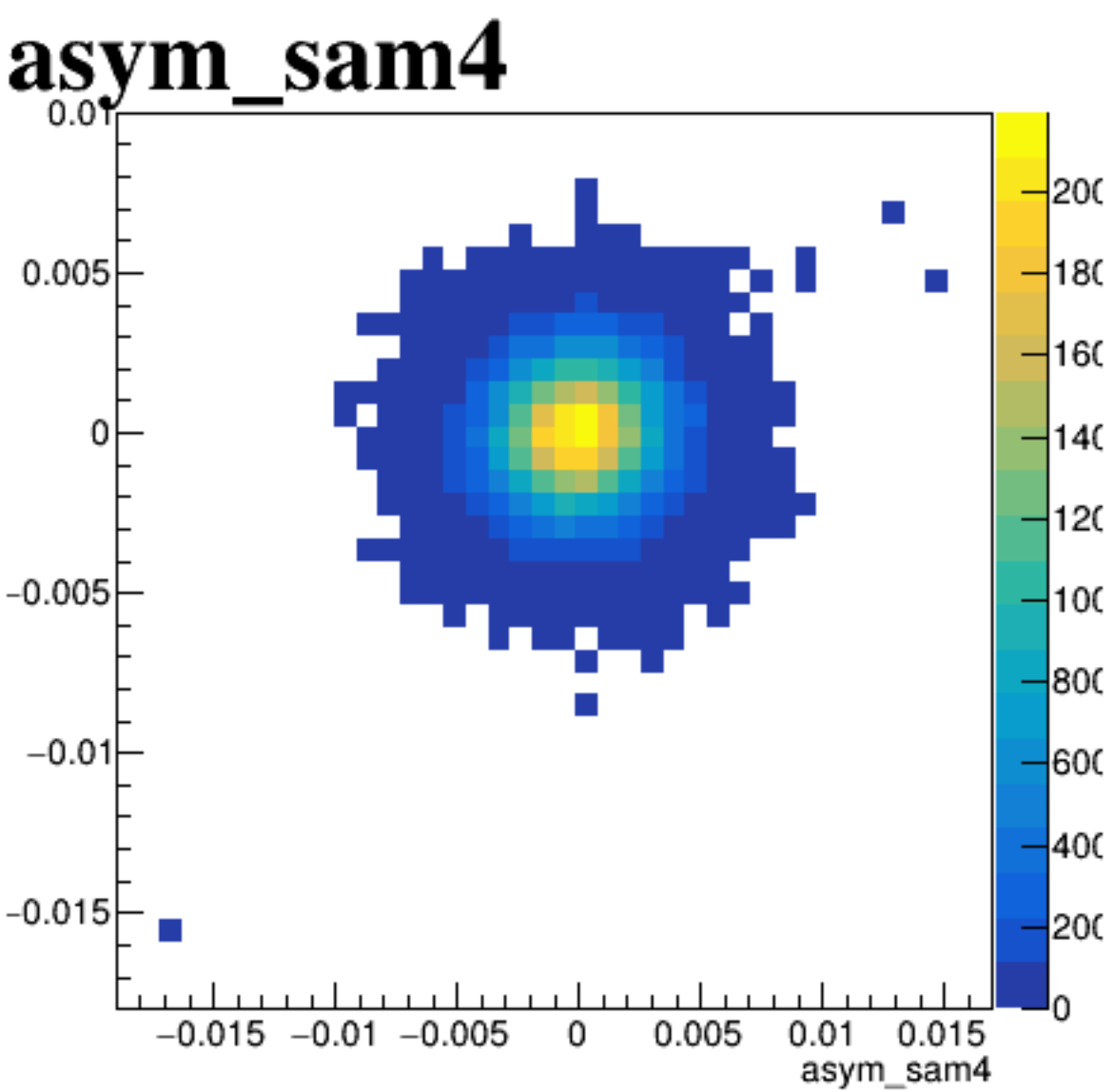
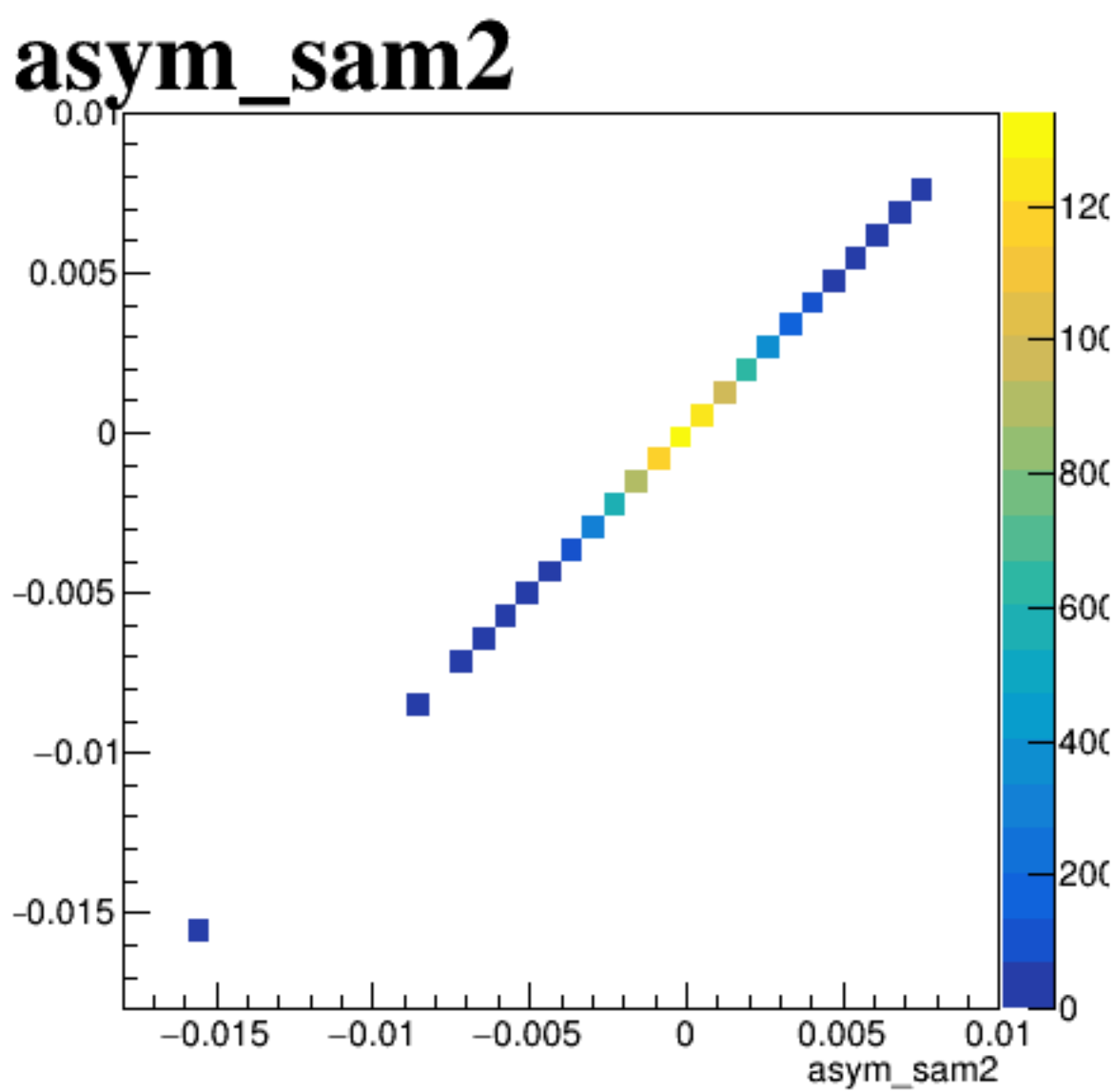
asym\_sam5/ppm {ErrorFlag==0}



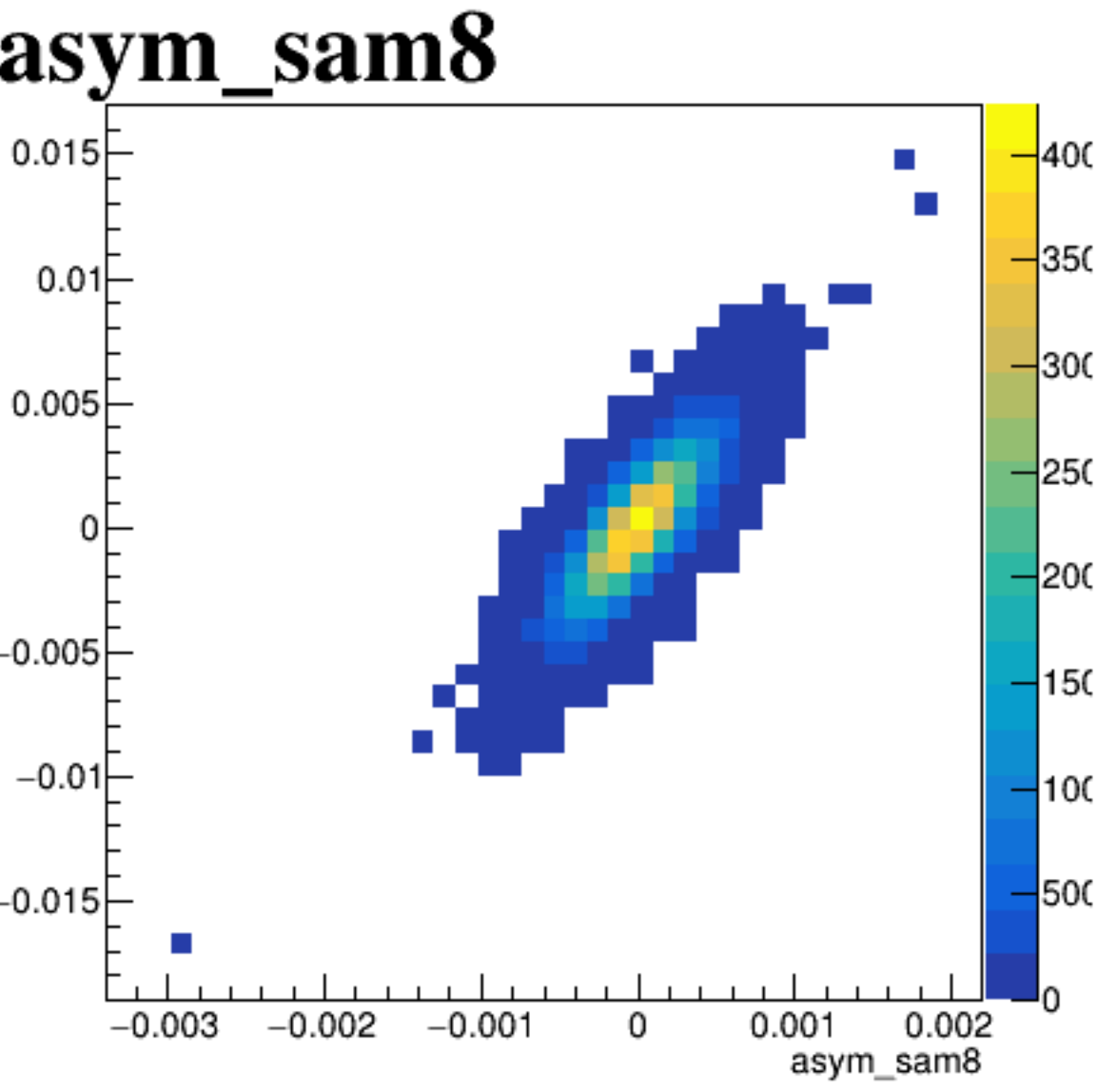
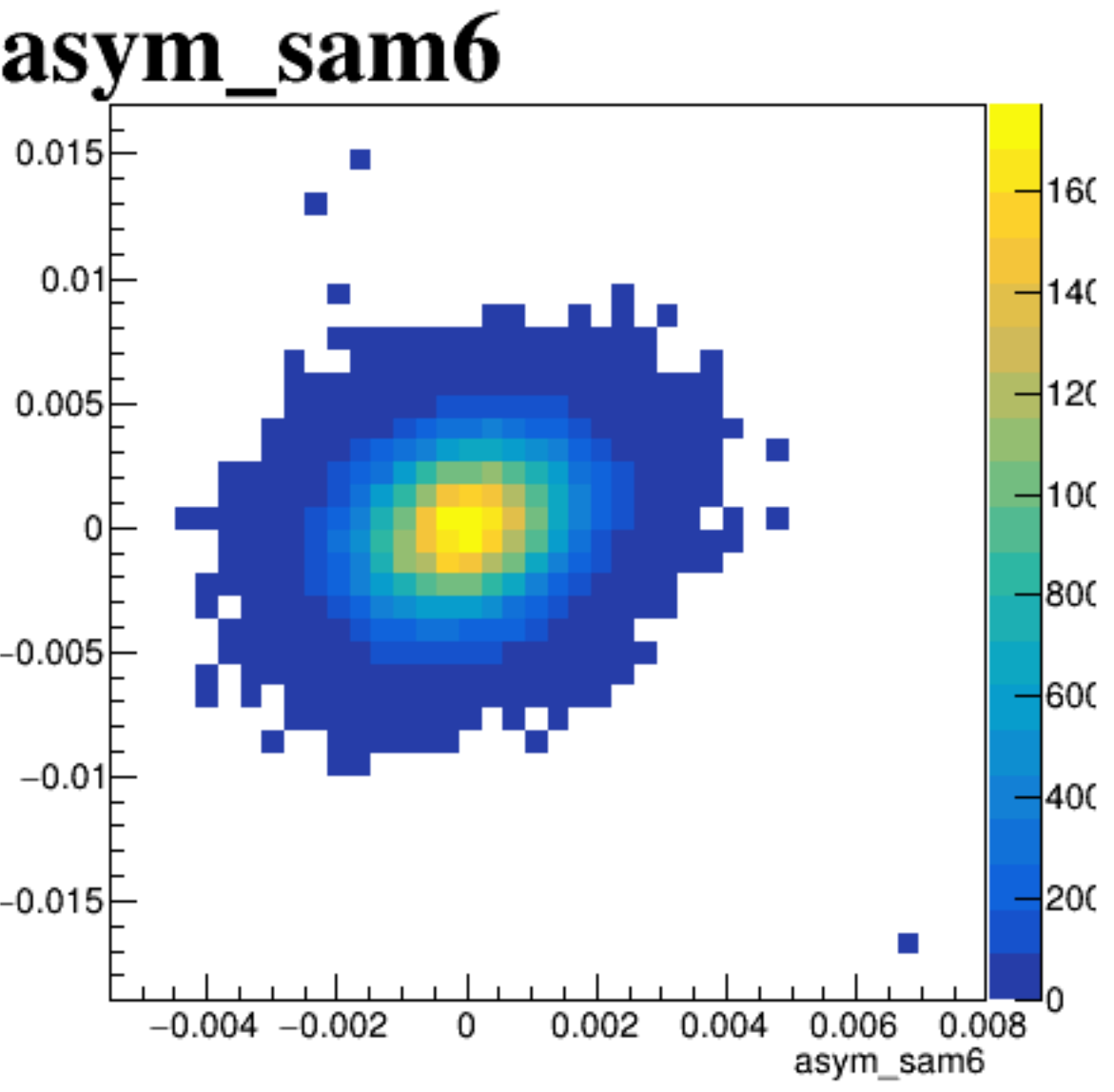
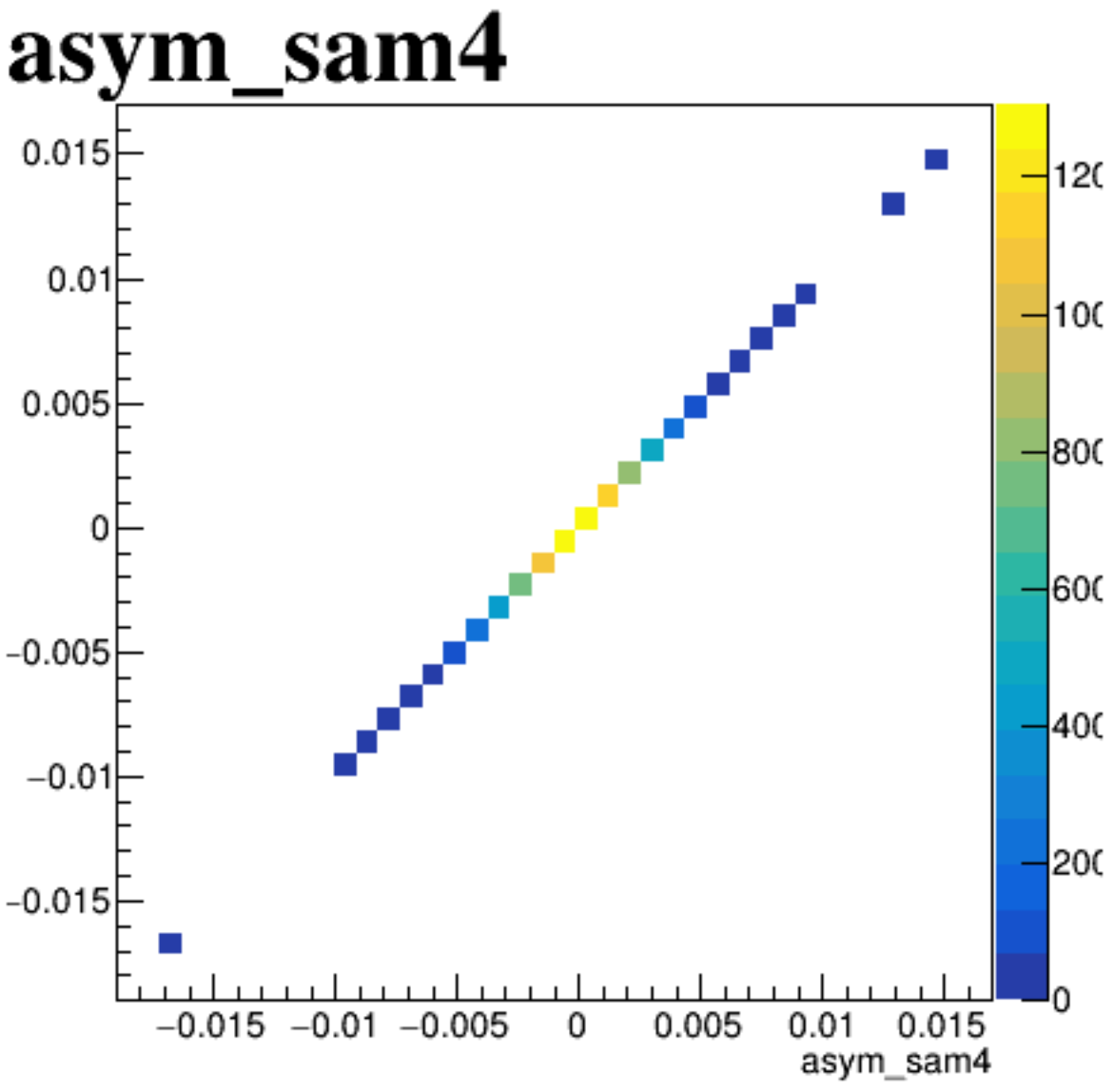
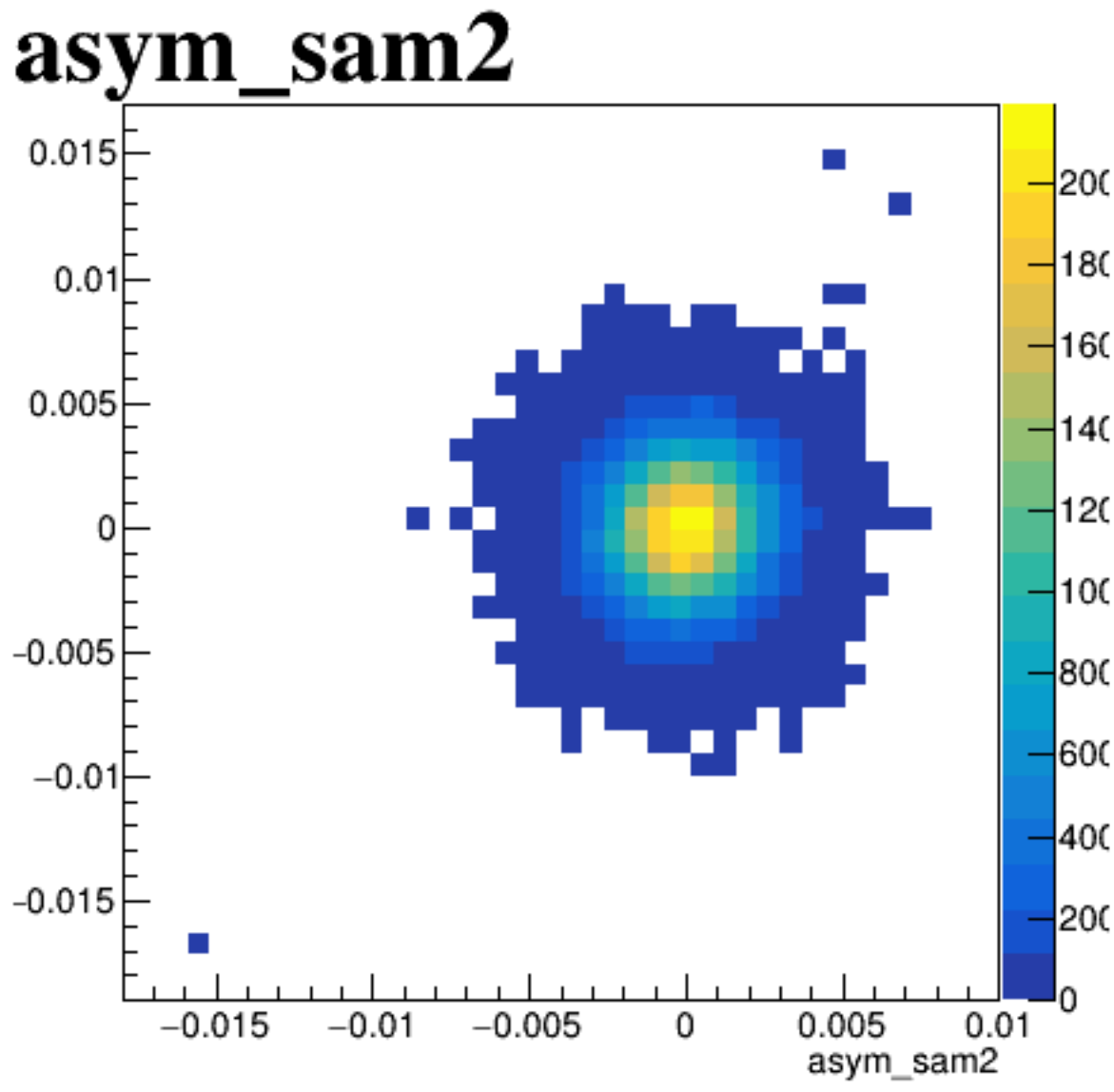




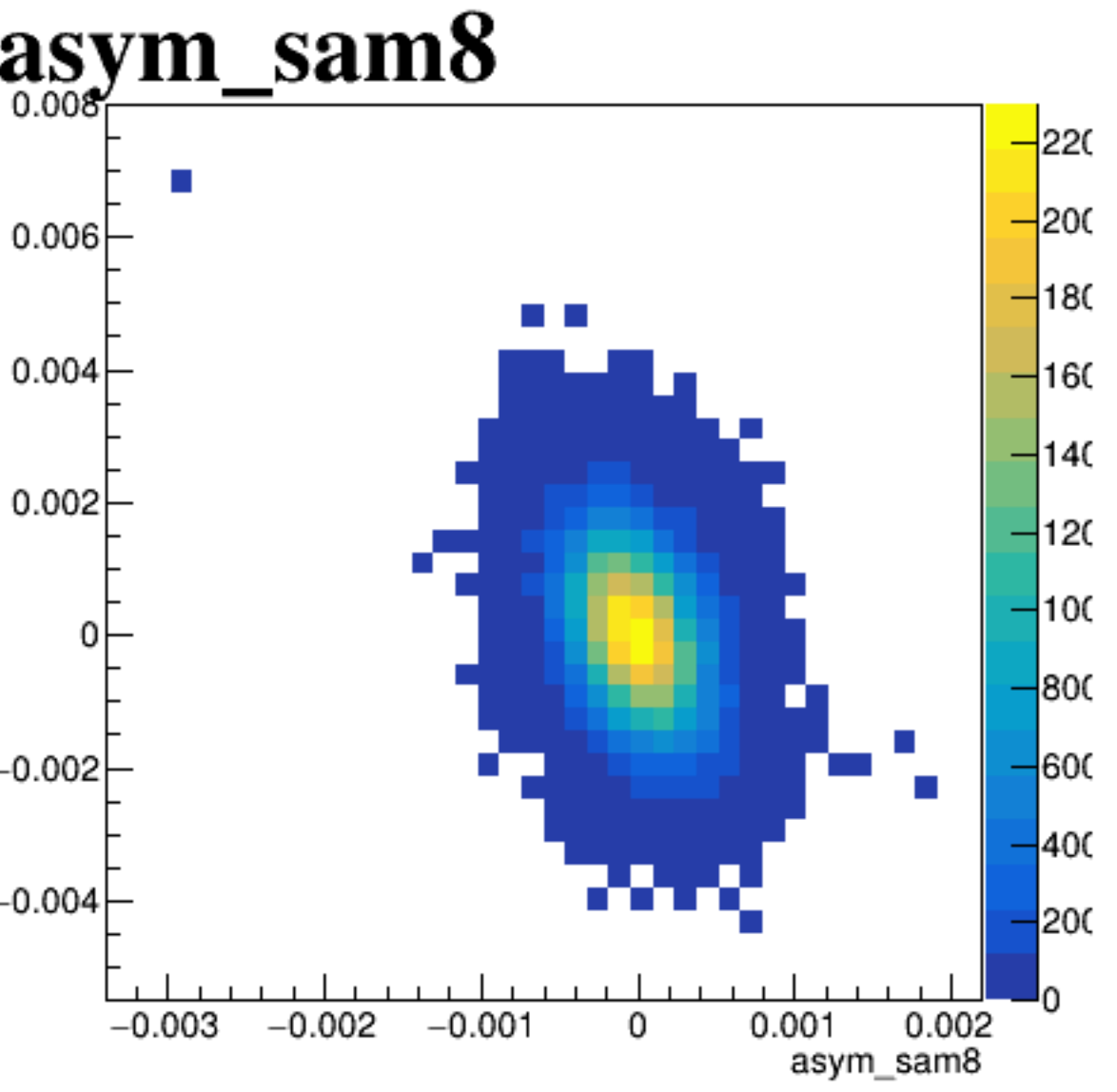
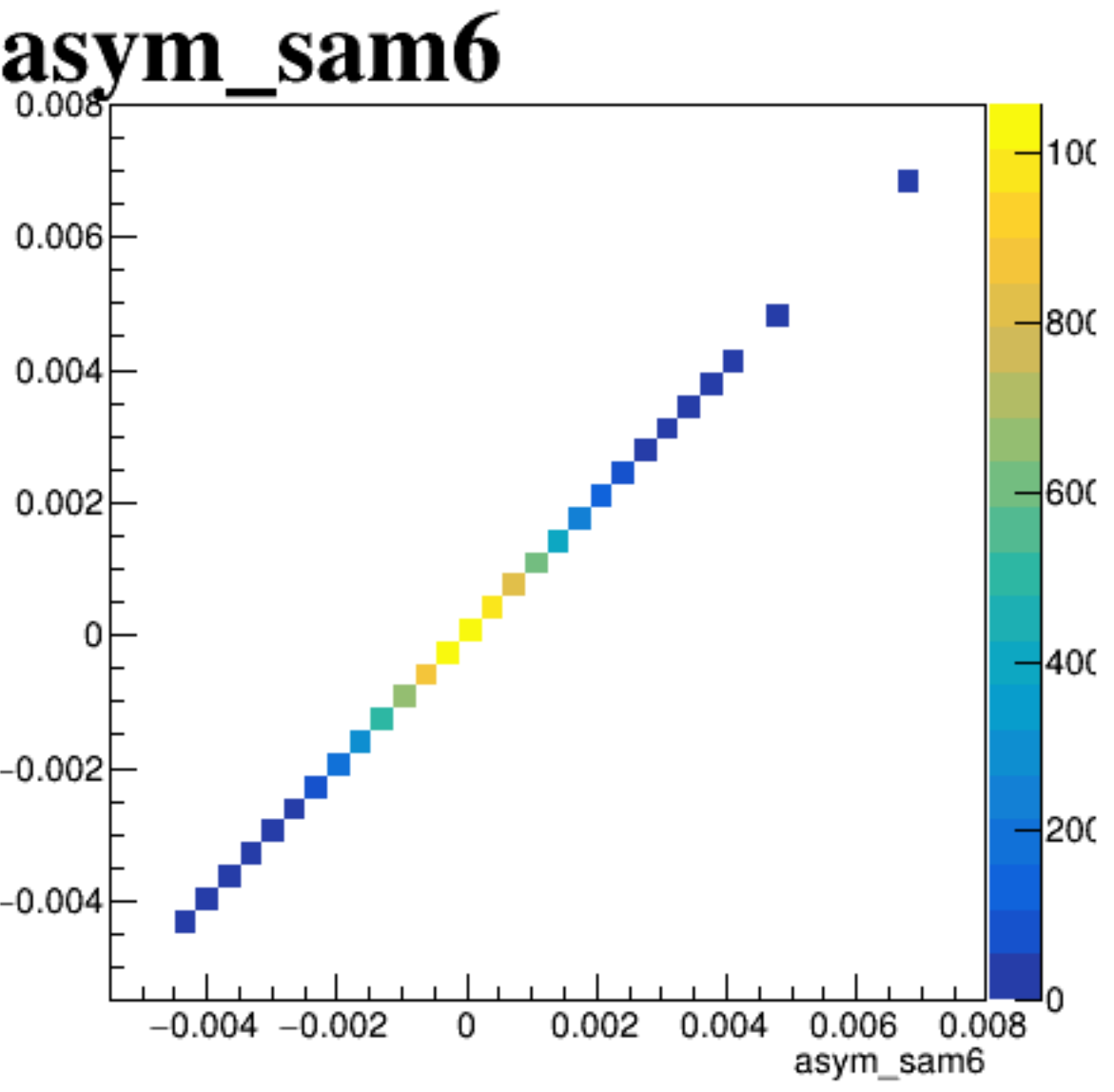
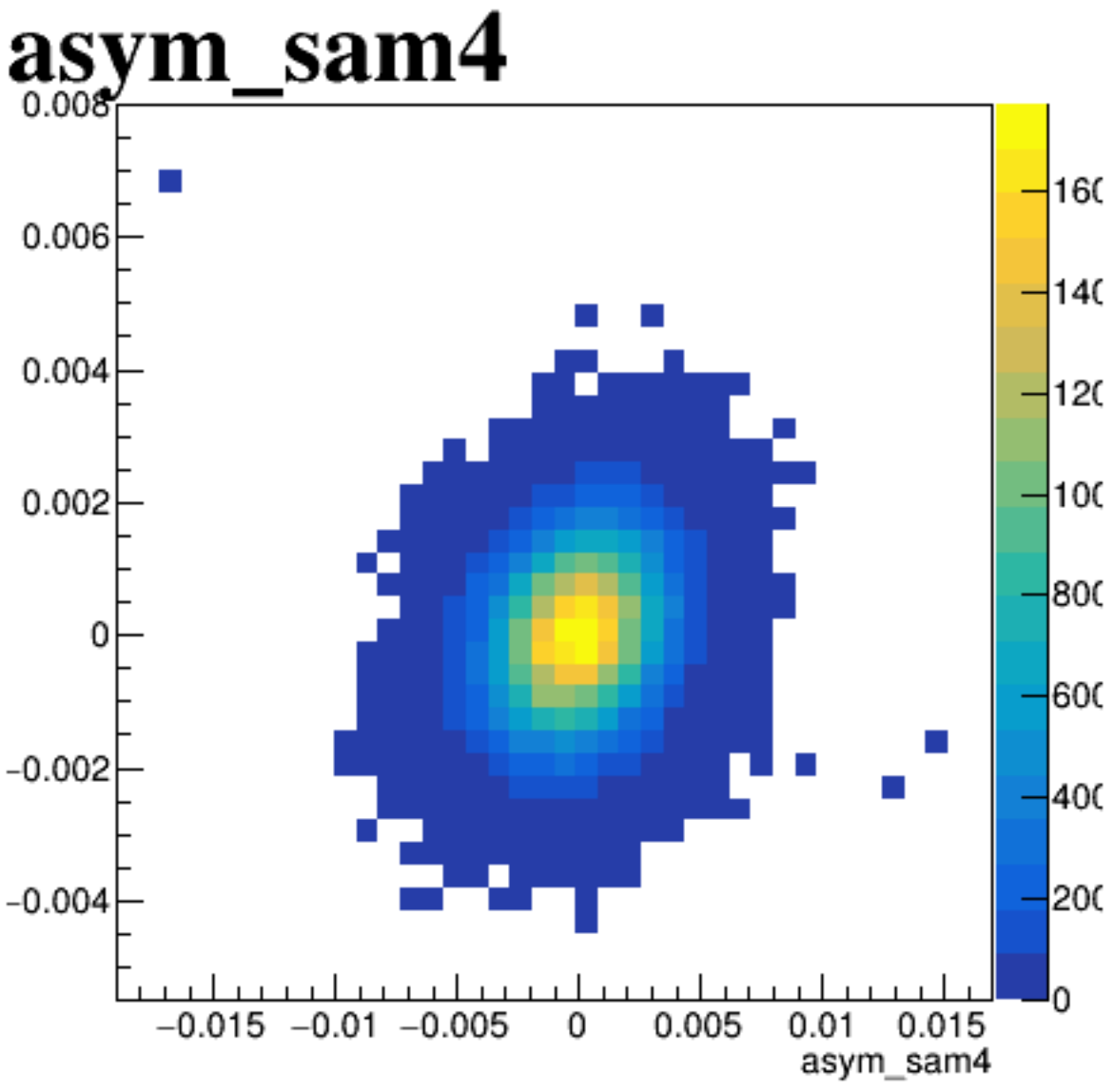
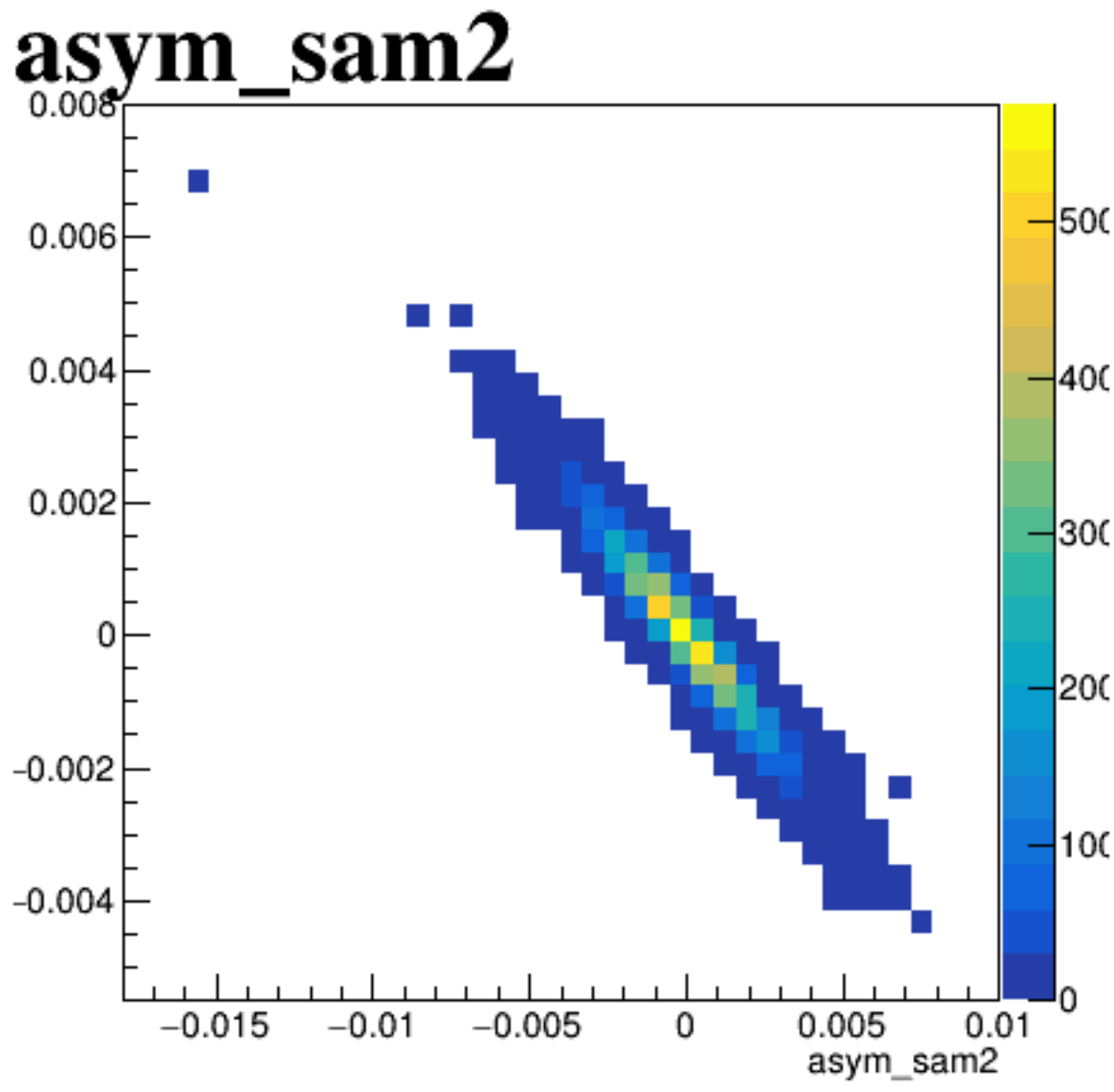
asym\_sam2



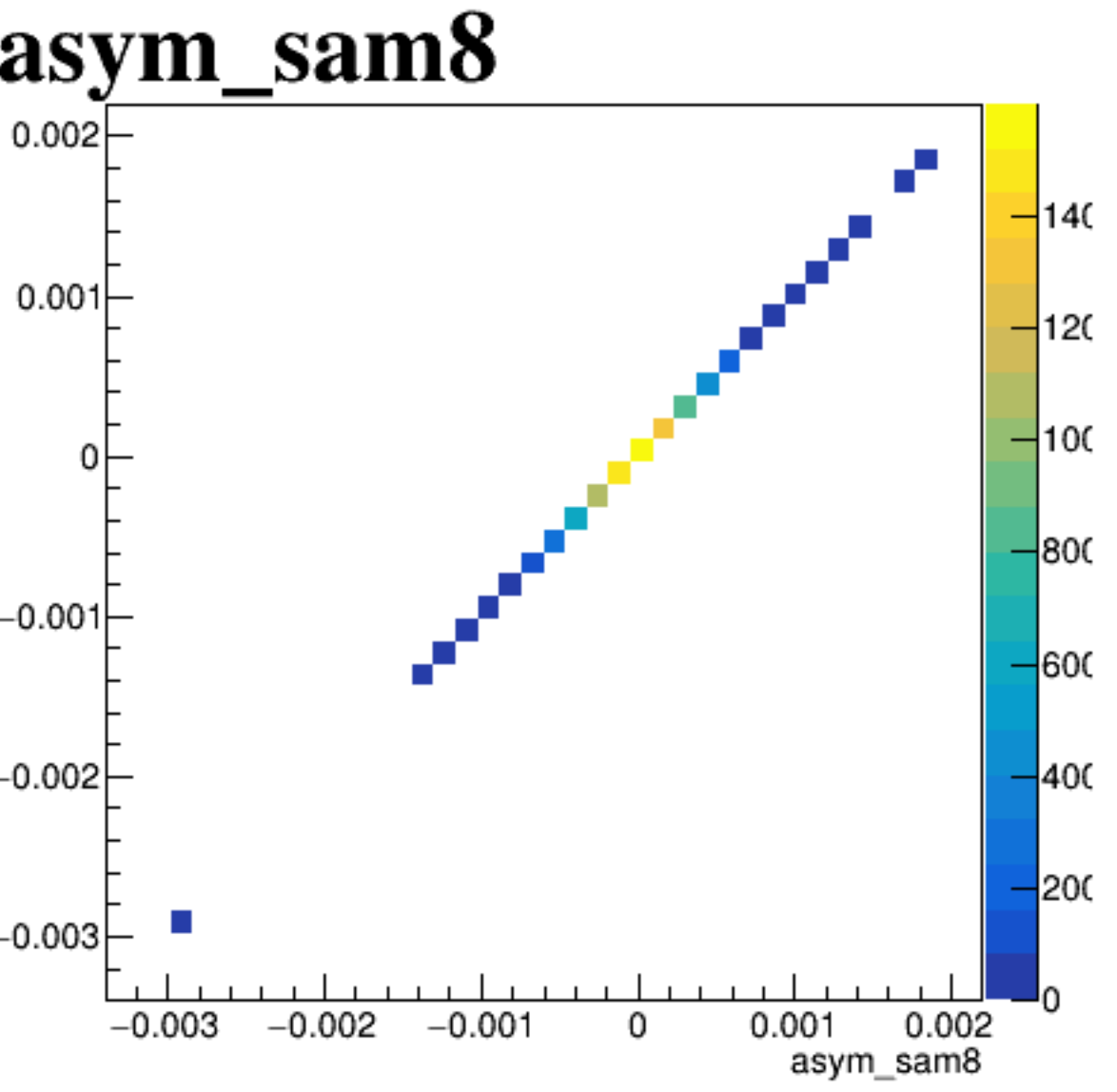
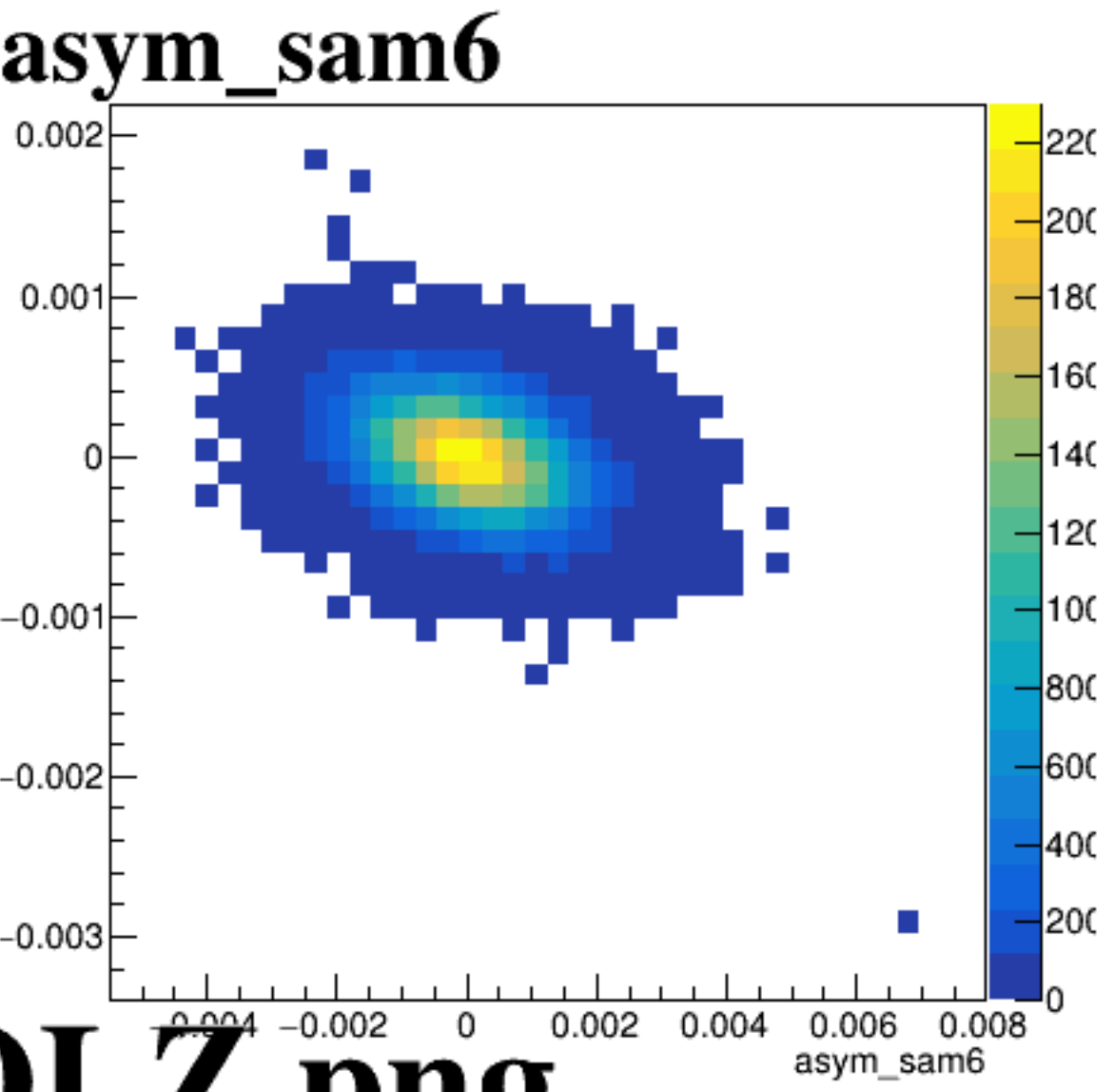
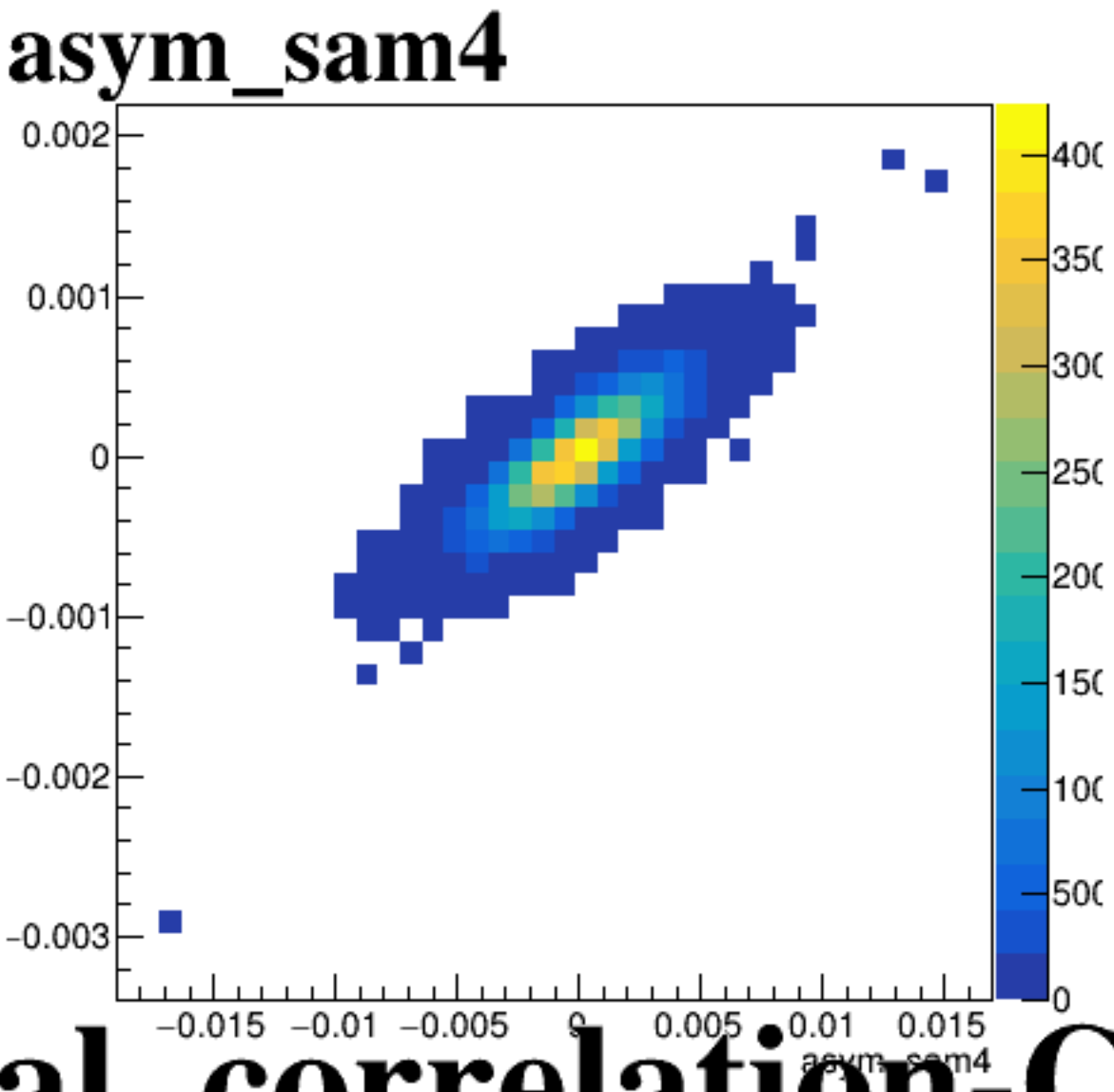
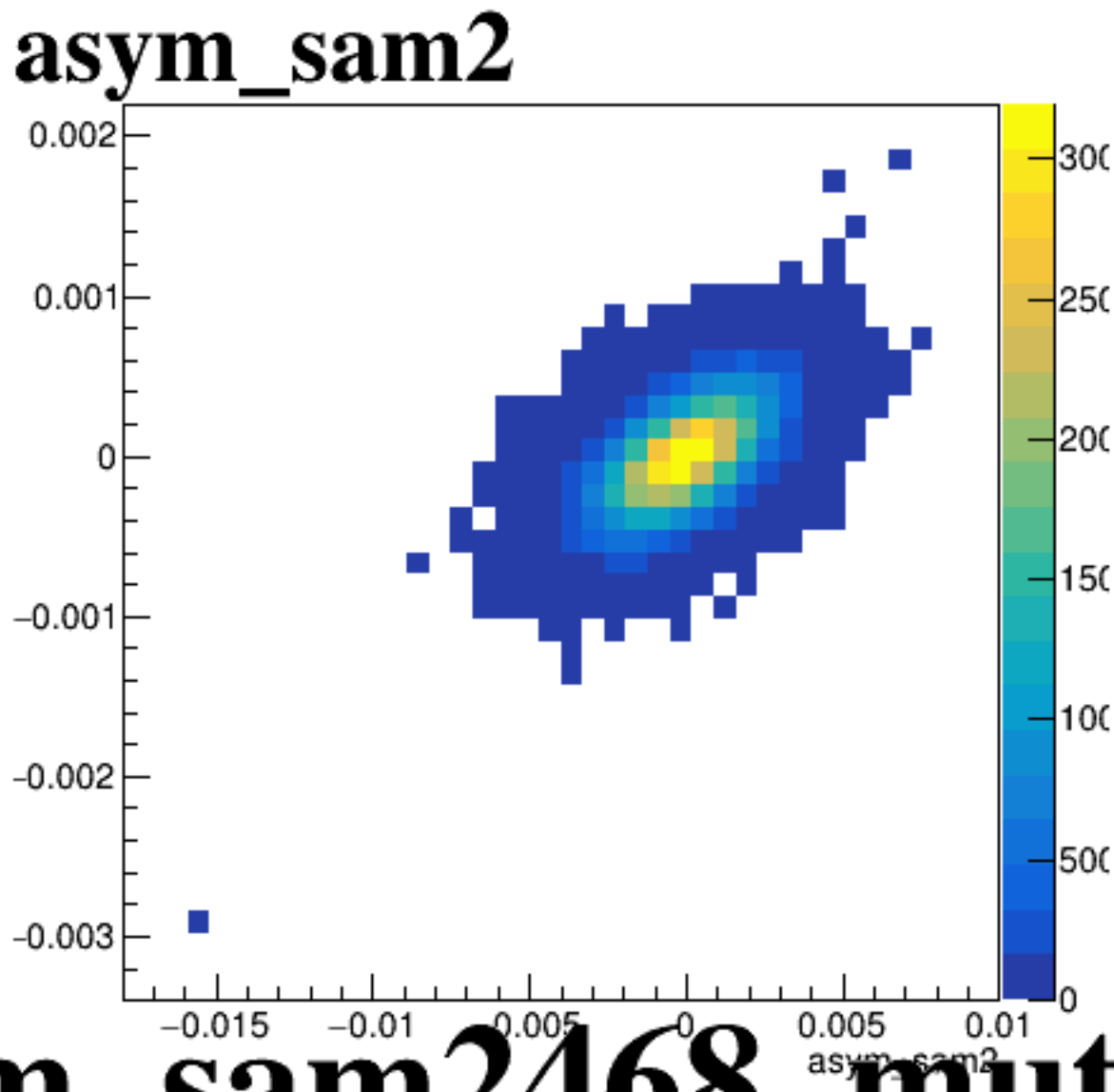
asym\_sam4



asym\_sam6

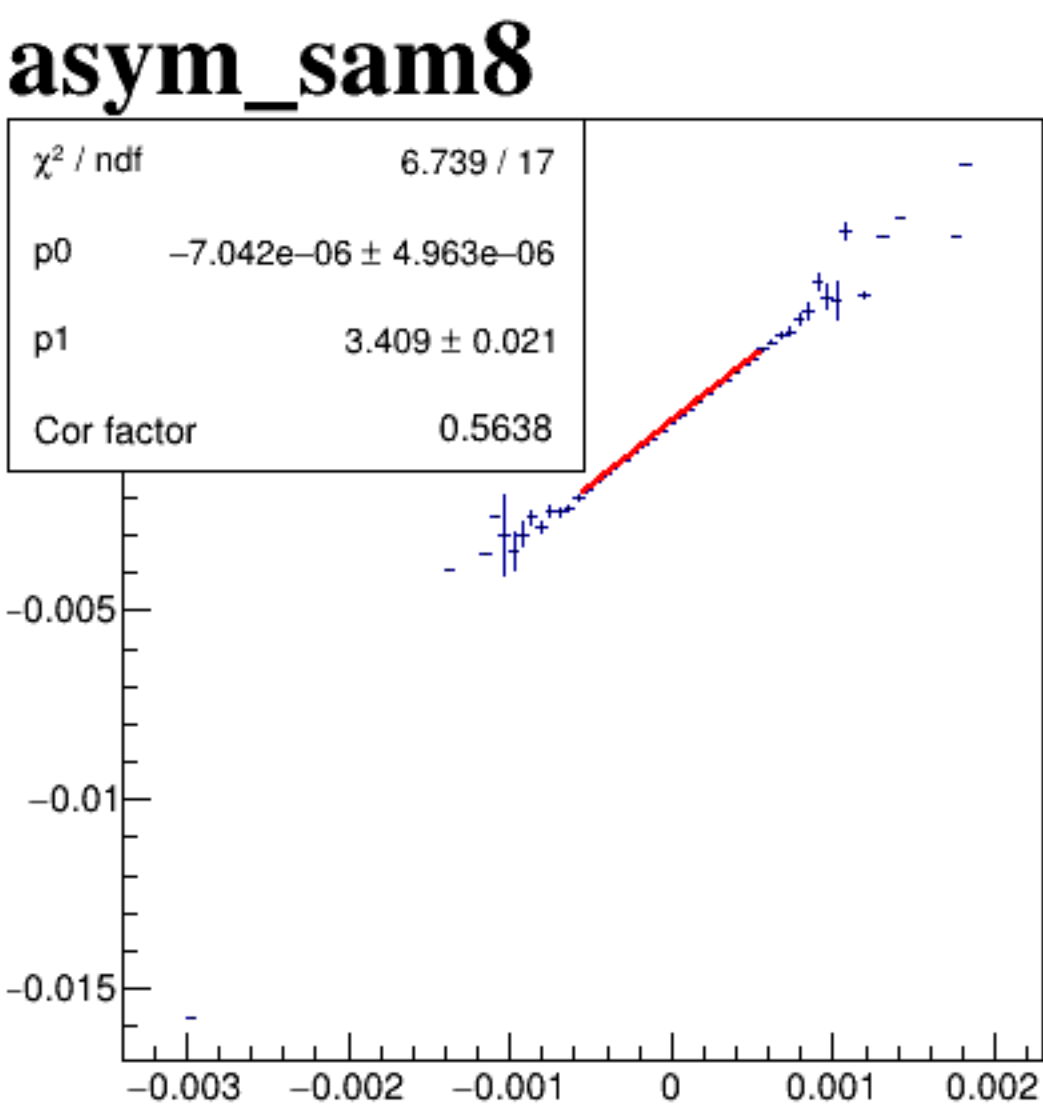
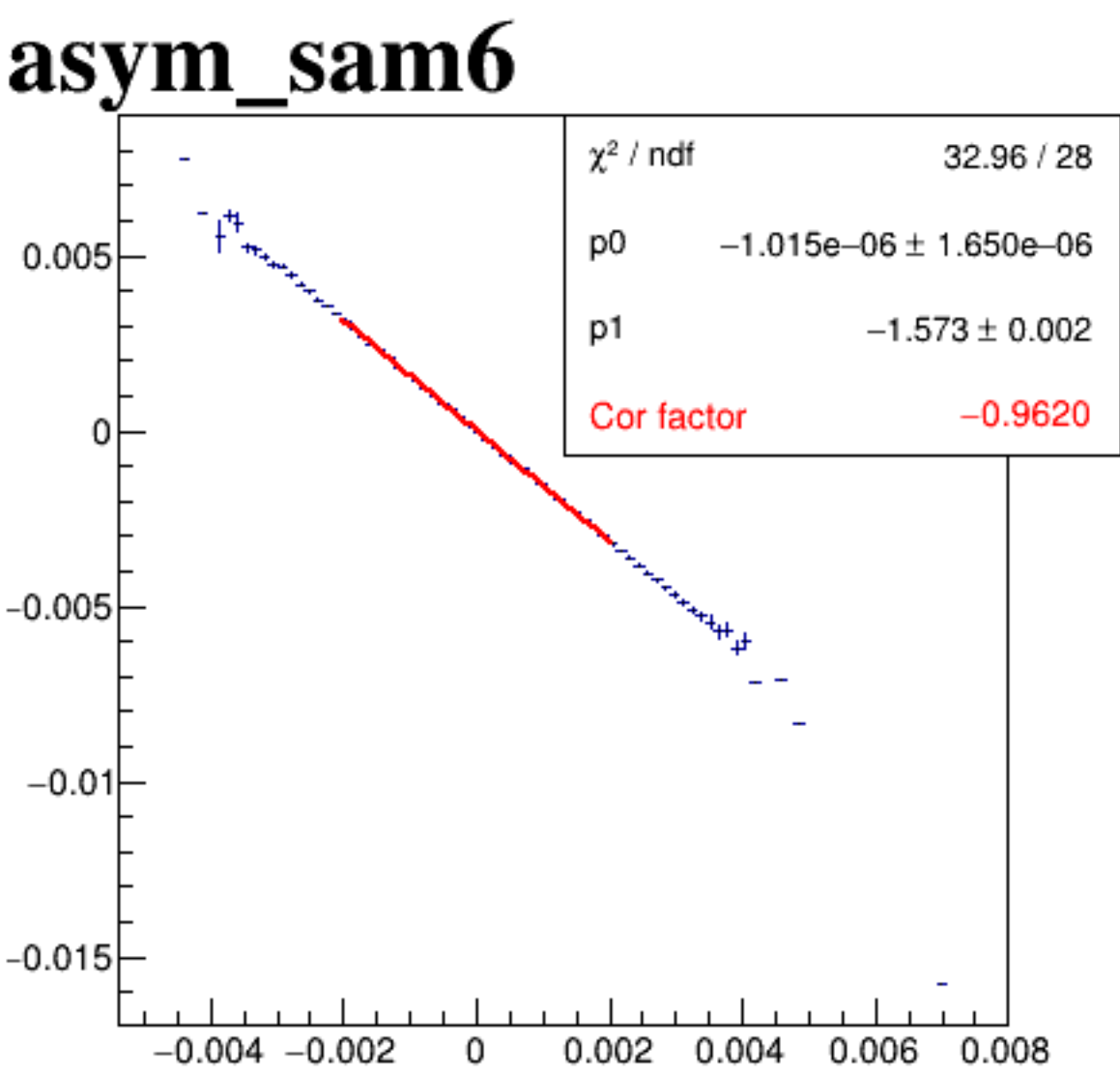
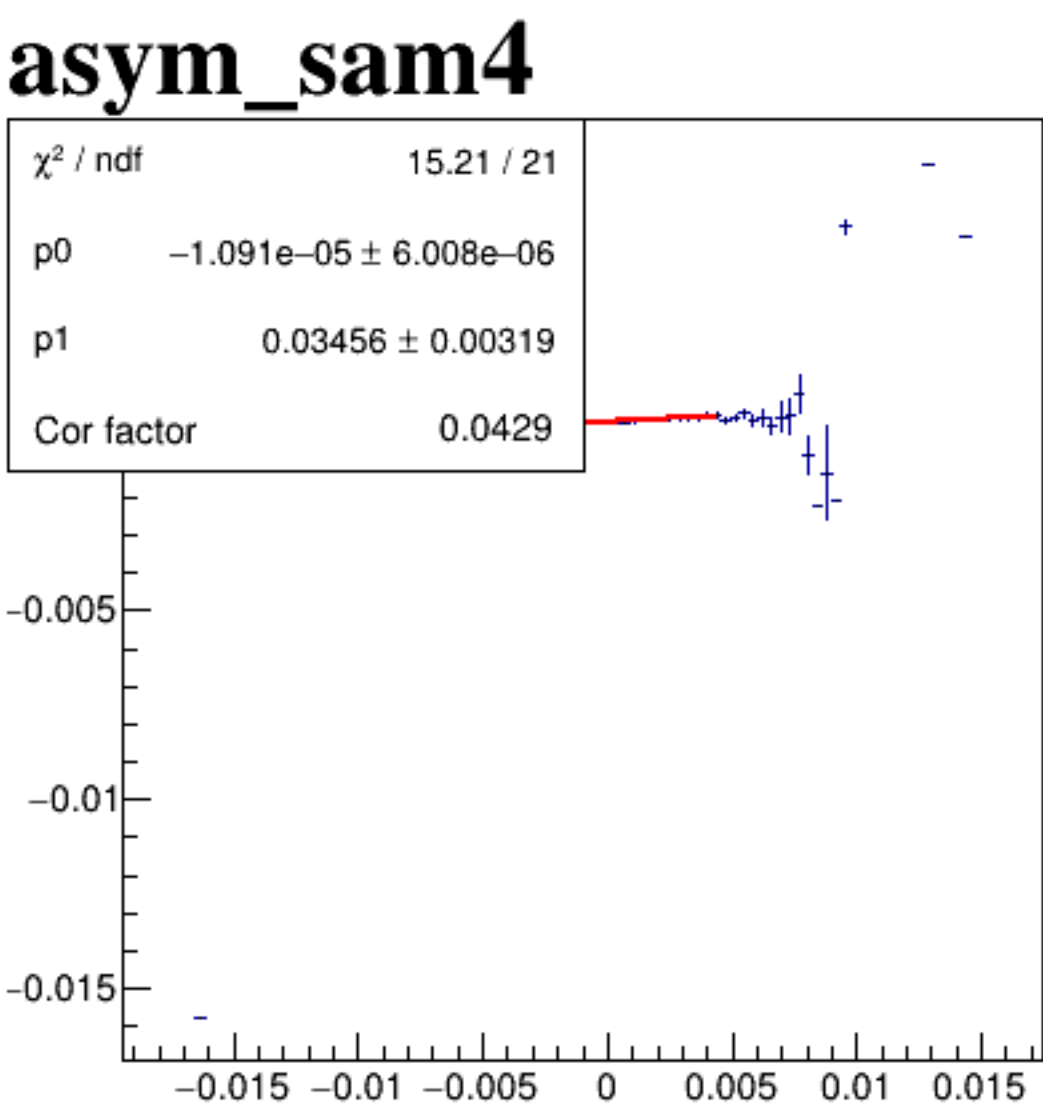
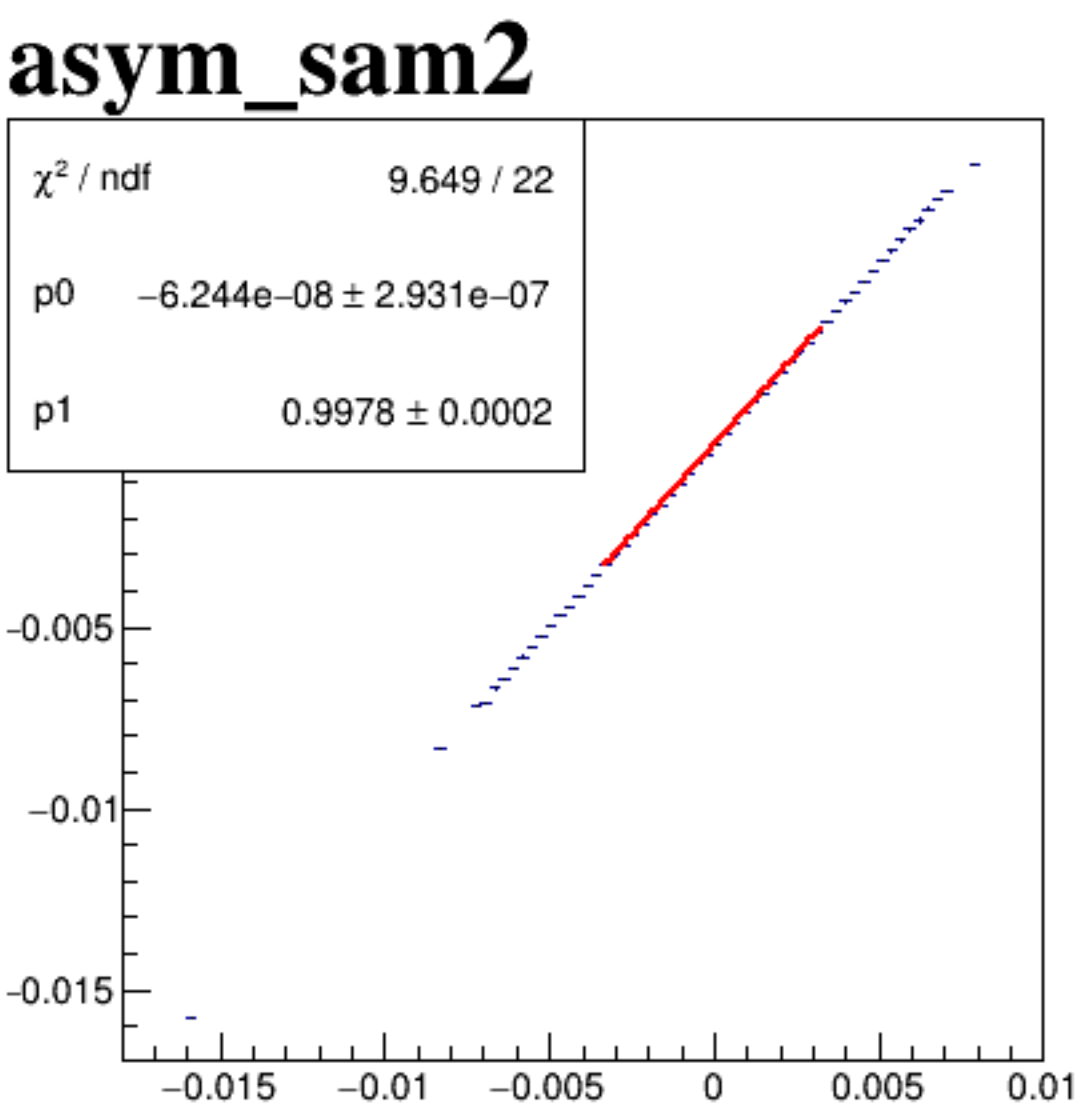


asym\_sam8

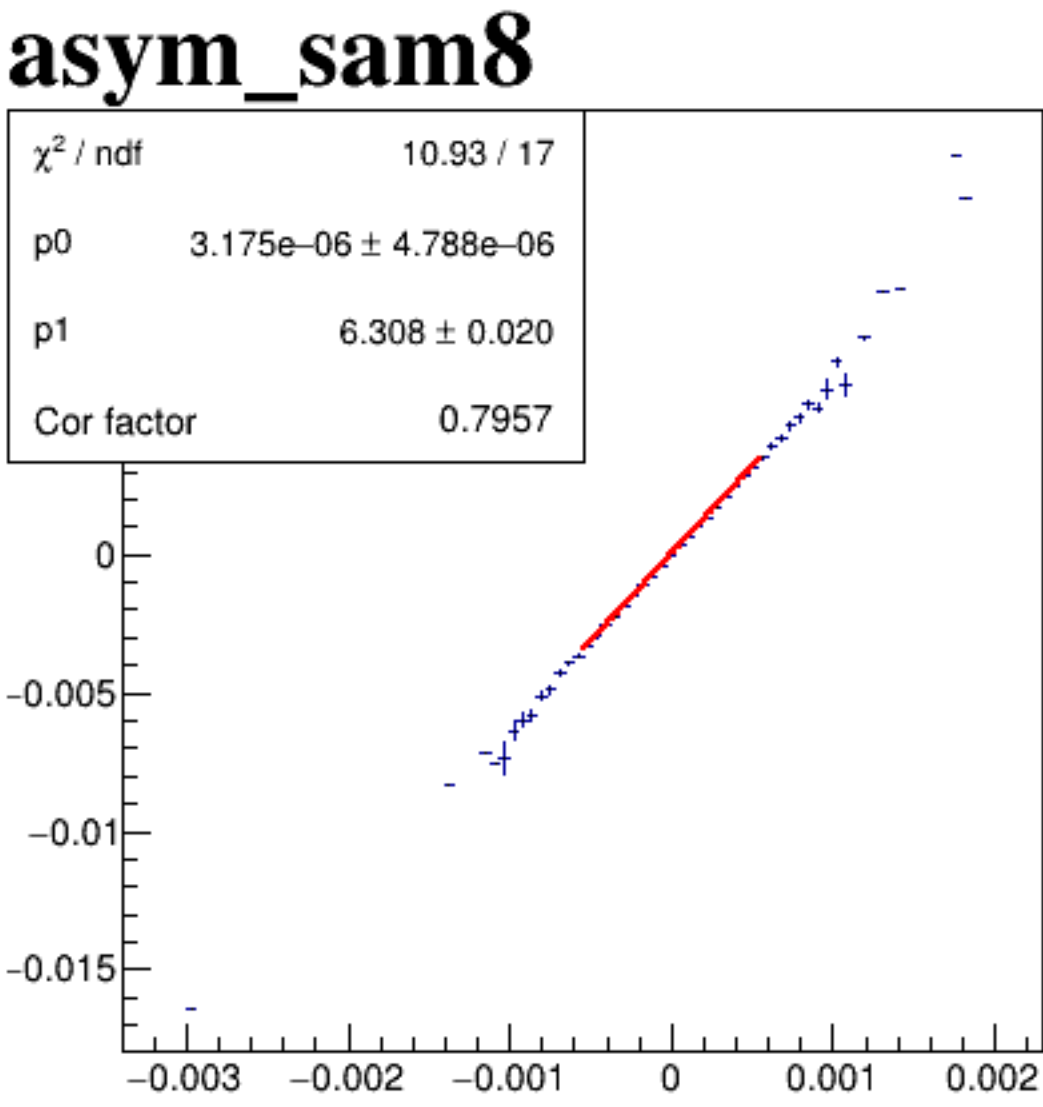
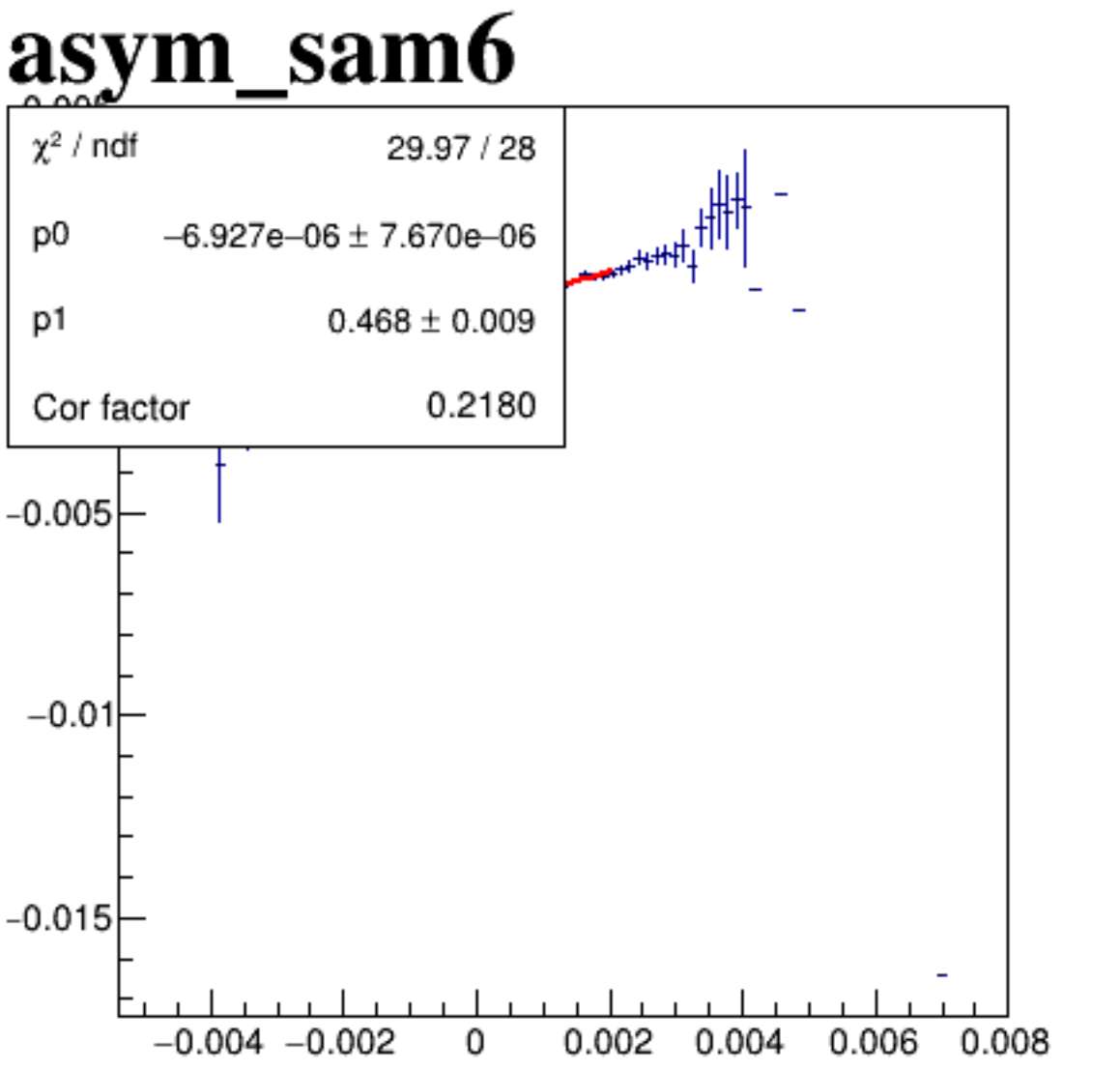
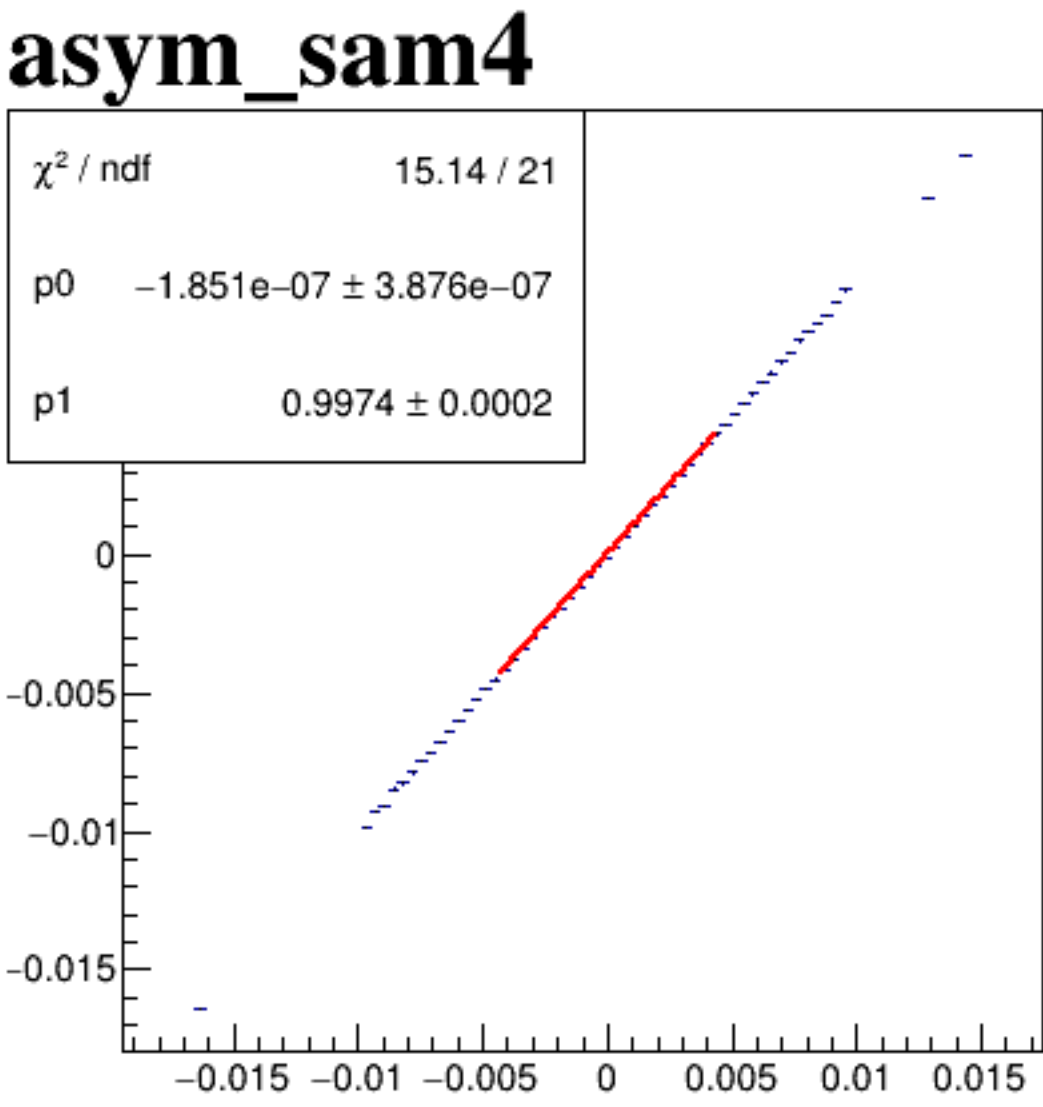
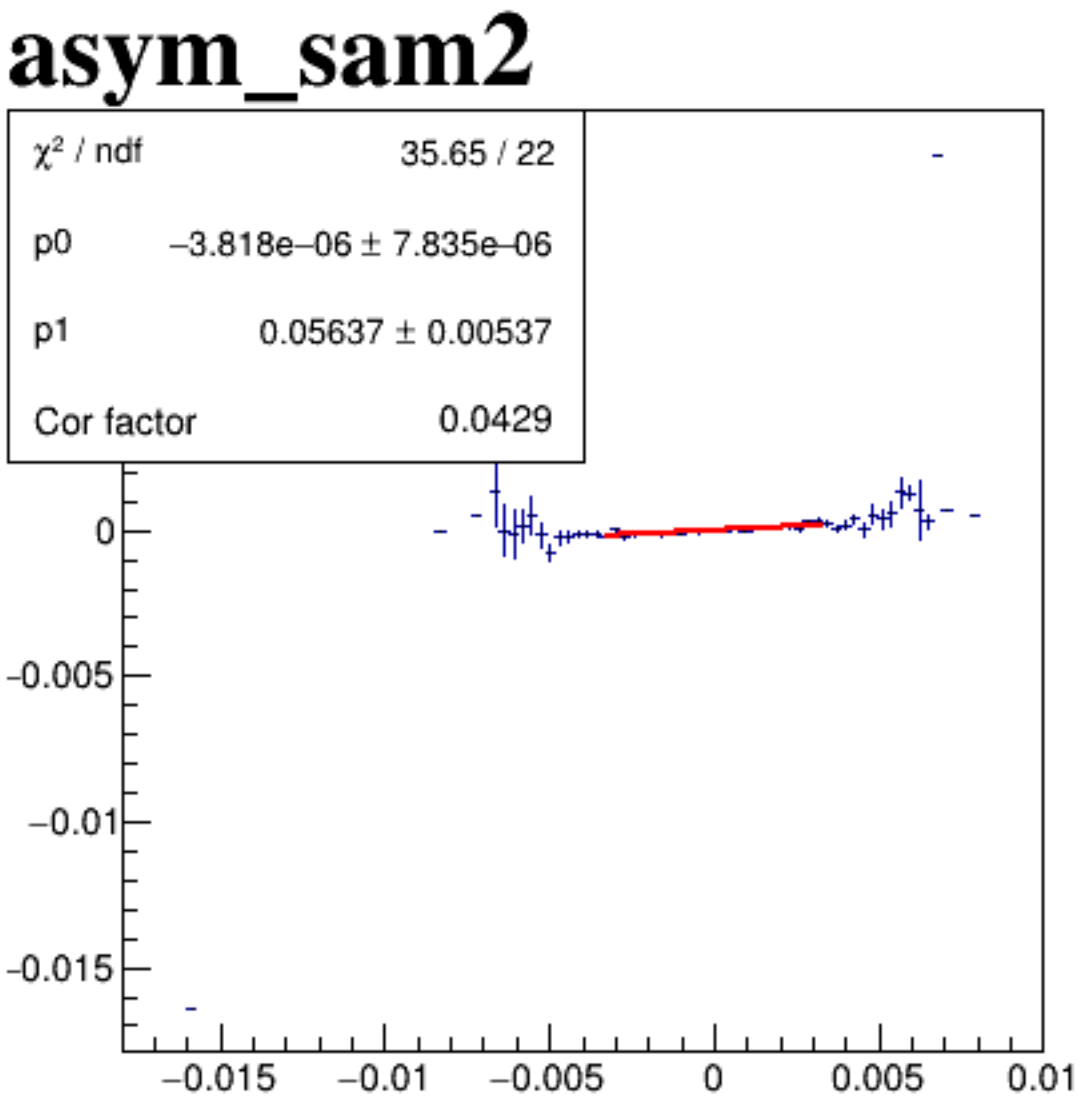




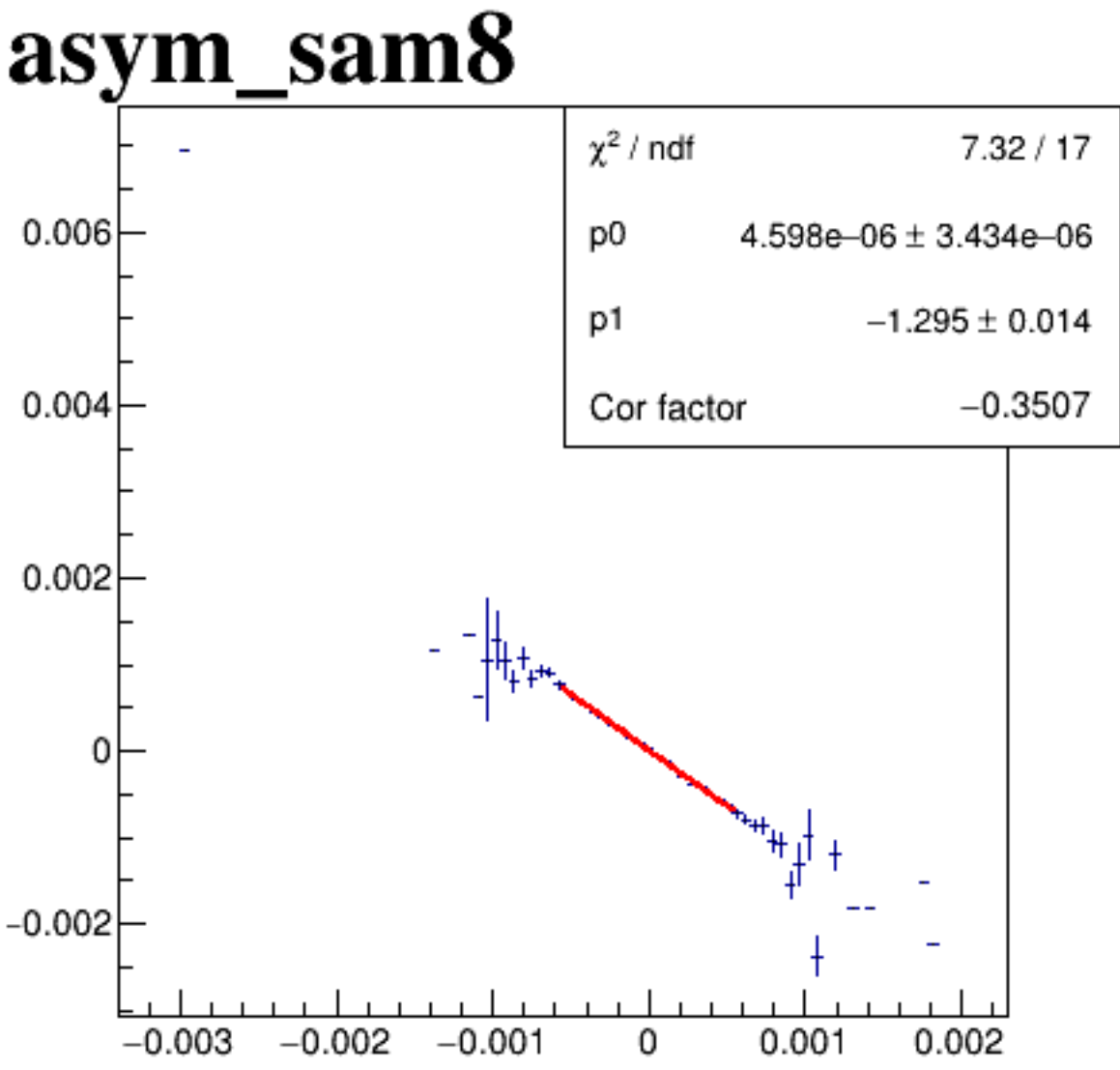
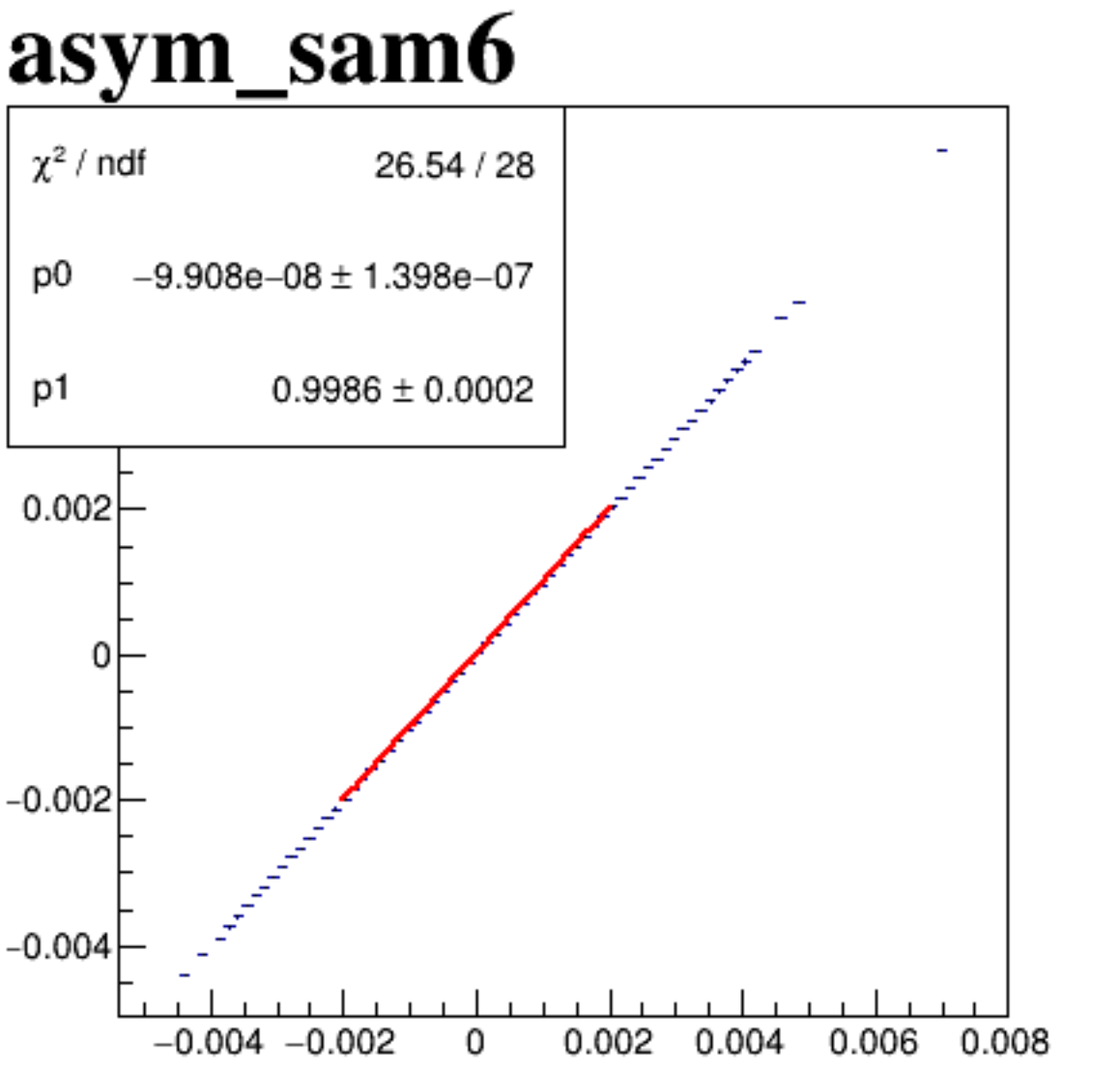
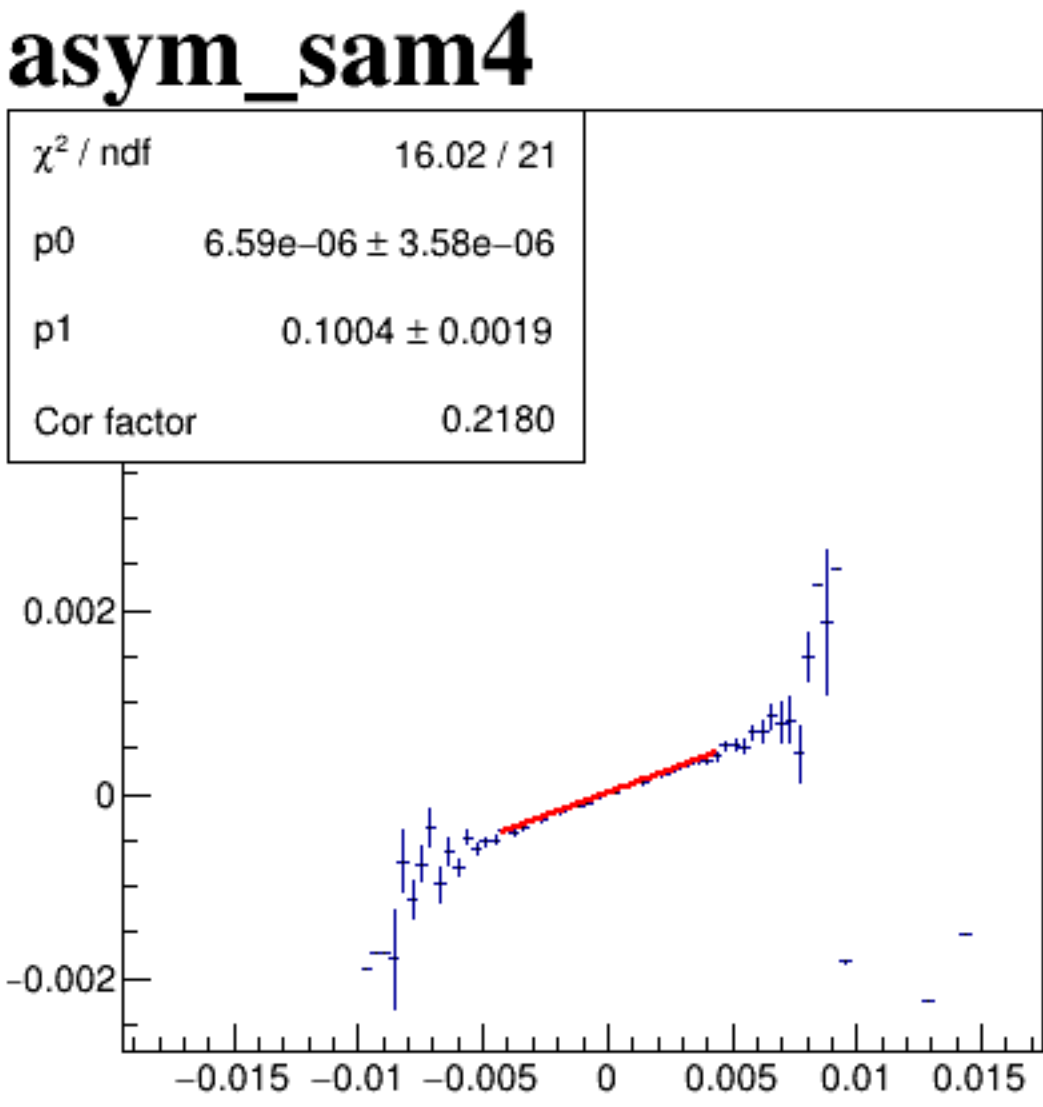
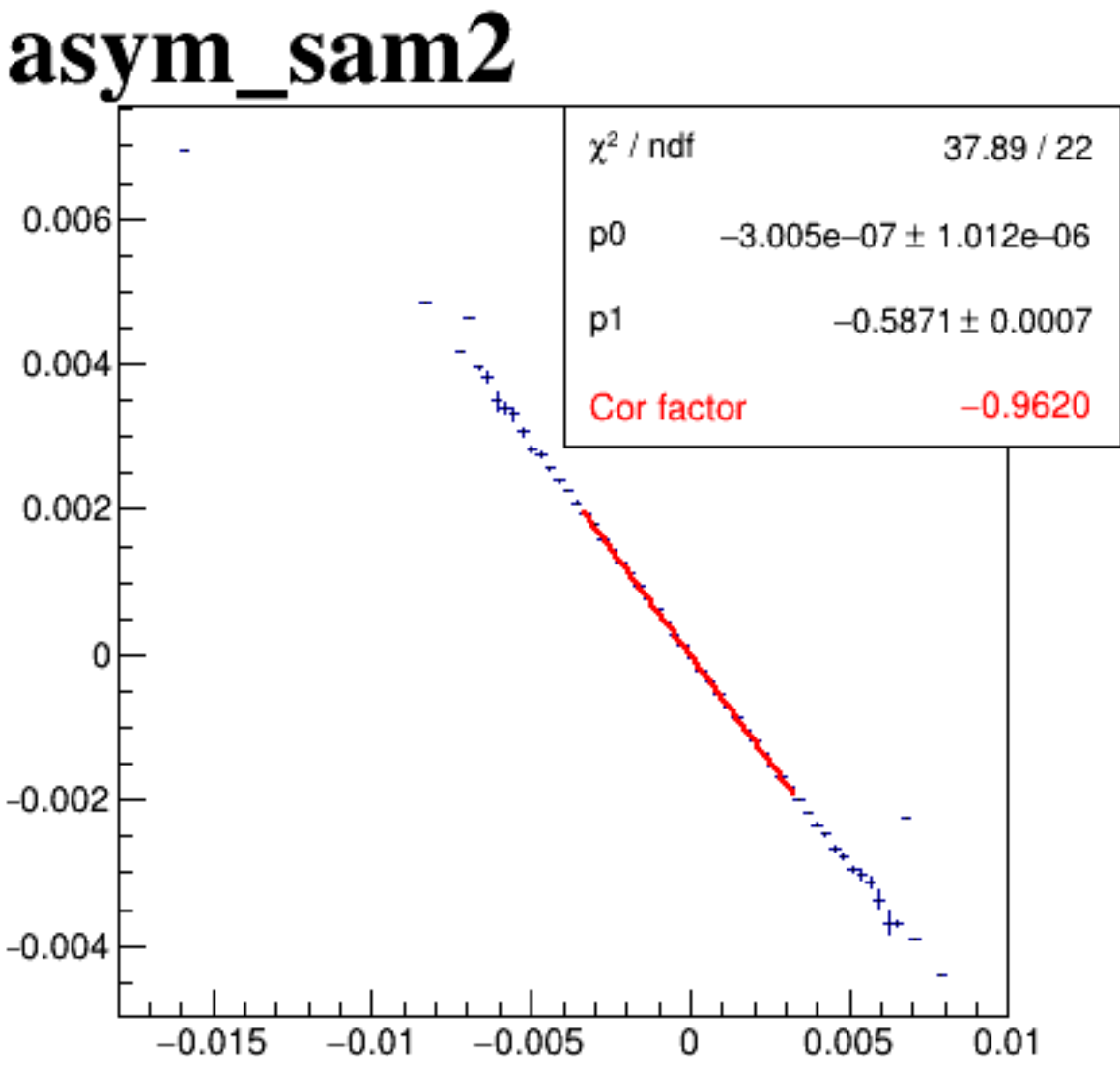
asym\_sam2



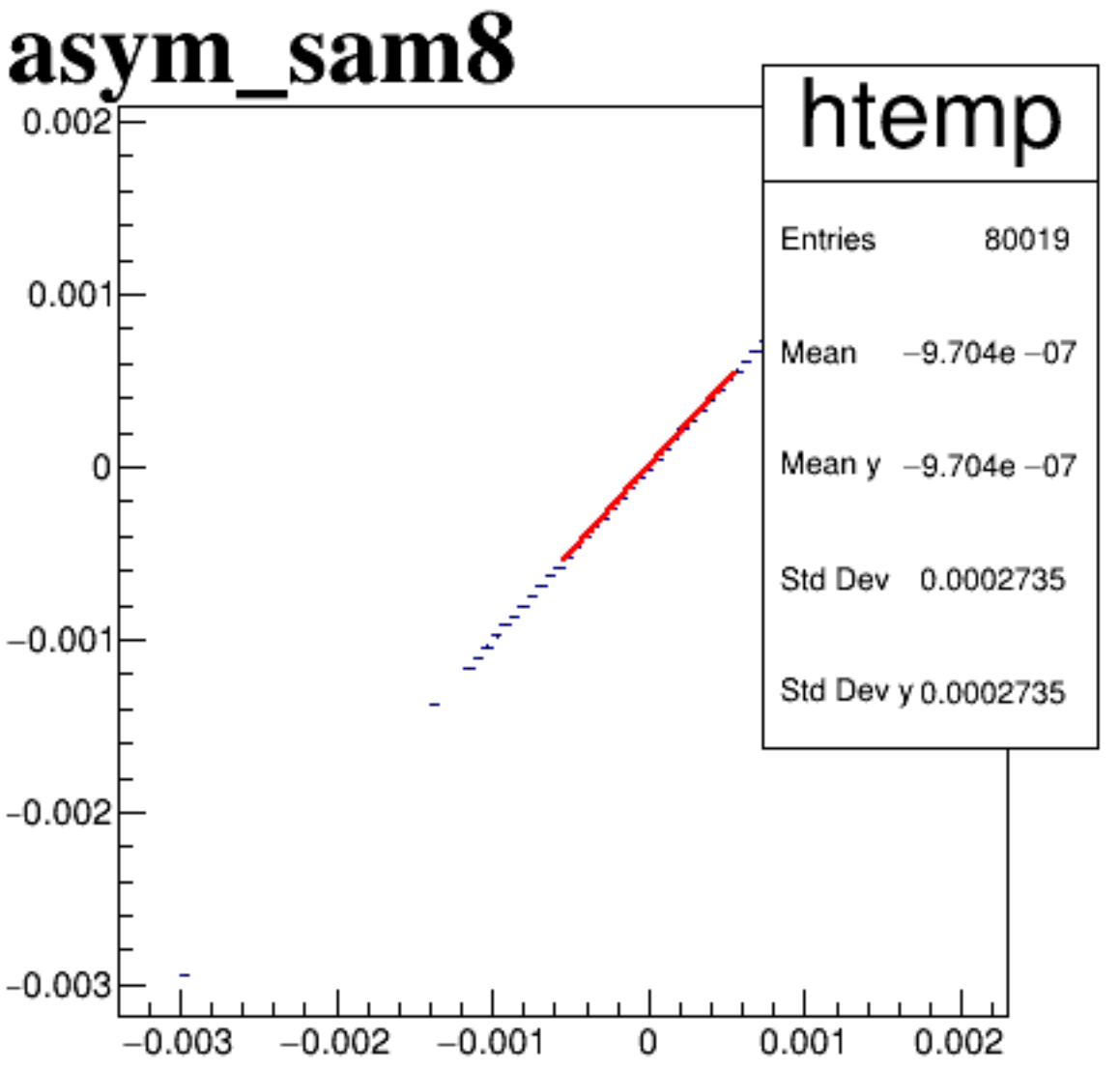
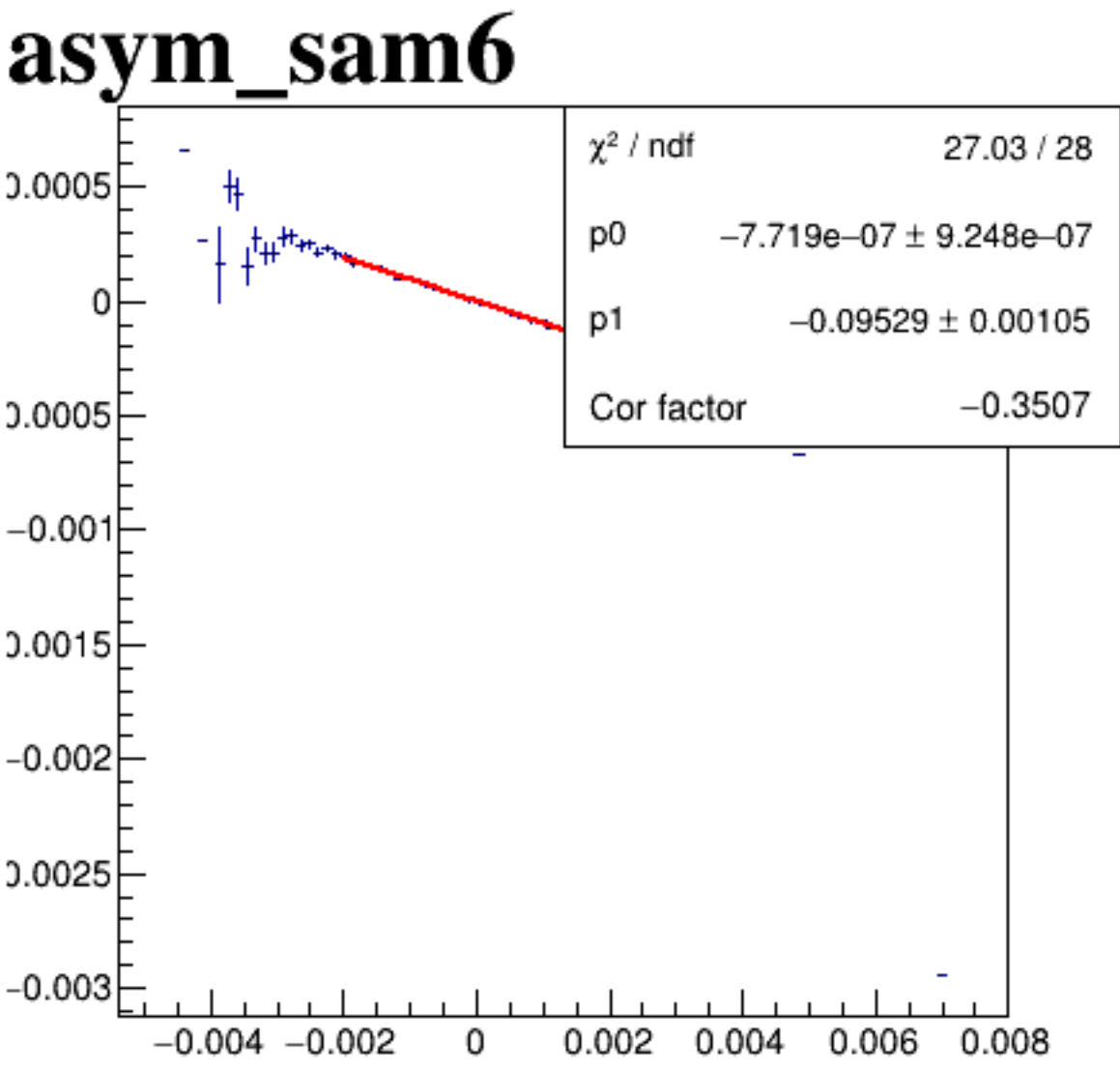
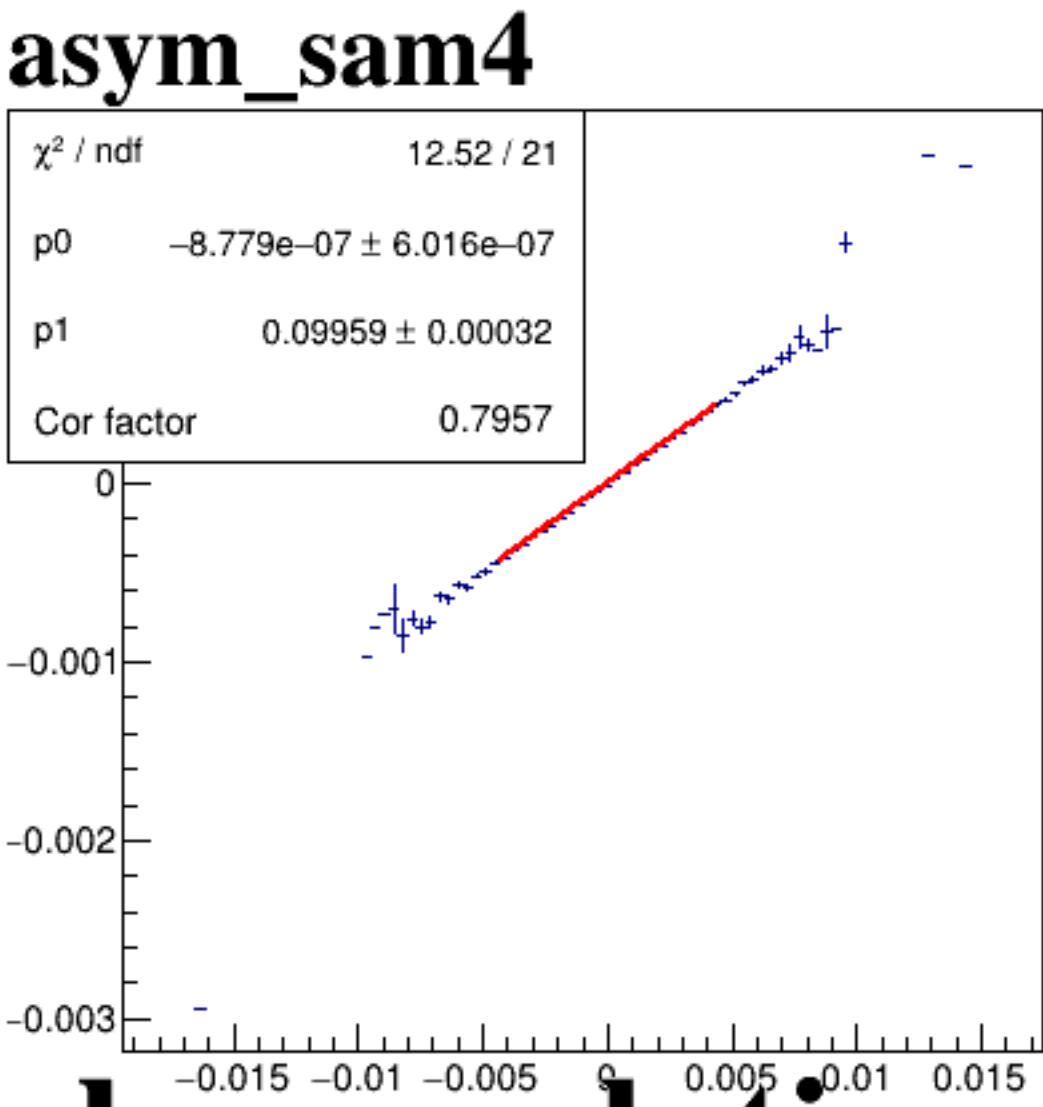
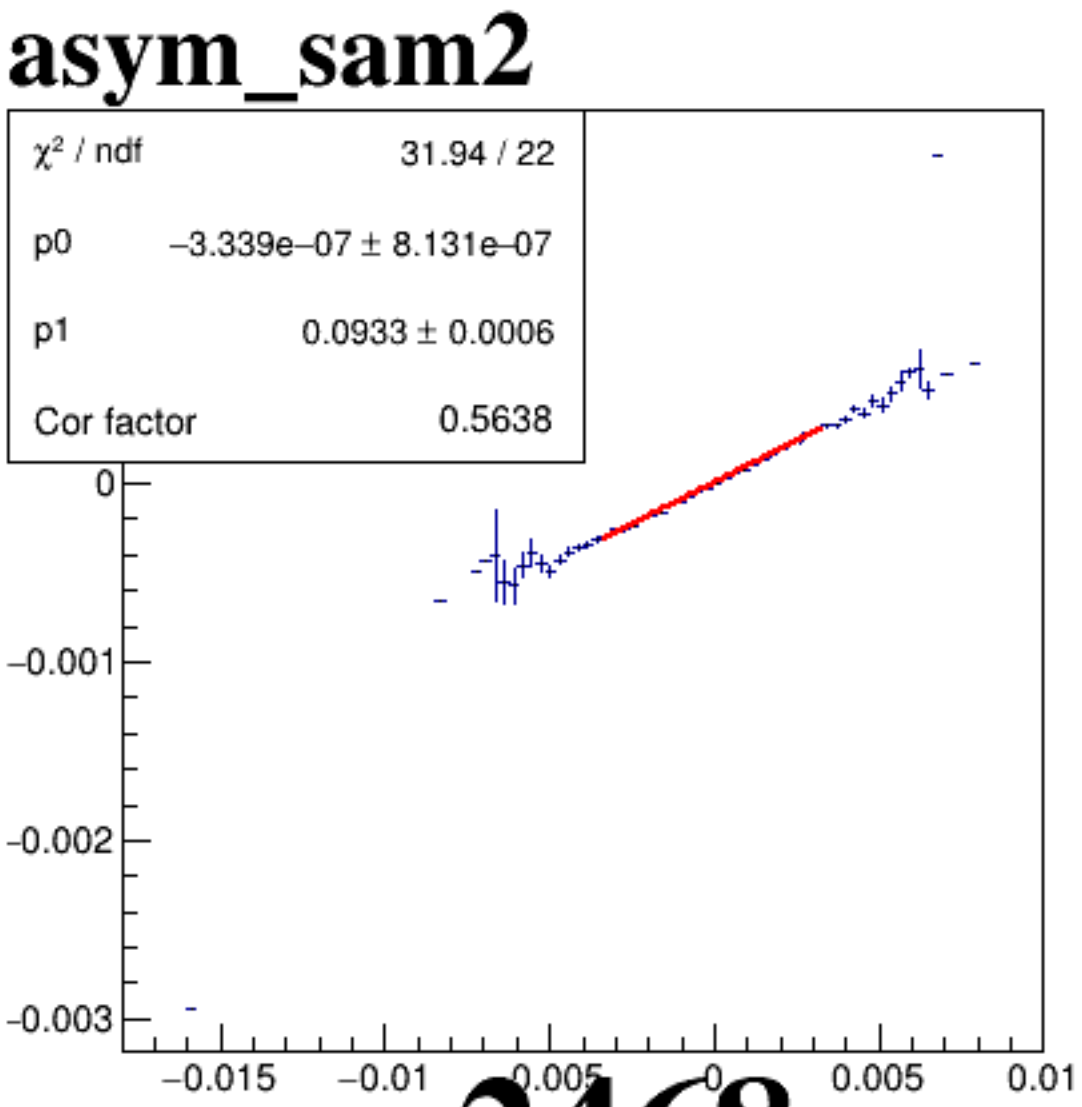
asym\_sam4



asym\_sam6

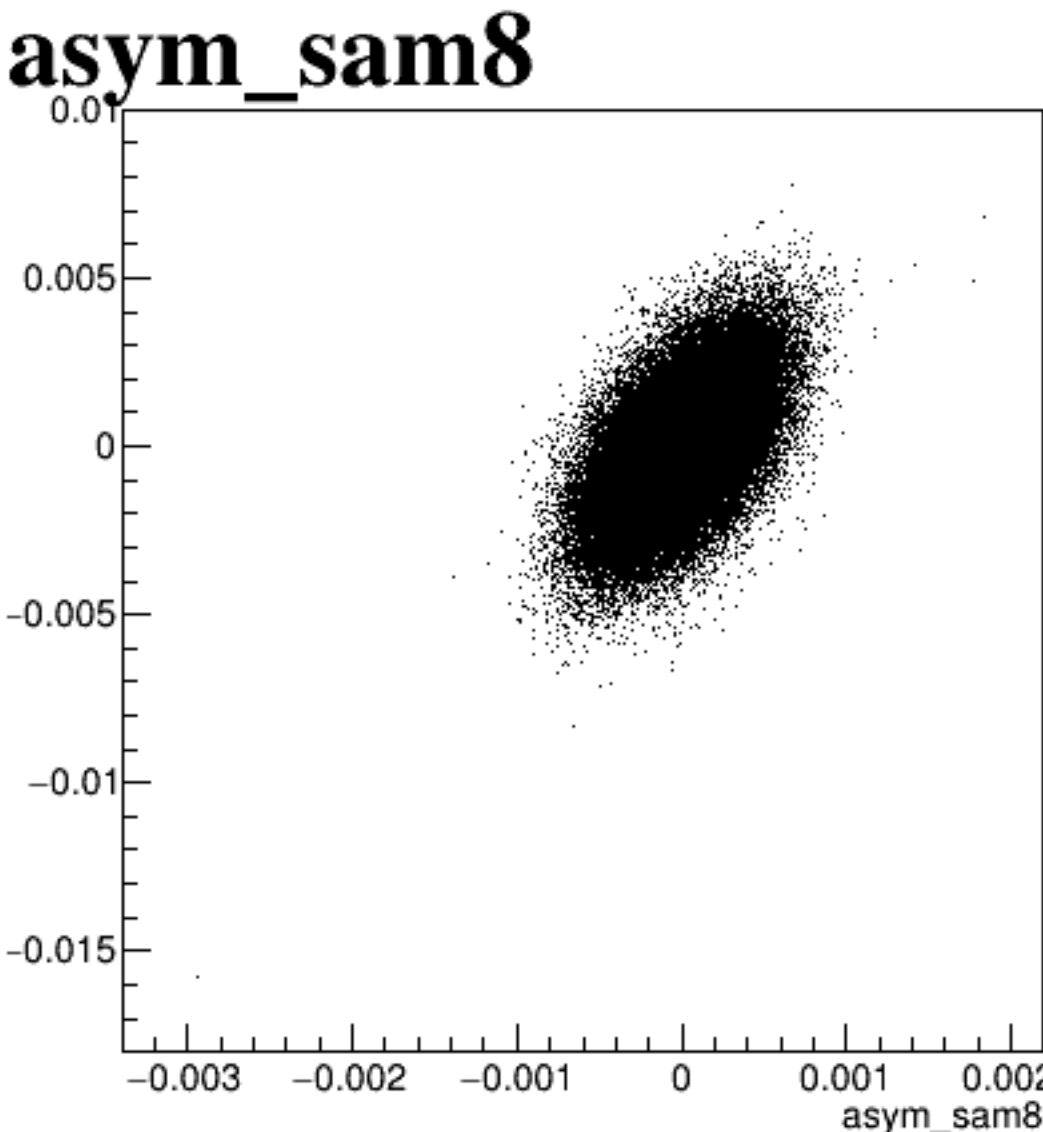
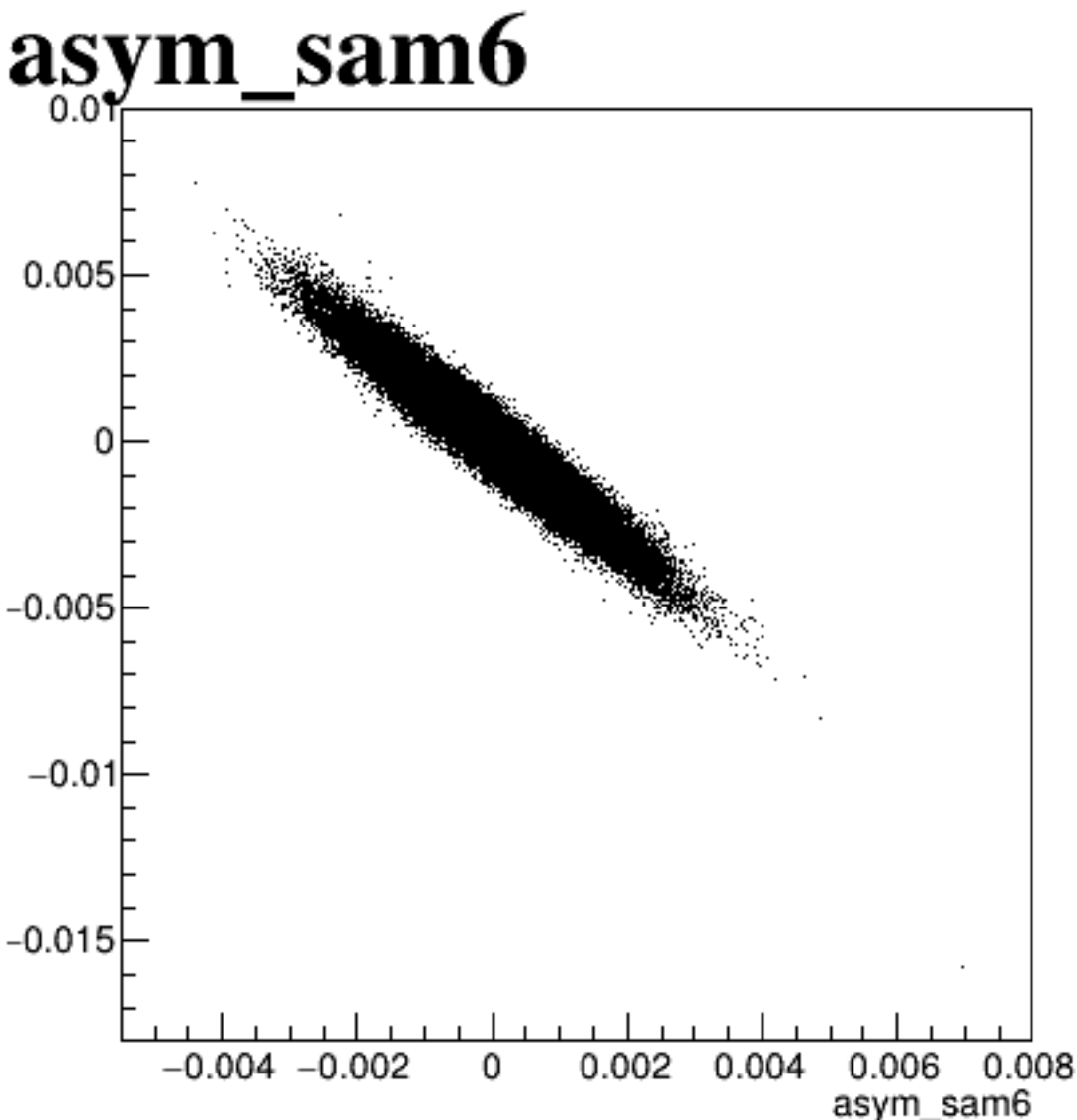
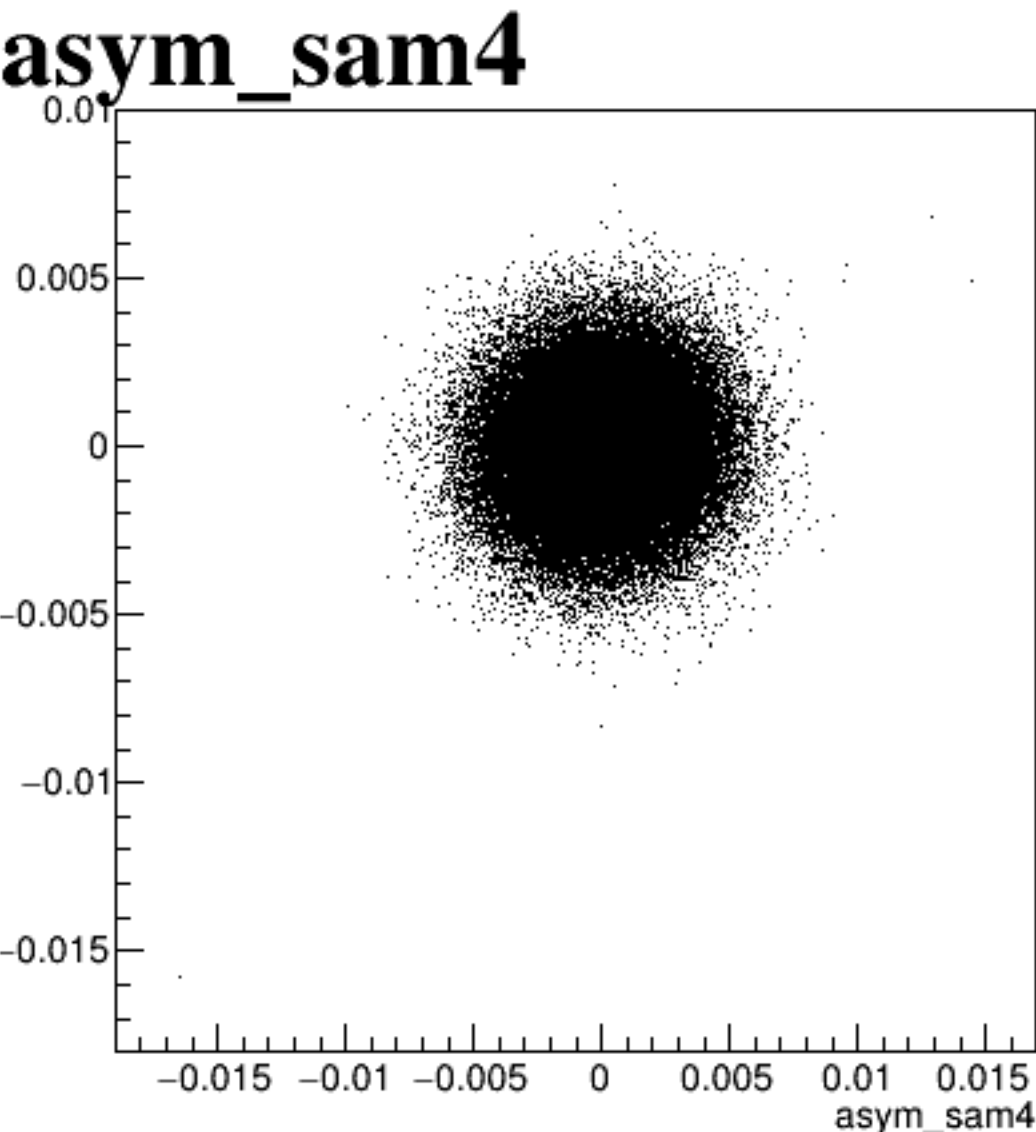
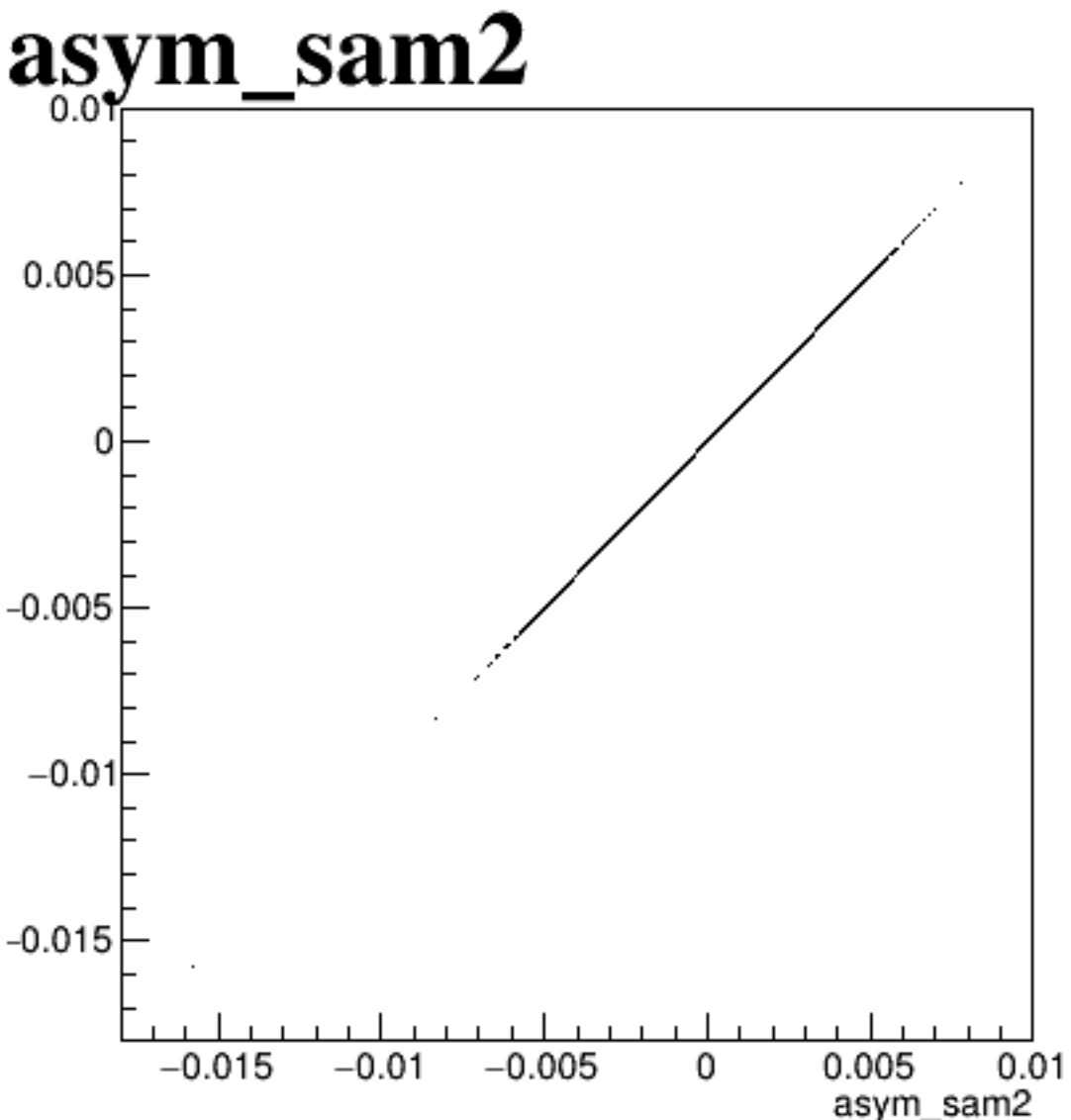


asym\_sam8

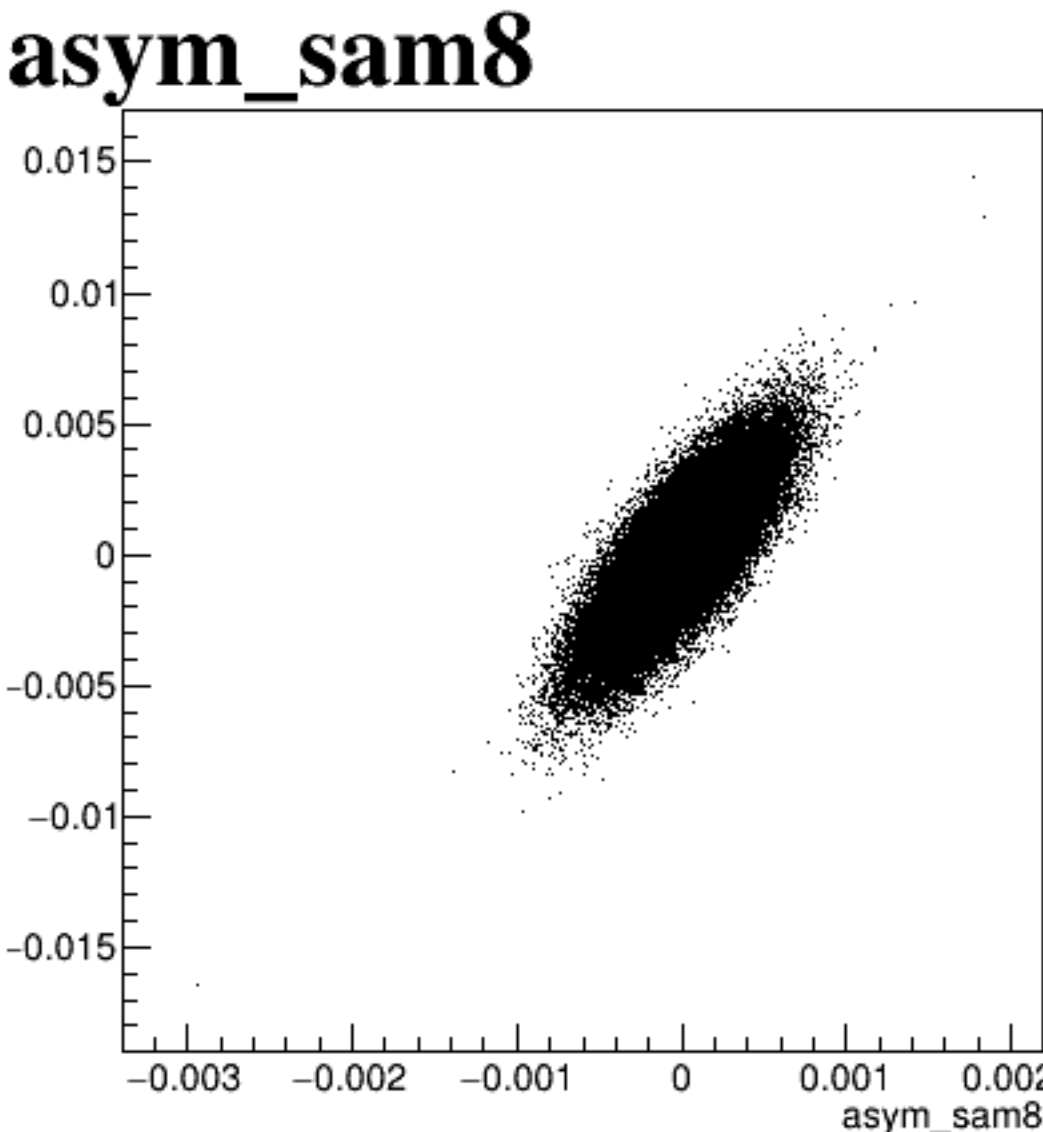
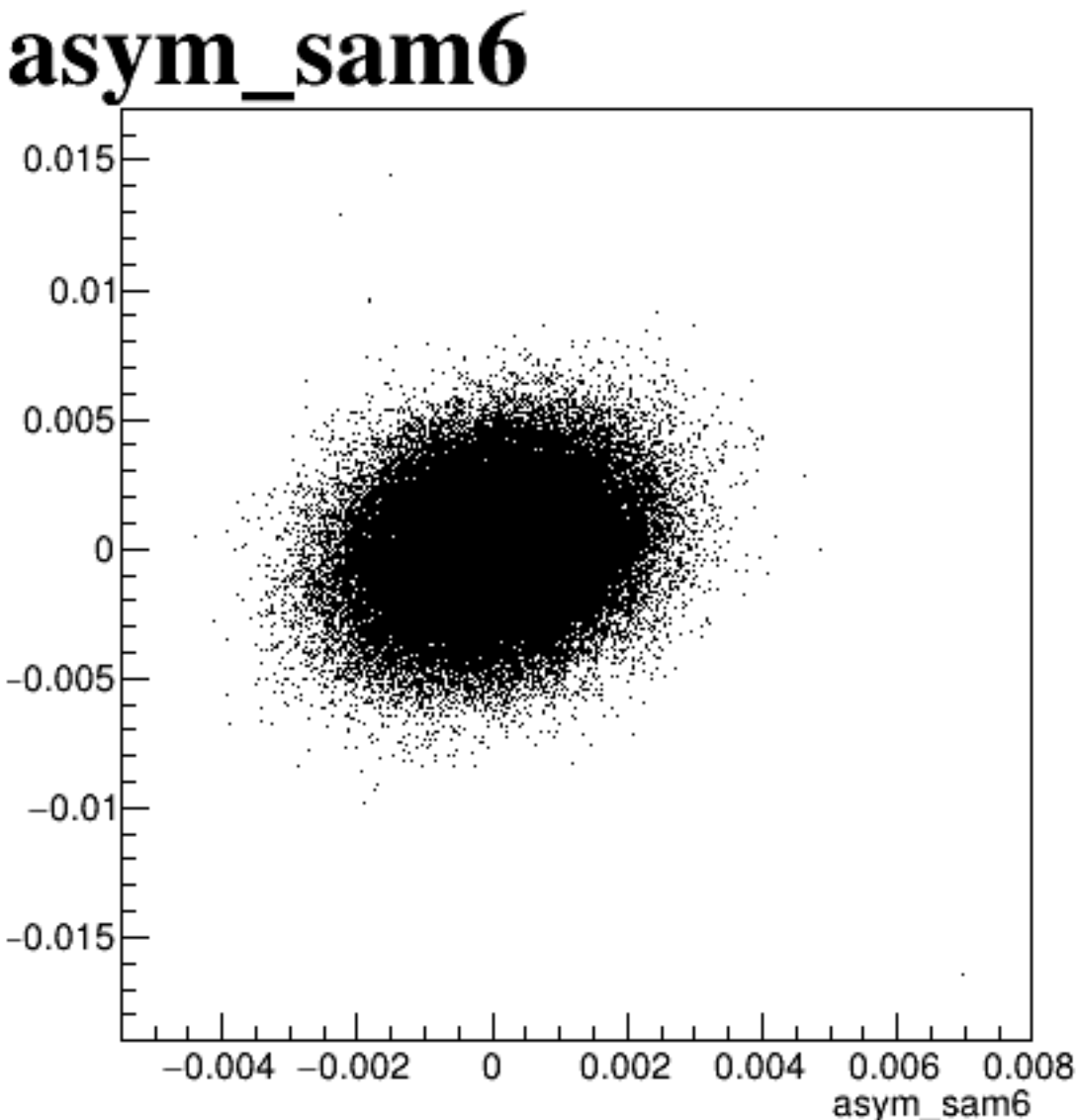
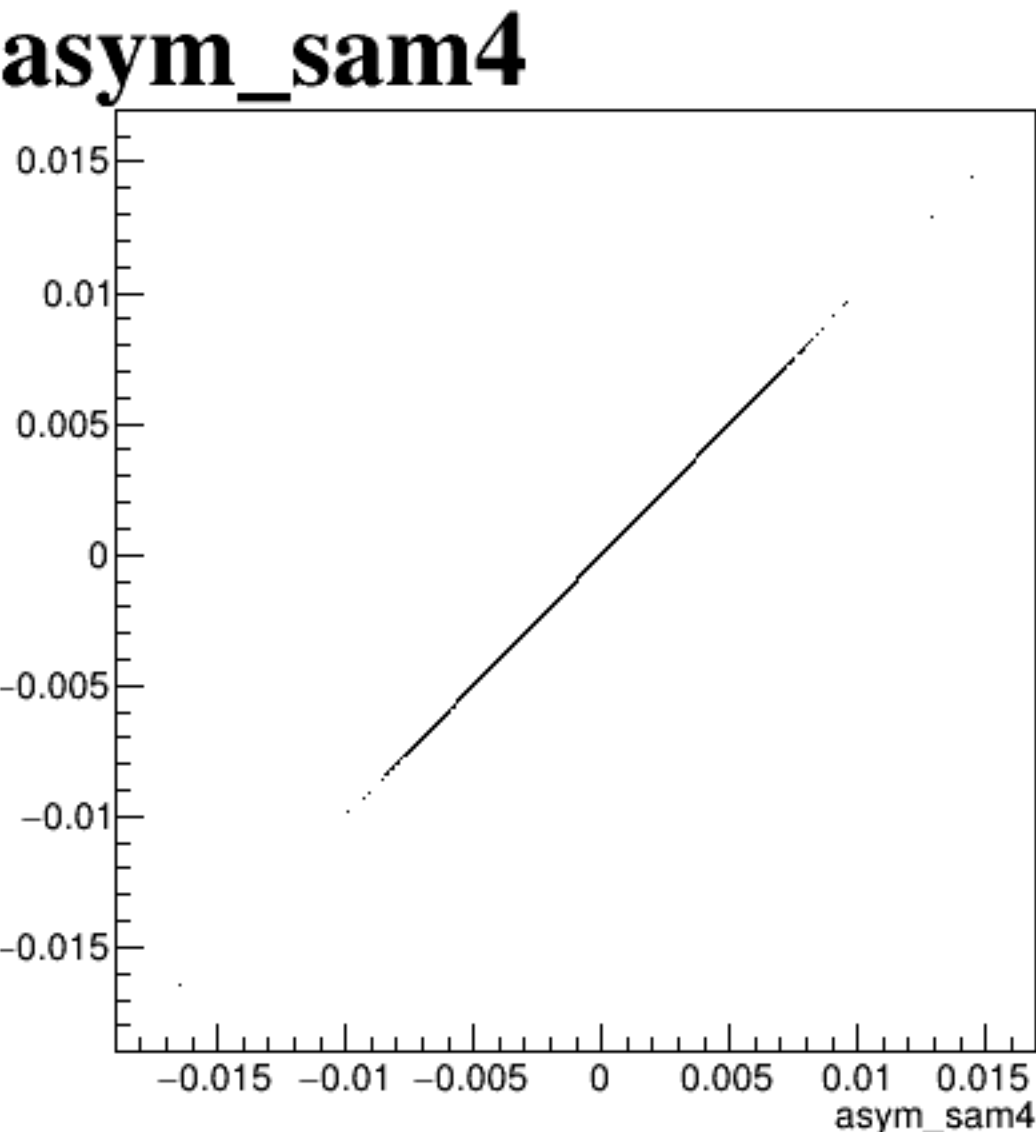
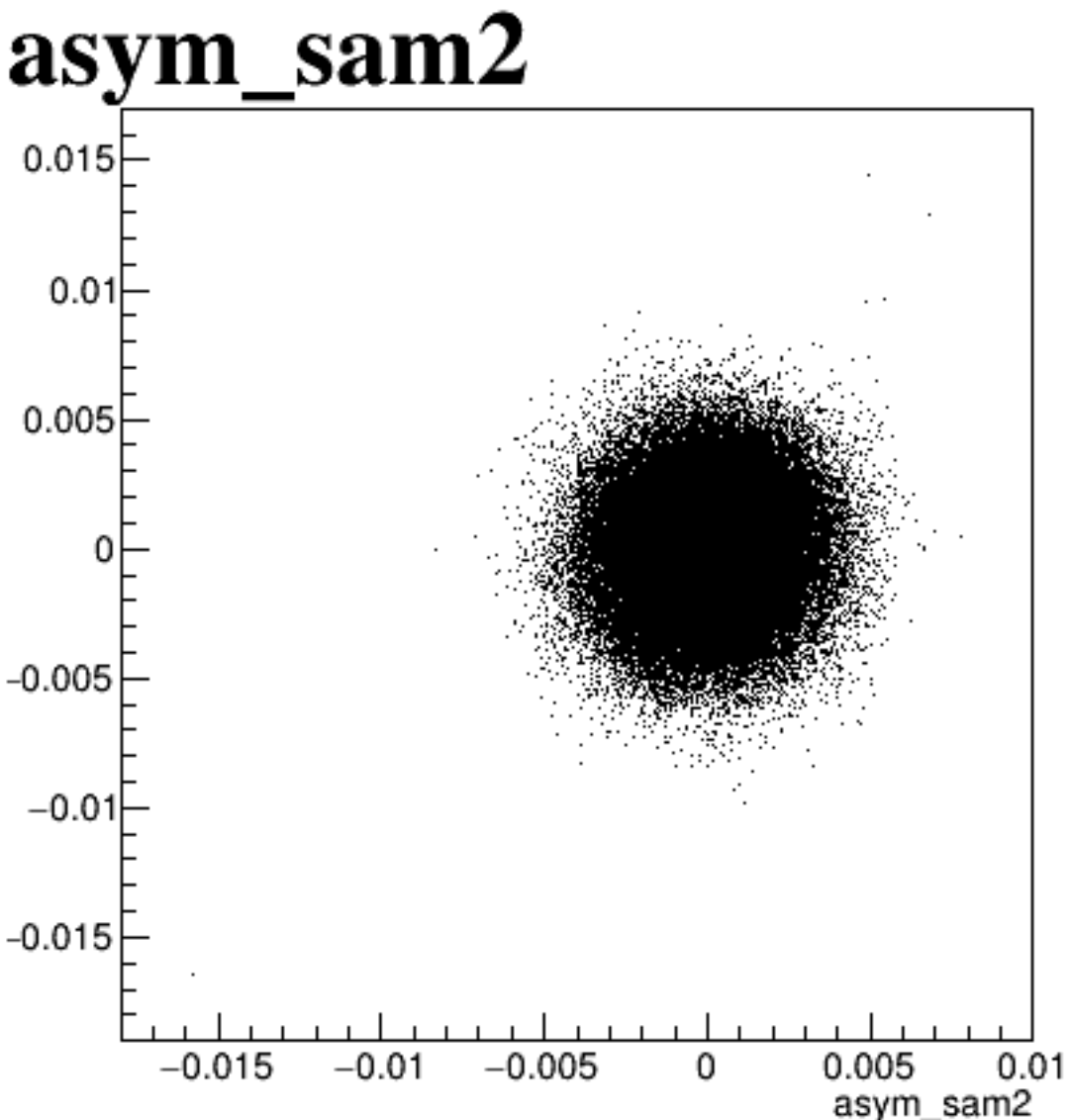




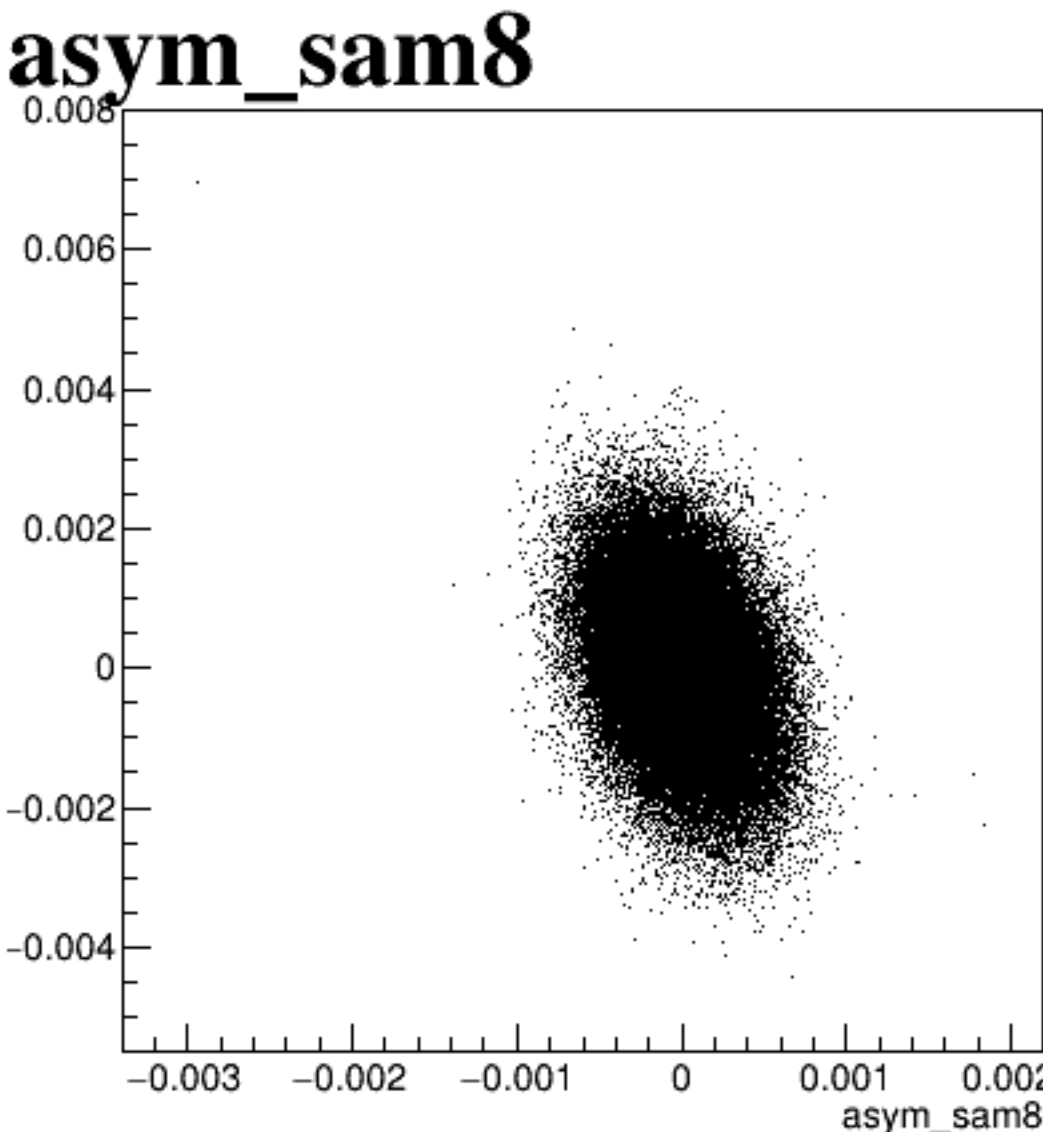
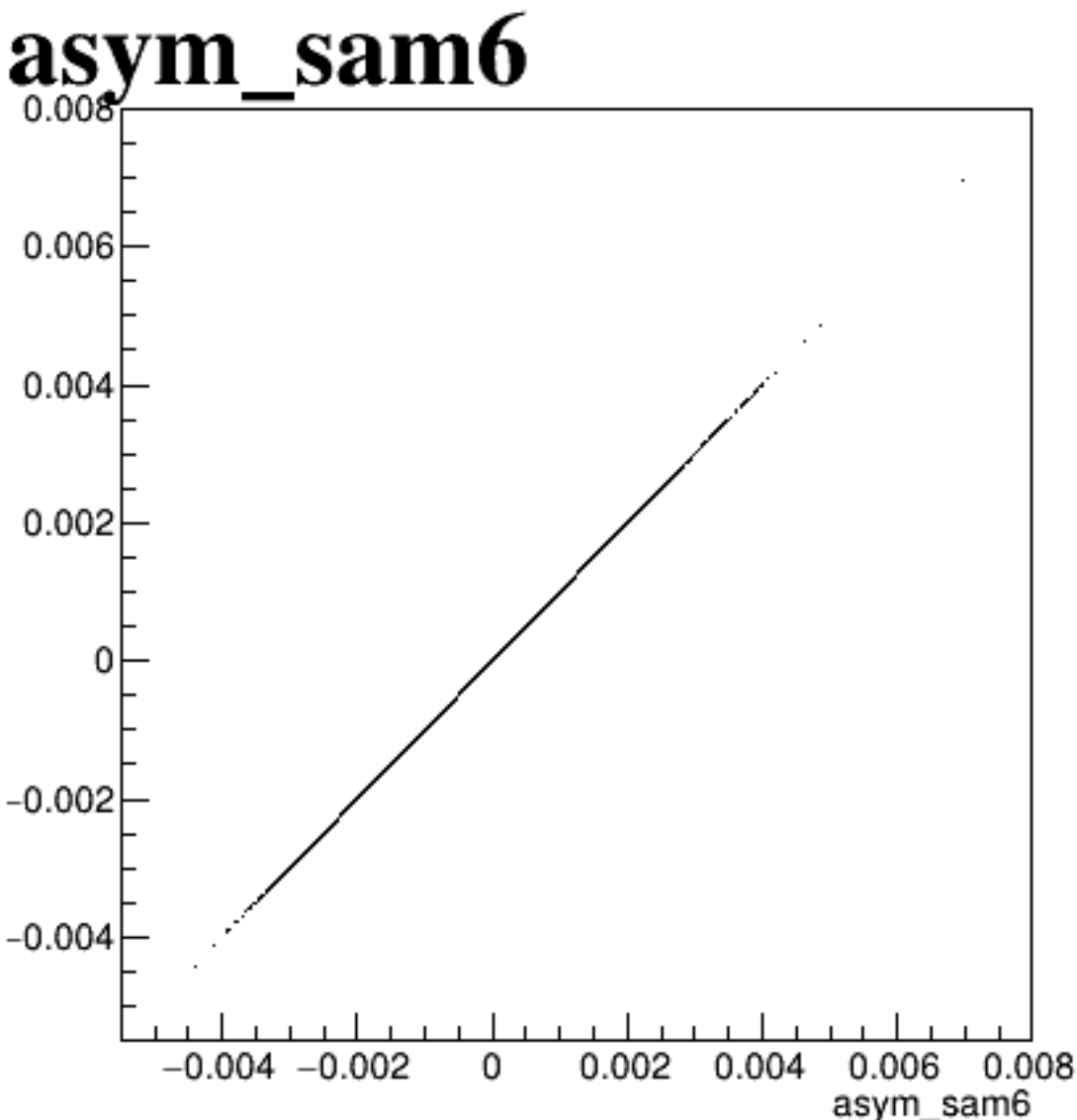
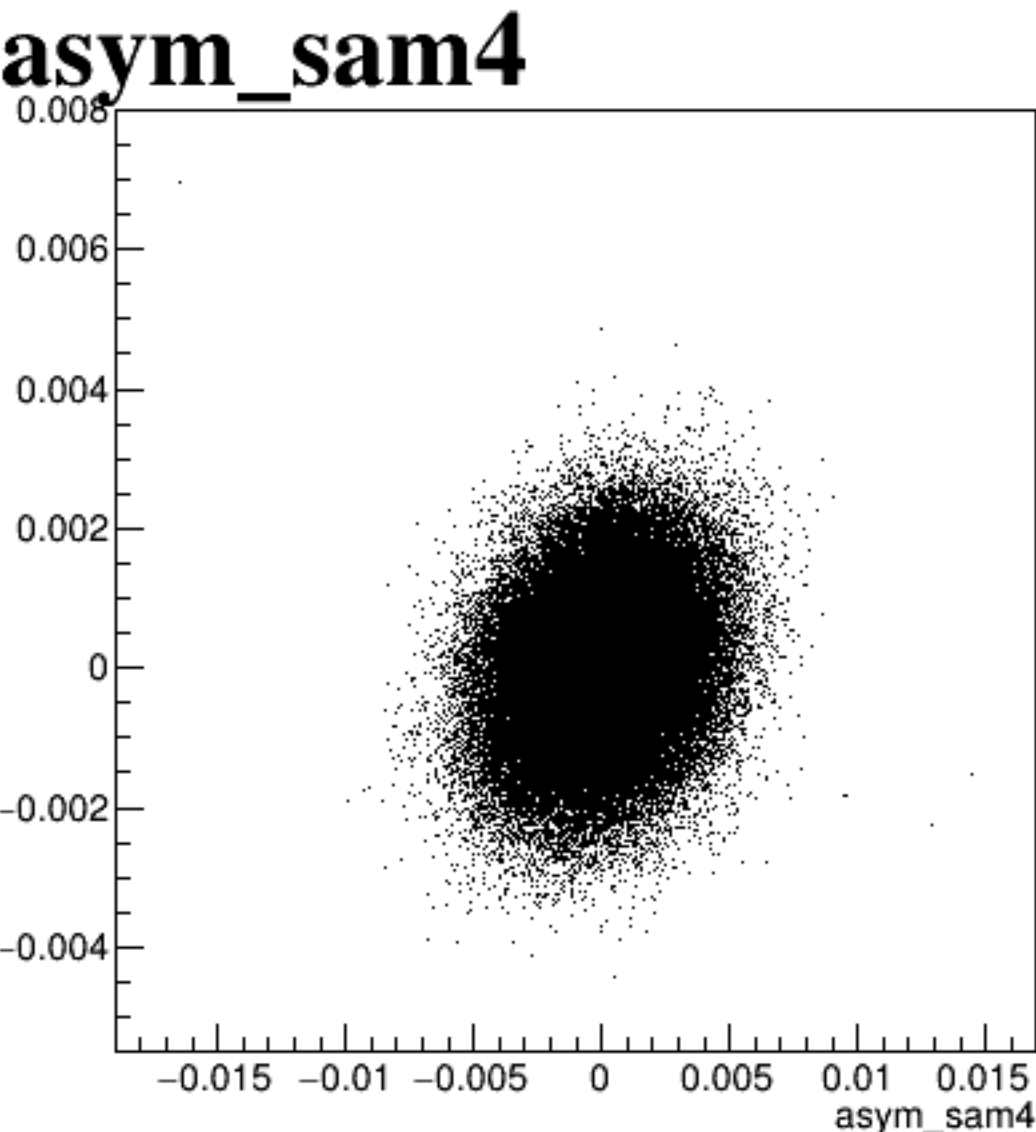
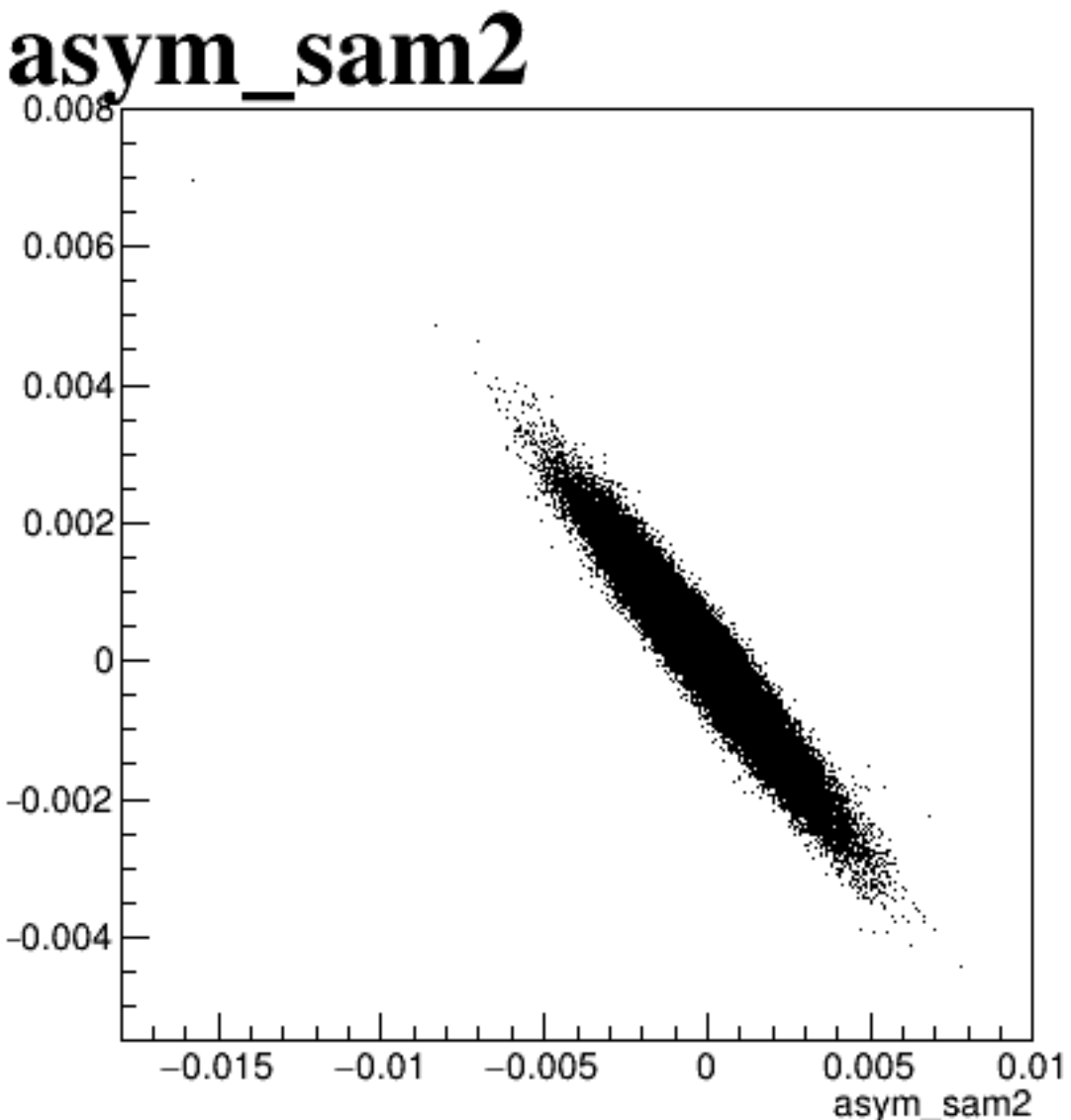
asym\_sam2



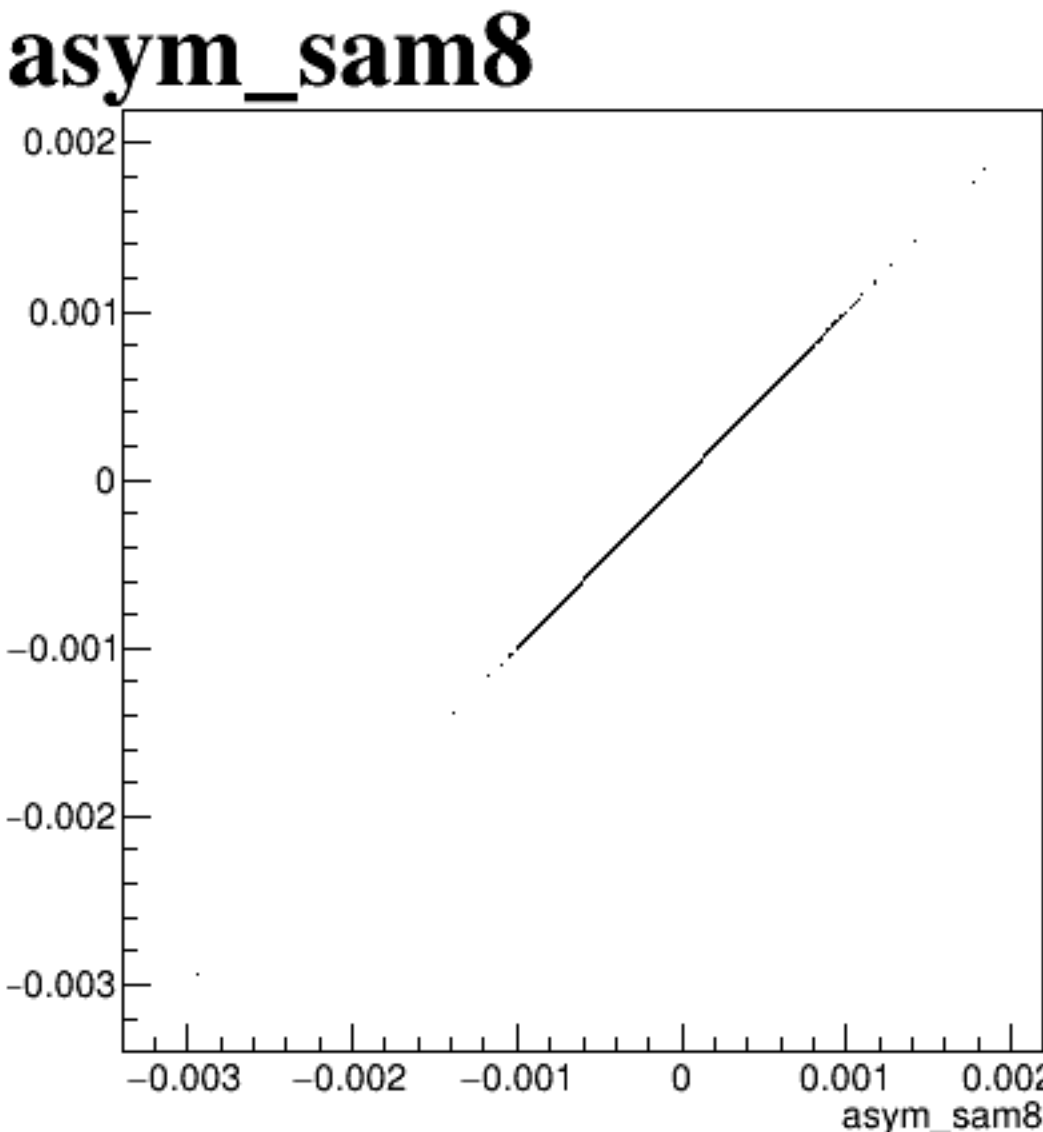
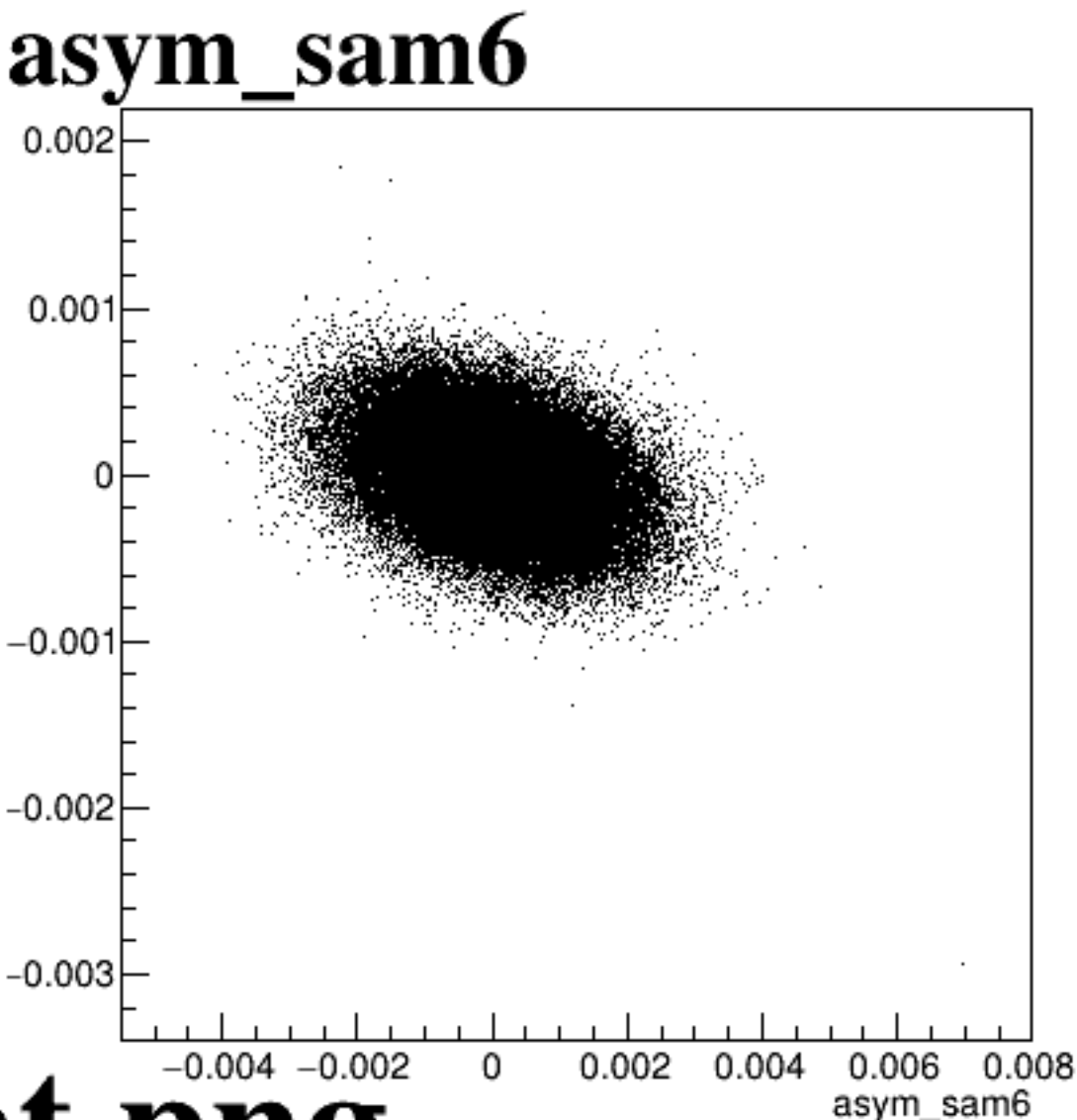
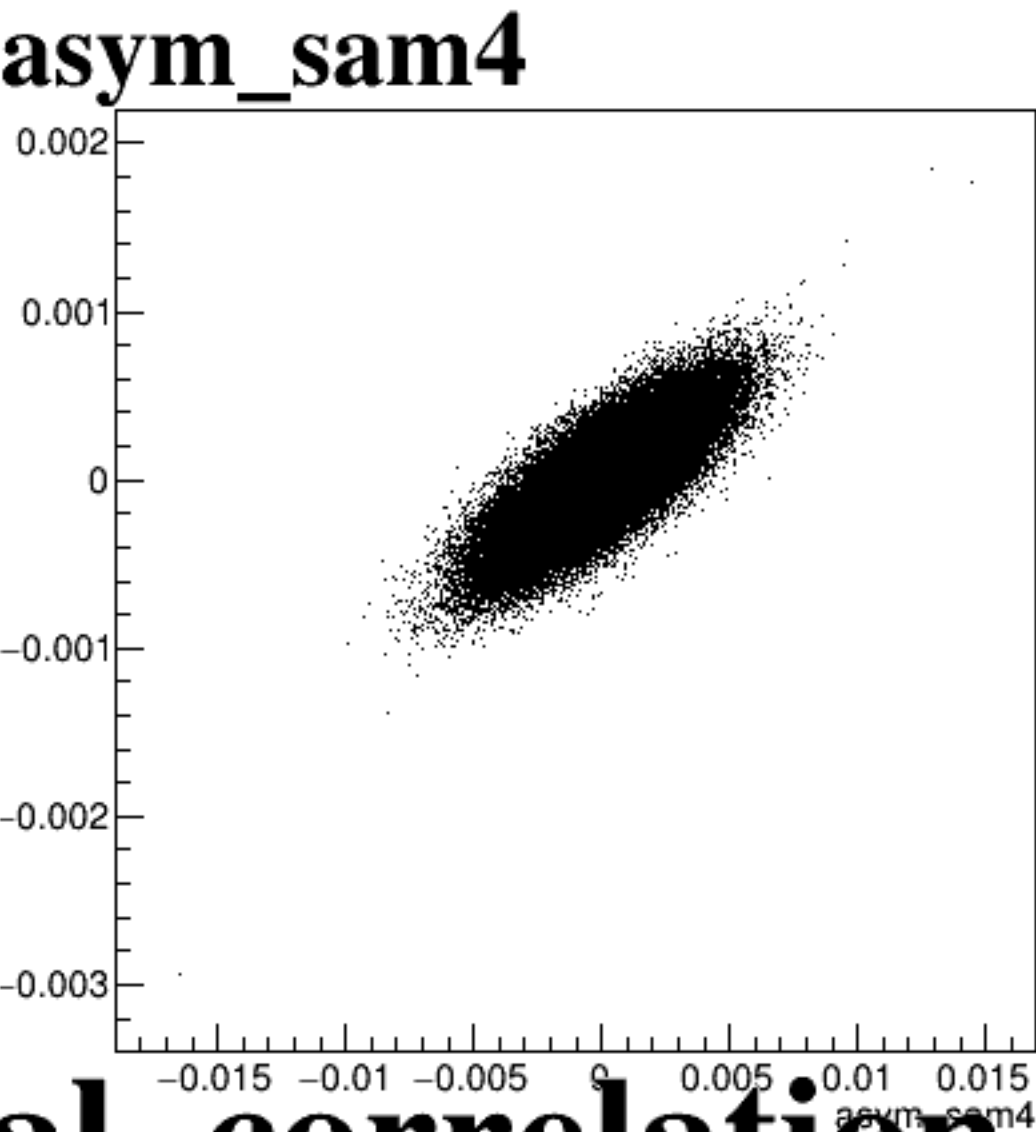
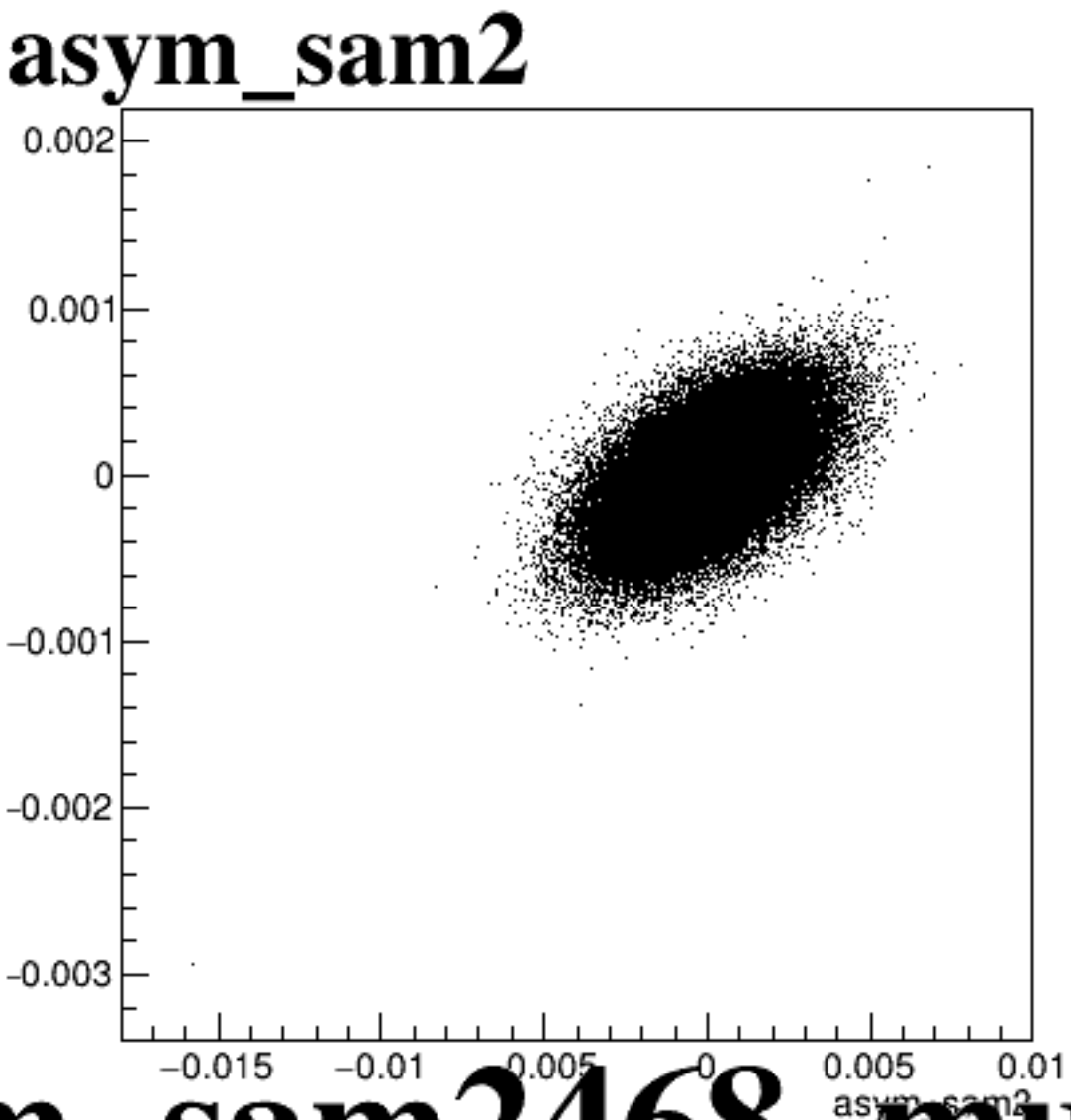
asym\_sam4



asym\_sam6

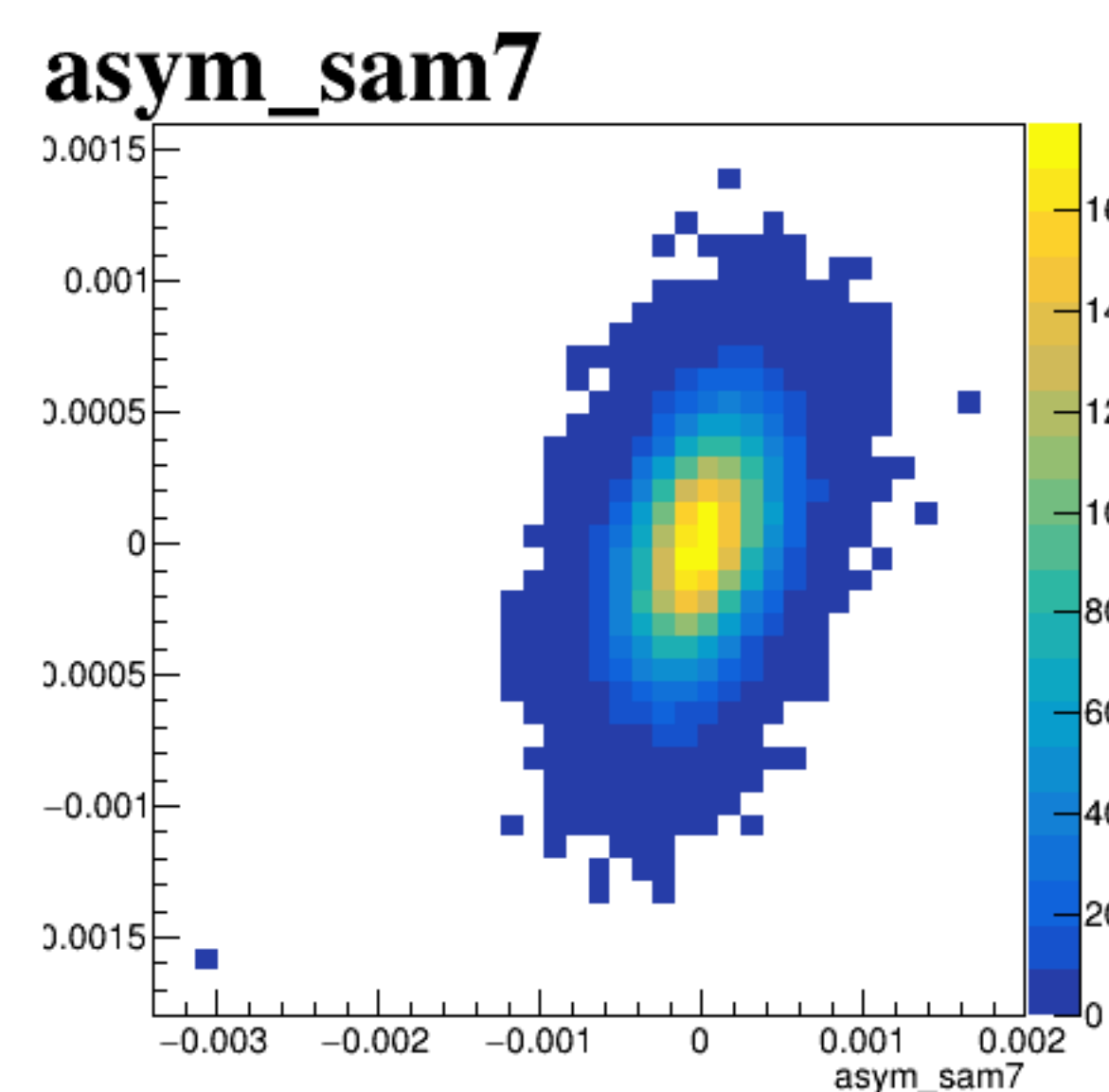
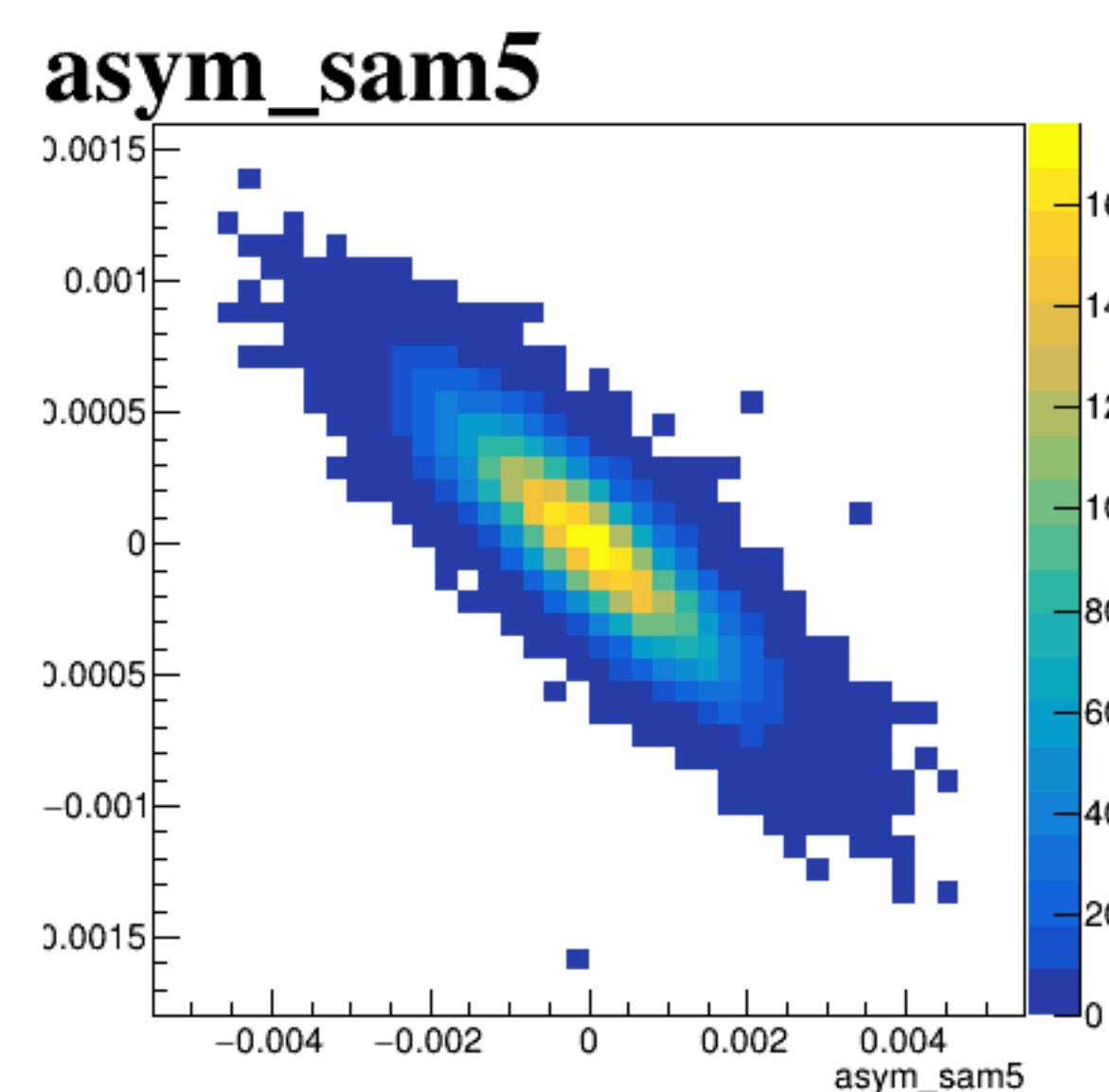
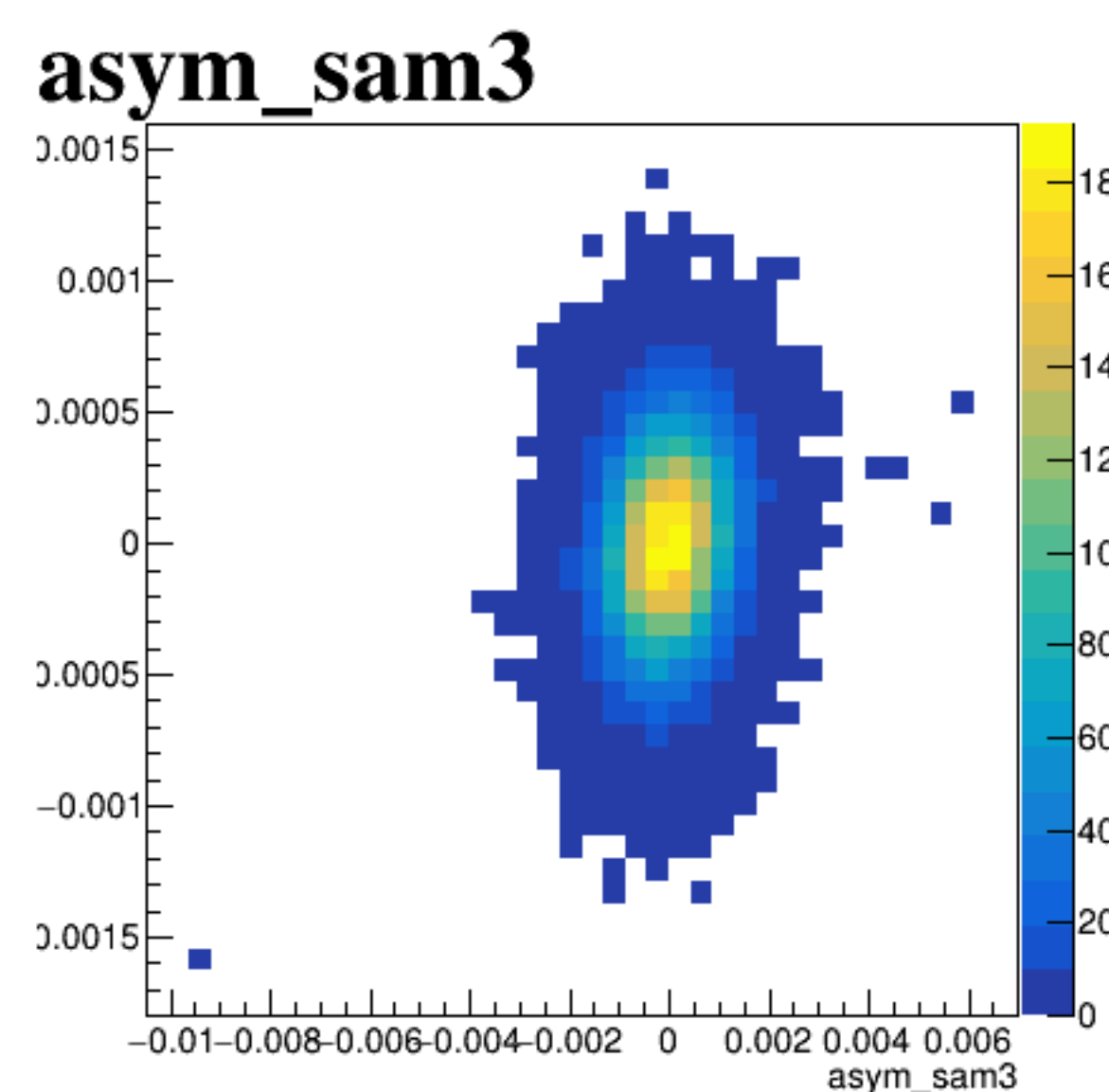
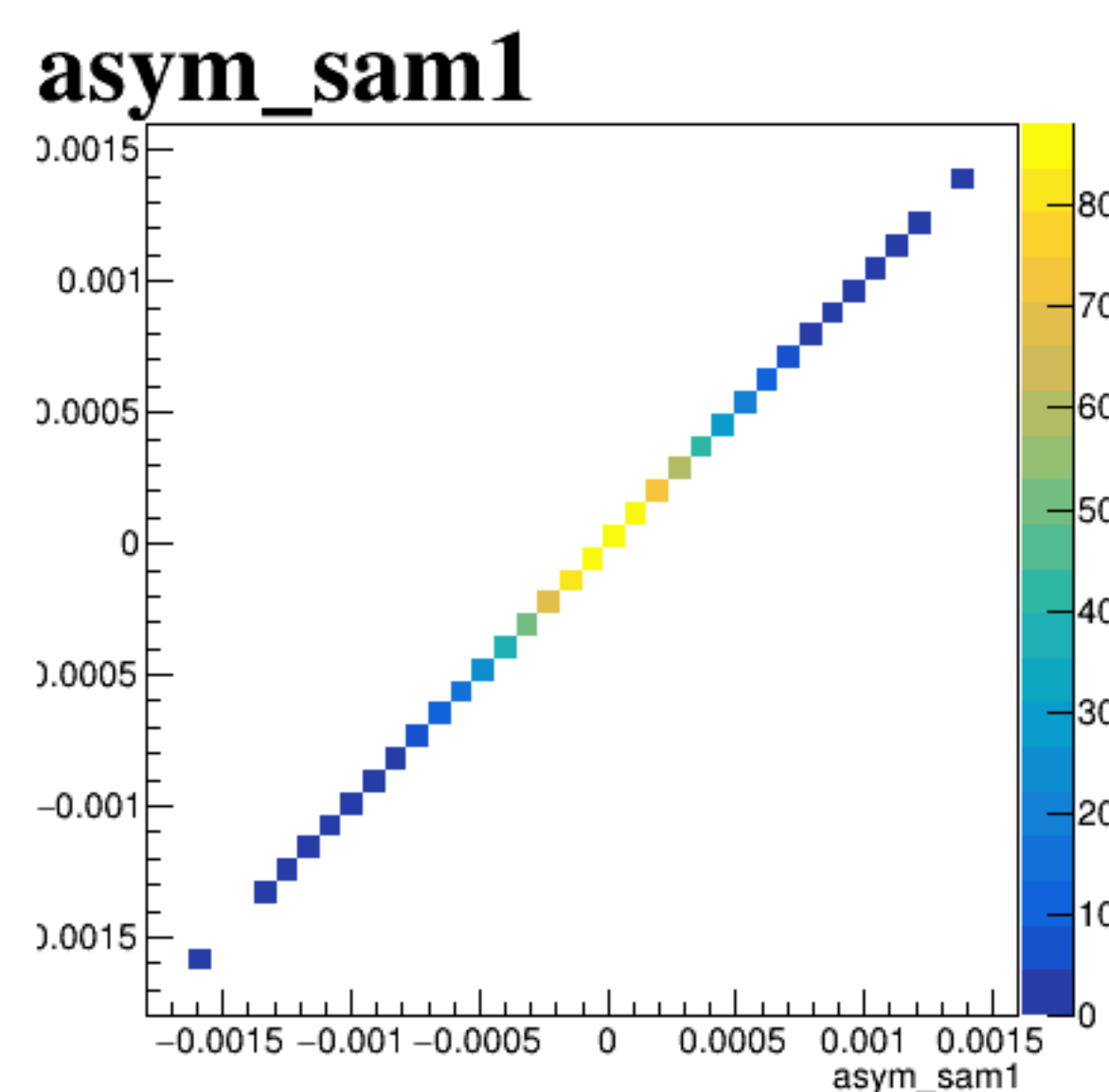


asym\_sam8

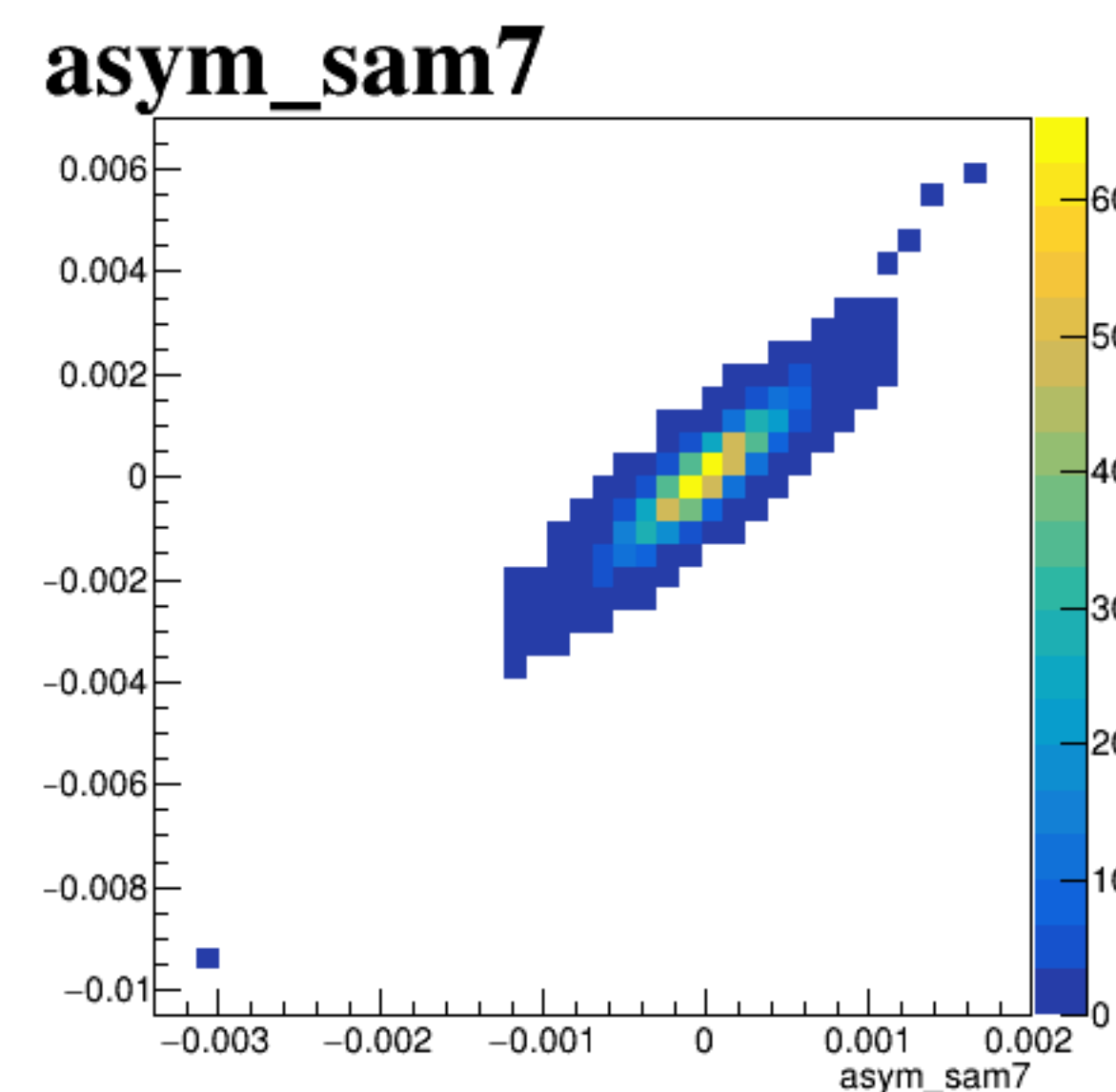
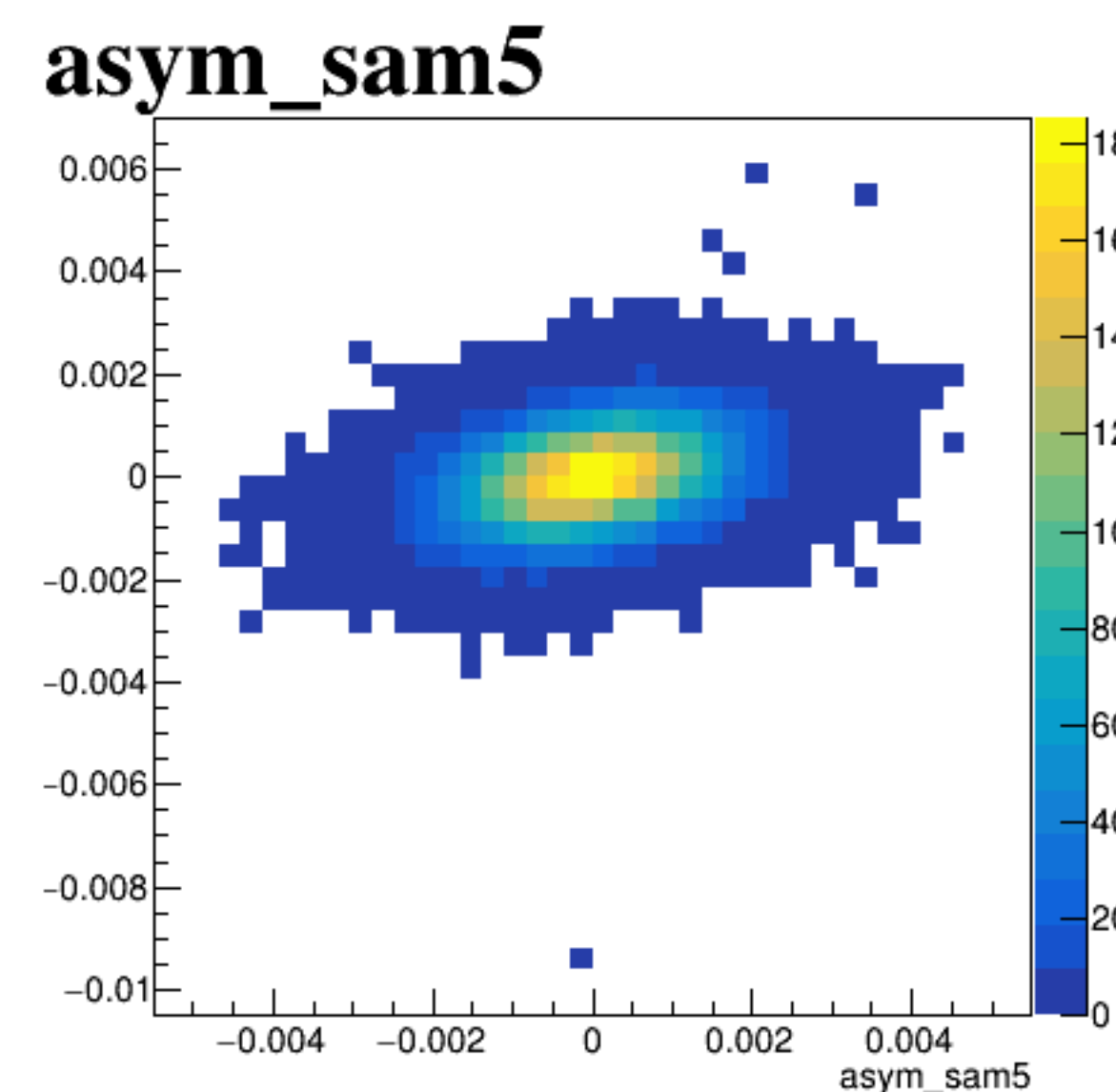
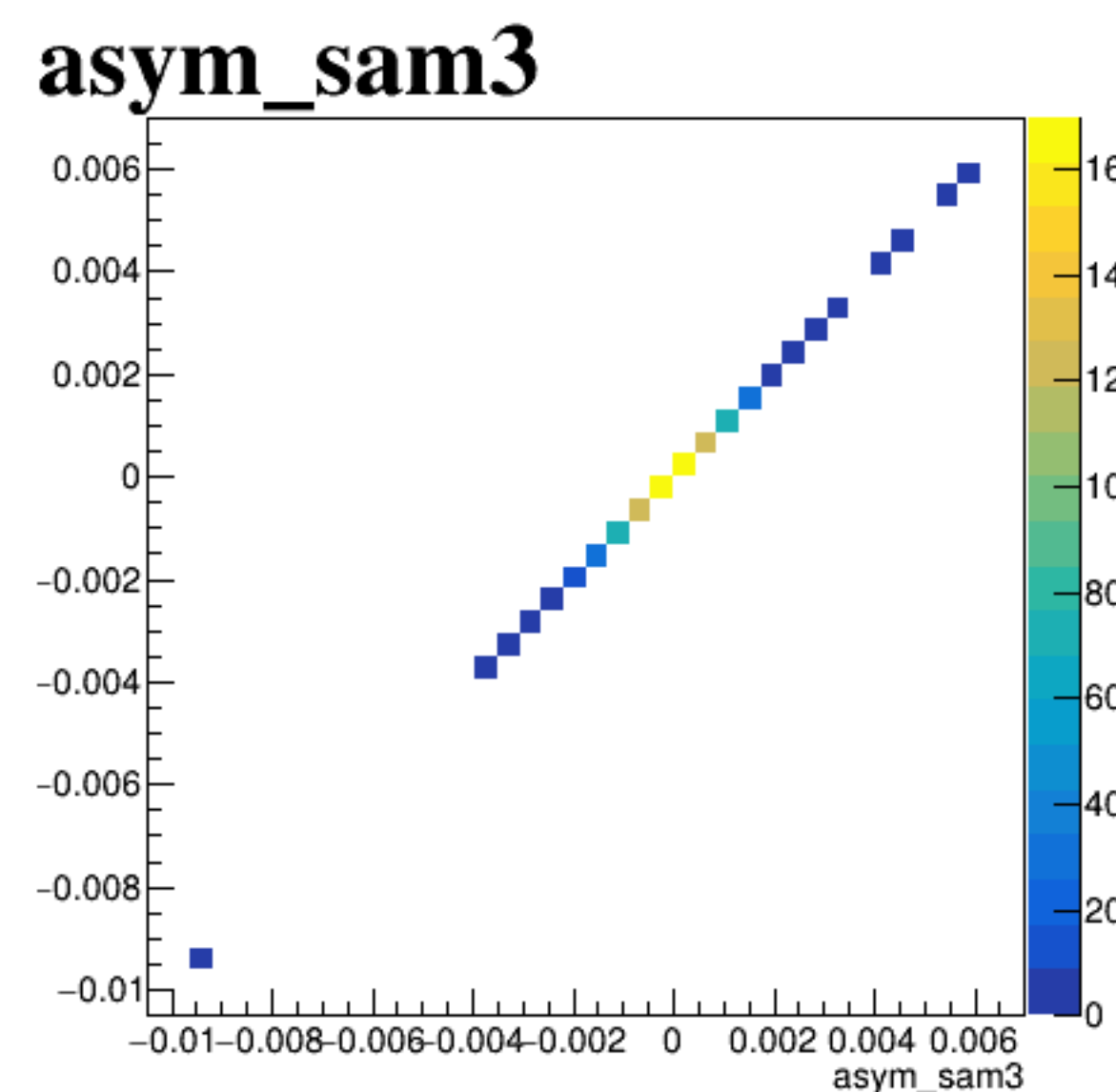
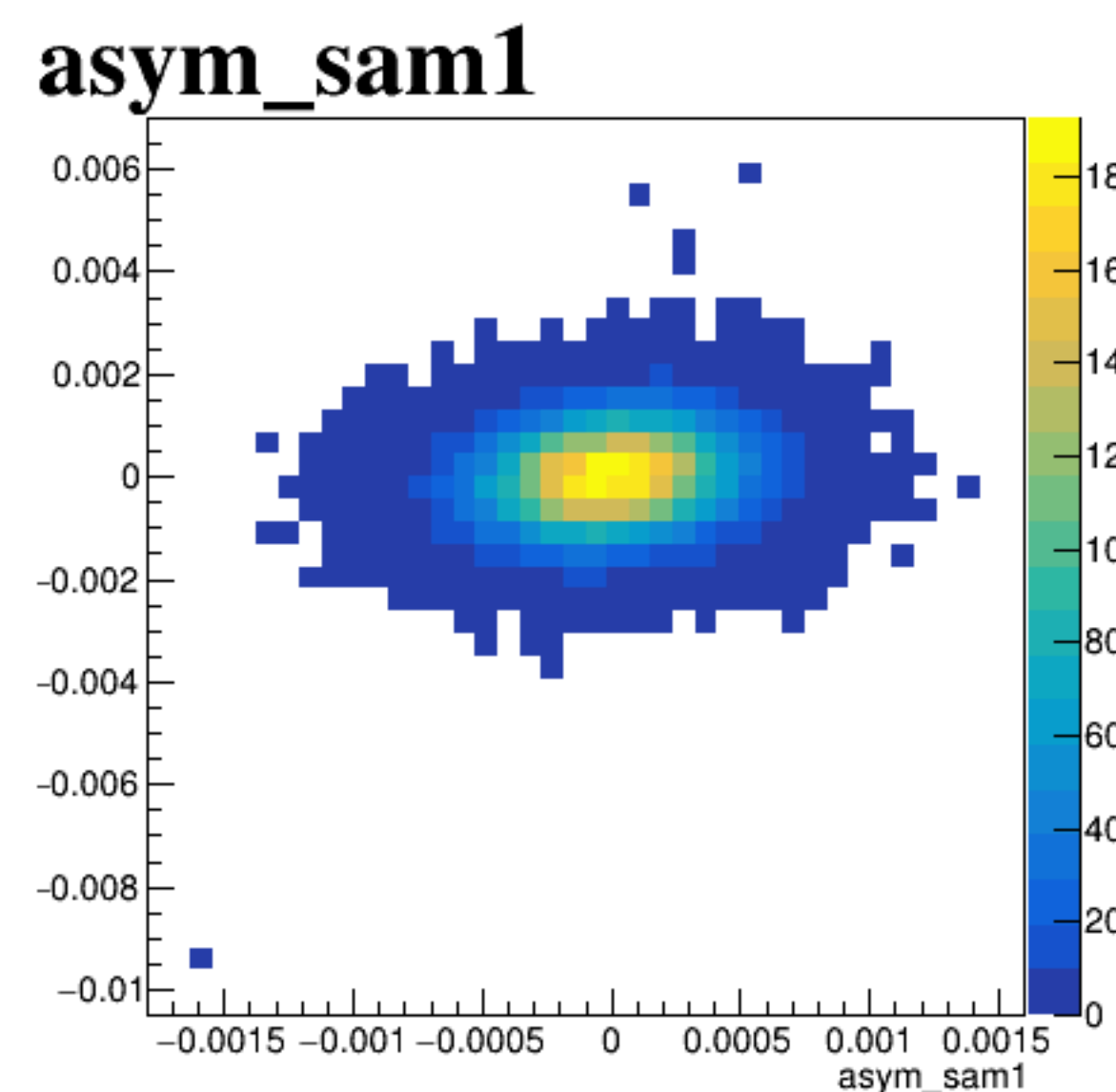
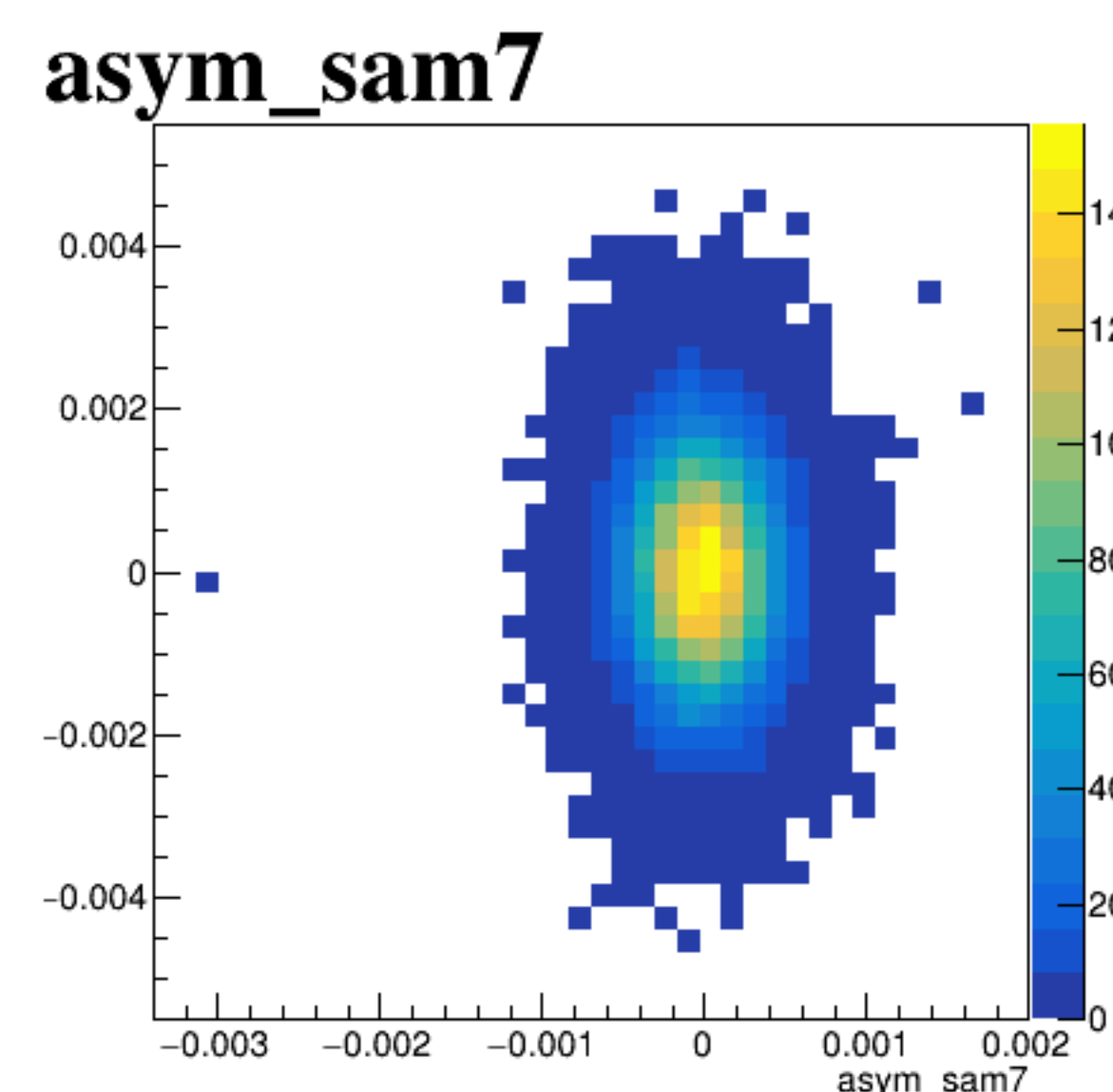
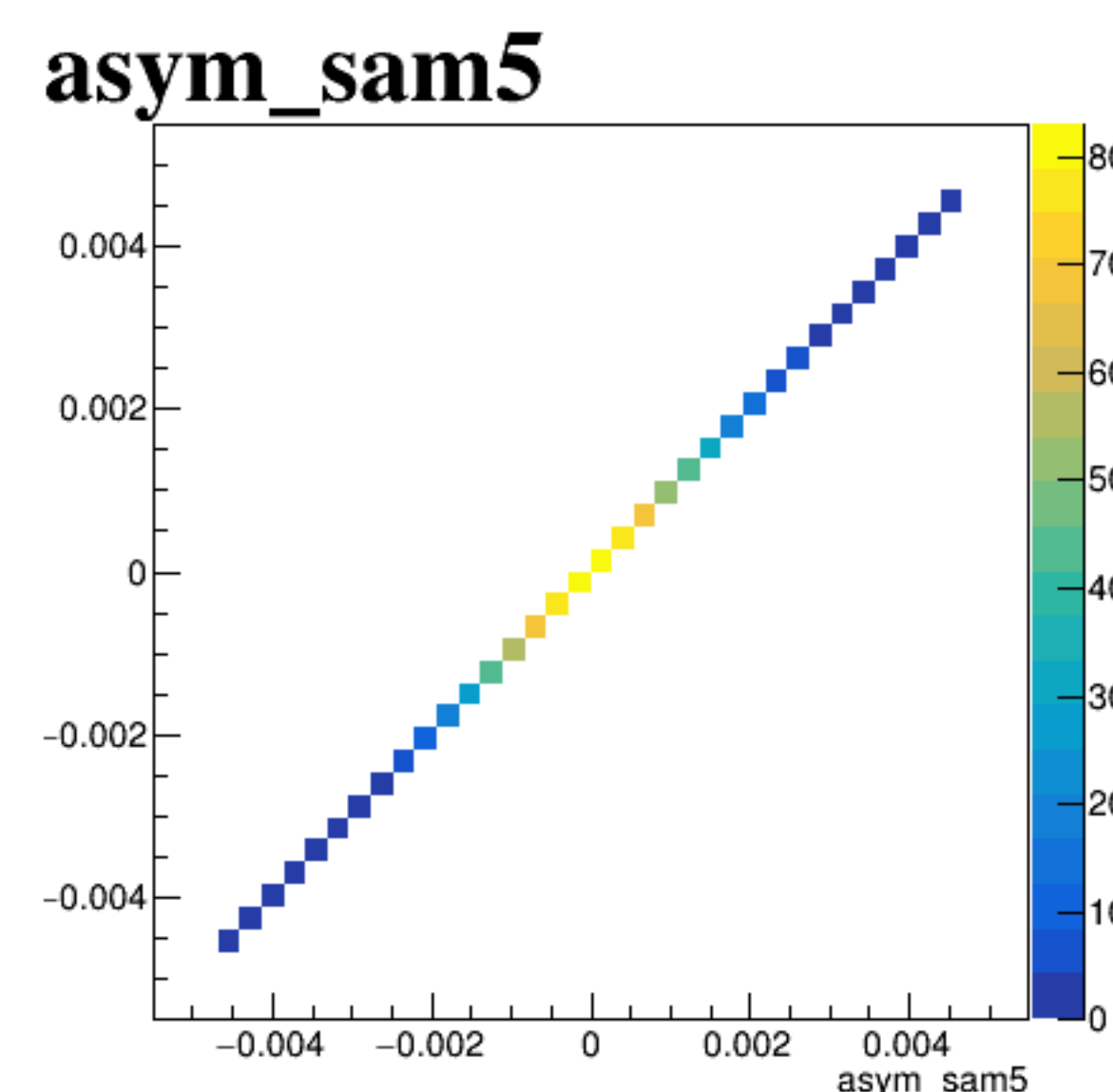
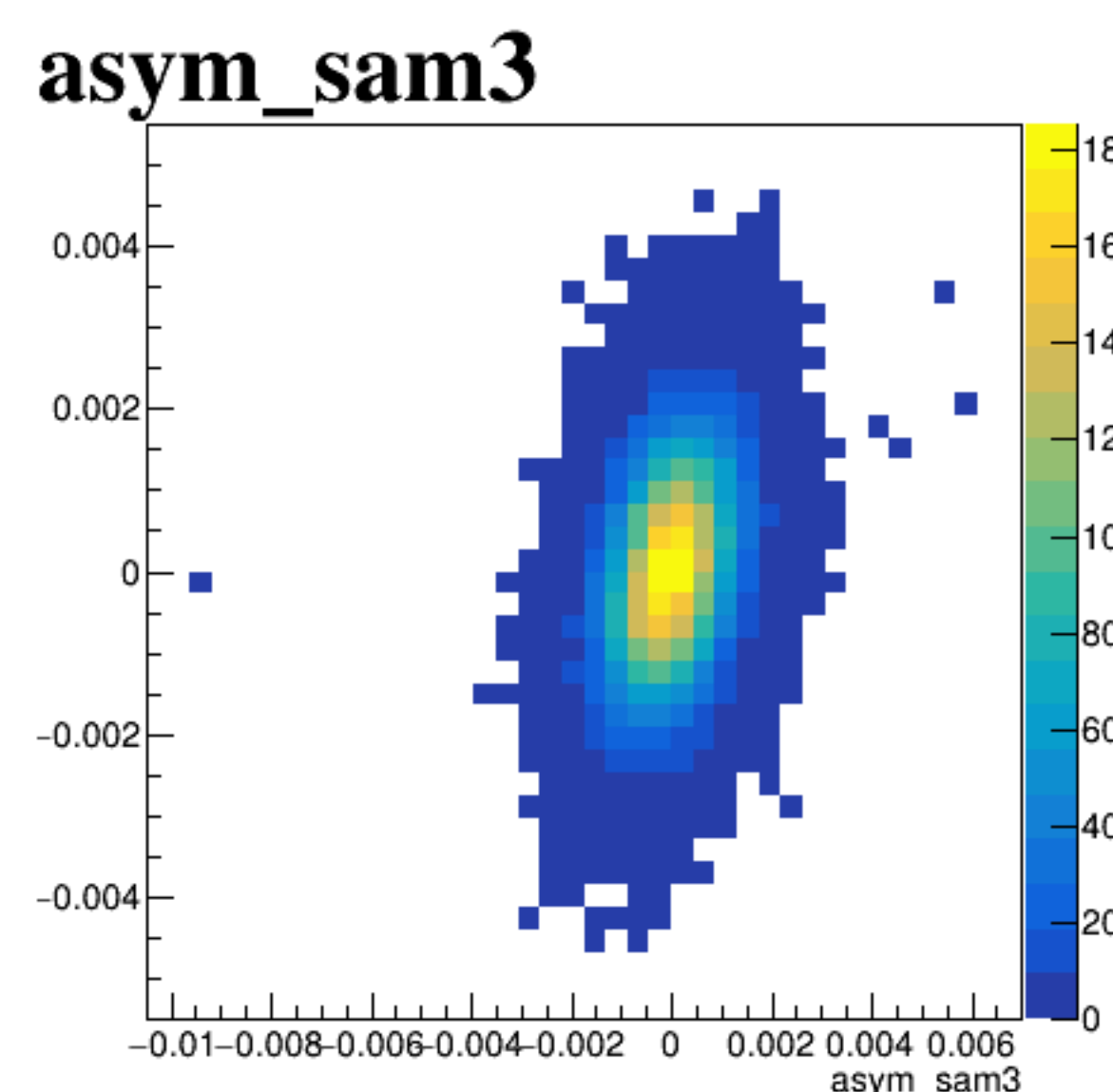
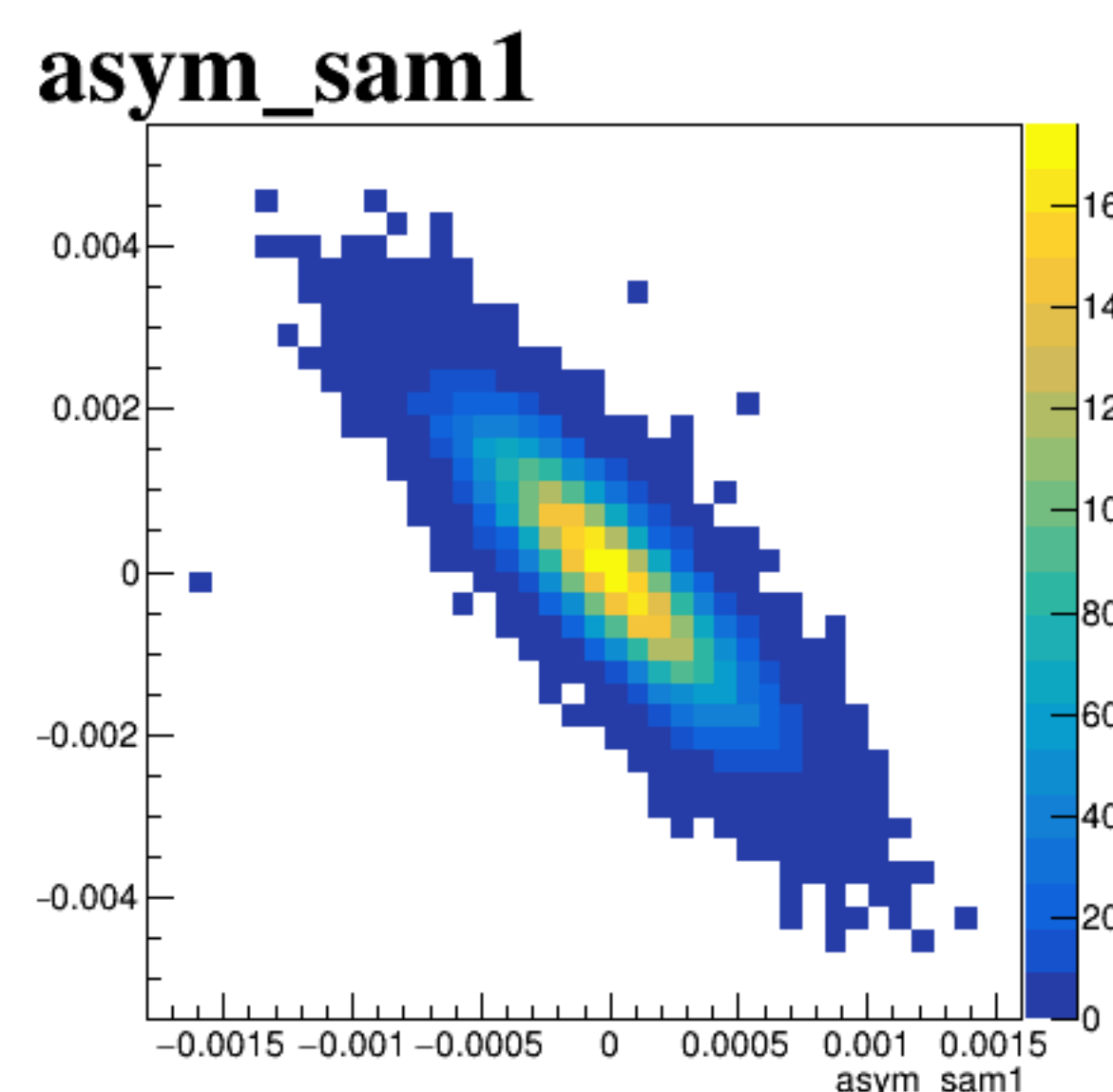




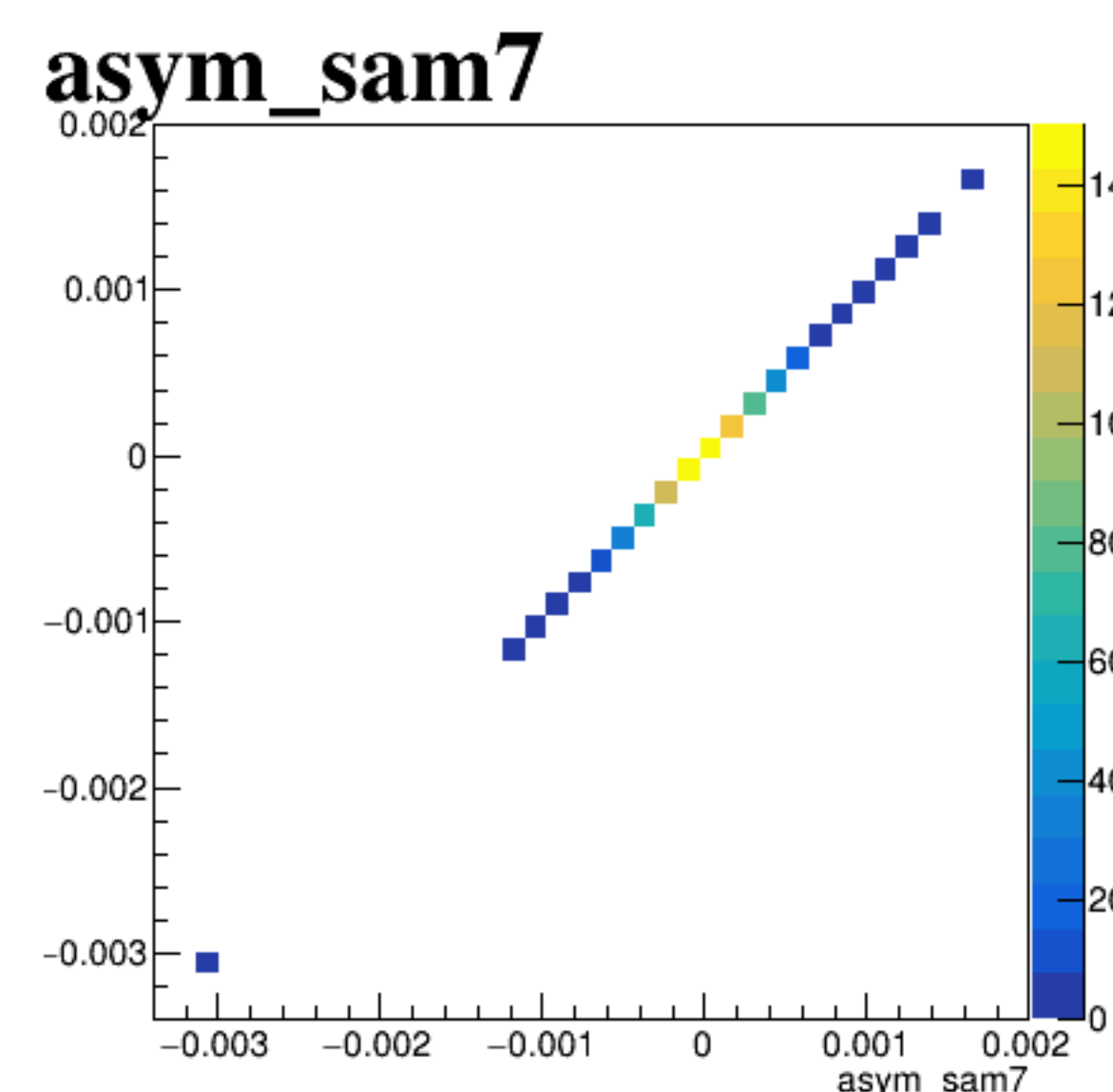
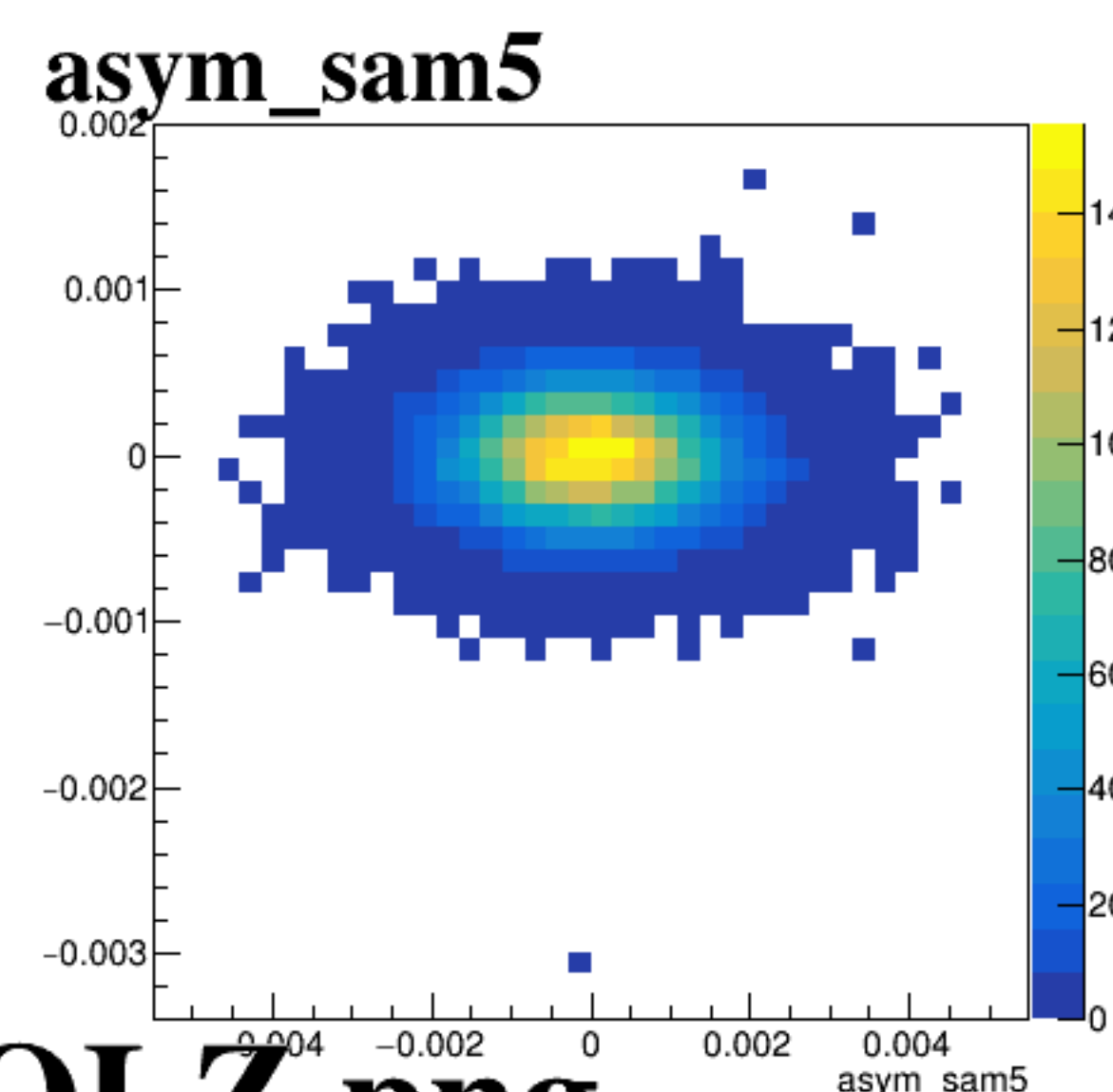
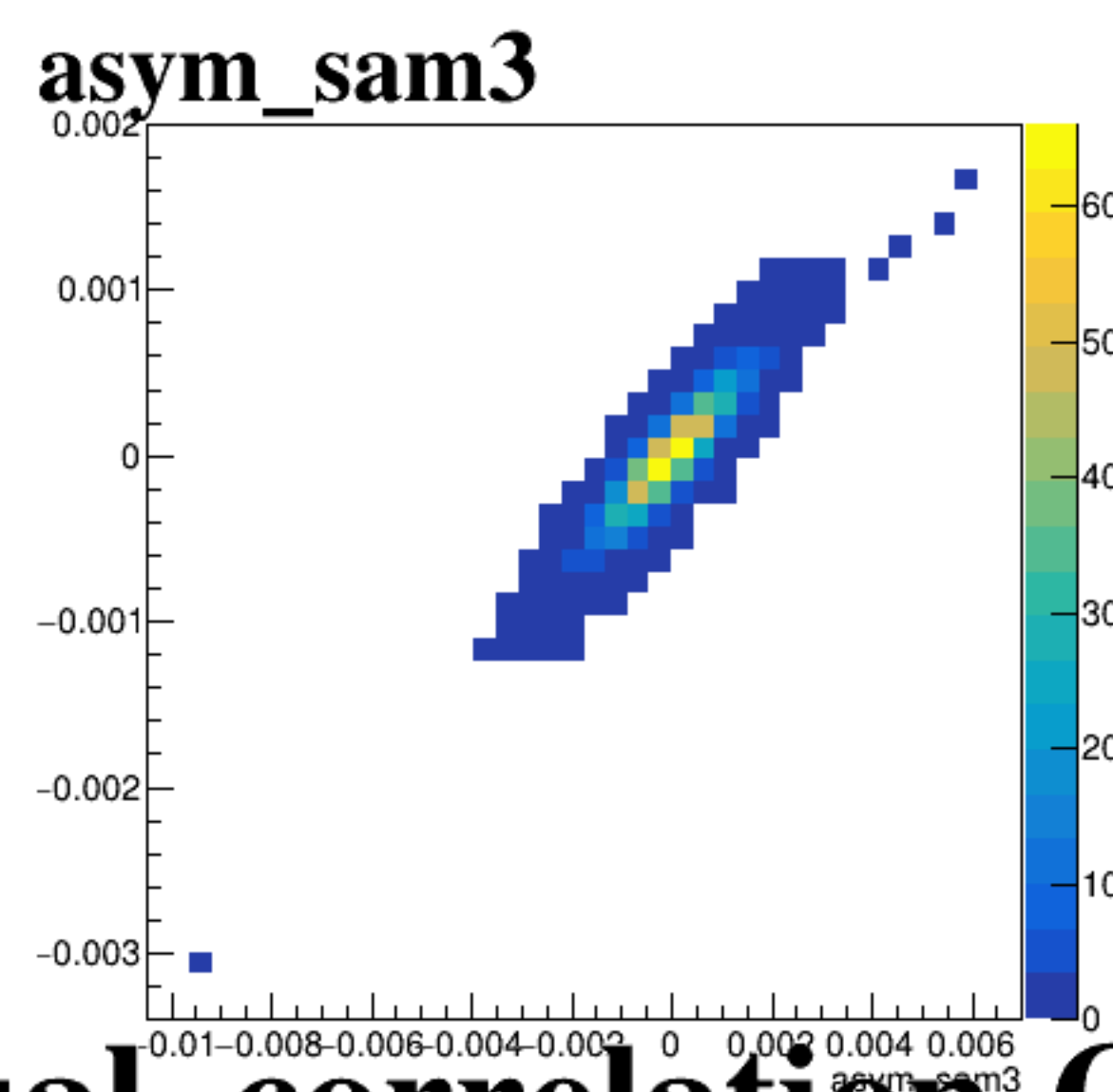
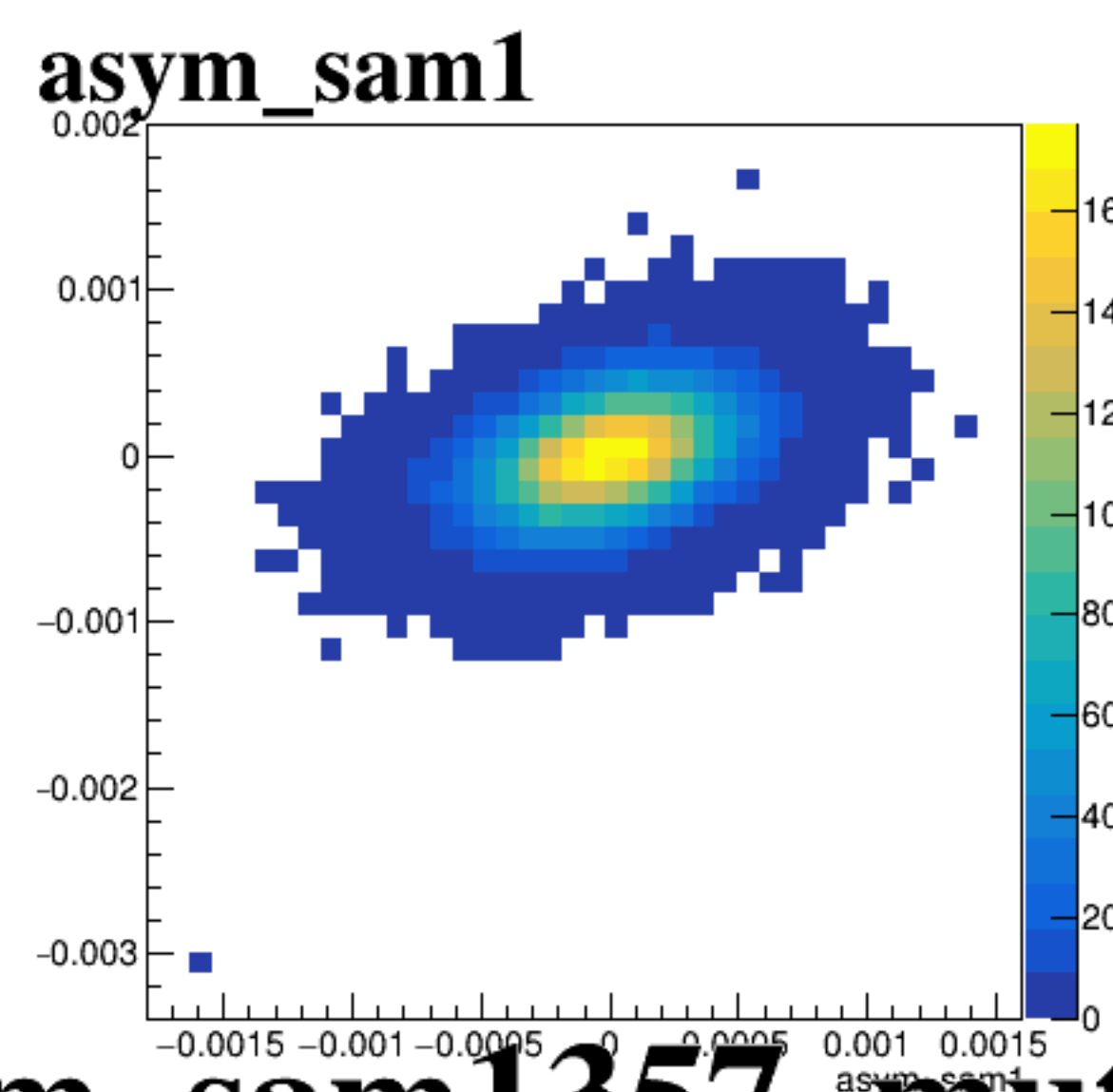
# asym\_sam1



## asym\_sam3

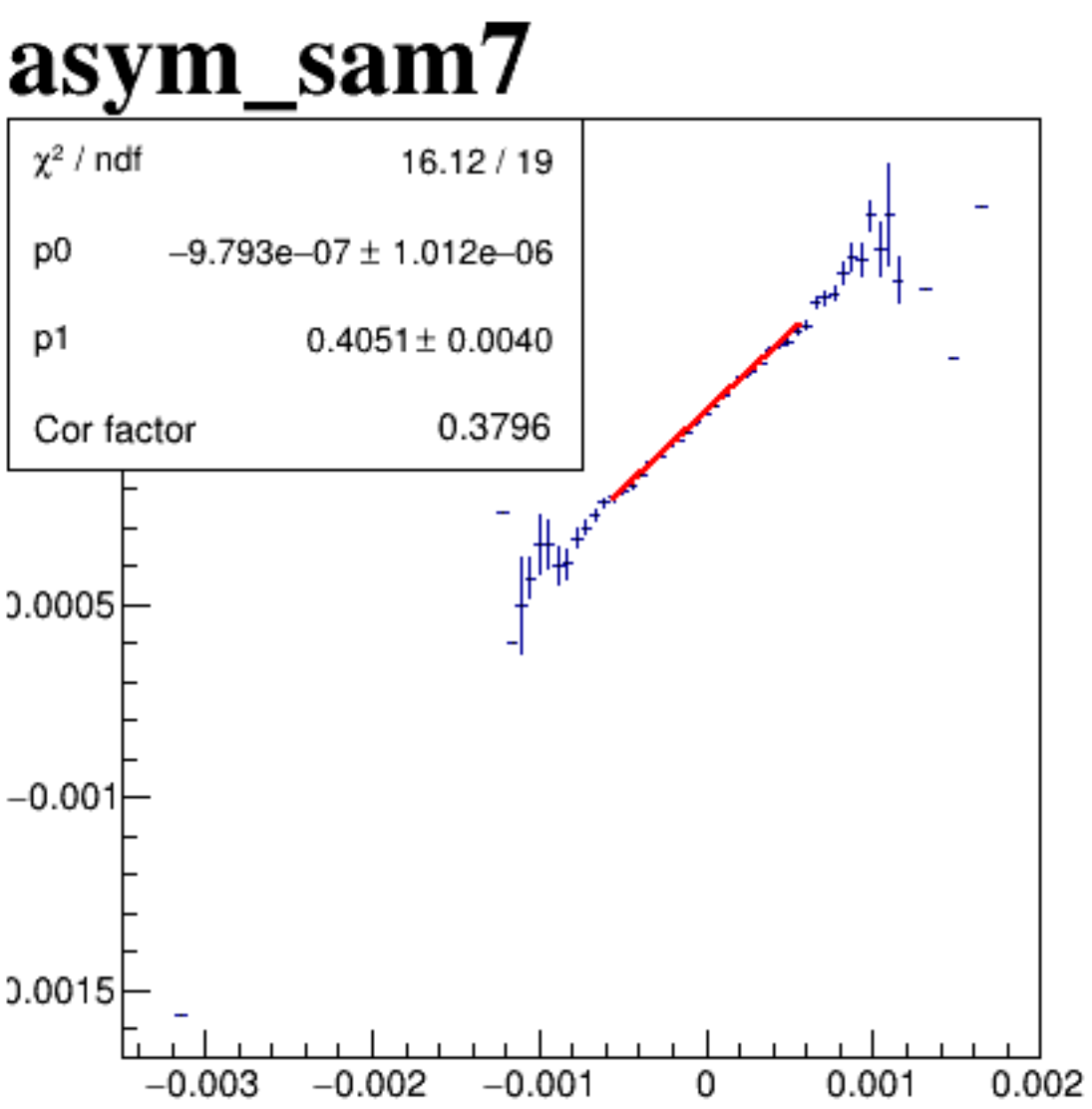
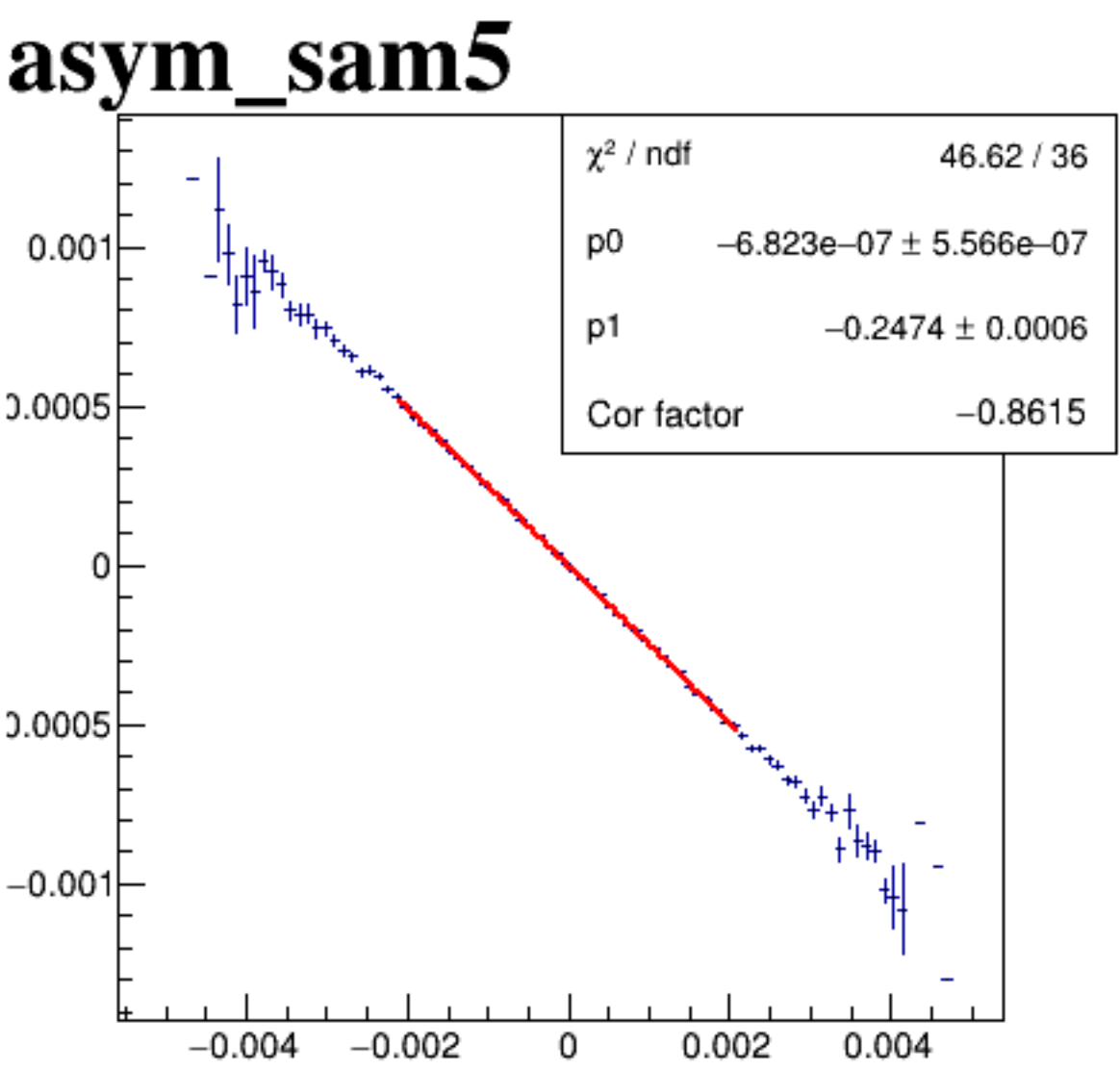
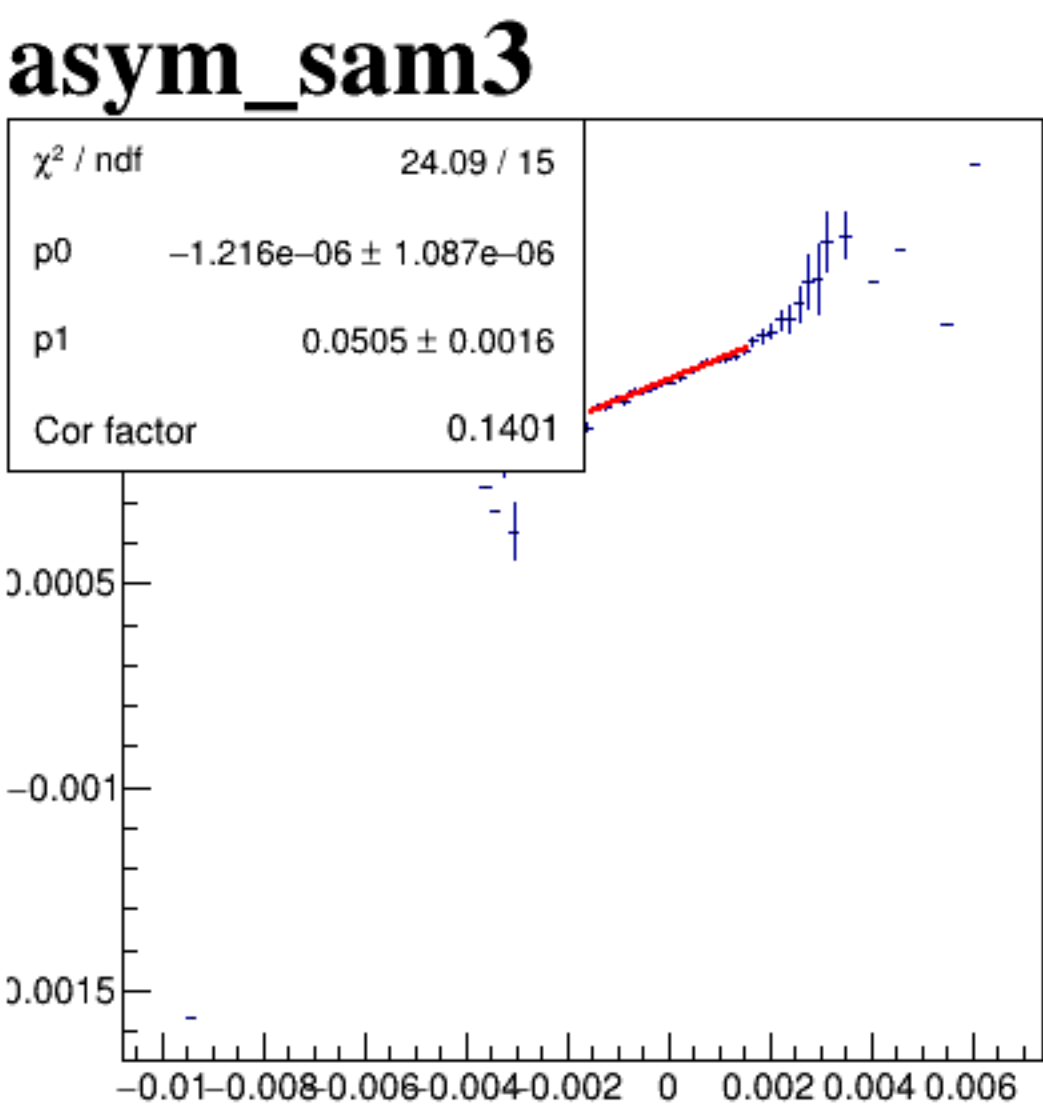
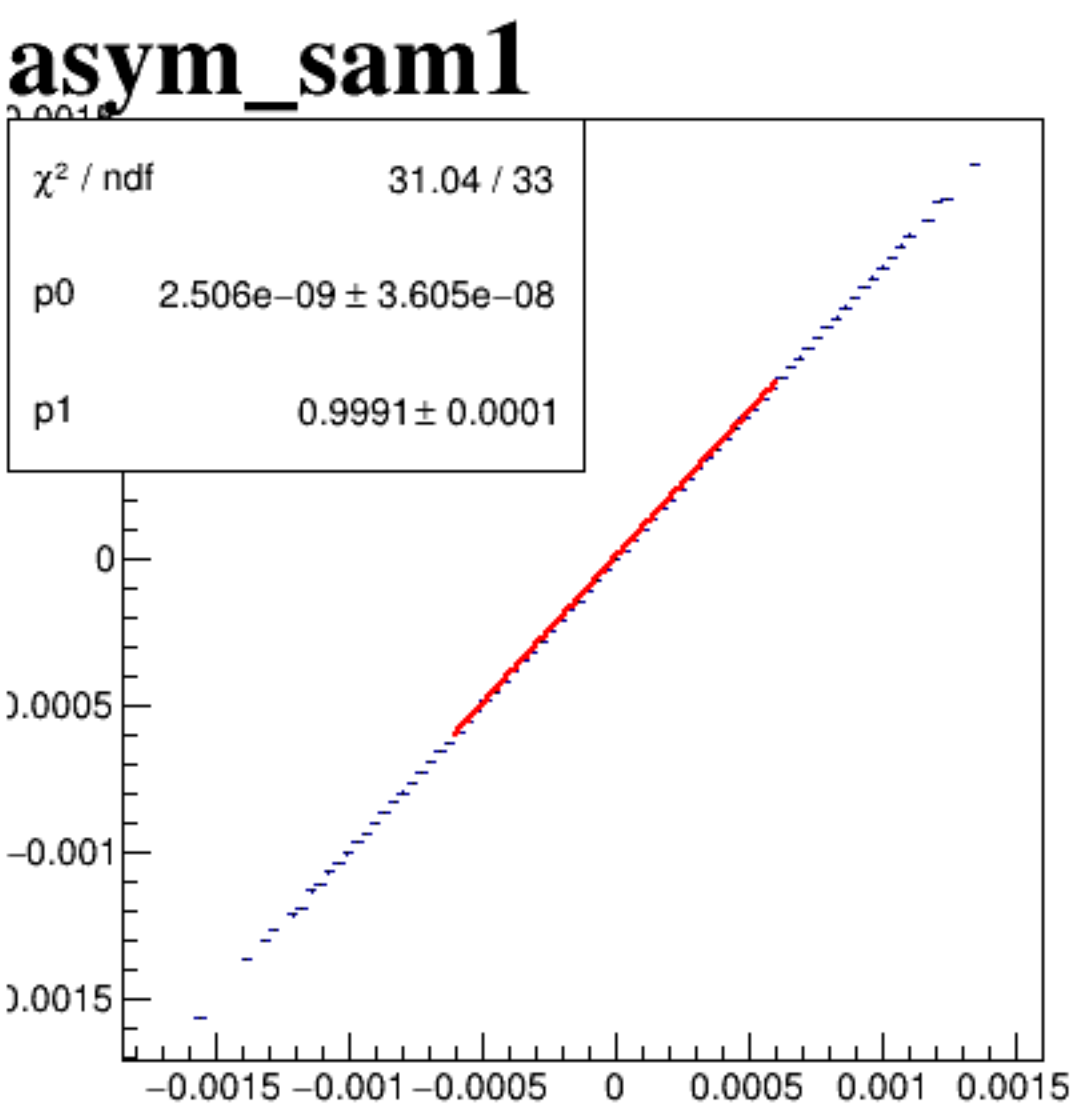
**asym\_sam5**

**asym\_sam7**

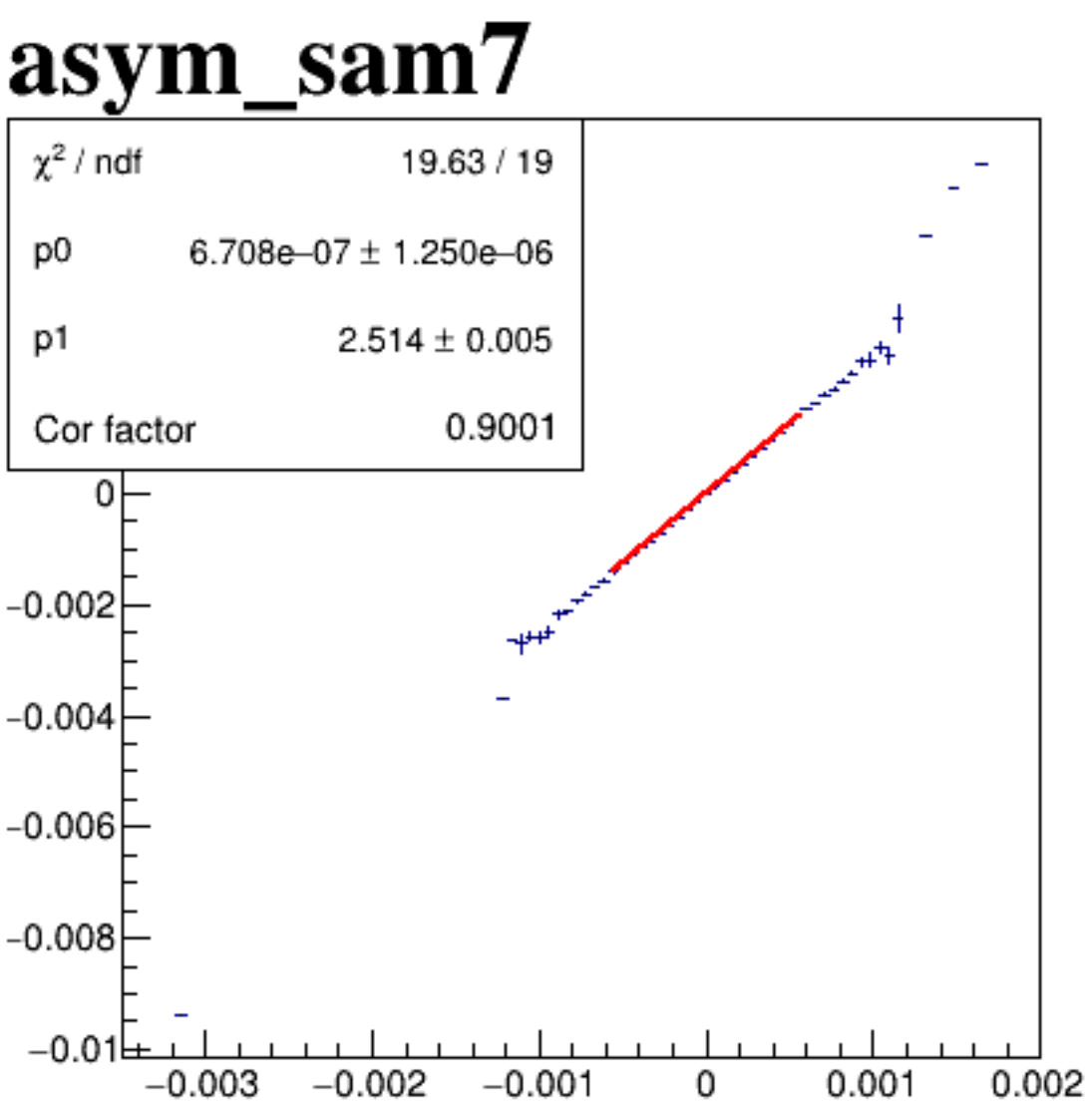
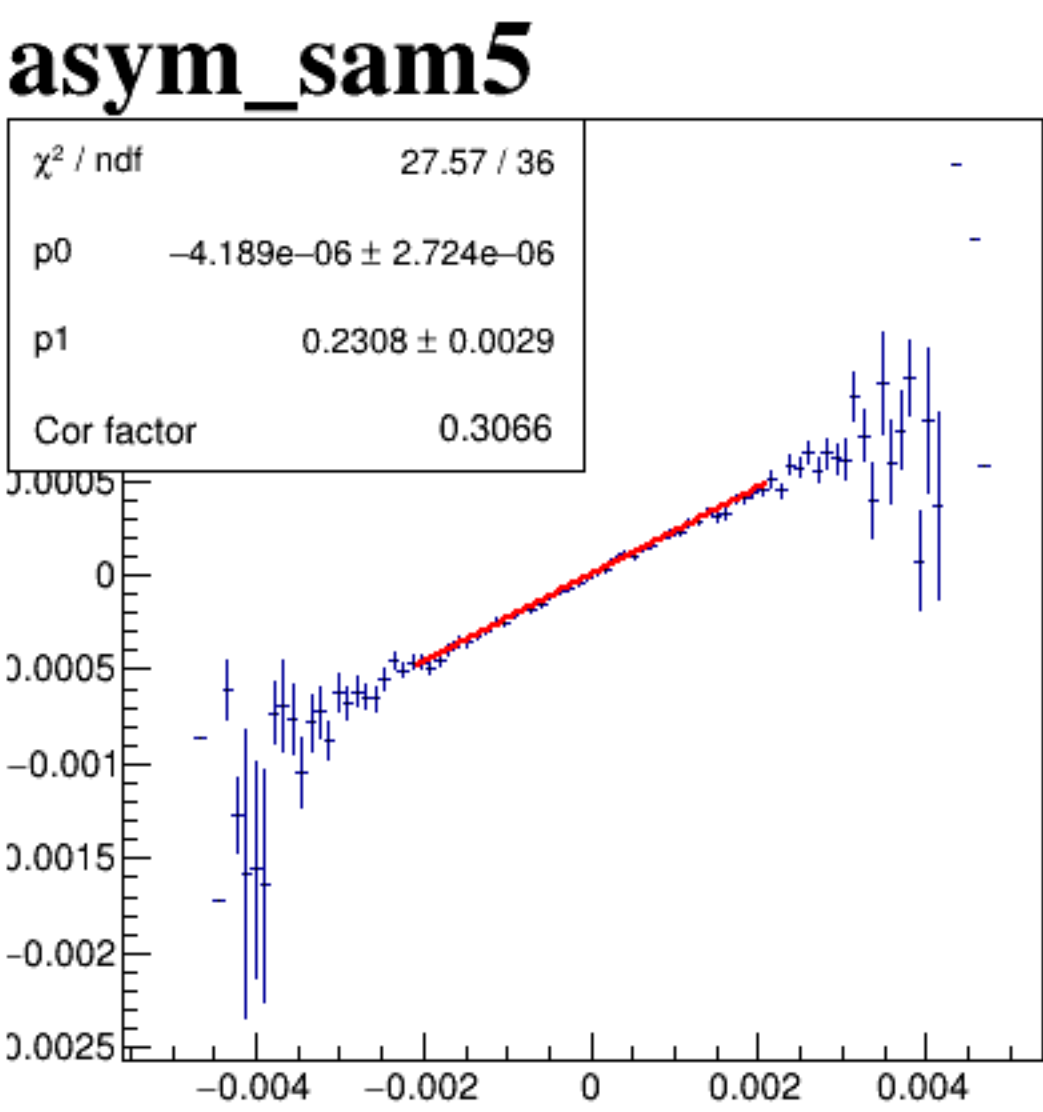
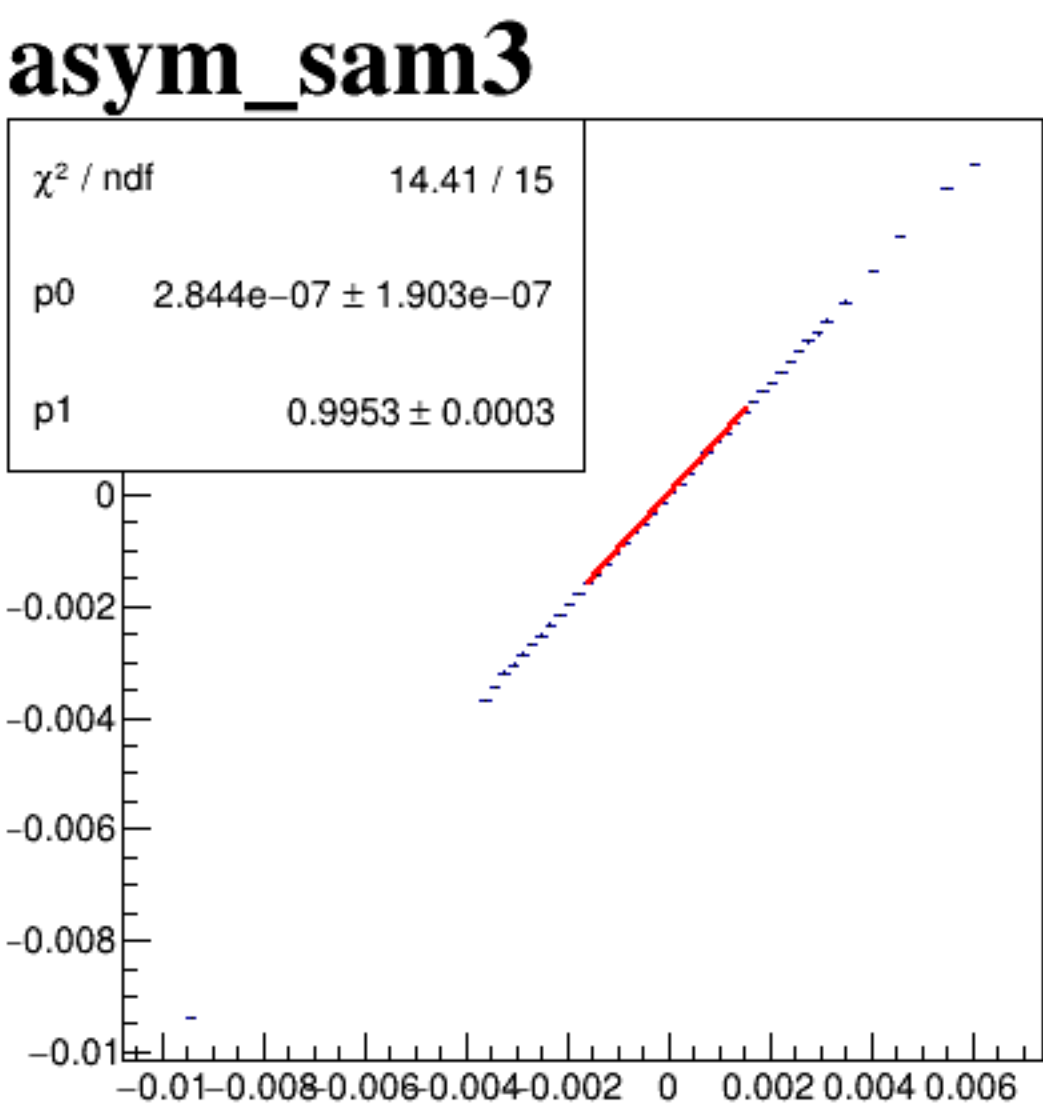
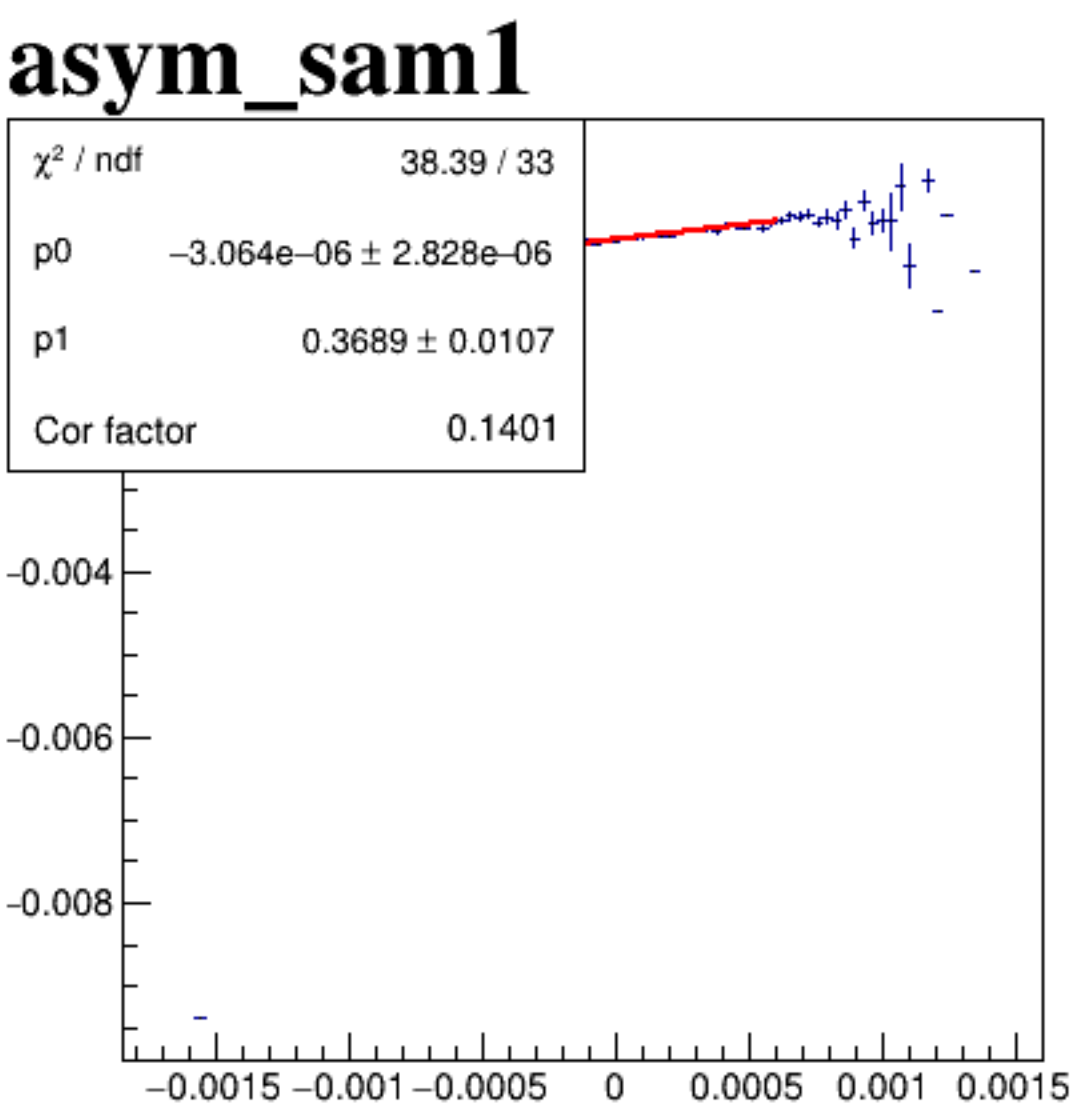




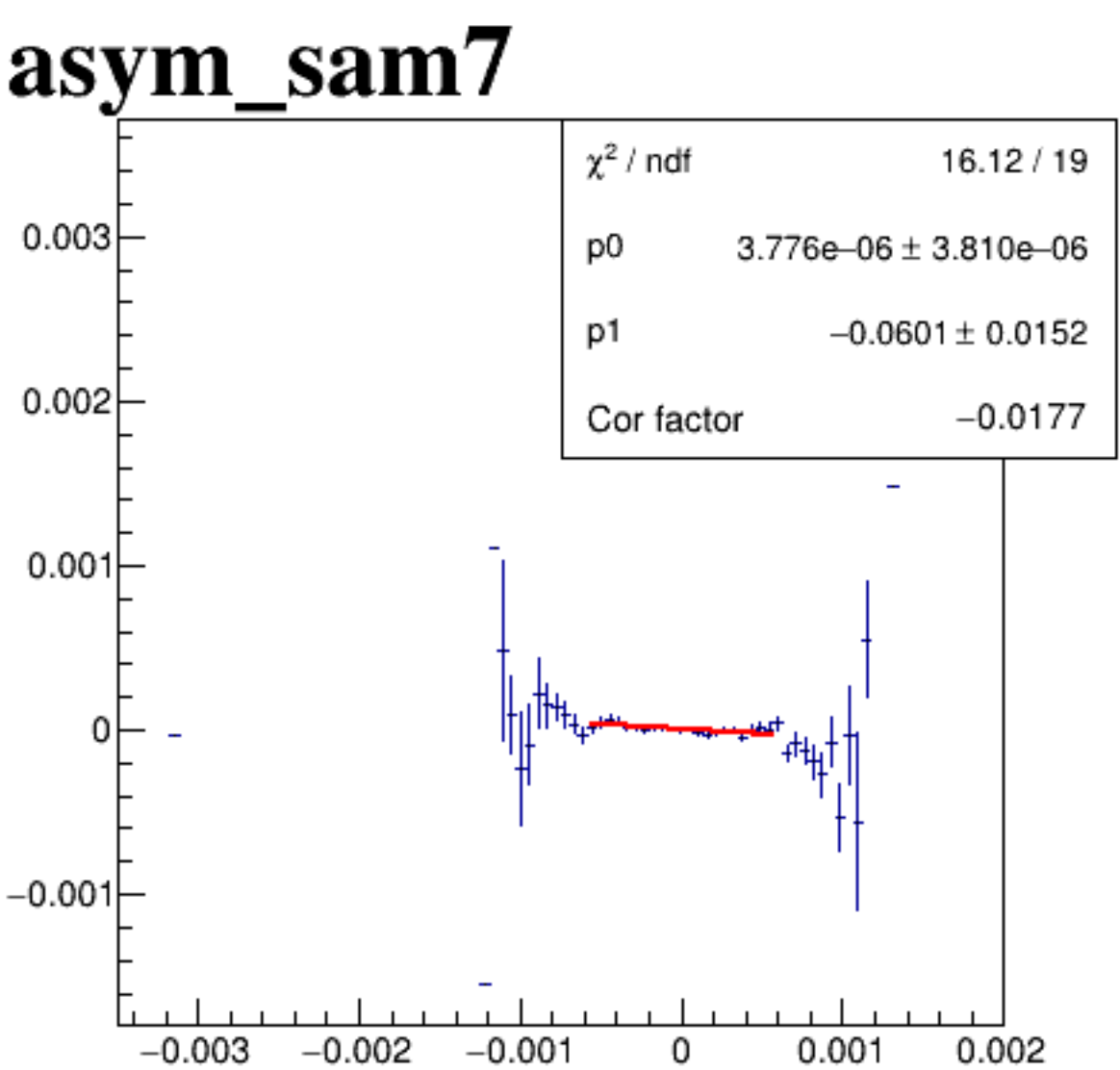
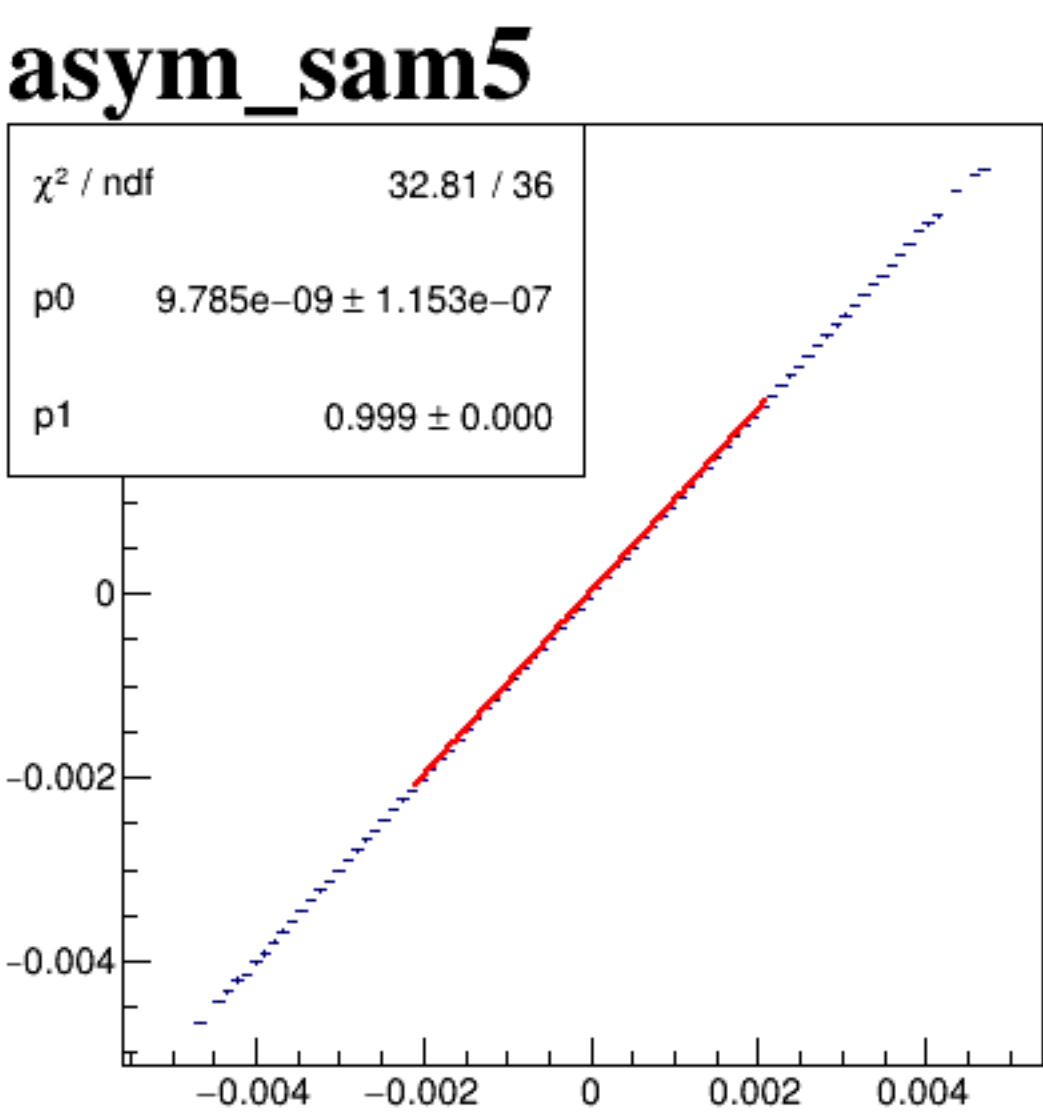
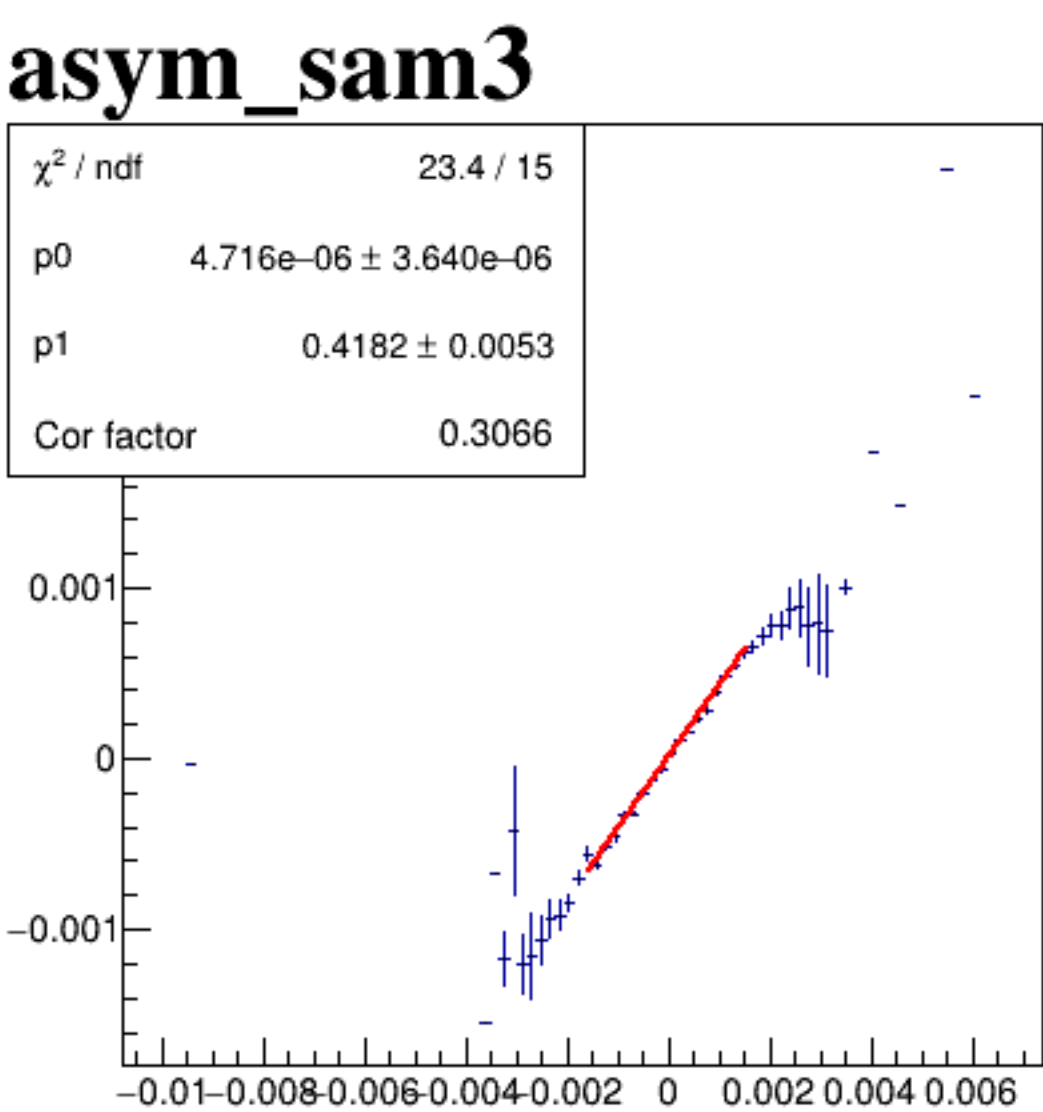
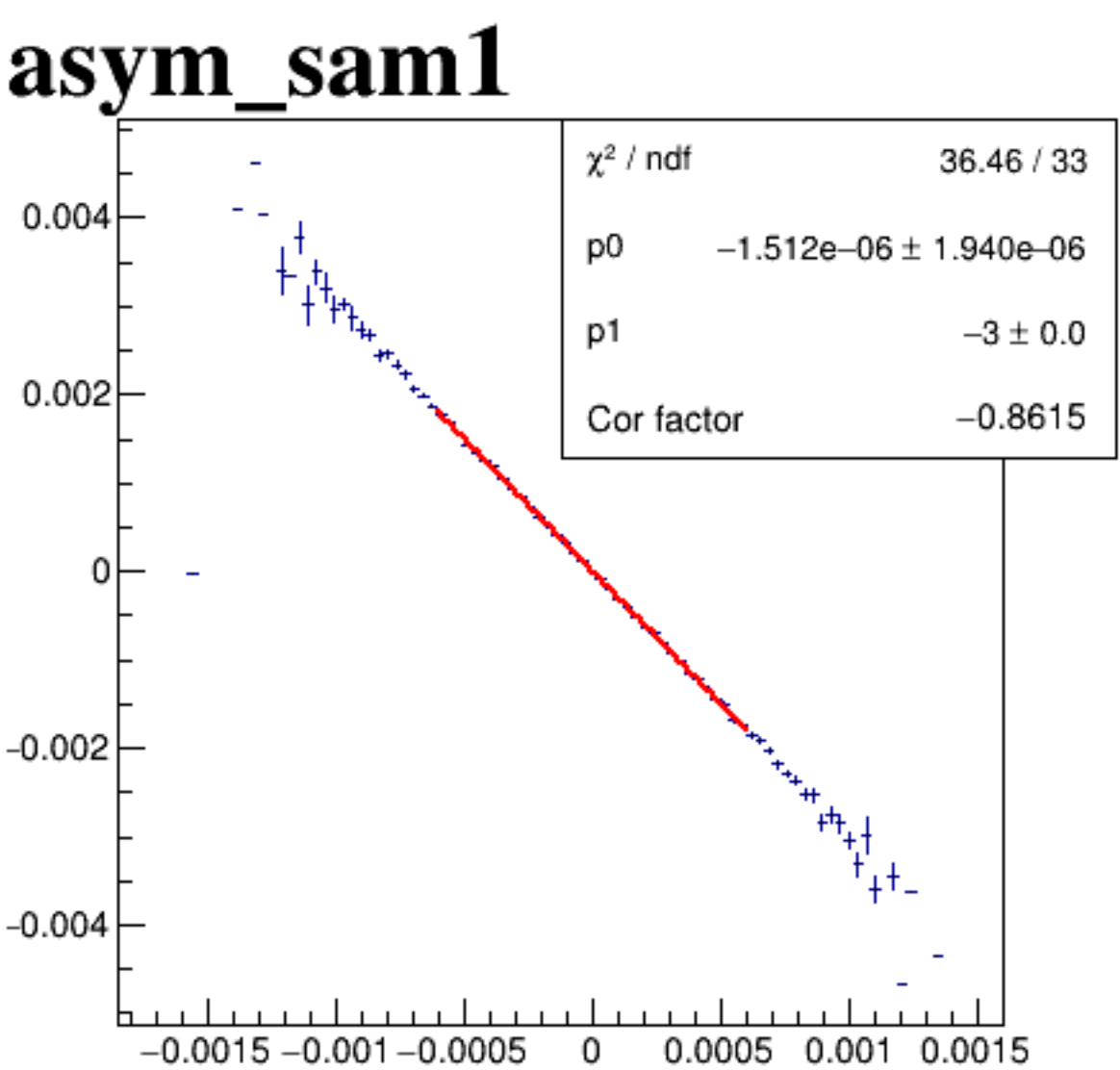
asym\_sam1



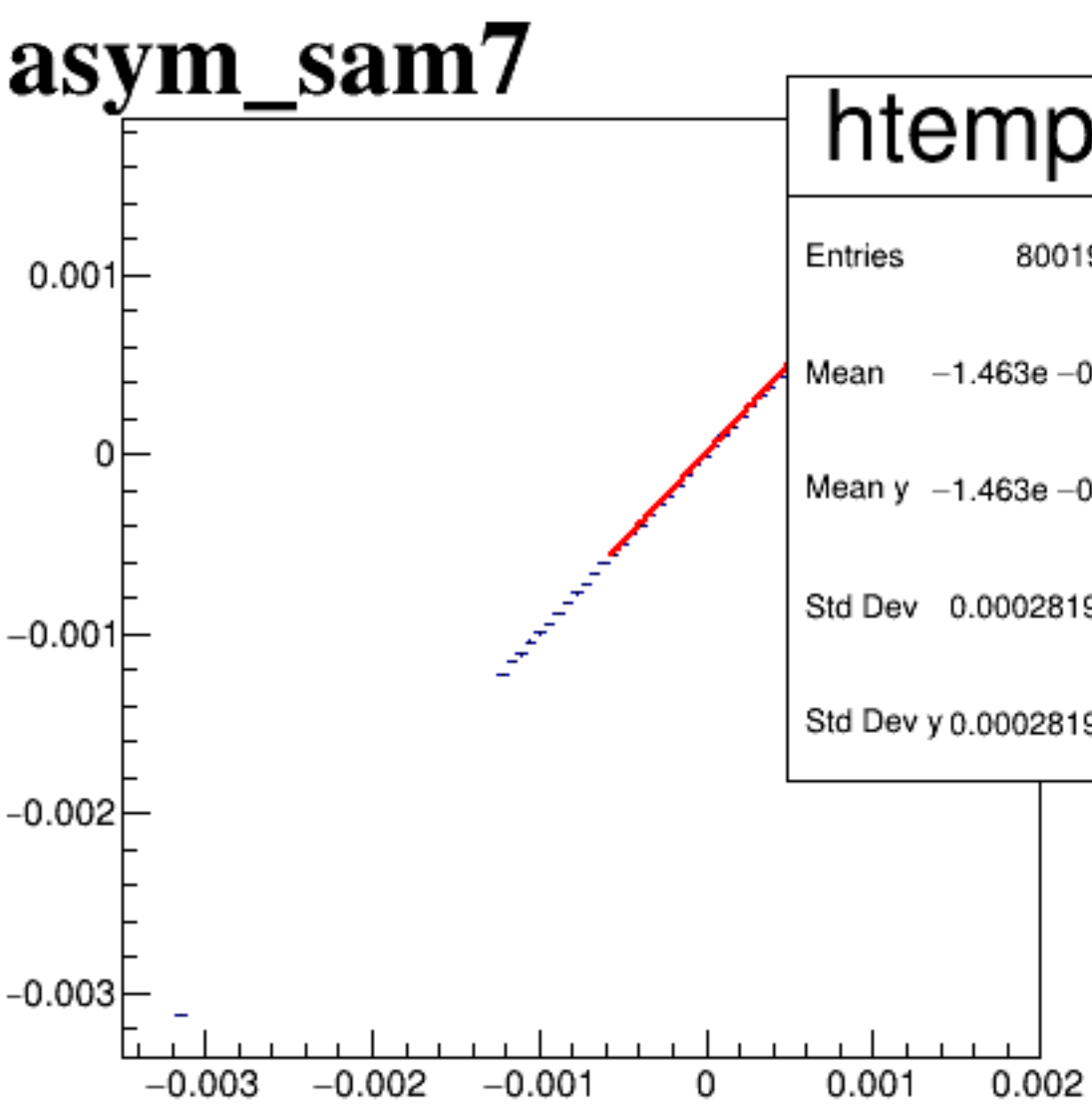
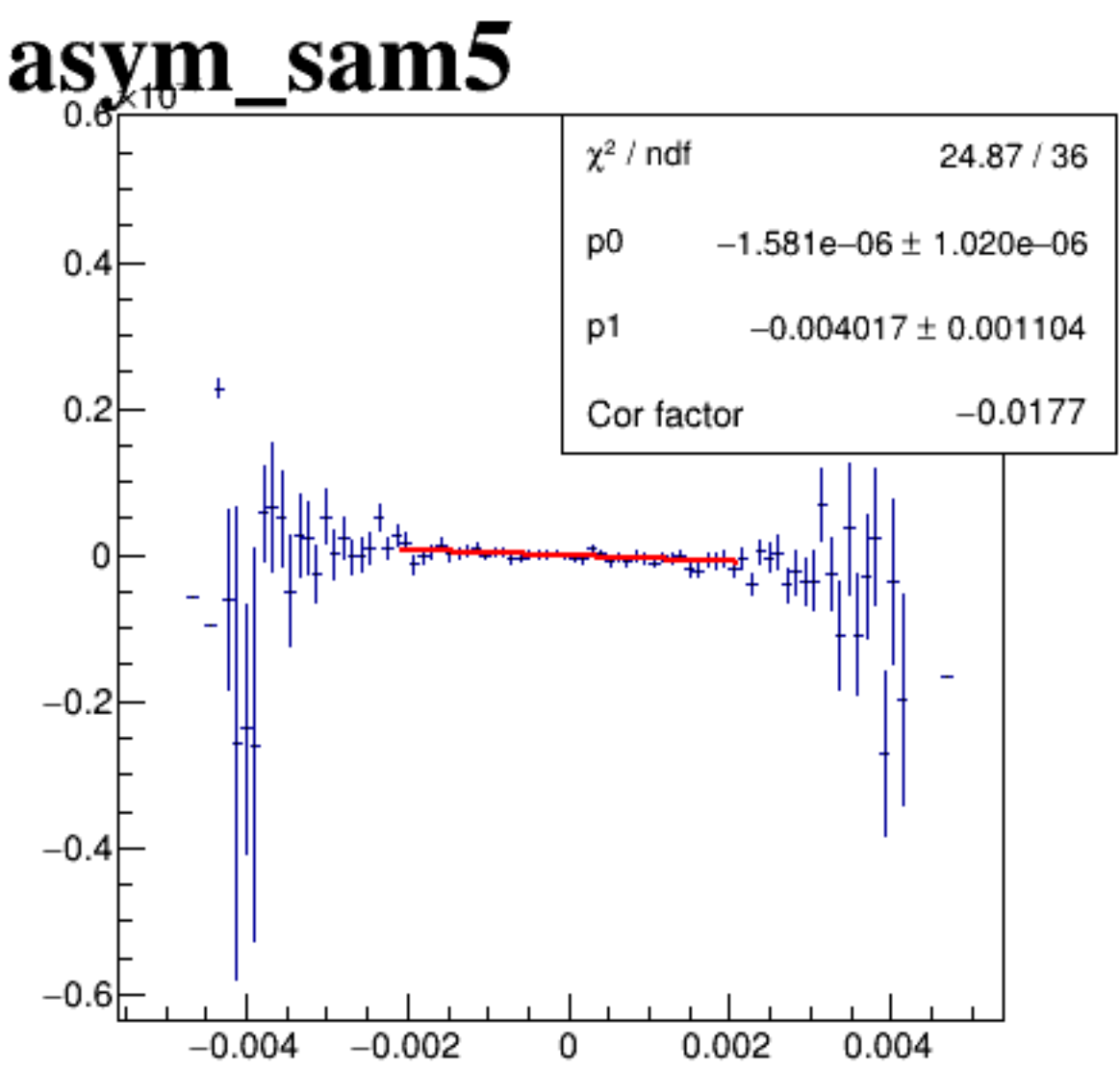
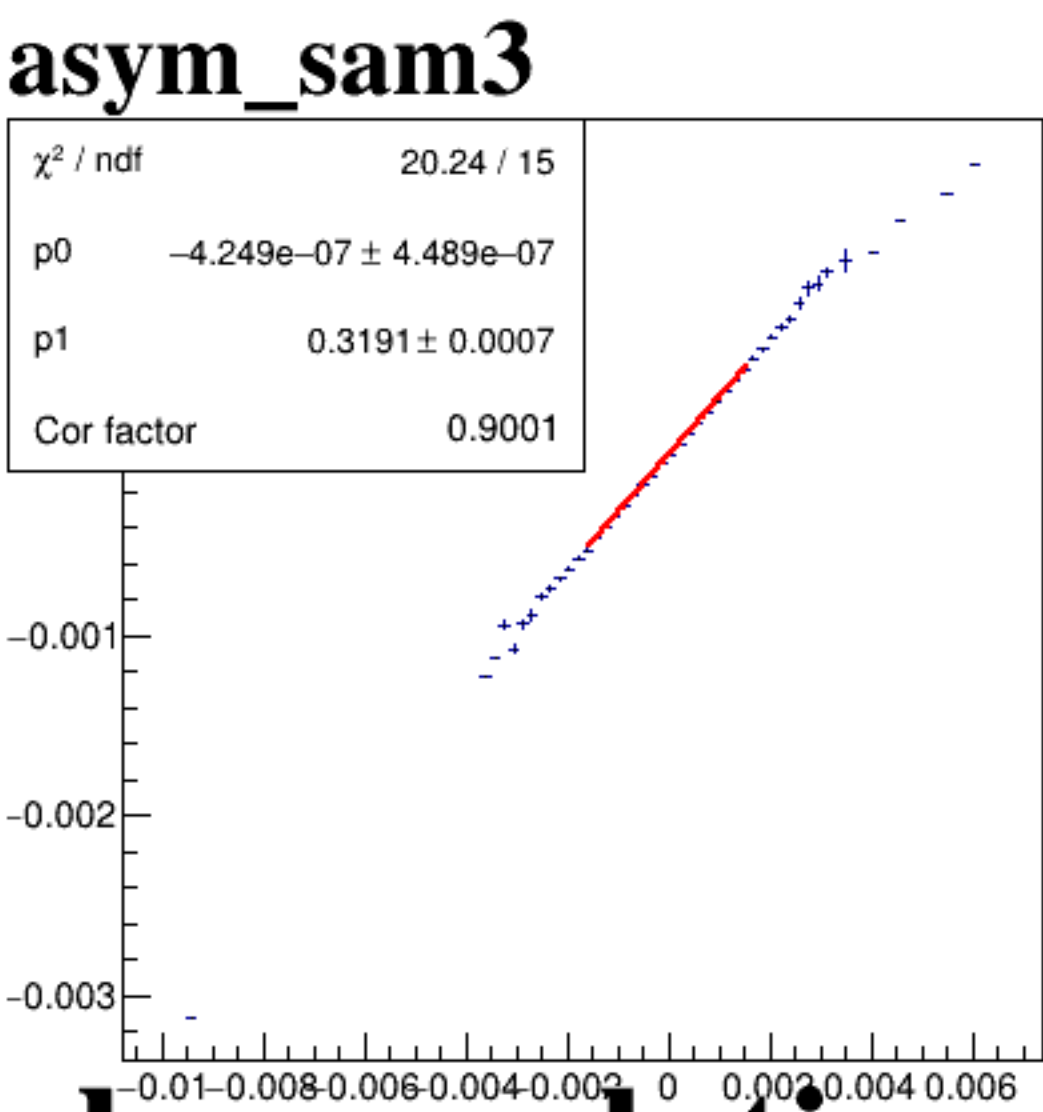
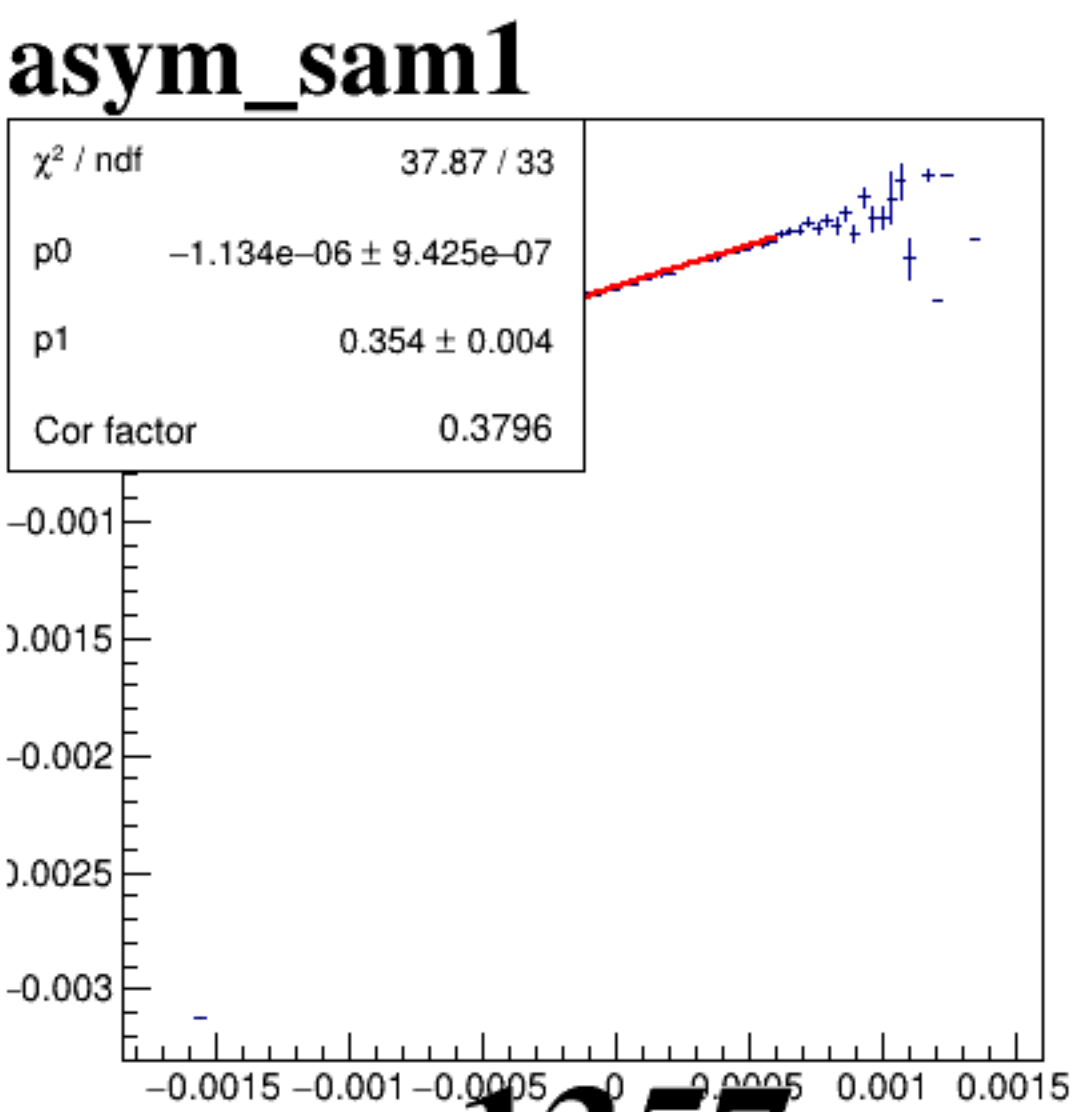
asym\_sam3



asym\_sam5

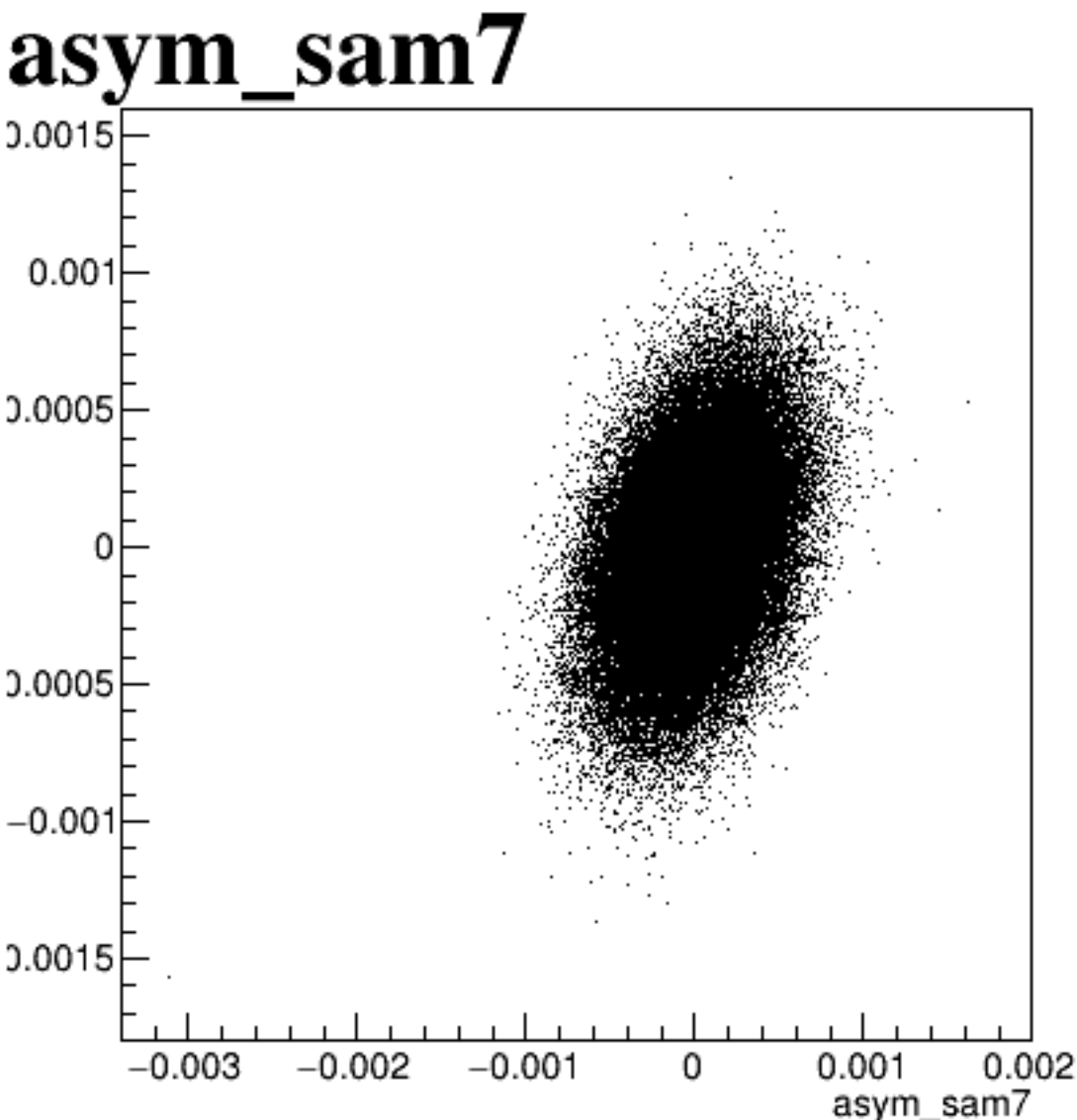
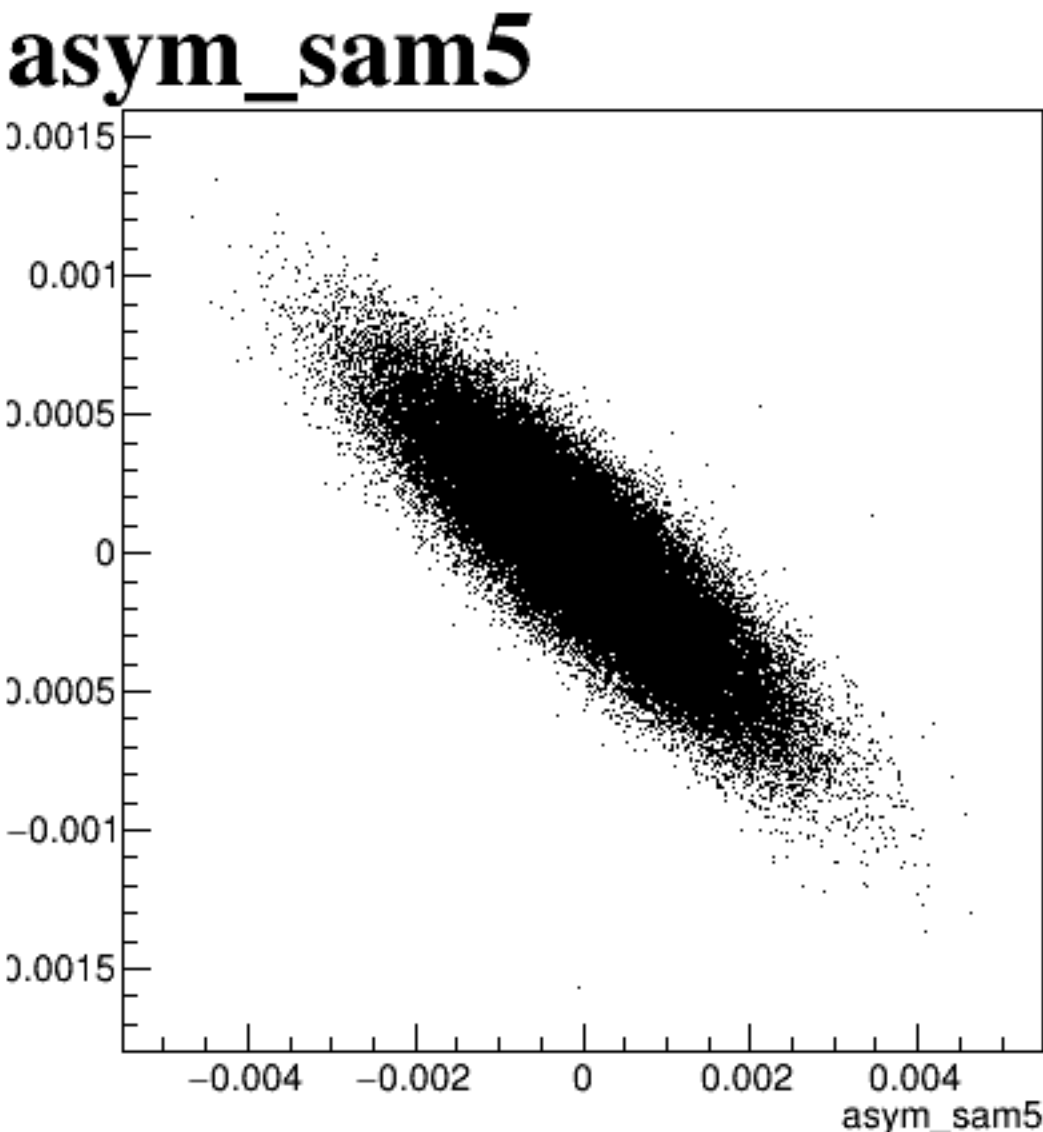
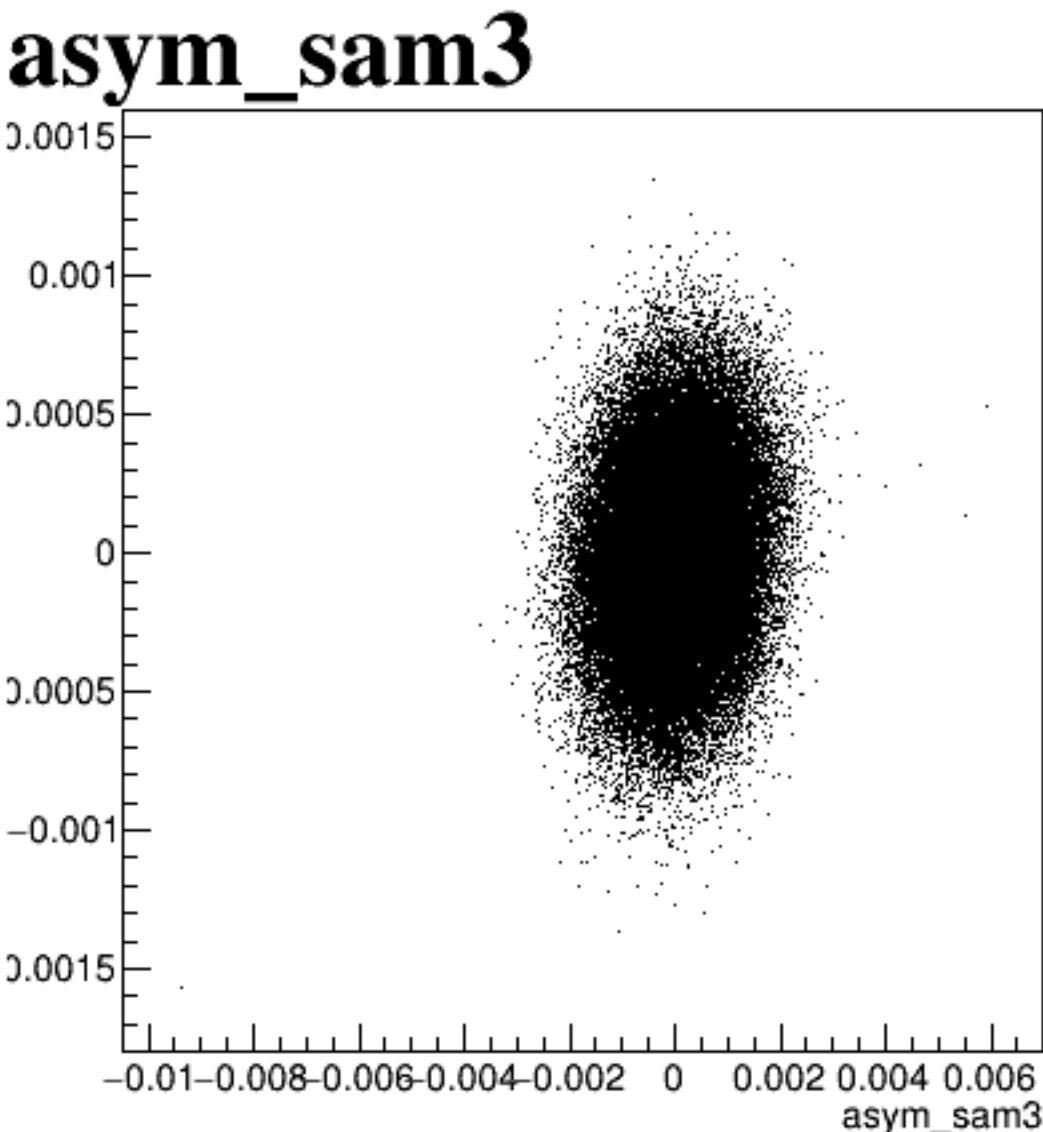
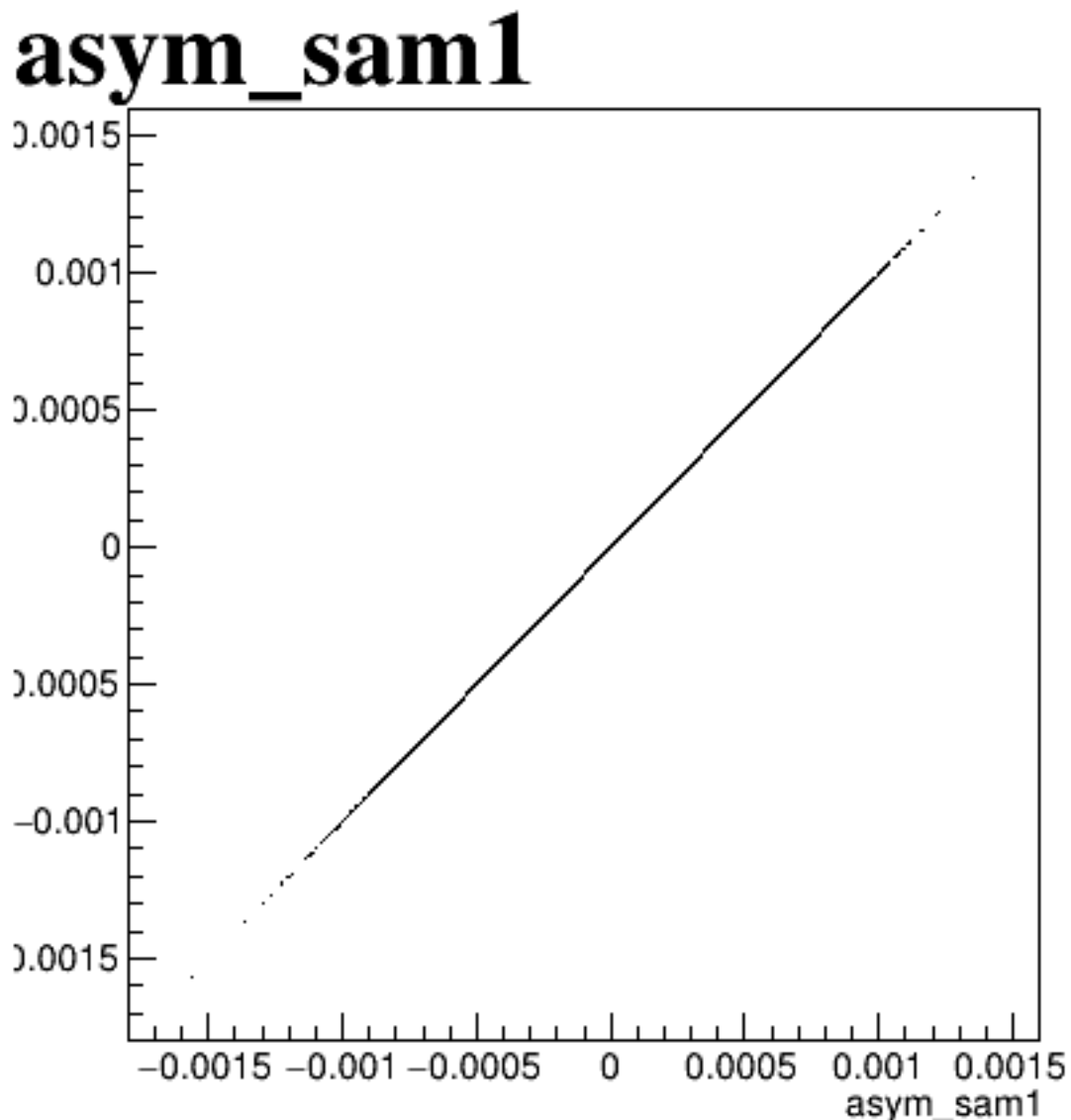


asym\_sam7

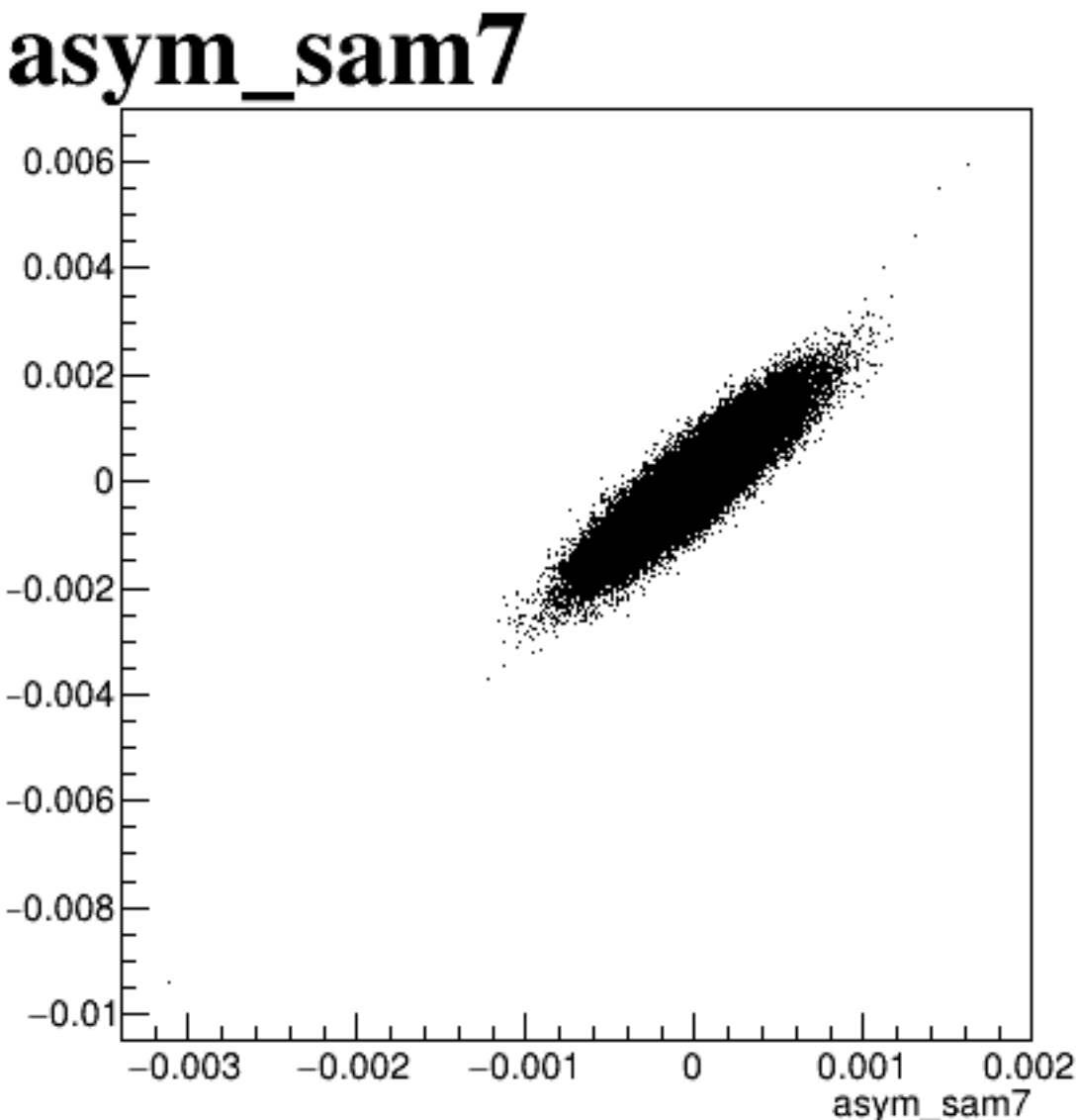
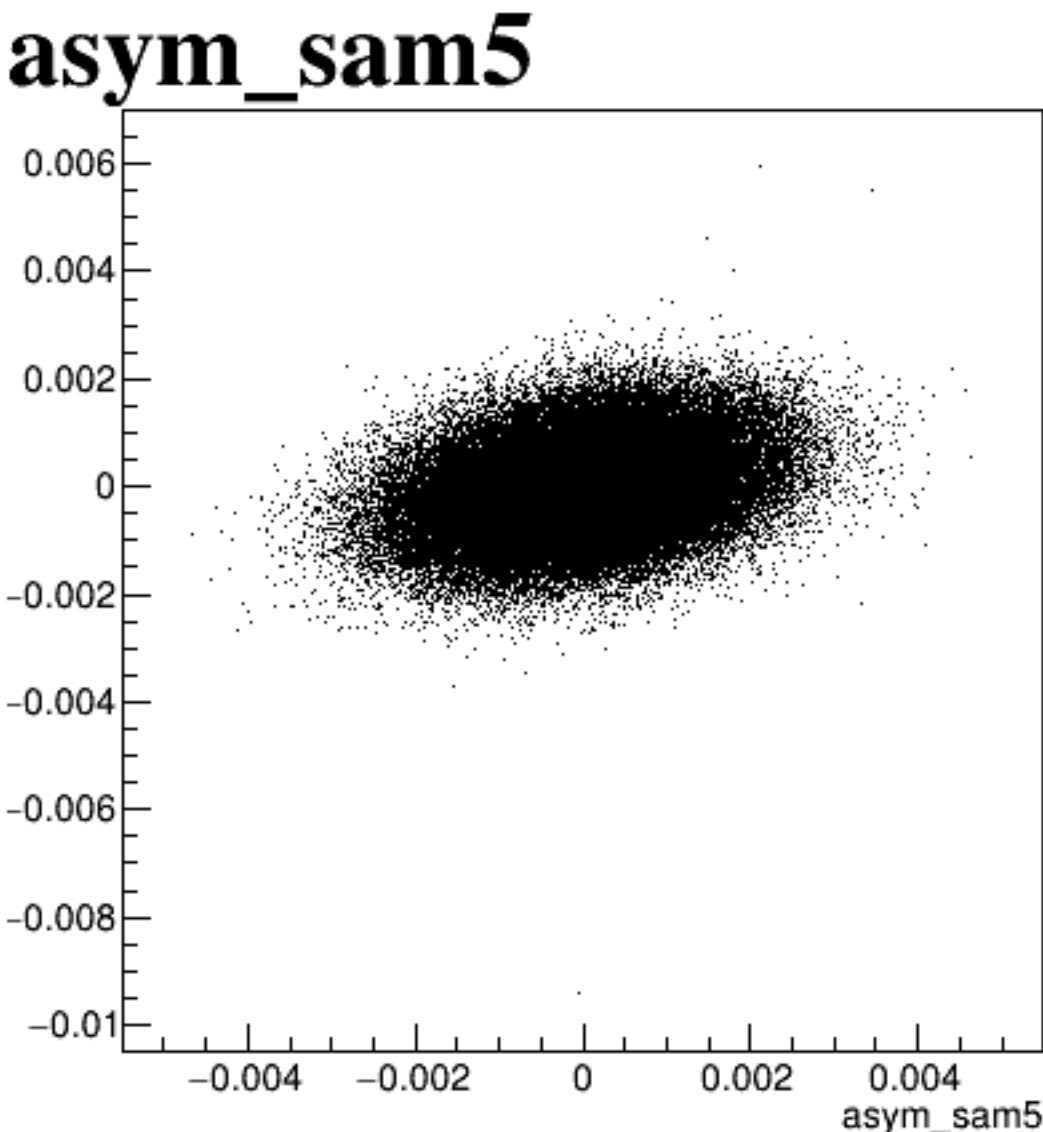
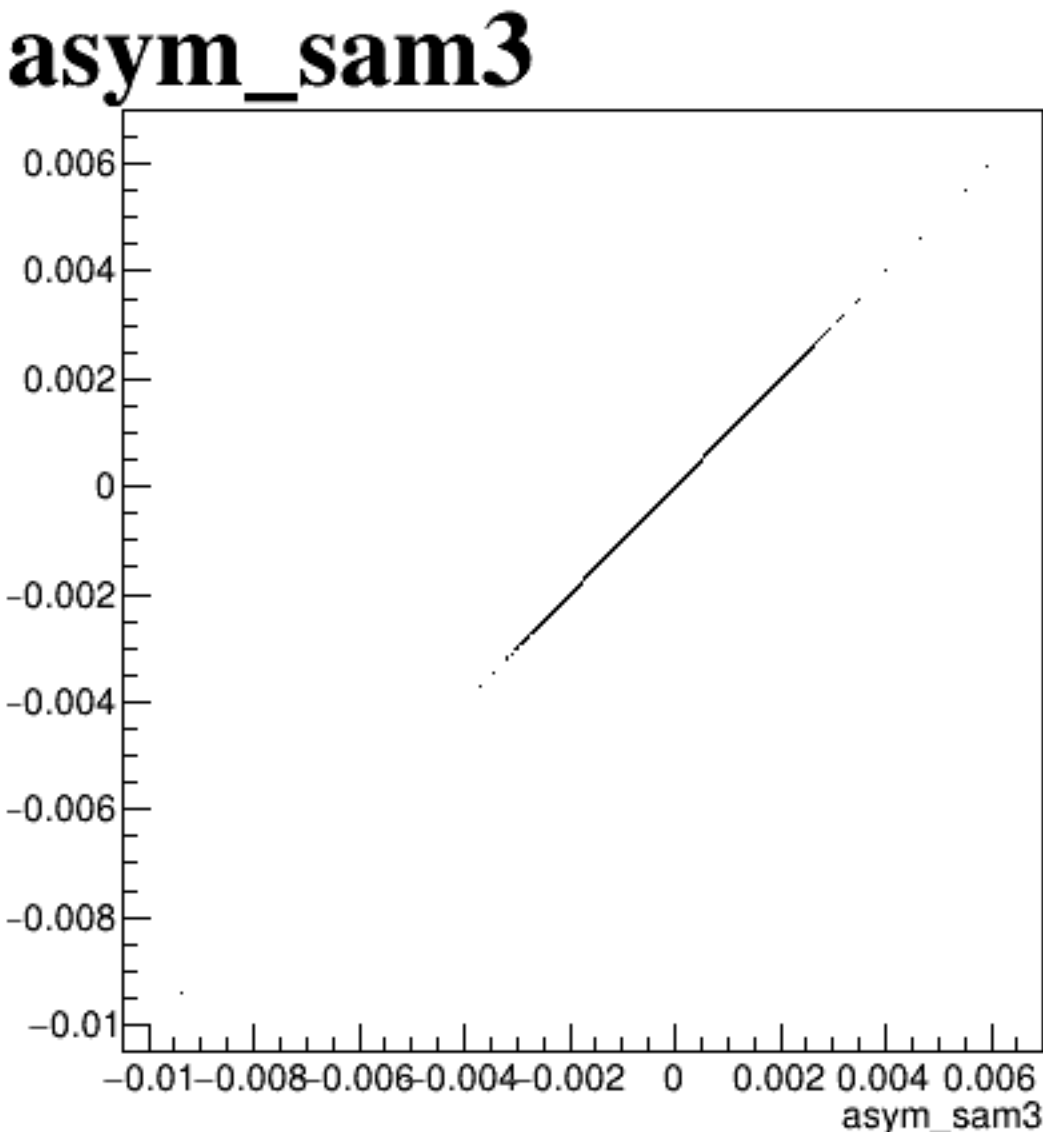
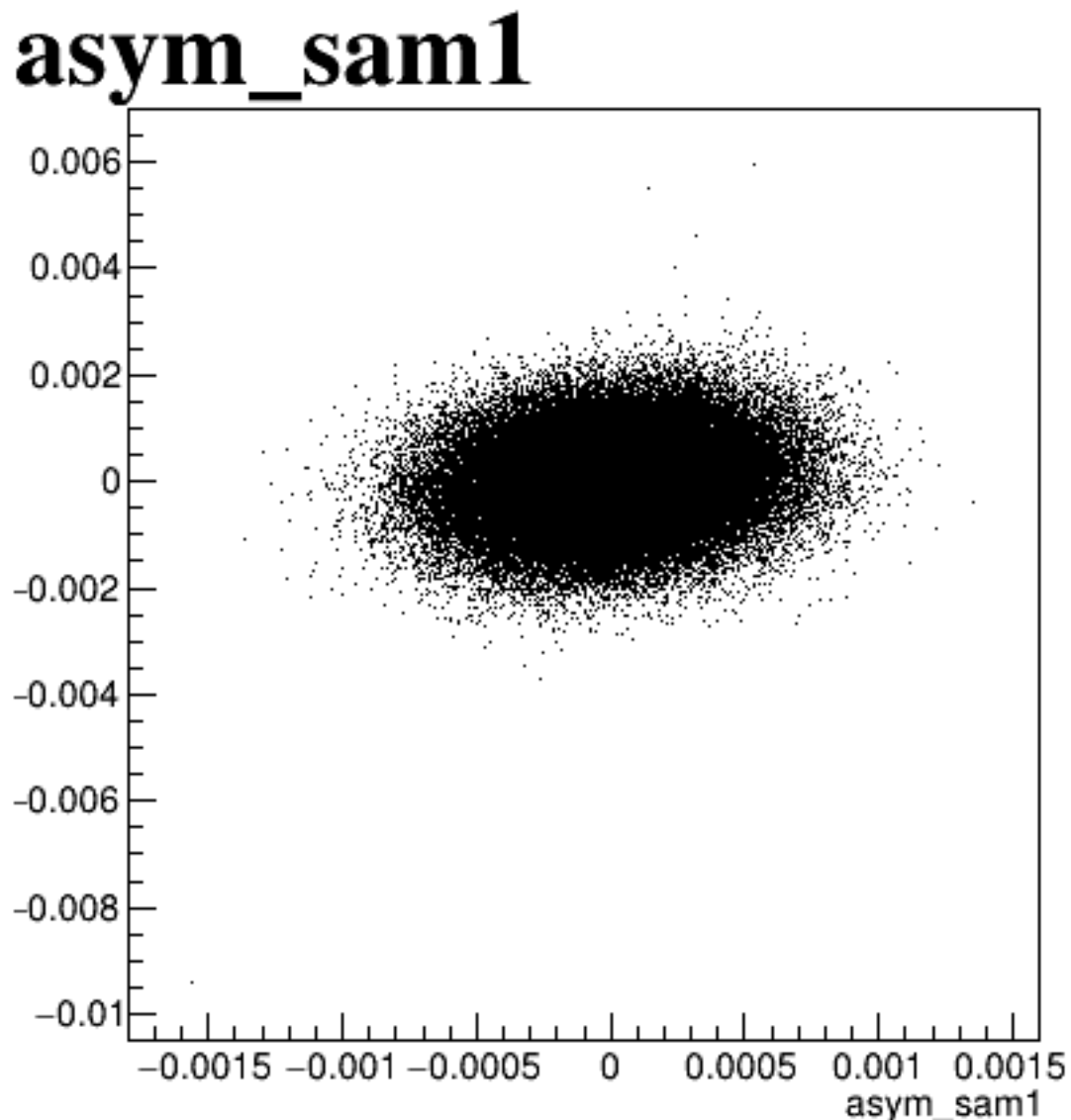




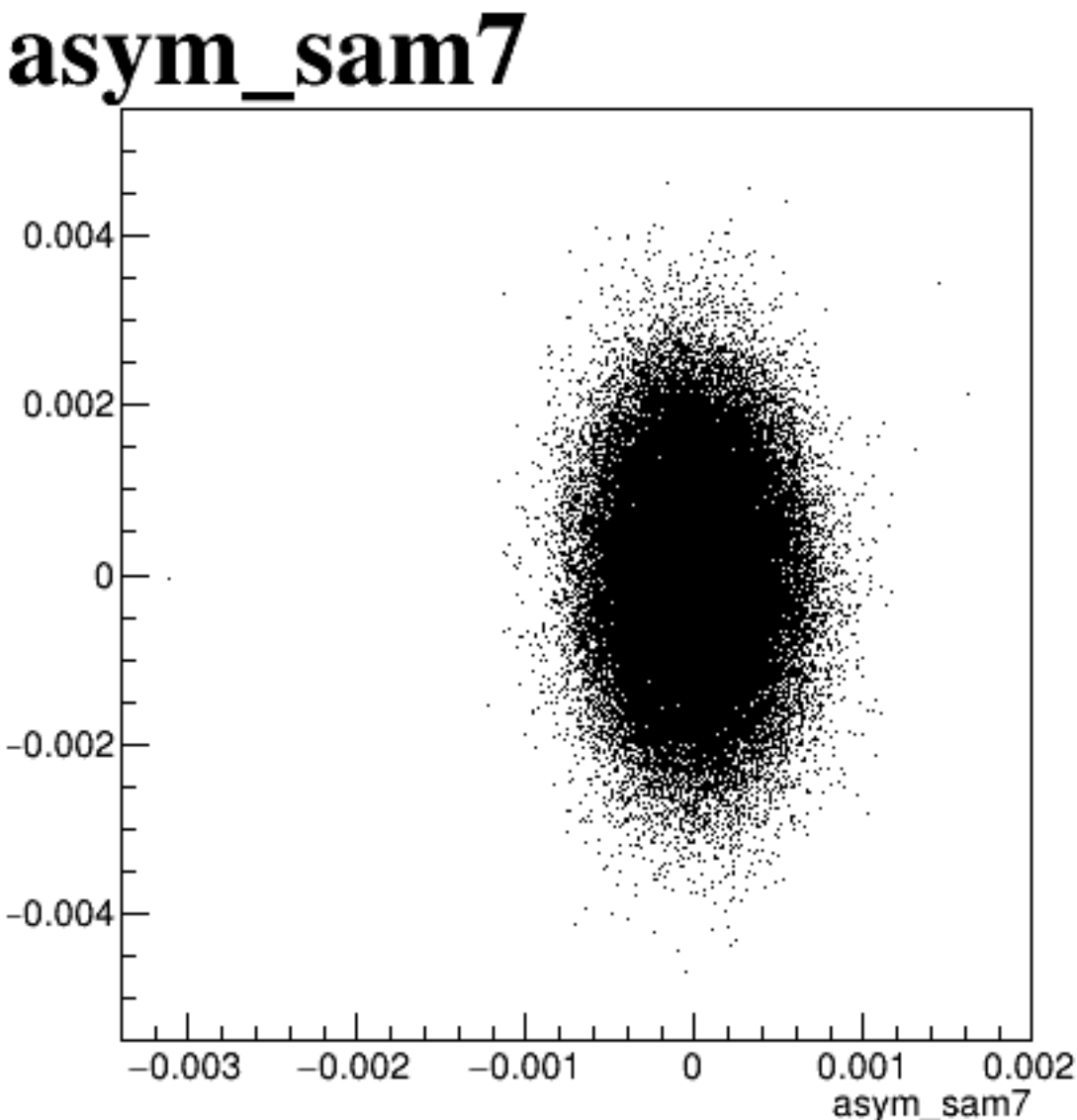
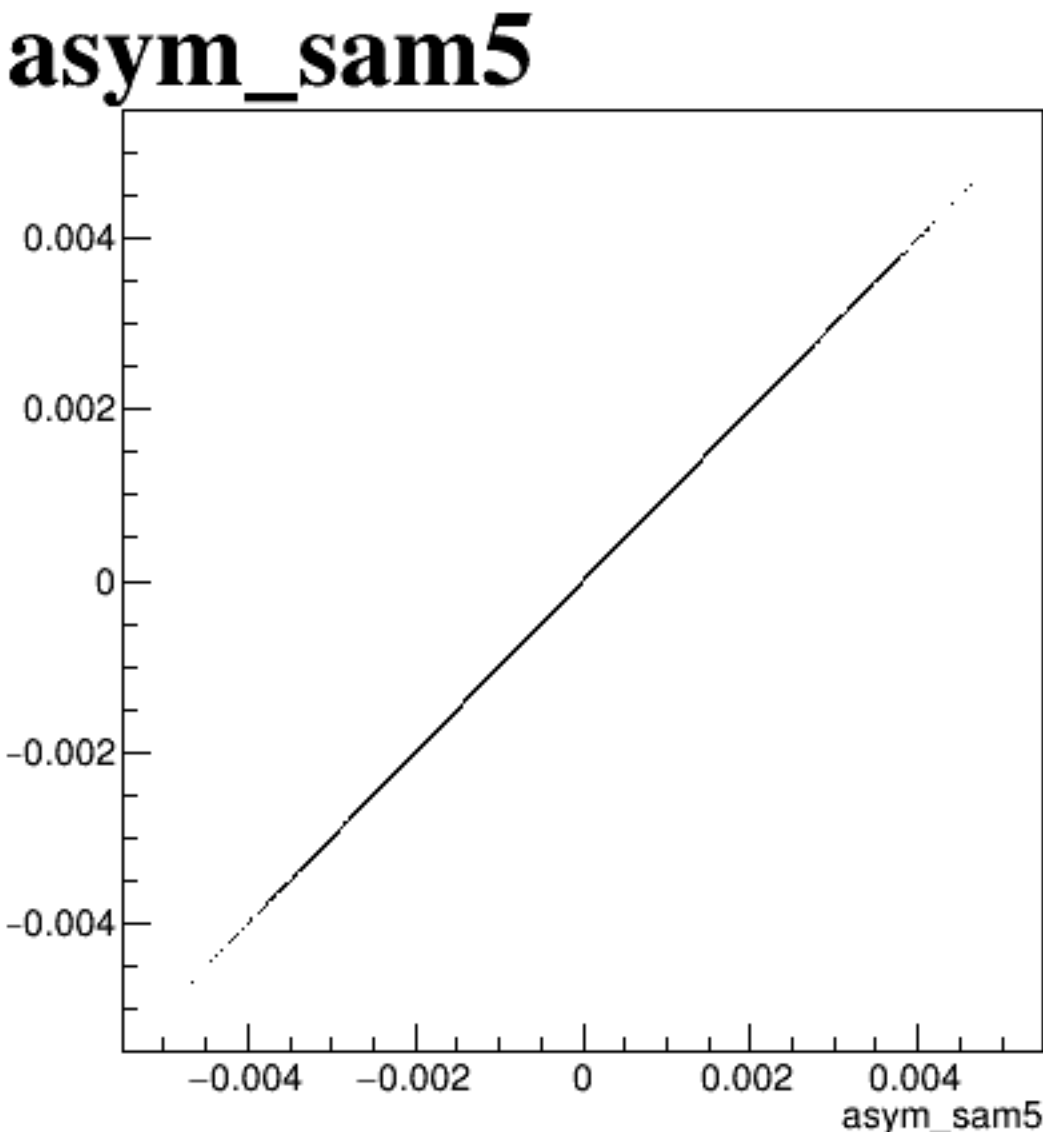
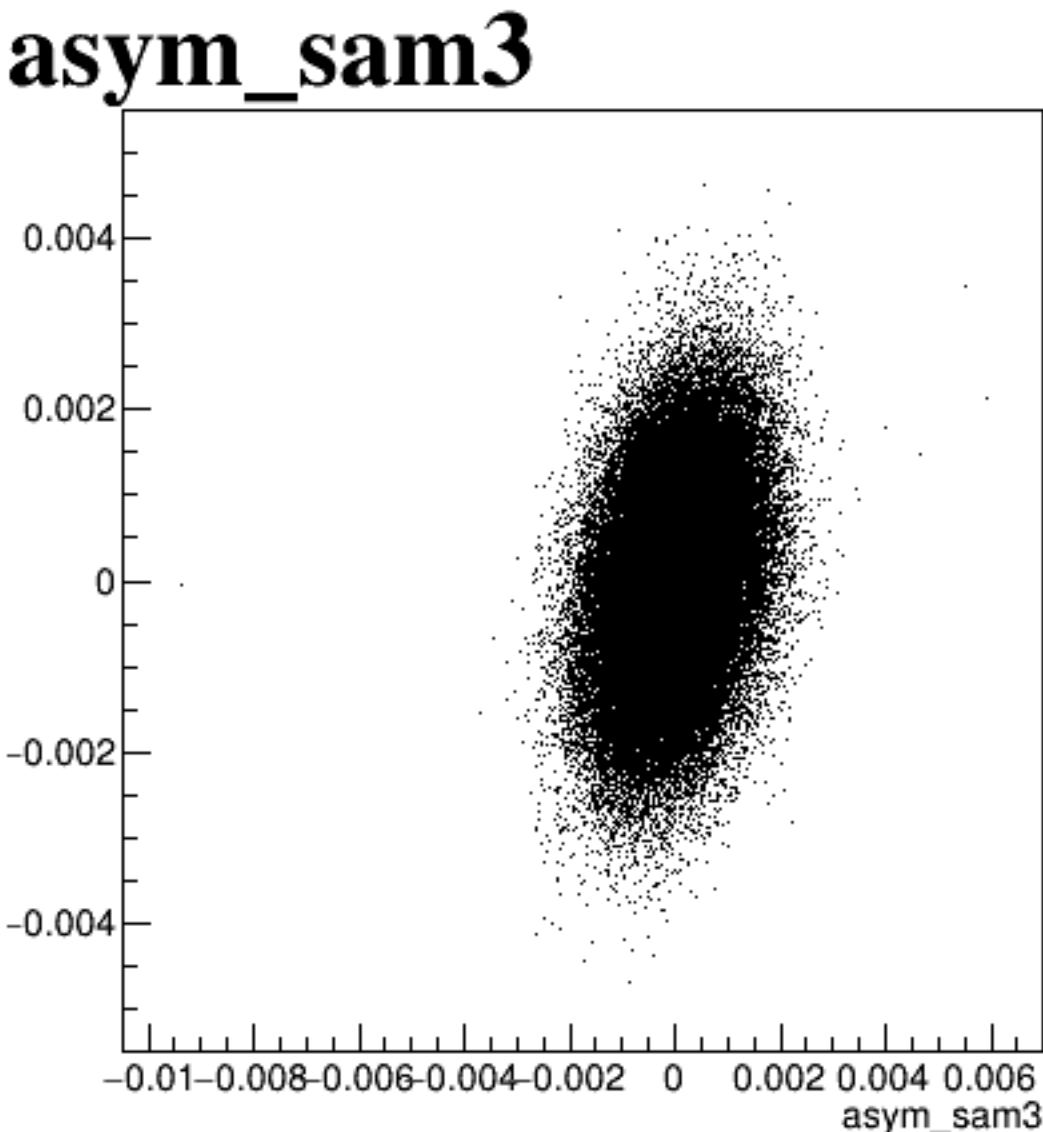
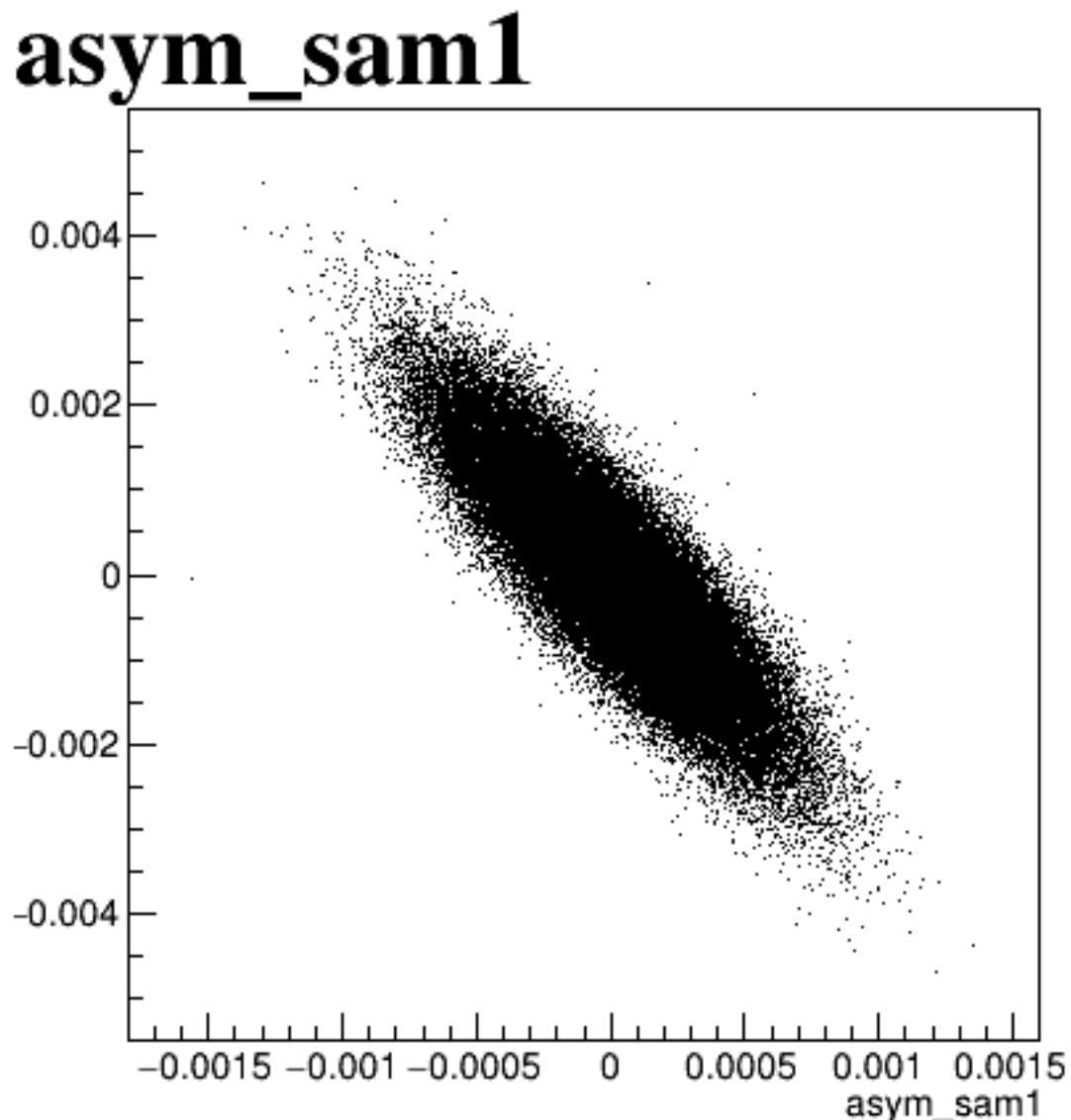
asym\_sam1



asym\_sam3



asym\_sam5



asym\_sam7

