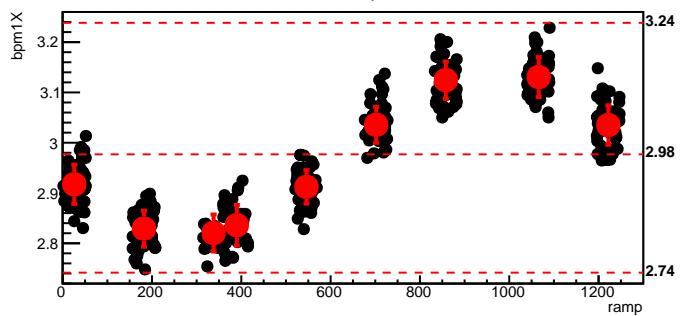
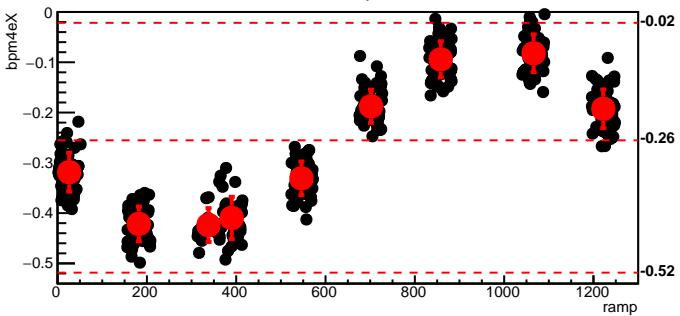


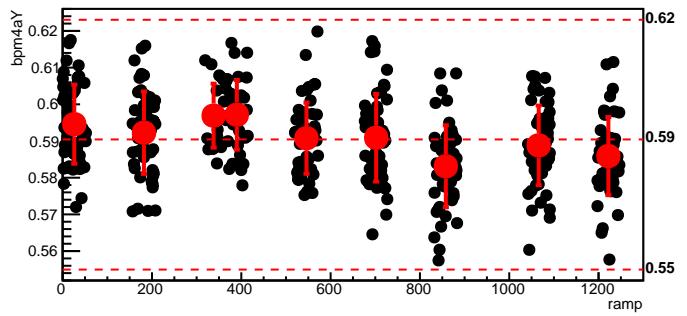
bmod Trim1,cyc=9048



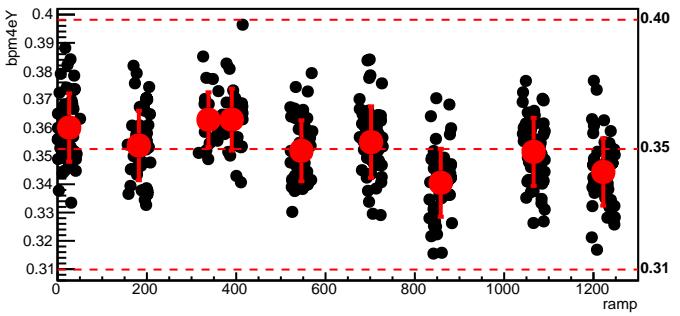
bmod Trim1,cyc=9048



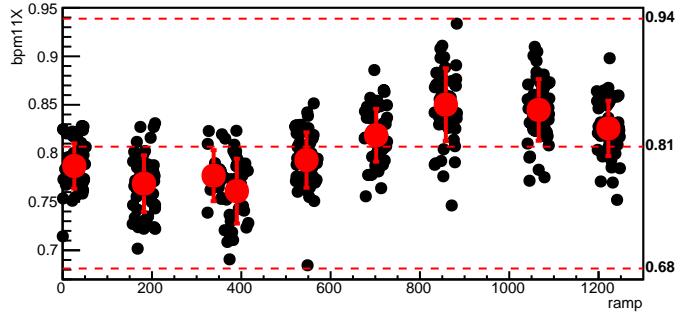
bmod Trim1,cyc=9048



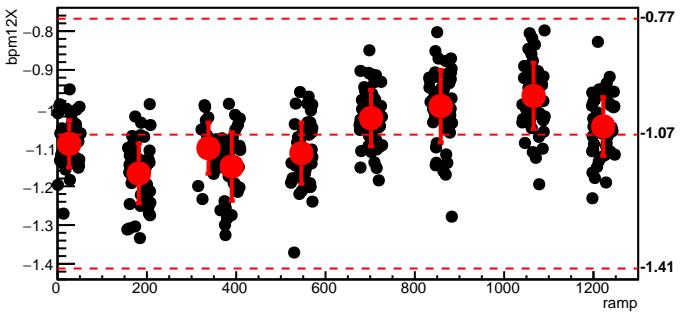
bmod Trim1,cyc=9048



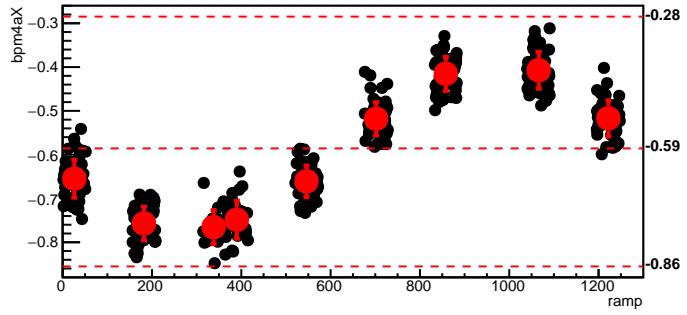
bmod Trim1,cyc=9048



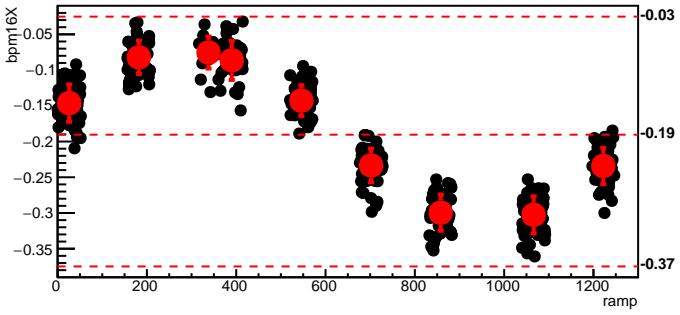
bmod Trim1,cyc=9048



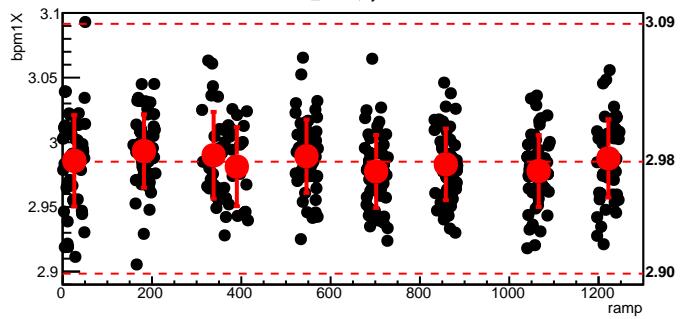
bmod Trim1,cyc=9048



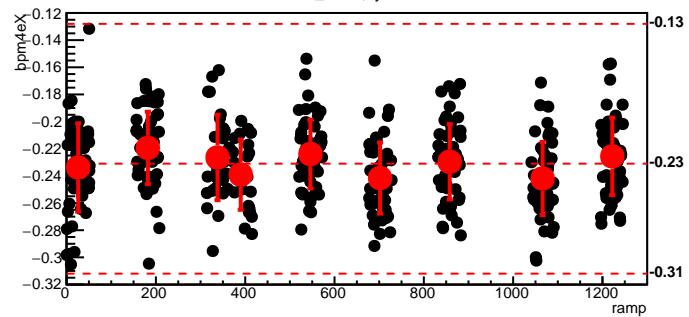
bmod Trim1,cyc=9048



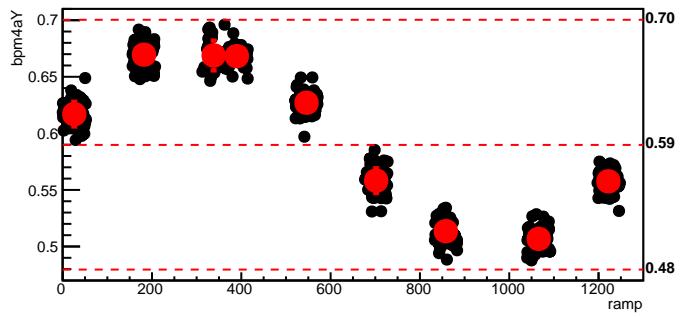
bmod Trim2,cyc=9048



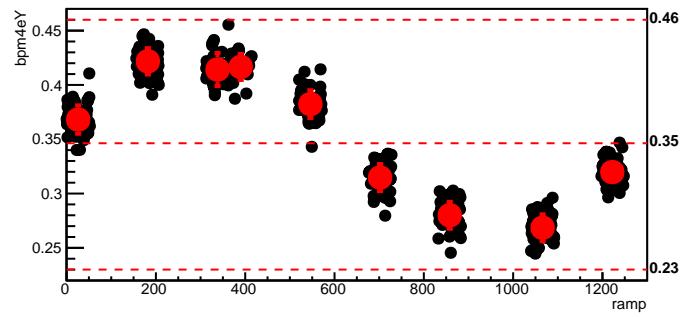
bmod Trim2,cyc=9048



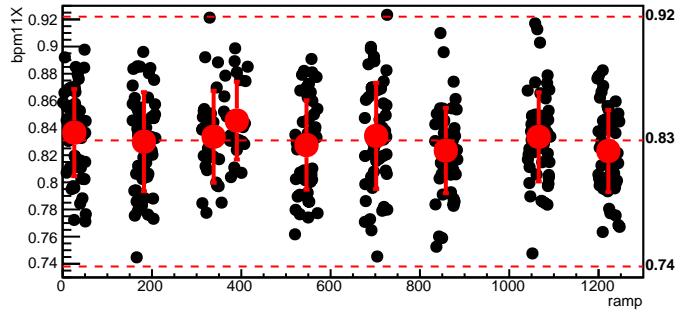
bmod Trim2,cyc=9048



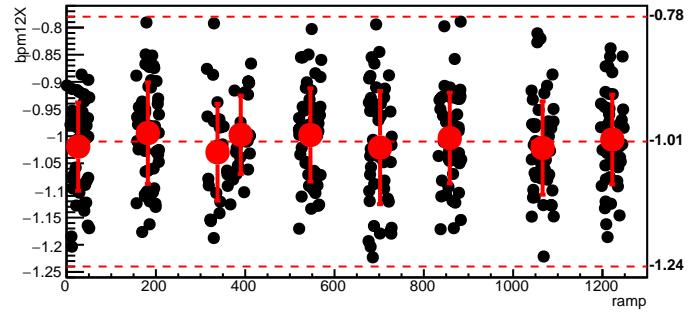
bmod Trim2,cyc=9048



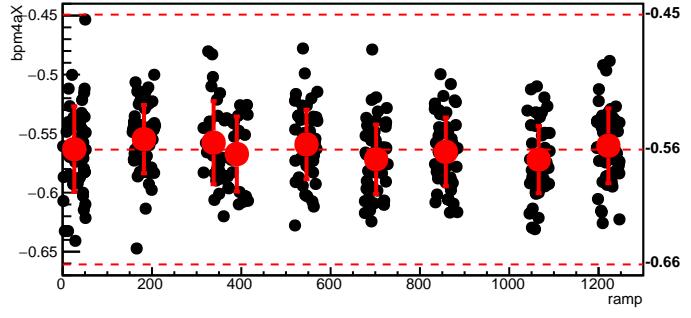
bmod Trim2,cyc=9048



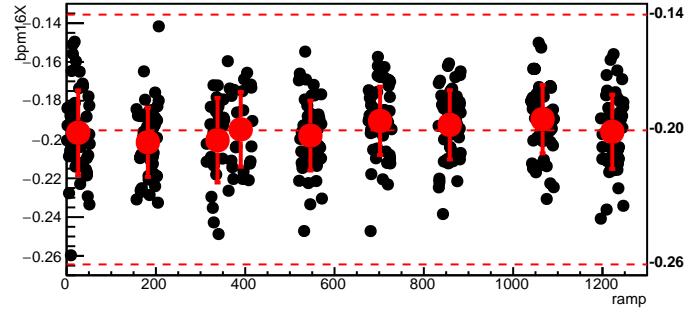
bmod Trim2,cyc=9048



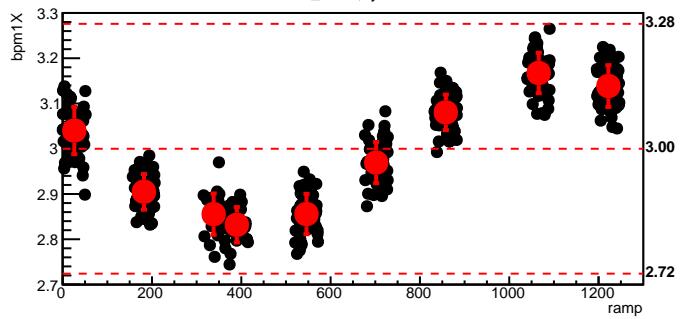
bmod Trim2,cyc=9048



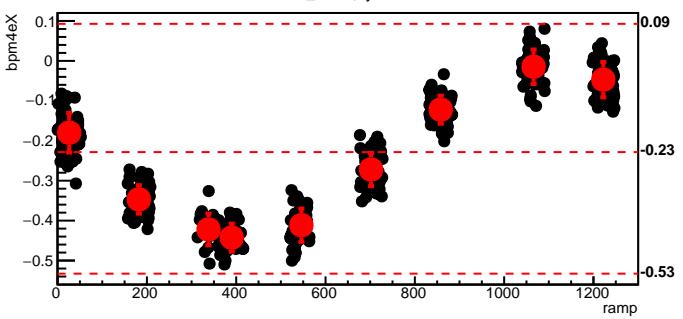
bmod Trim2,cyc=9048



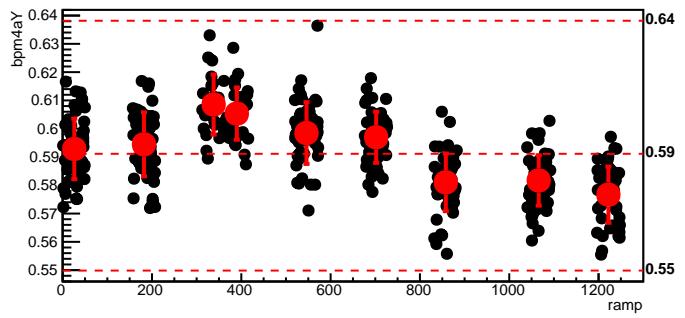
bmod Trim3,cyc=9048



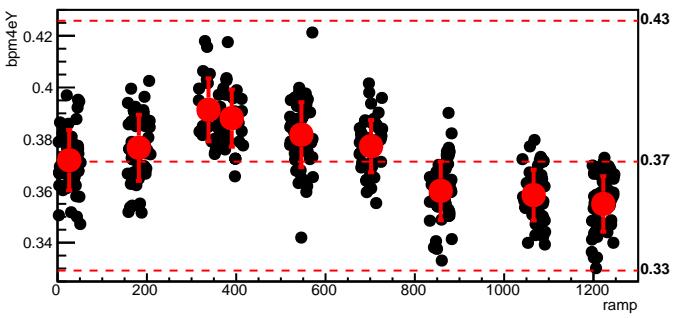
bmod Trim3,cyc=9048



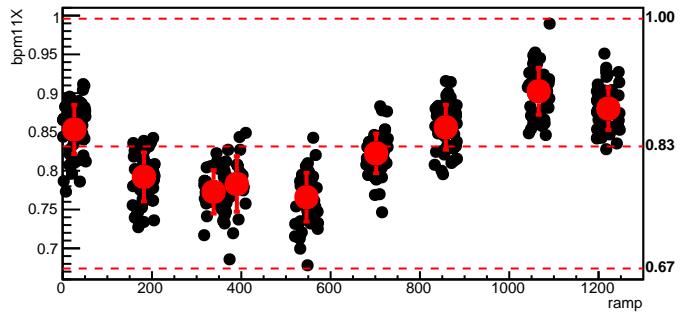
bmod Trim3,cyc=9048



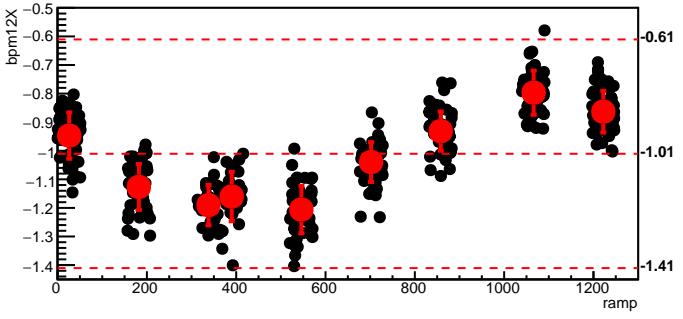
bmod Trim3,cyc=9048



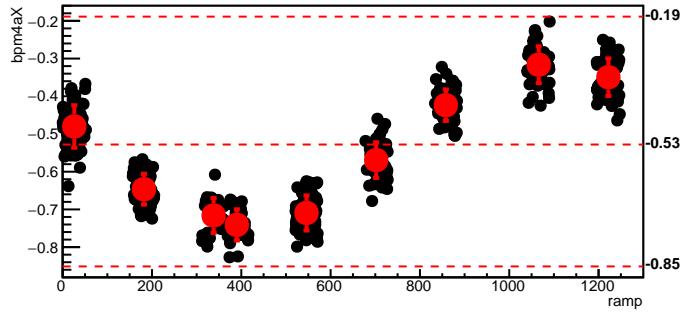
bmod Trim3,cyc=9048



bmod Trim3,cyc=9048



bmod Trim3,cyc=9048



bmod Trim3,cyc=9048

