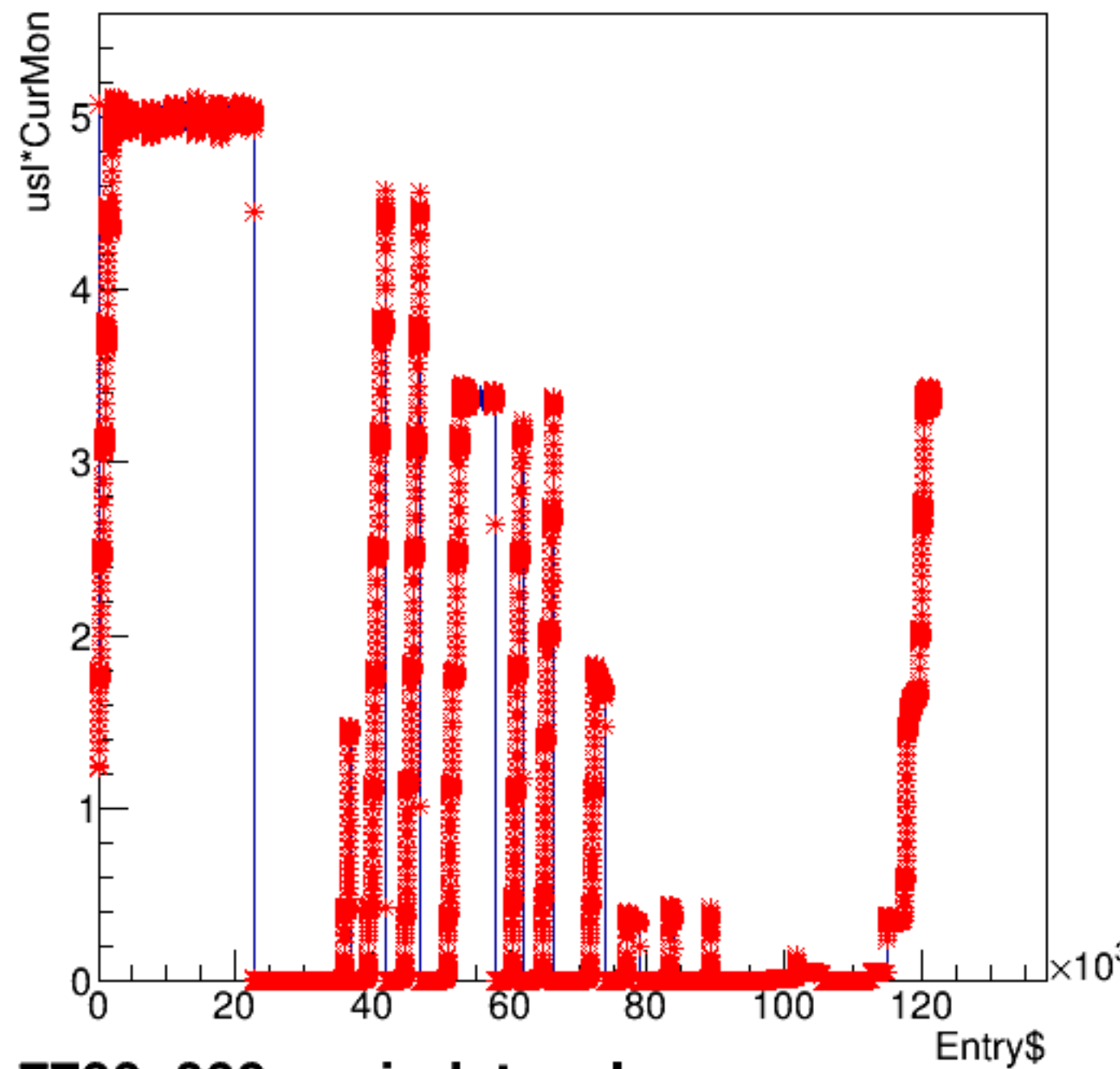
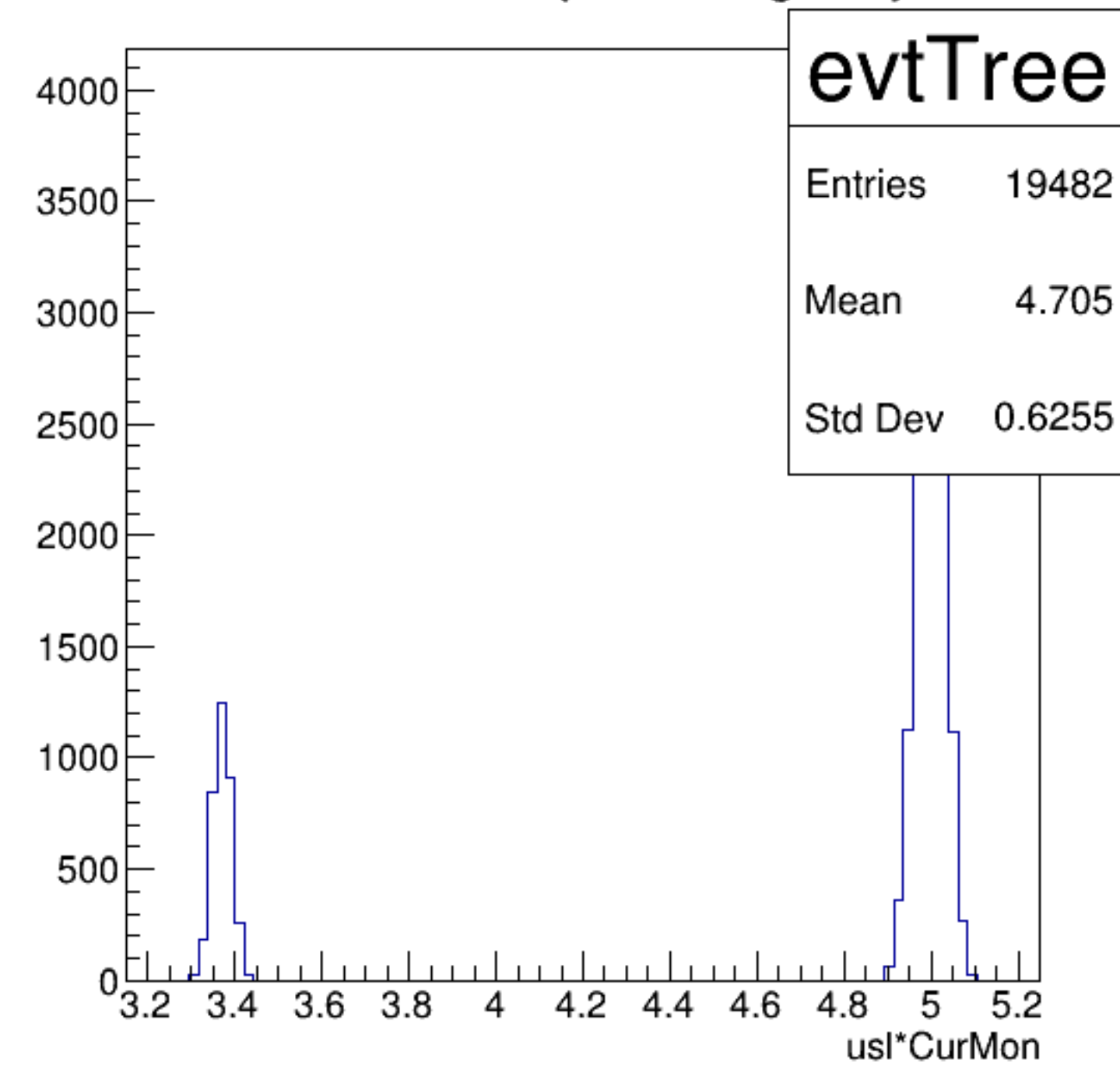


usl*CurMon:Entry\$



run7700_000_maindet_usl.png

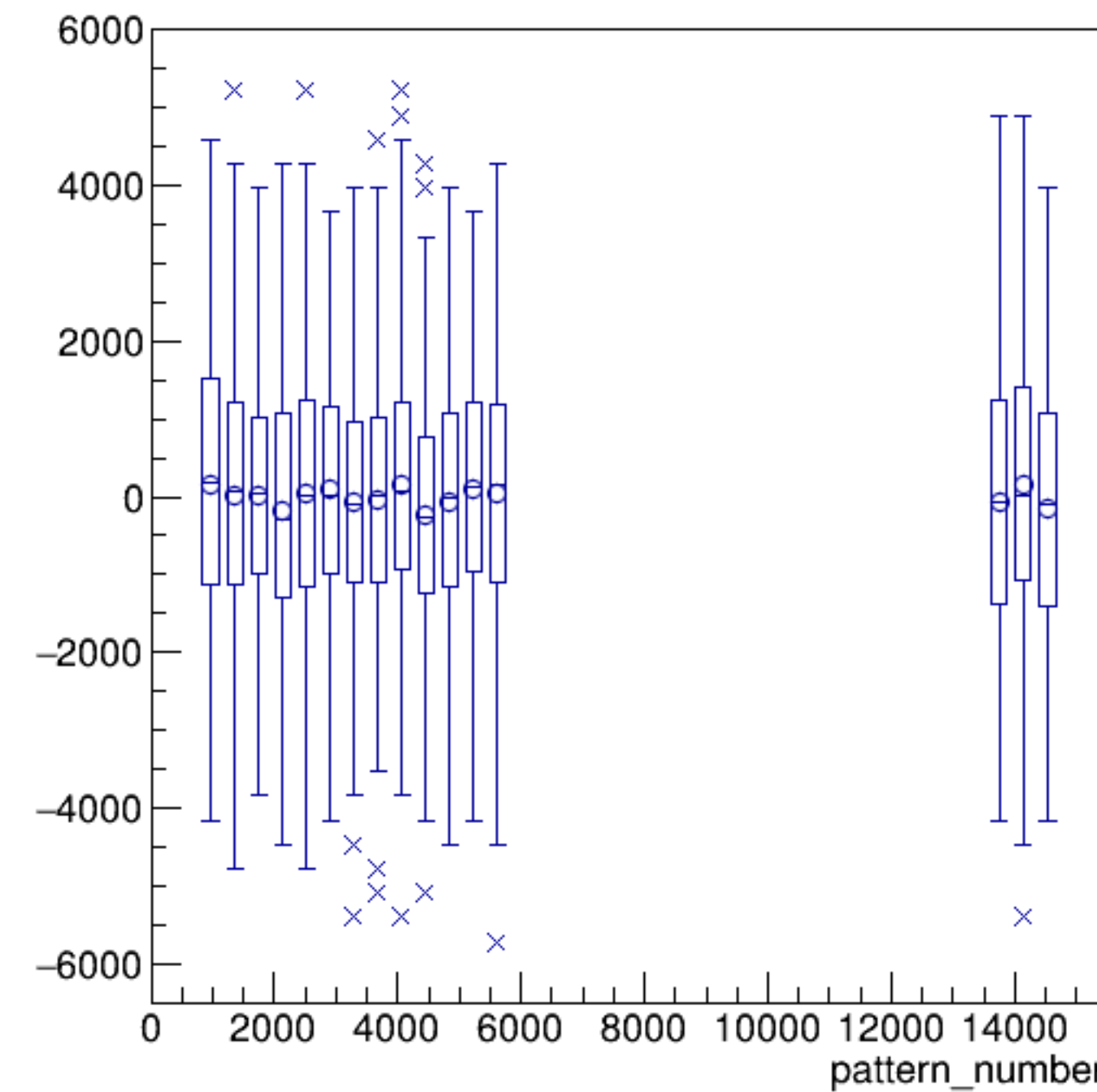
usl*CurMon {ErrorFlag==0}



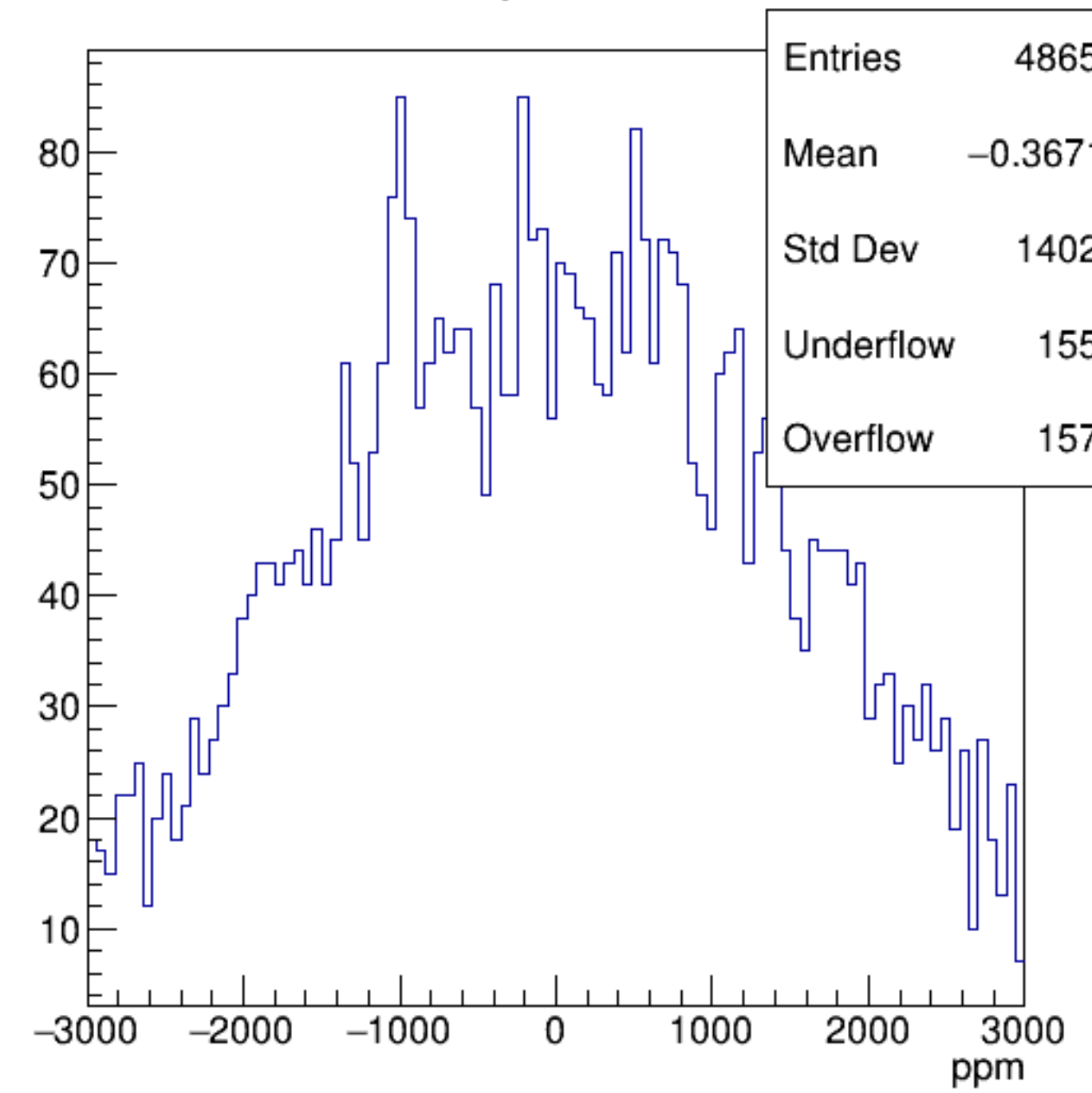
evtTree

Entries 19482
Mean 4.705
Std Dev 0.6255

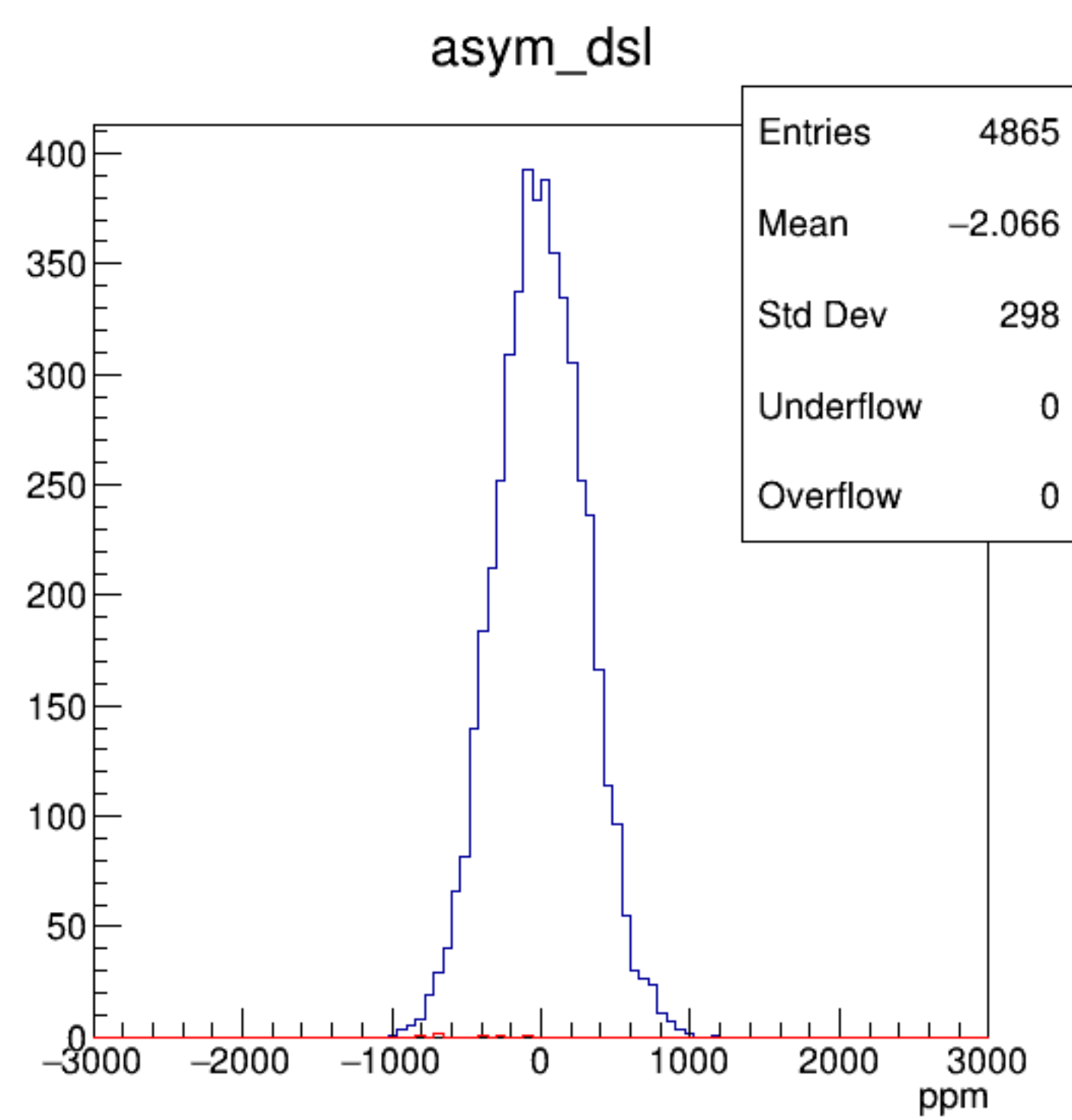
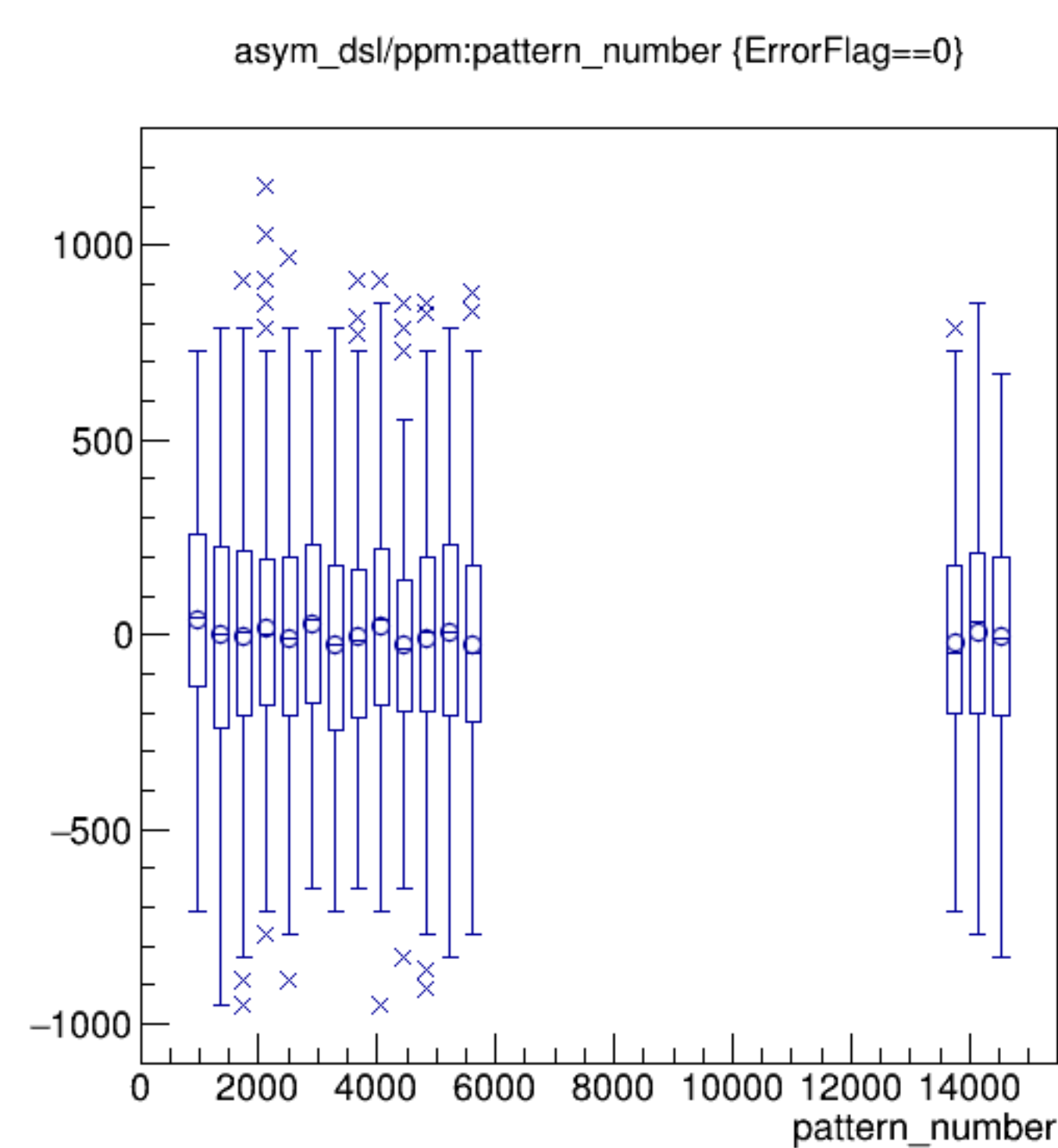
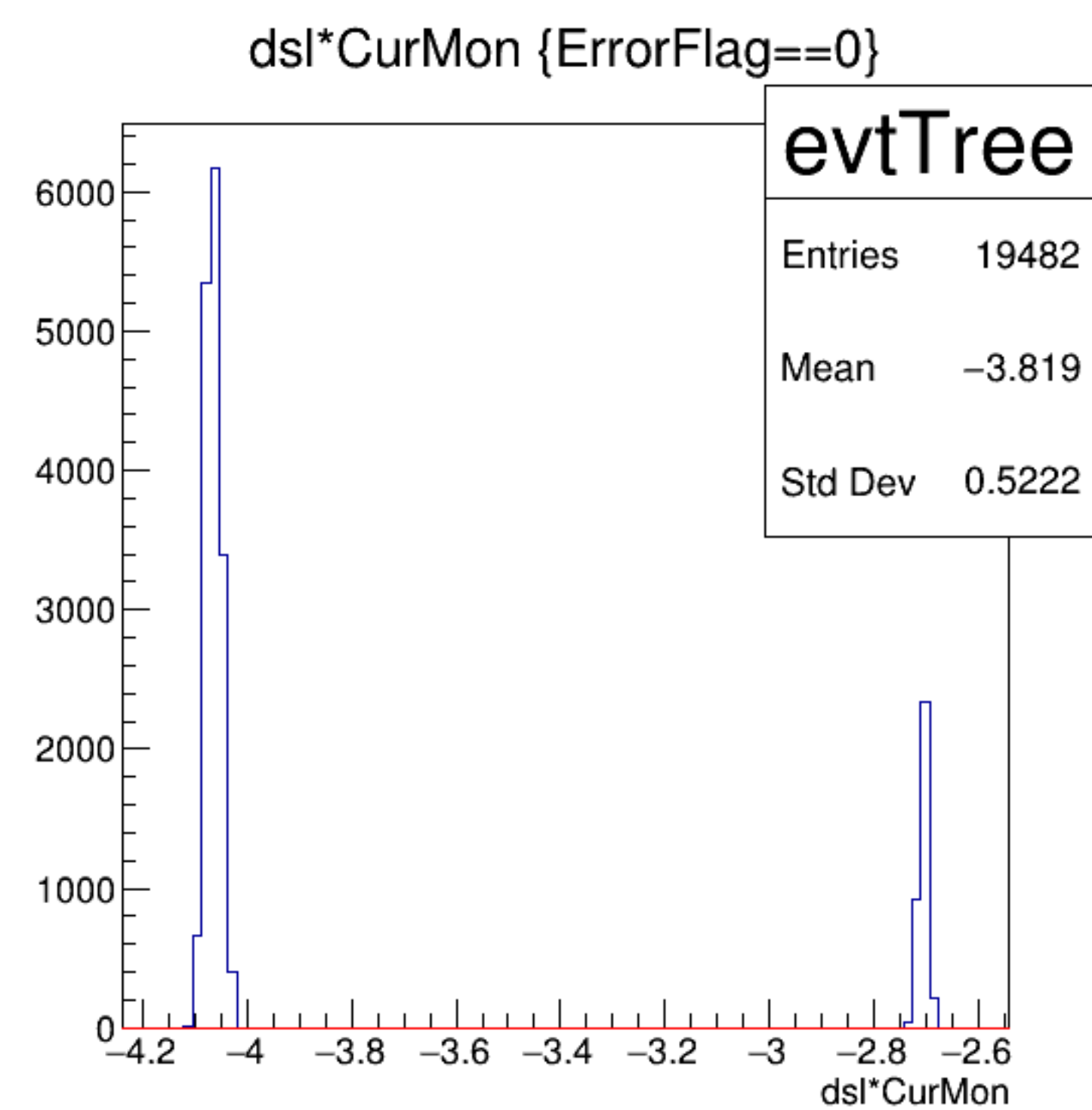
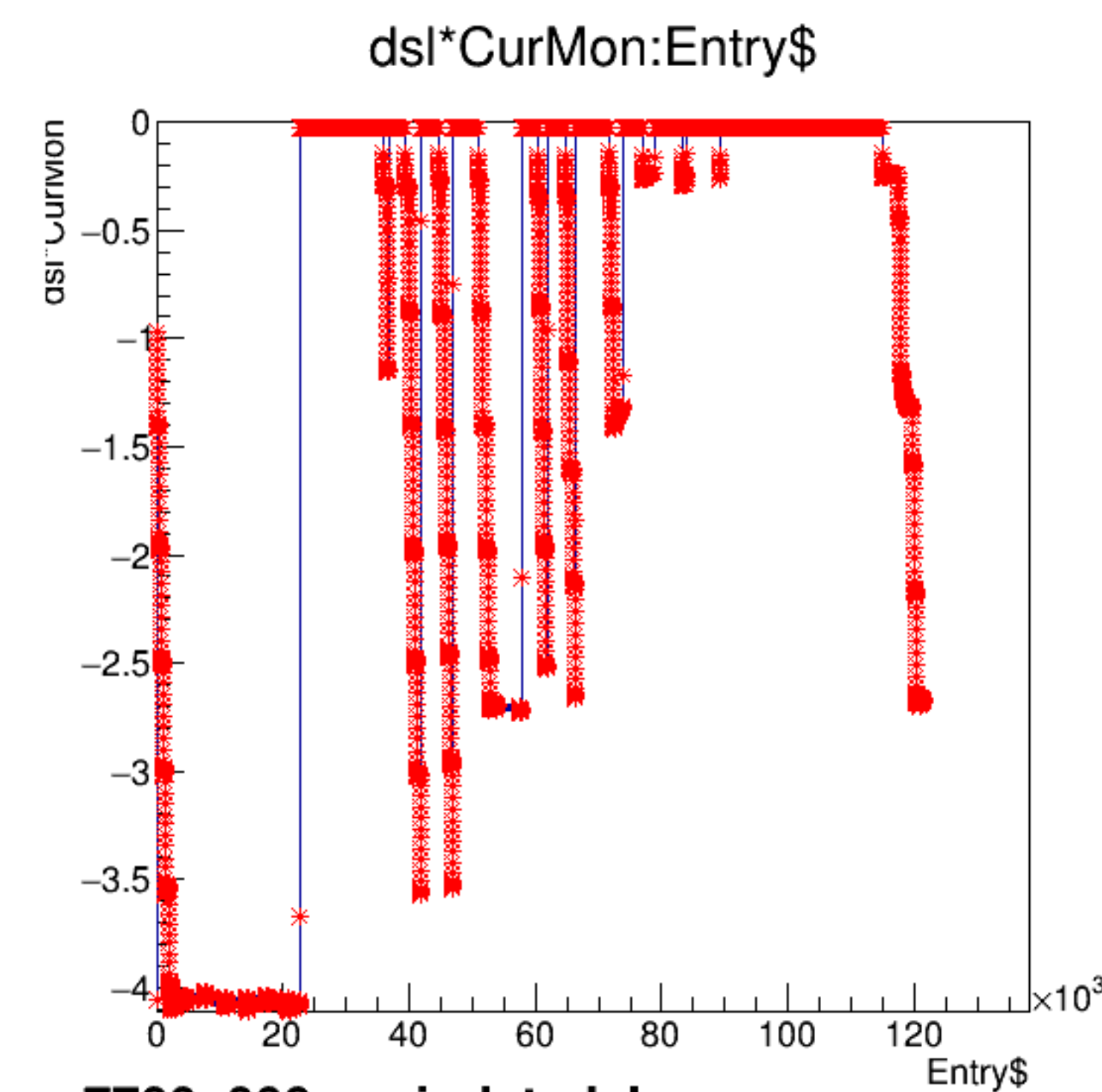
asym_usl/ppm:pattern_number {ErrorFlag==0}



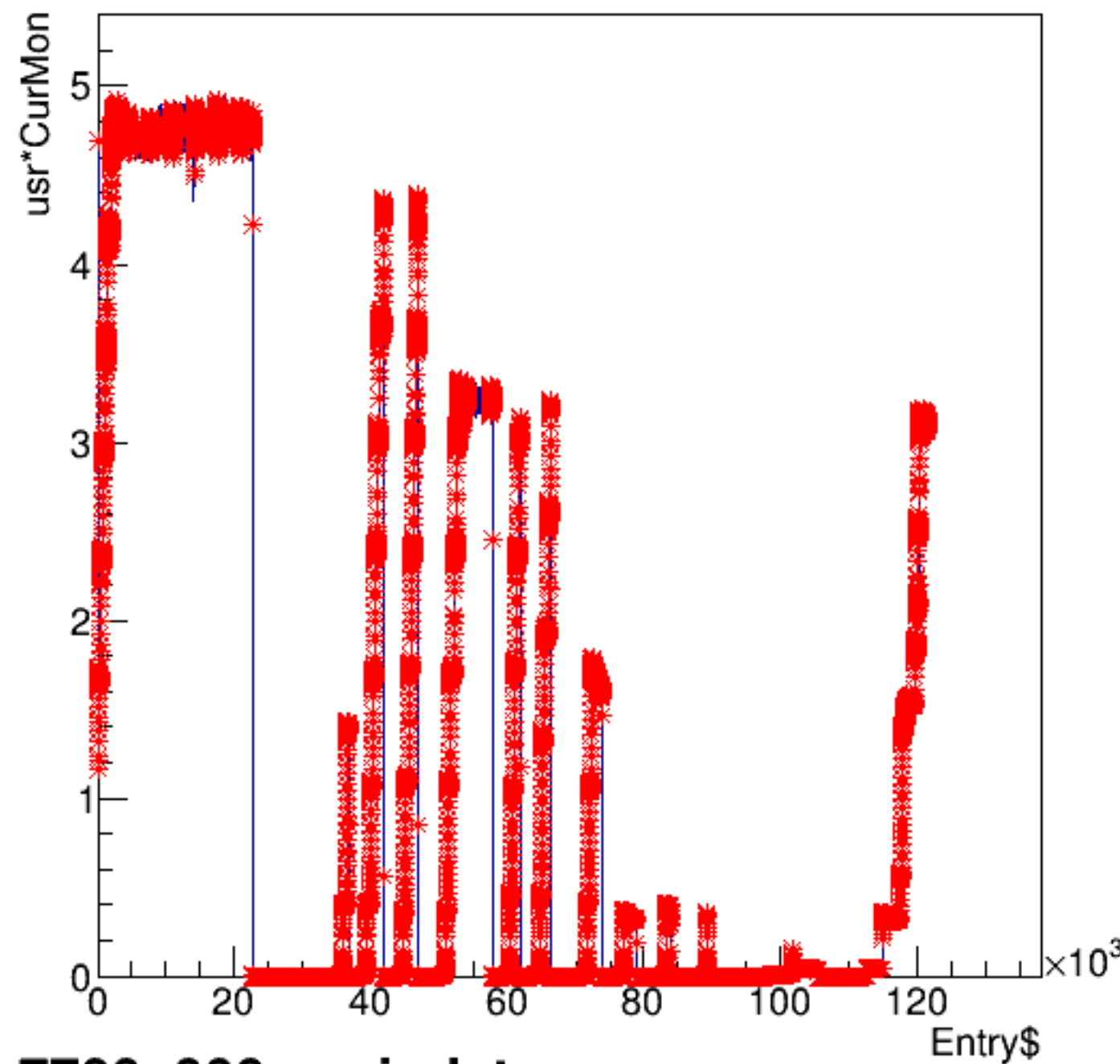
asym_usl



Entries 4865
Mean -0.3671
Std Dev 1402
Underflow 155
Overflow 157

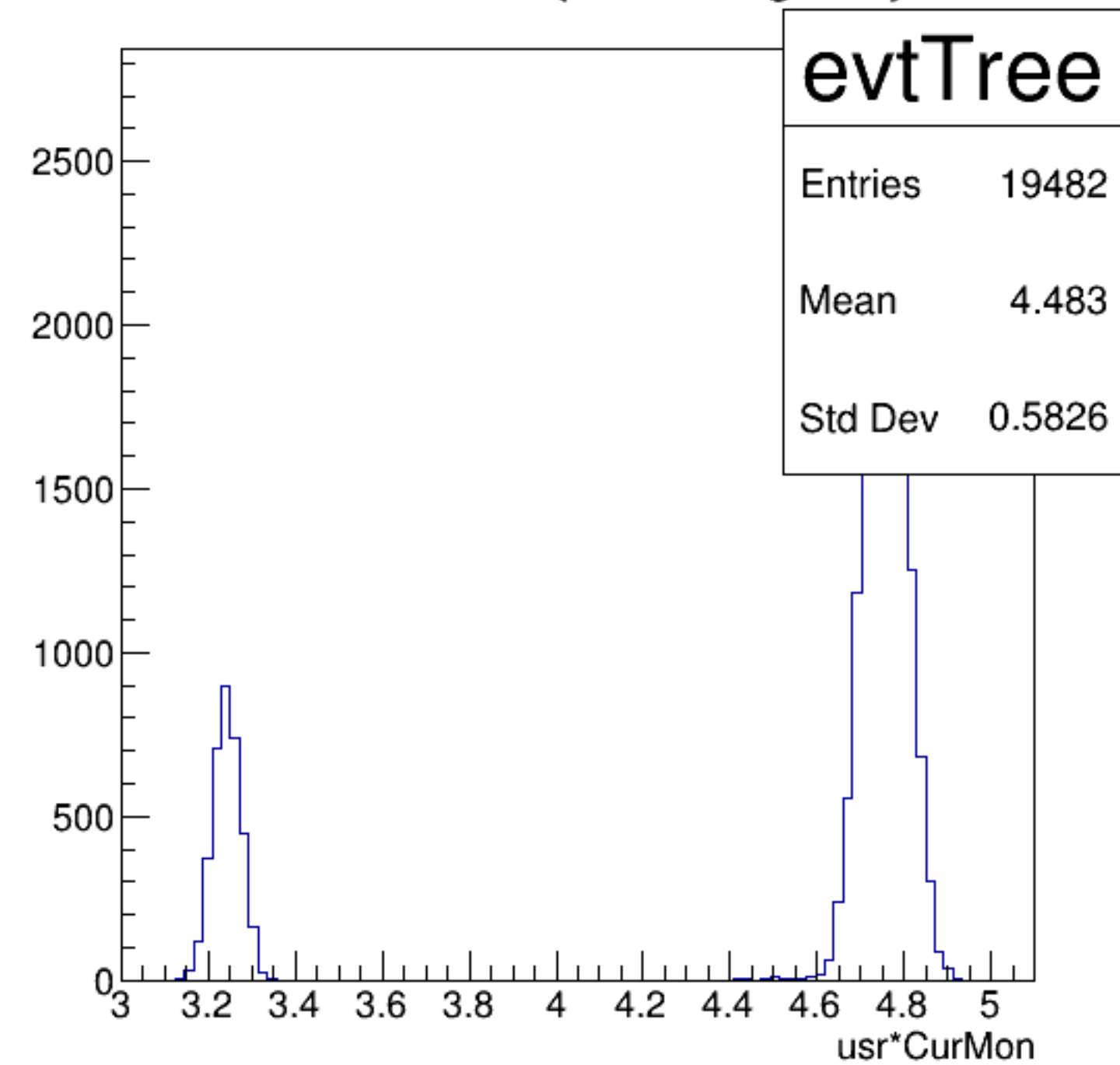


usr*CurMon:Entry\$

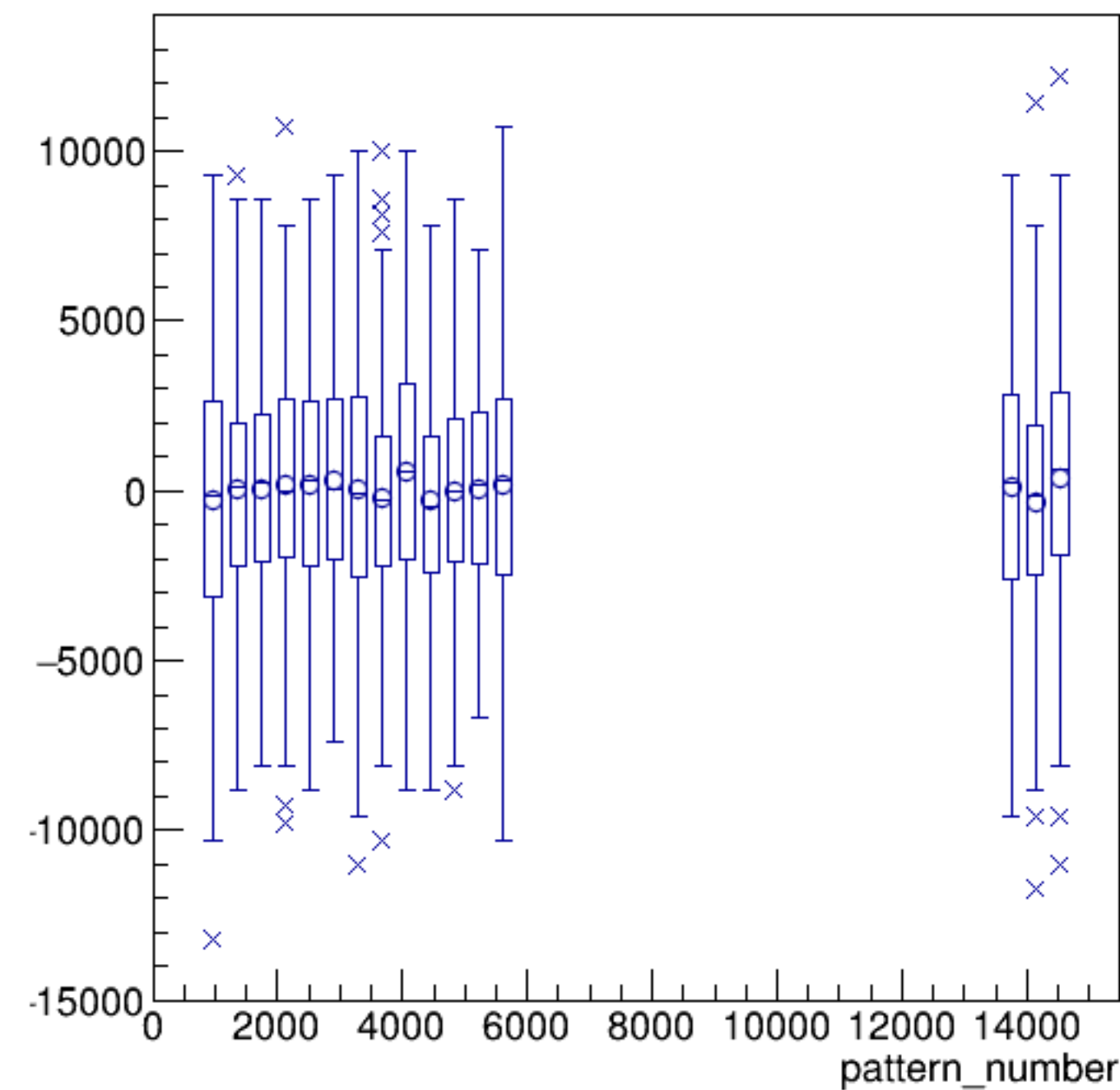


run7700_000_maindet_usr.png

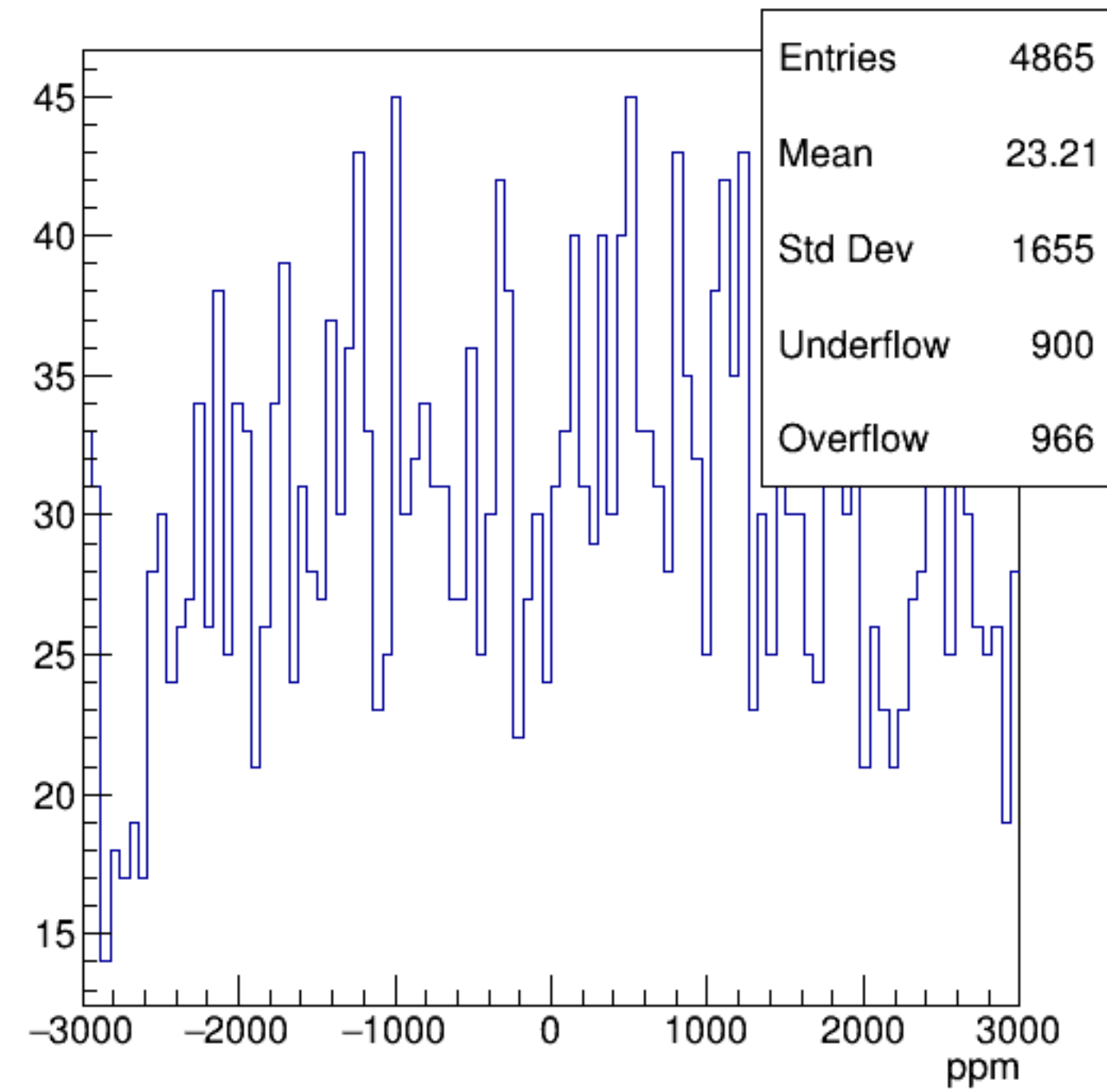
usr*CurMon {ErrorFlag==0}

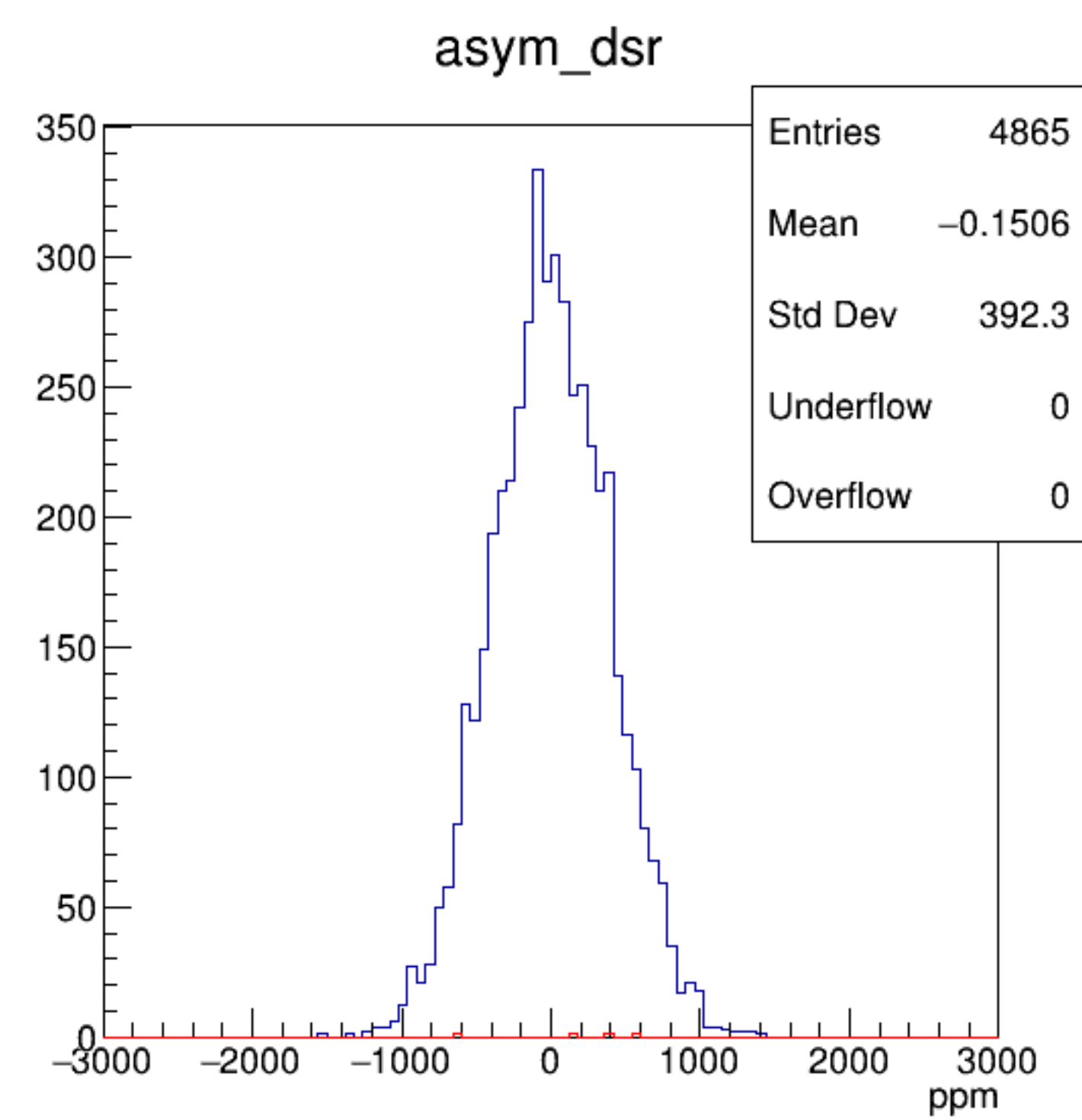
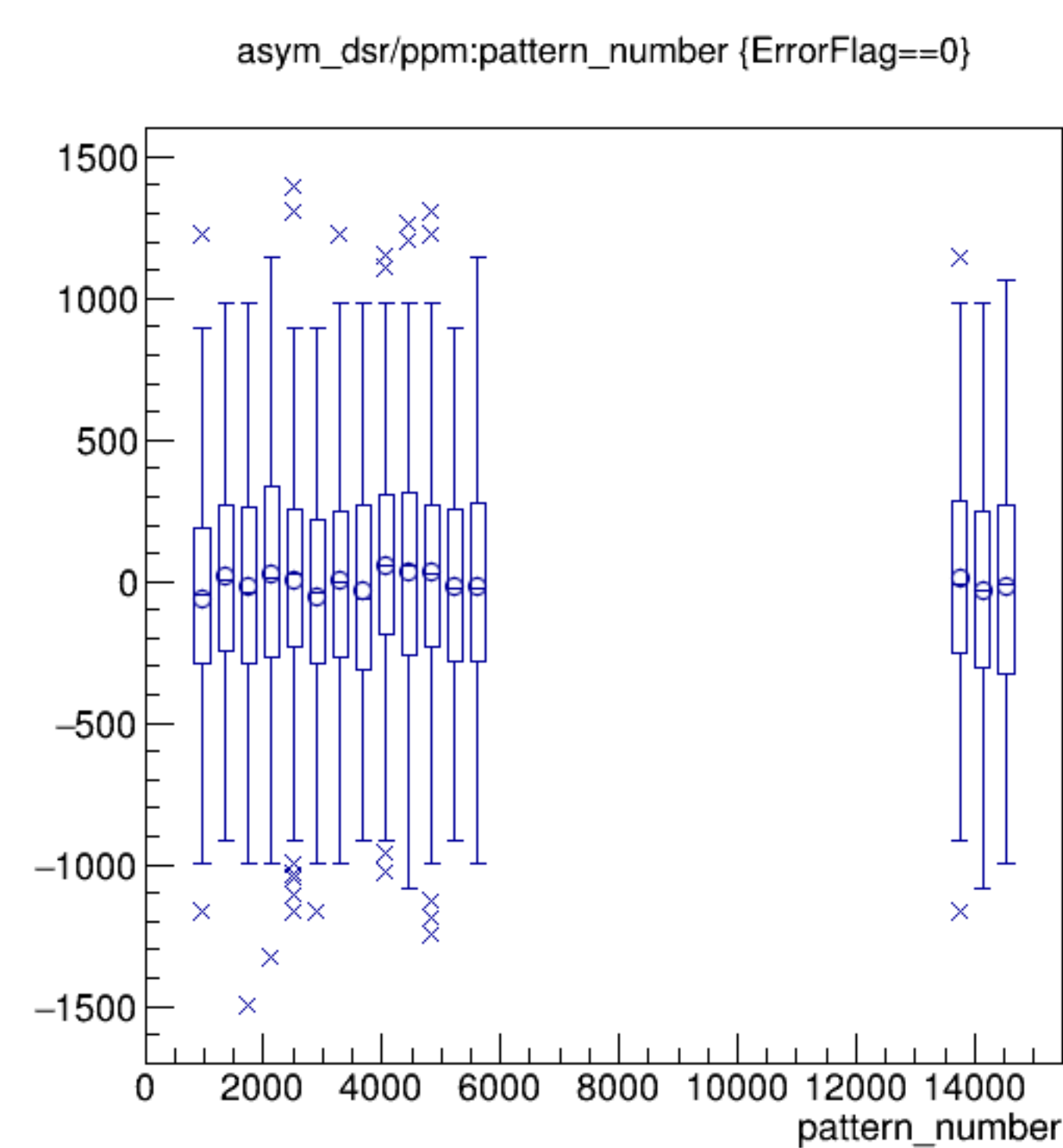
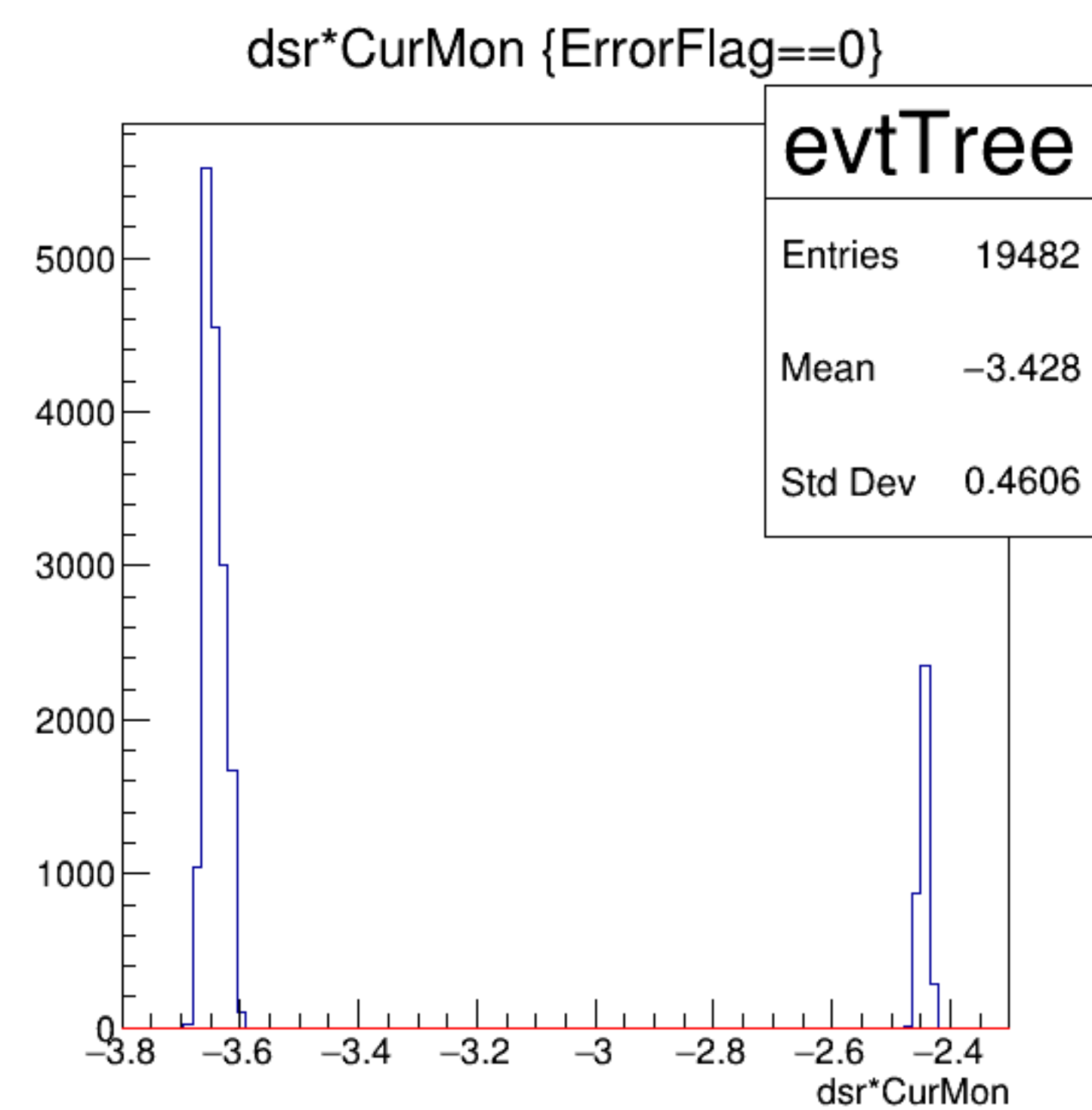
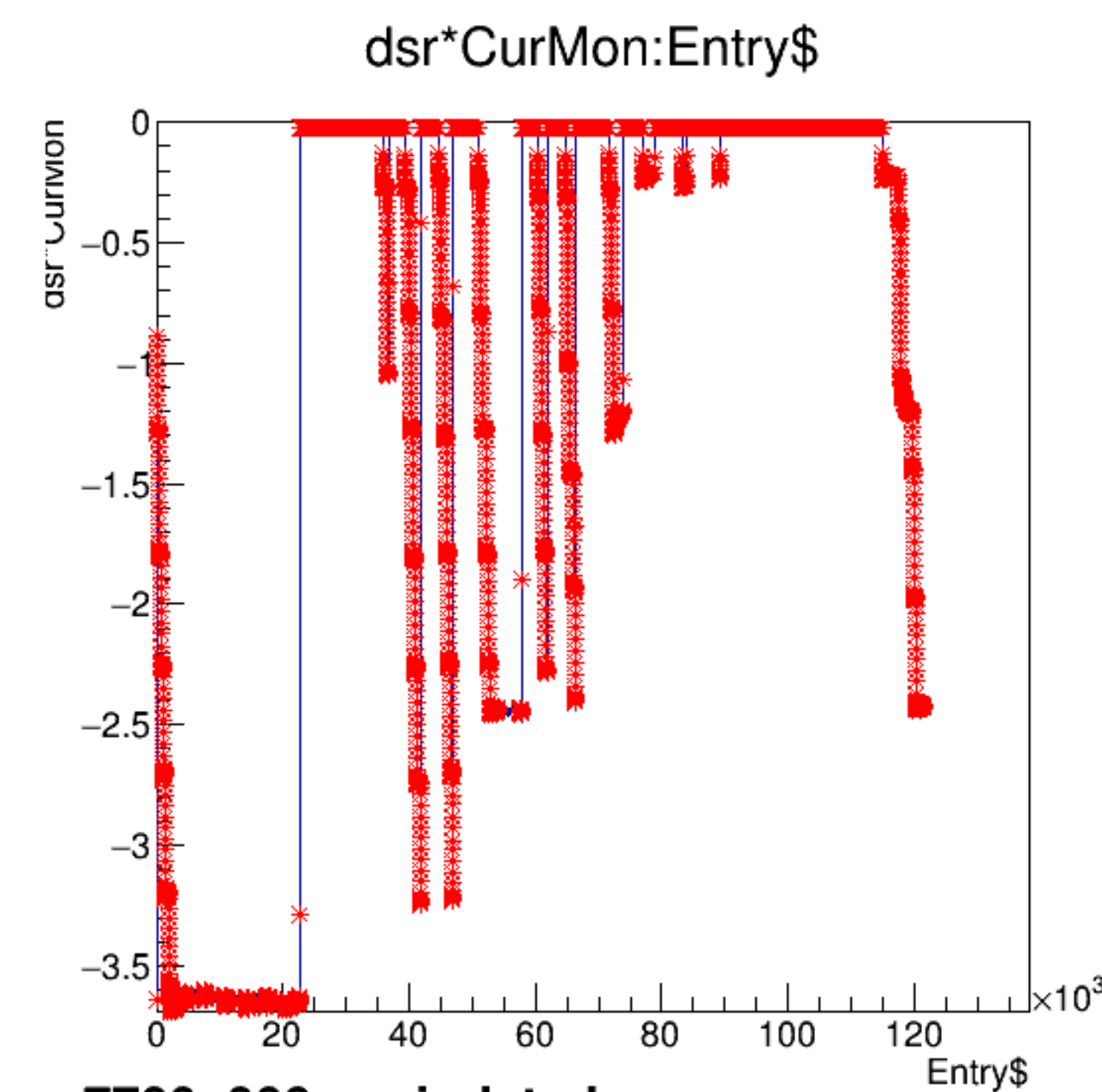


asym_usr/ppm:pattern_number {ErrorFlag==0}

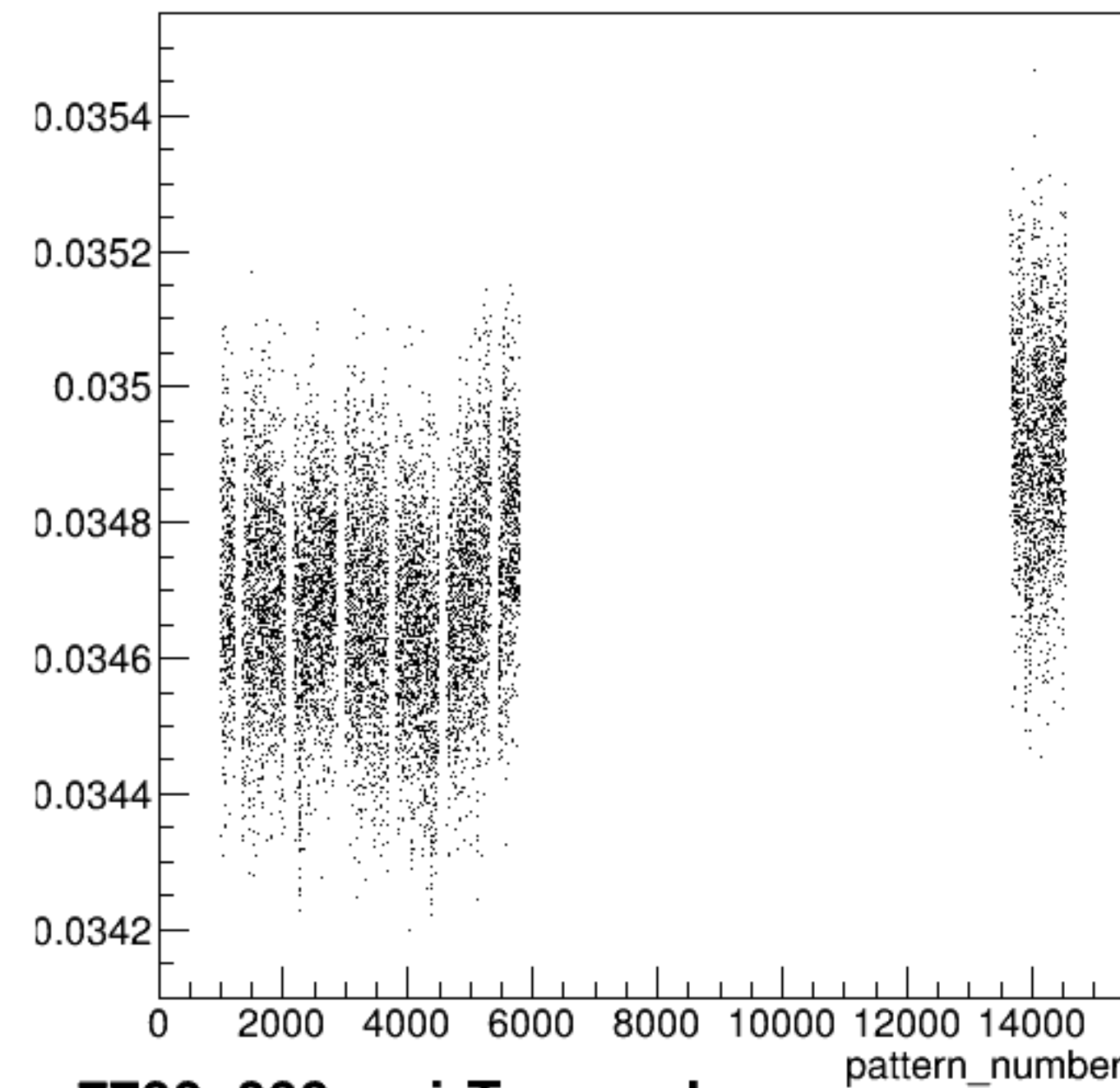


asym_usr



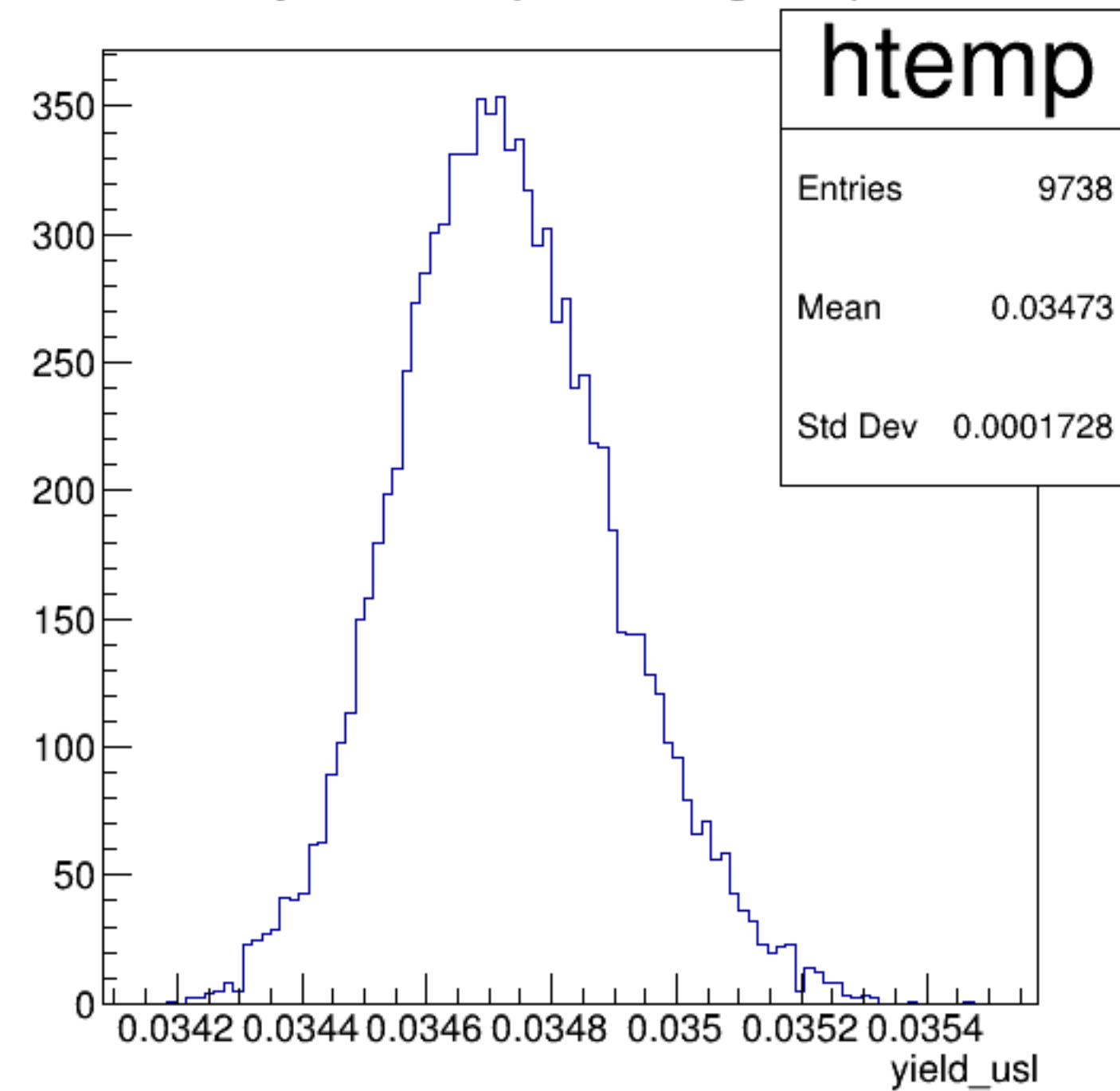


yield_usl:pattern_number {ErrorFlag==0}

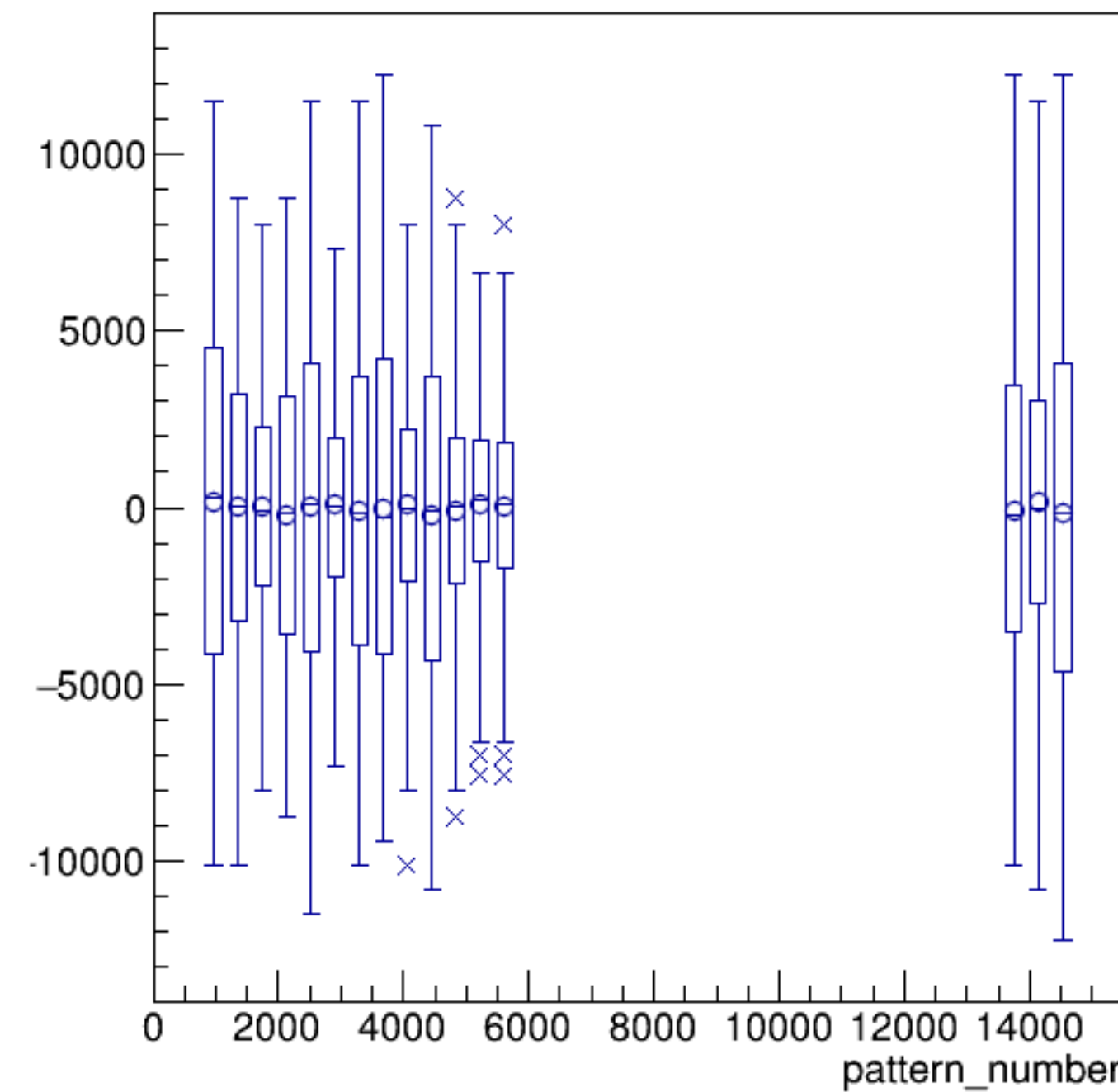


run7700_000_pairTree_usl.png

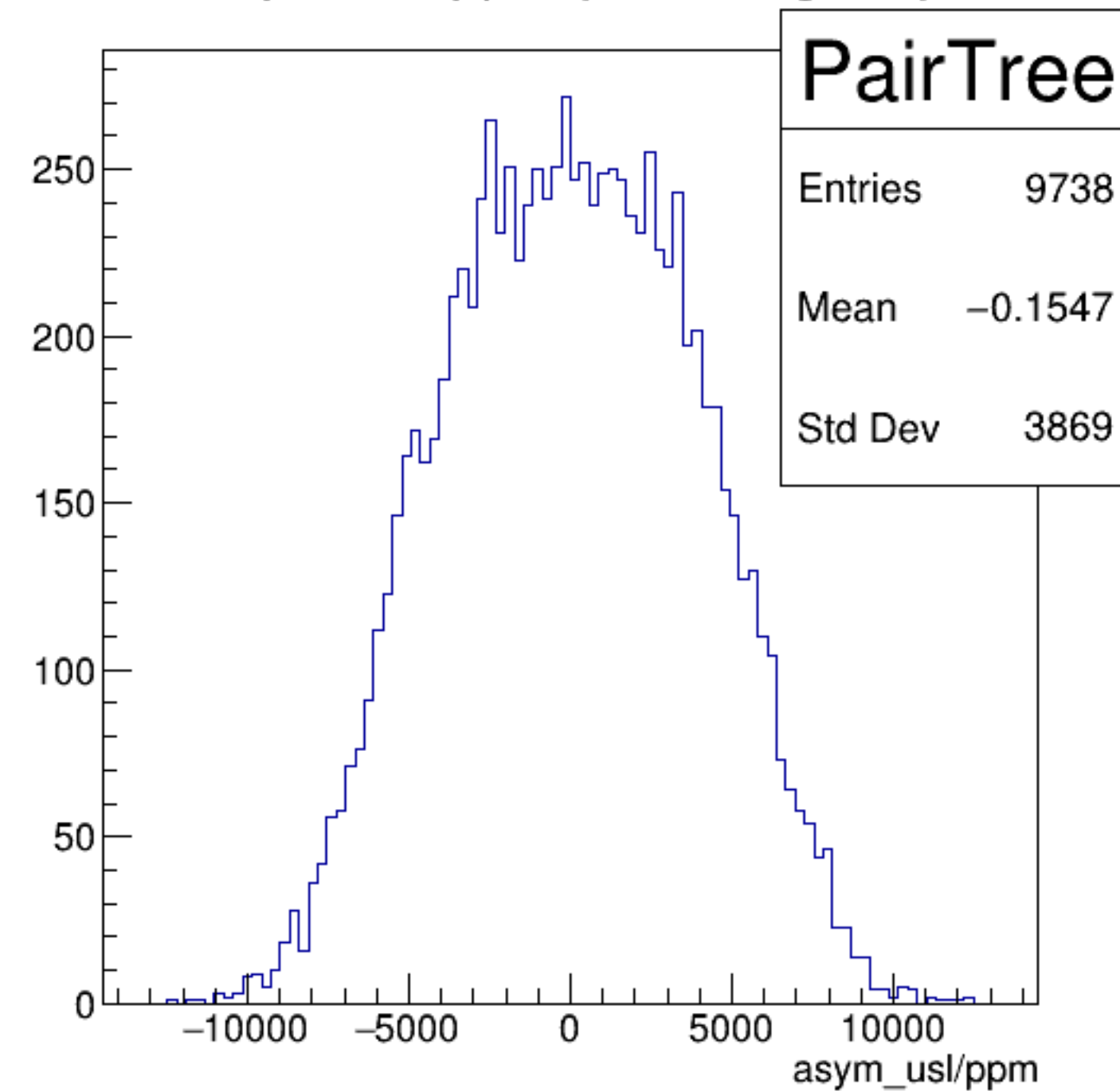
yield_usl {ErrorFlag==0}



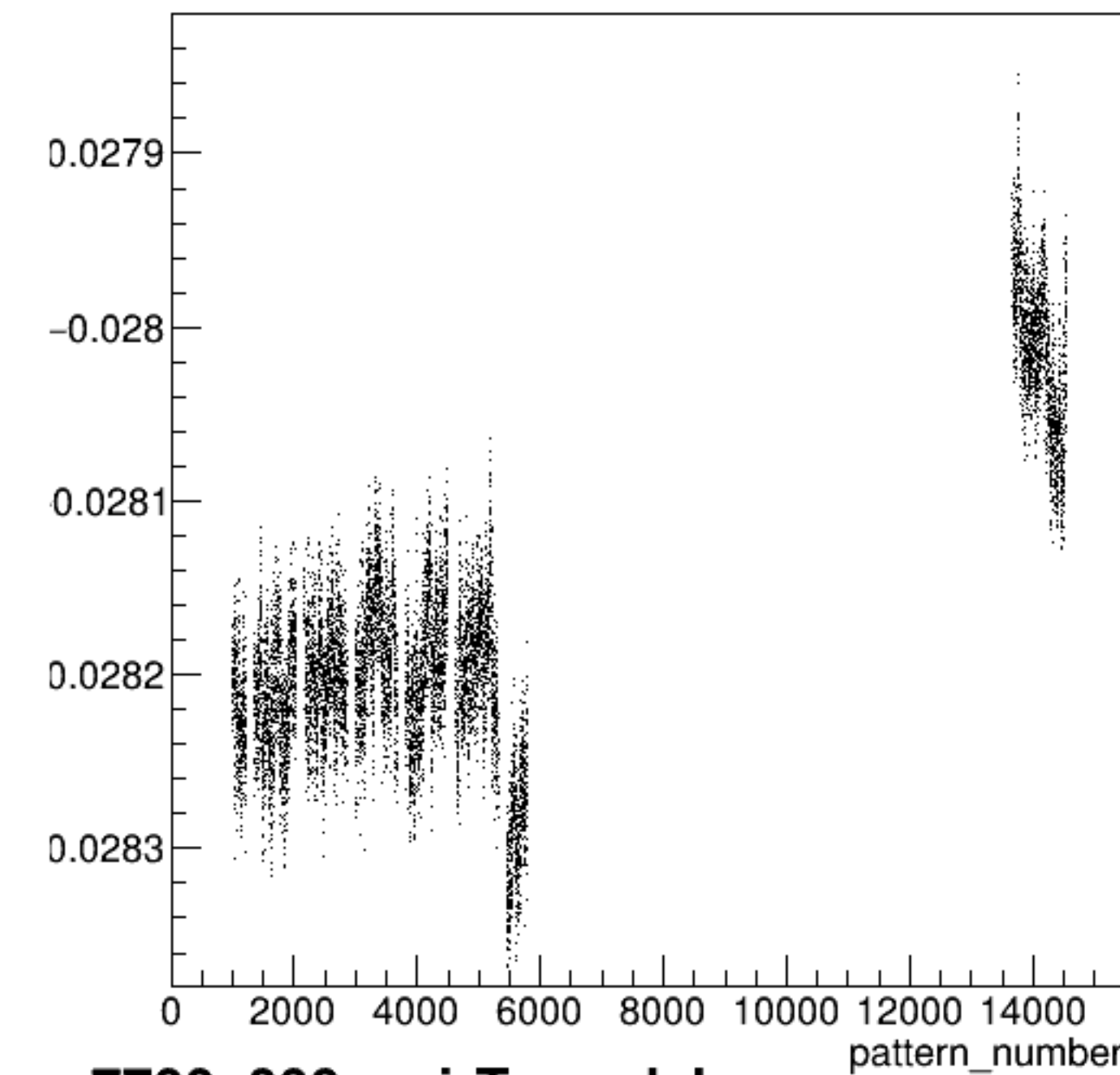
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl/ppm {ErrorFlag==0}

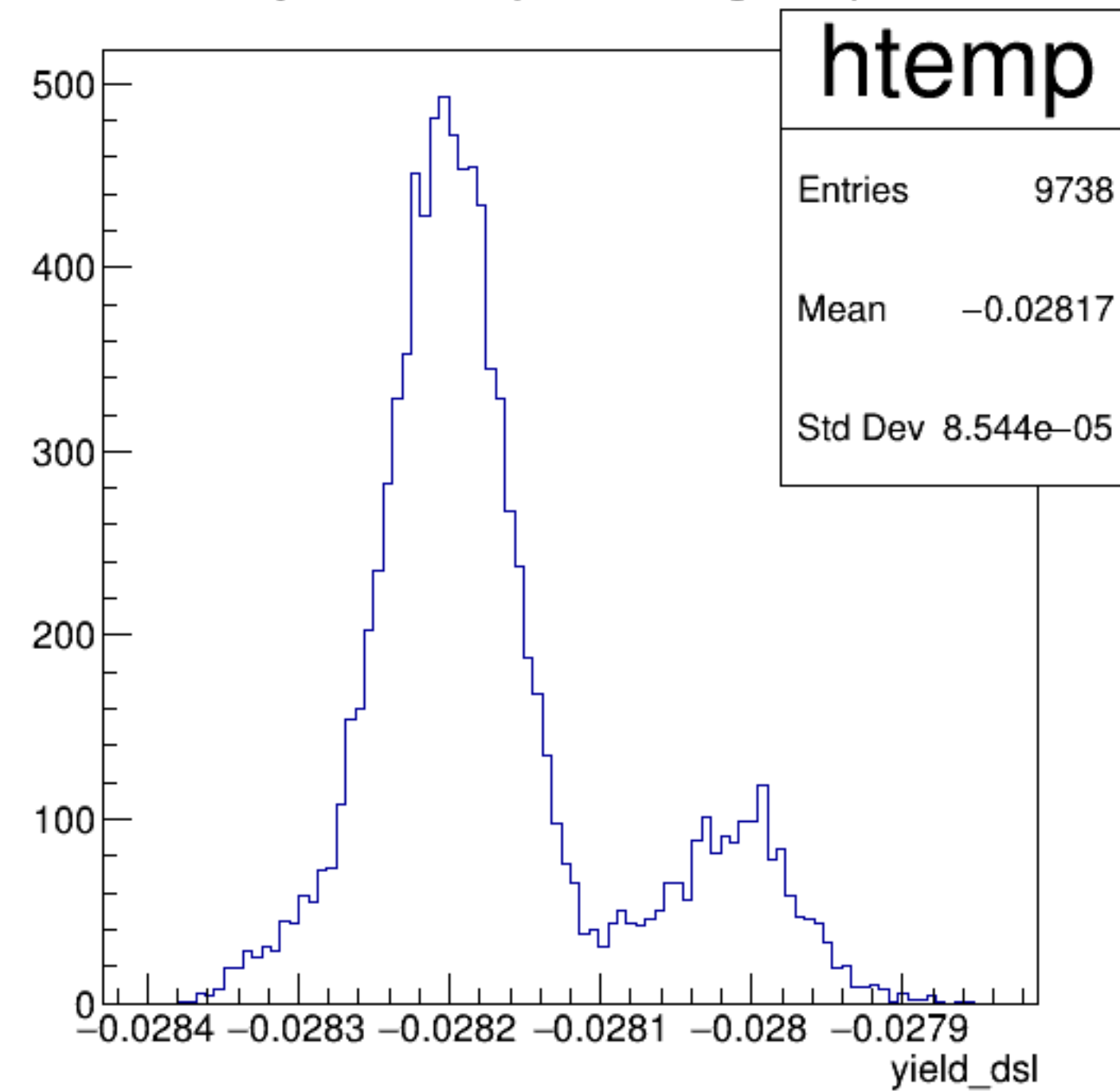


yield_dsl:pattern_number {ErrorFlag==0}

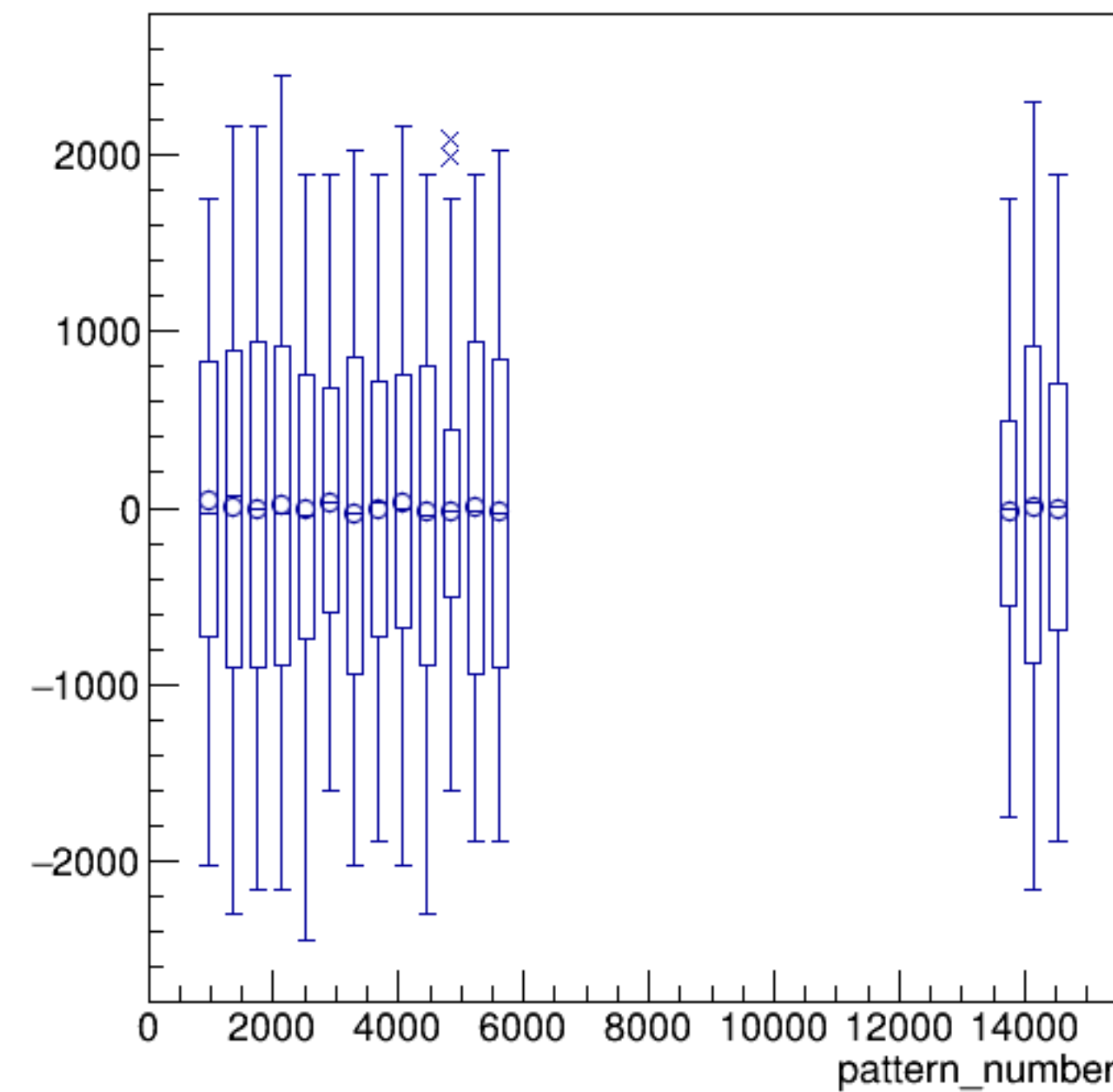


run7700_000_pairTree_dsl.png

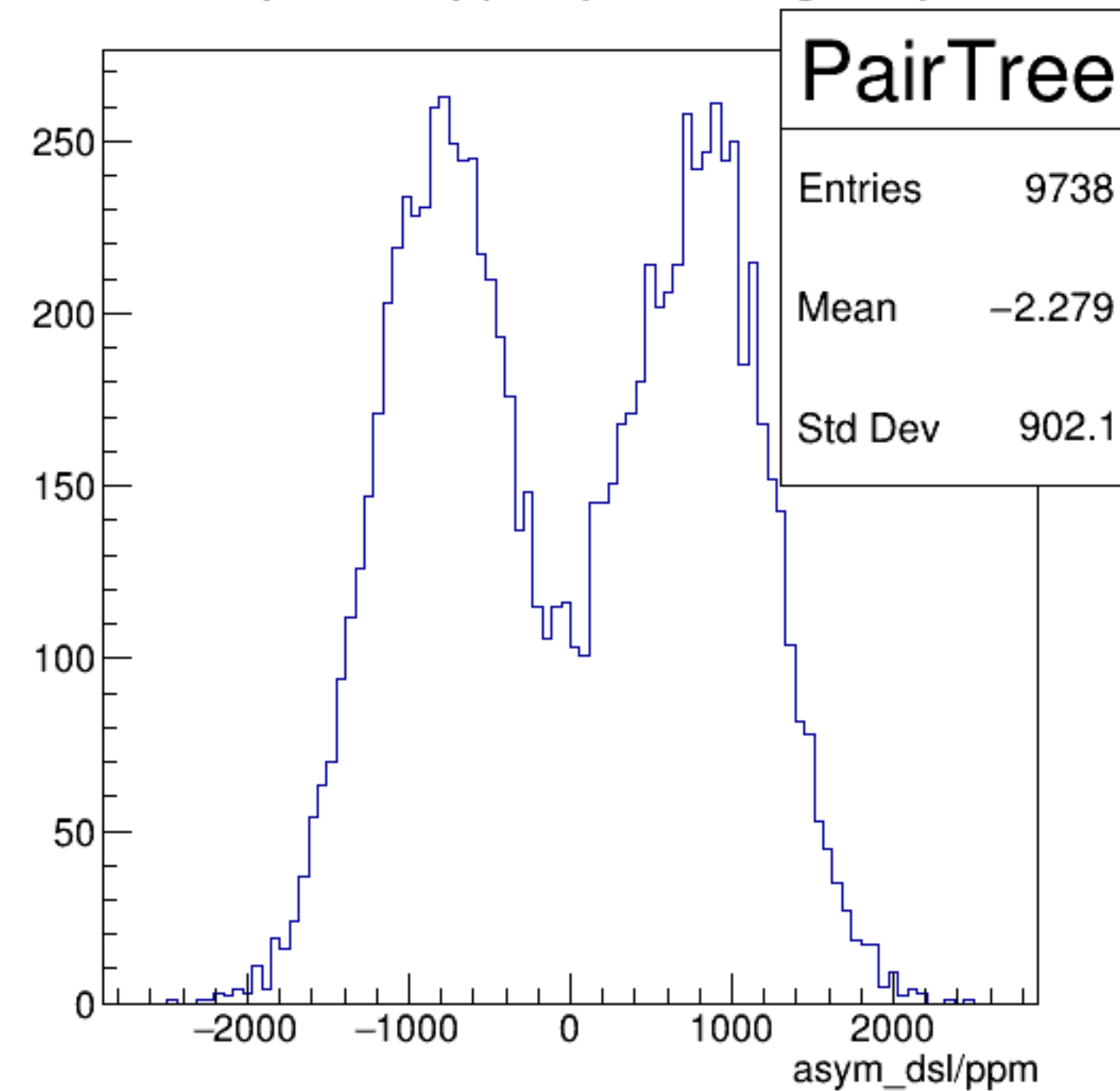
yield_dsl {ErrorFlag==0}



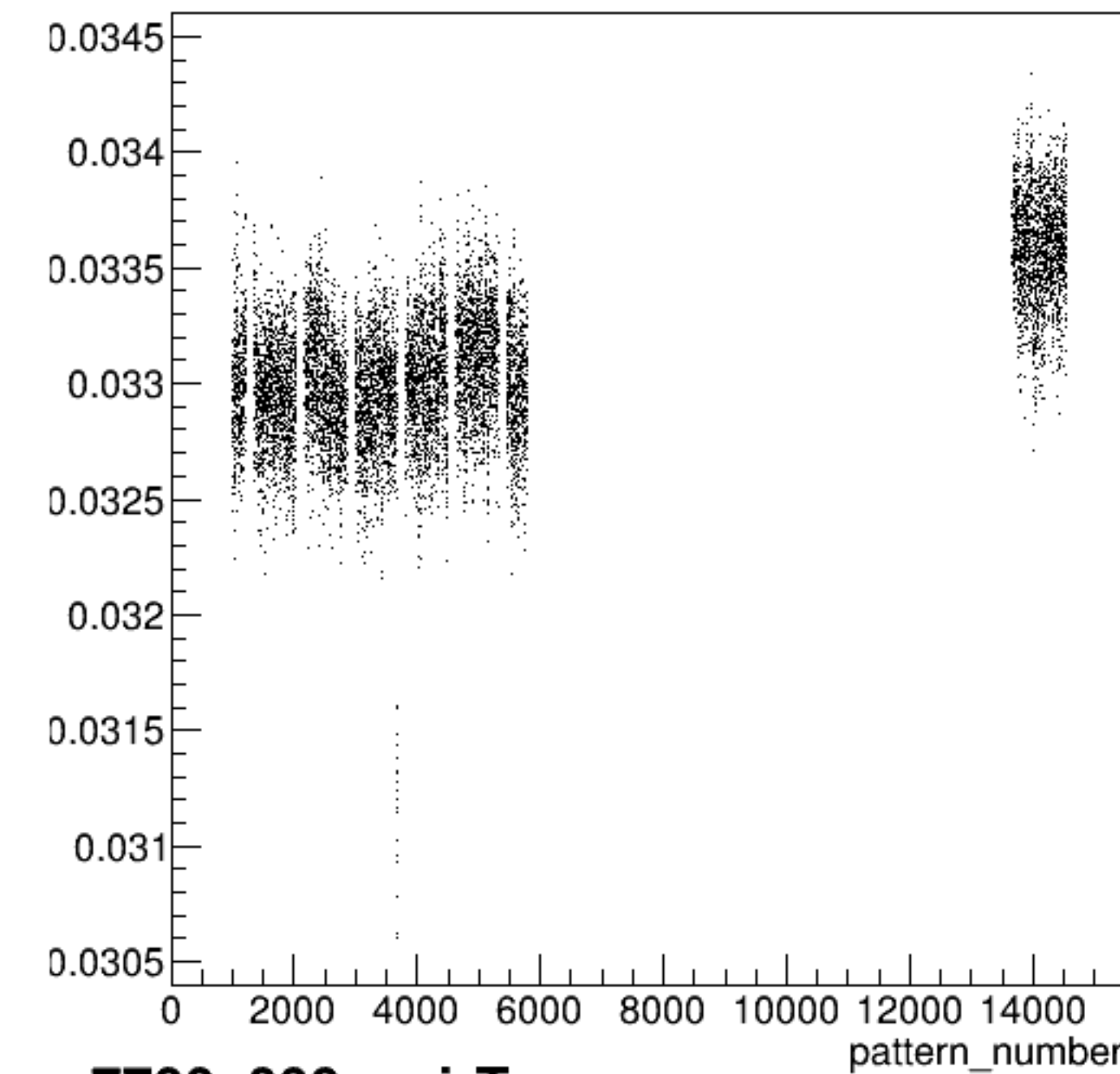
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl/ppm {ErrorFlag==0}

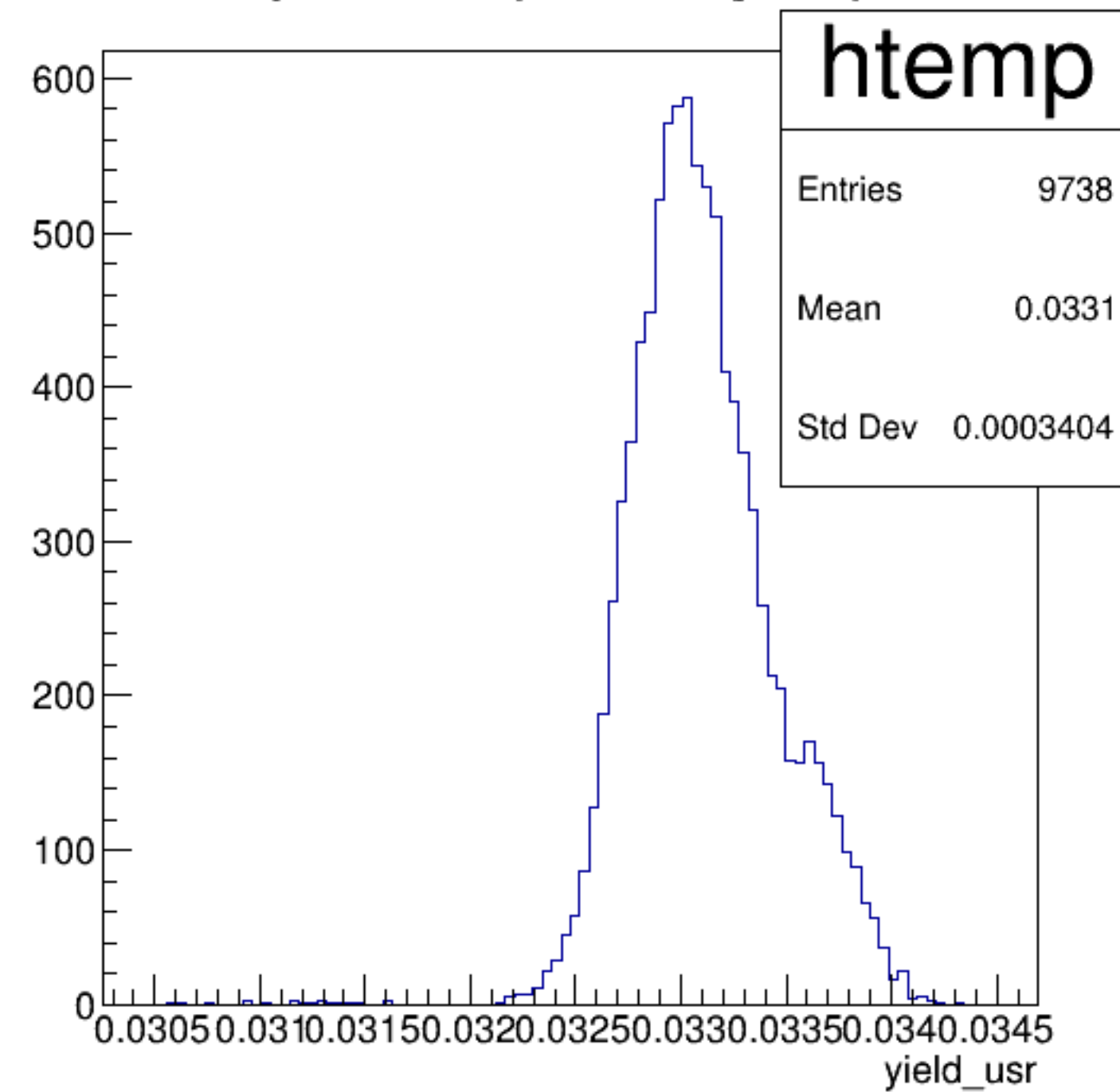


yield_usr:pattern_number {ErrorFlag==0}

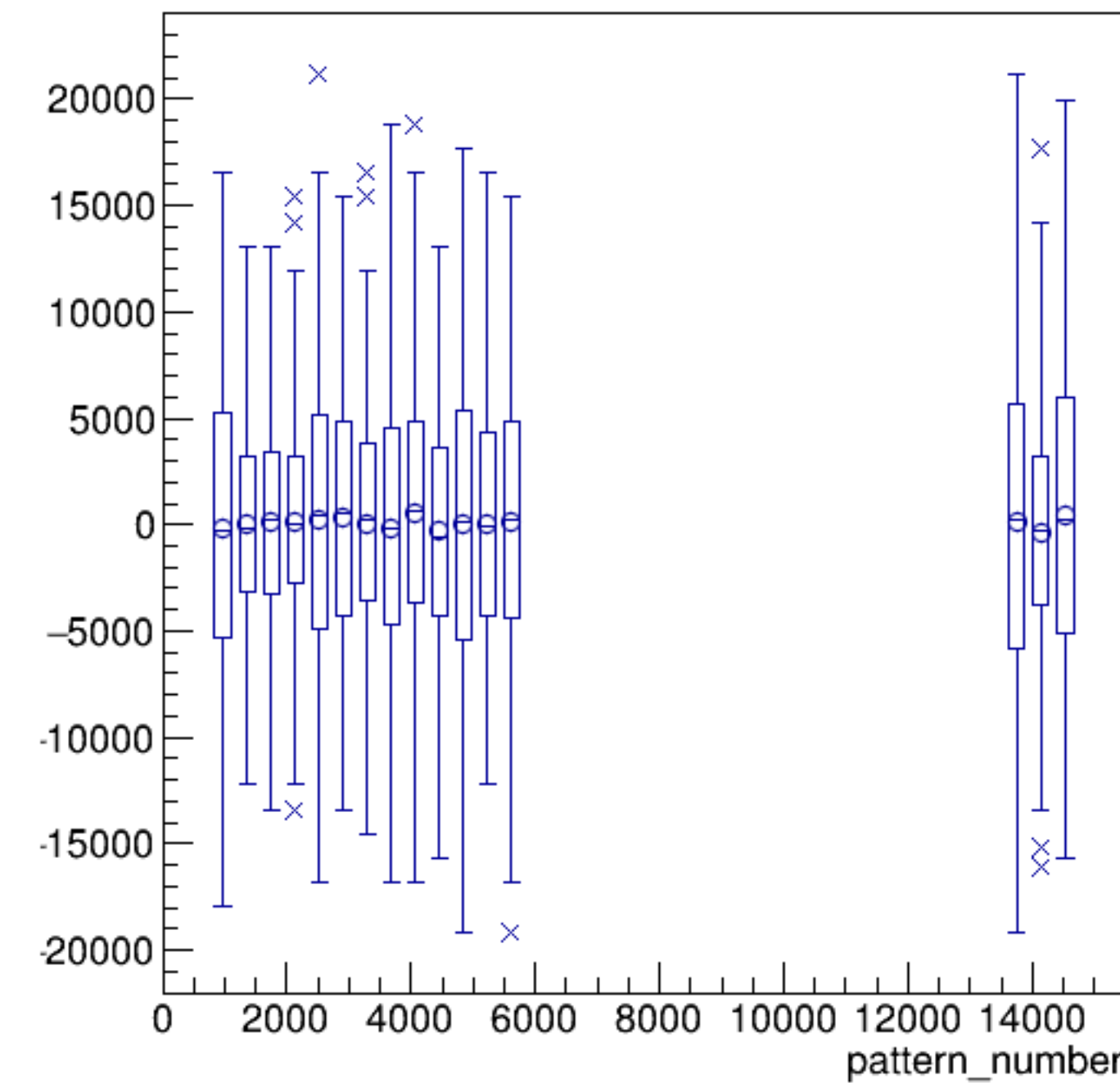


run7700_000_pairTree_usr.png

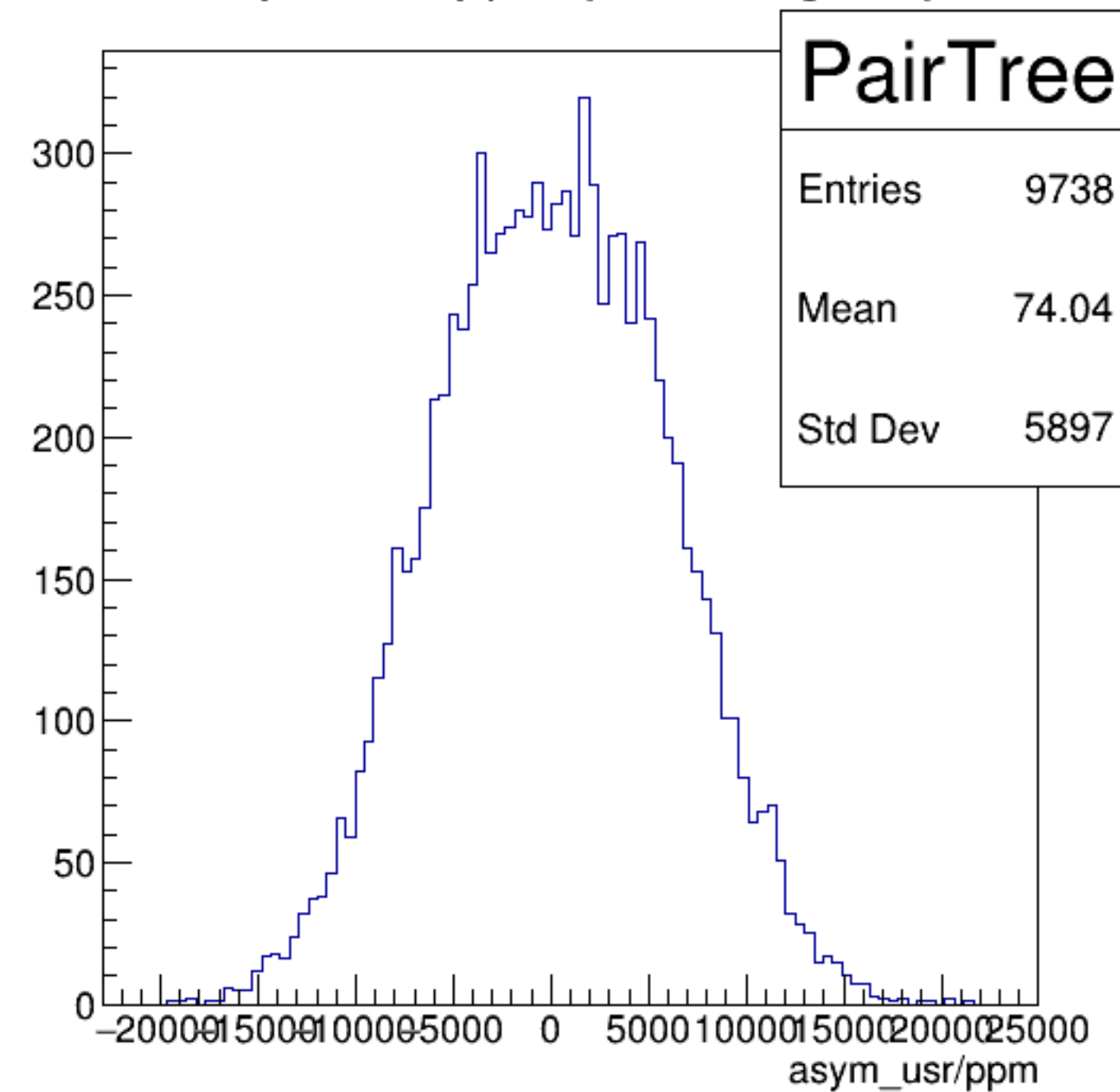
yield_usr {ErrorFlag==0}



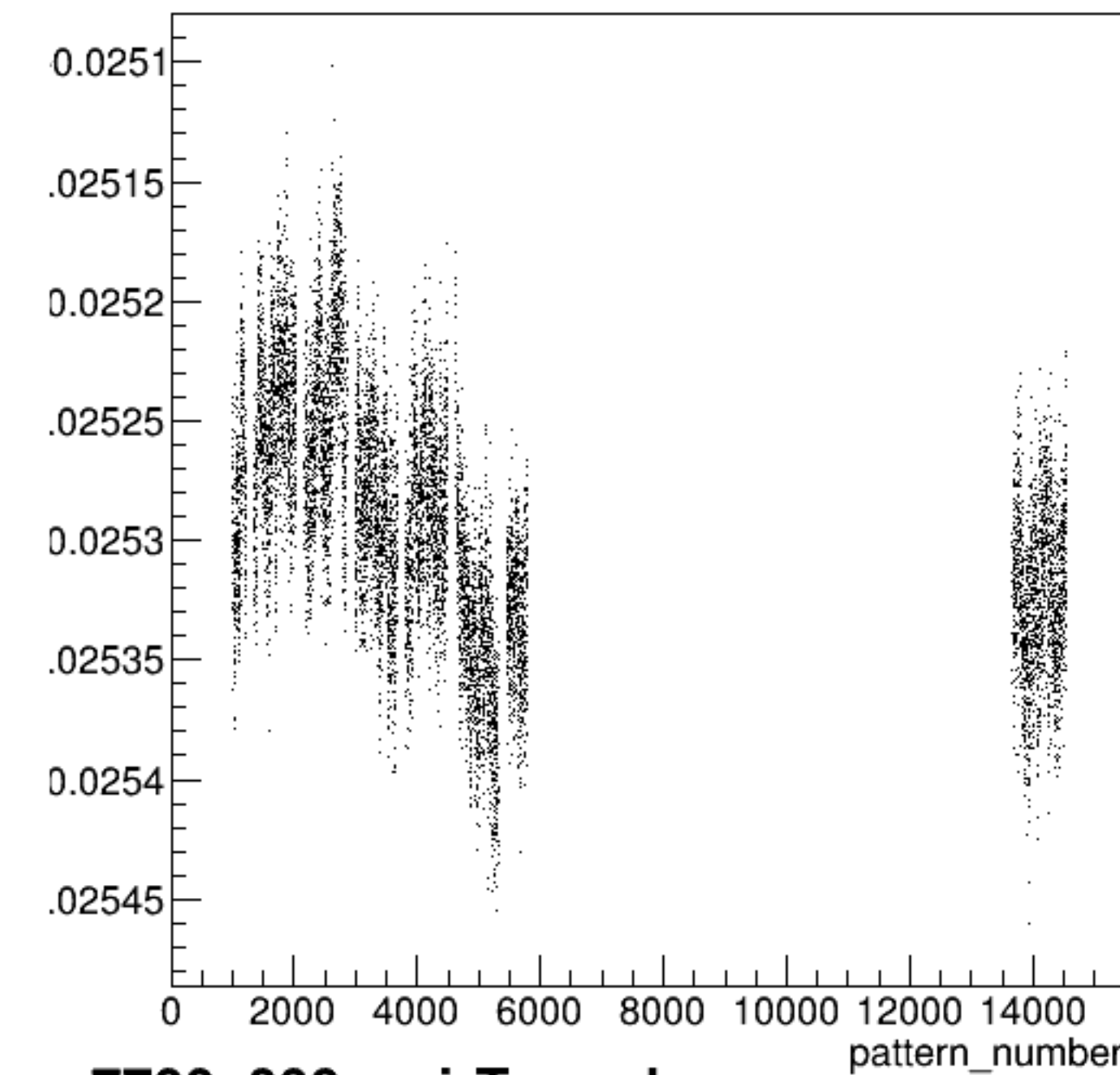
asym_usr/ppm:pattern_number {ErrorFlag==0}



asym_usr/ppm {ErrorFlag==0}

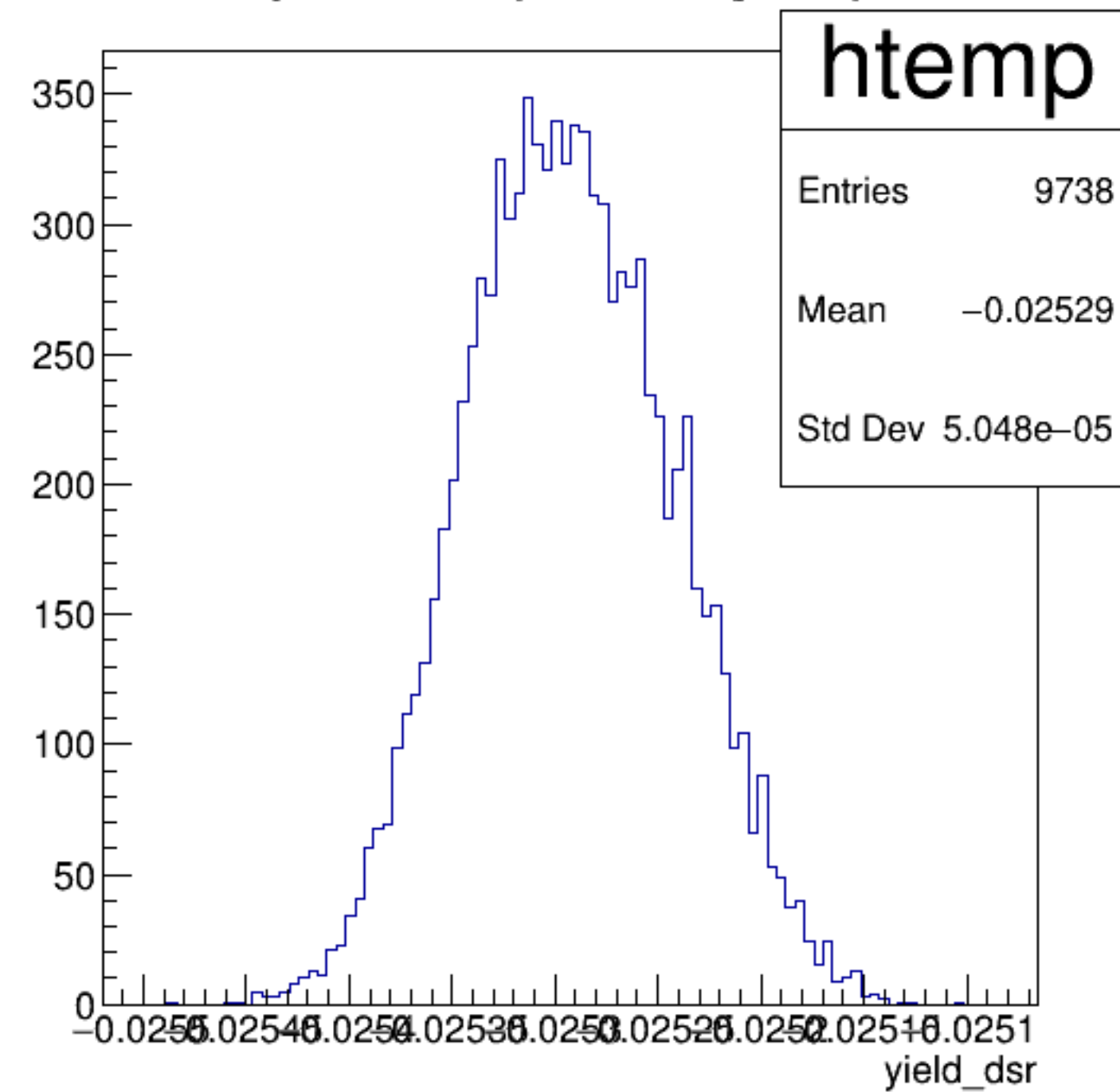


yield_dsr:pattern_number {ErrorFlag==0}

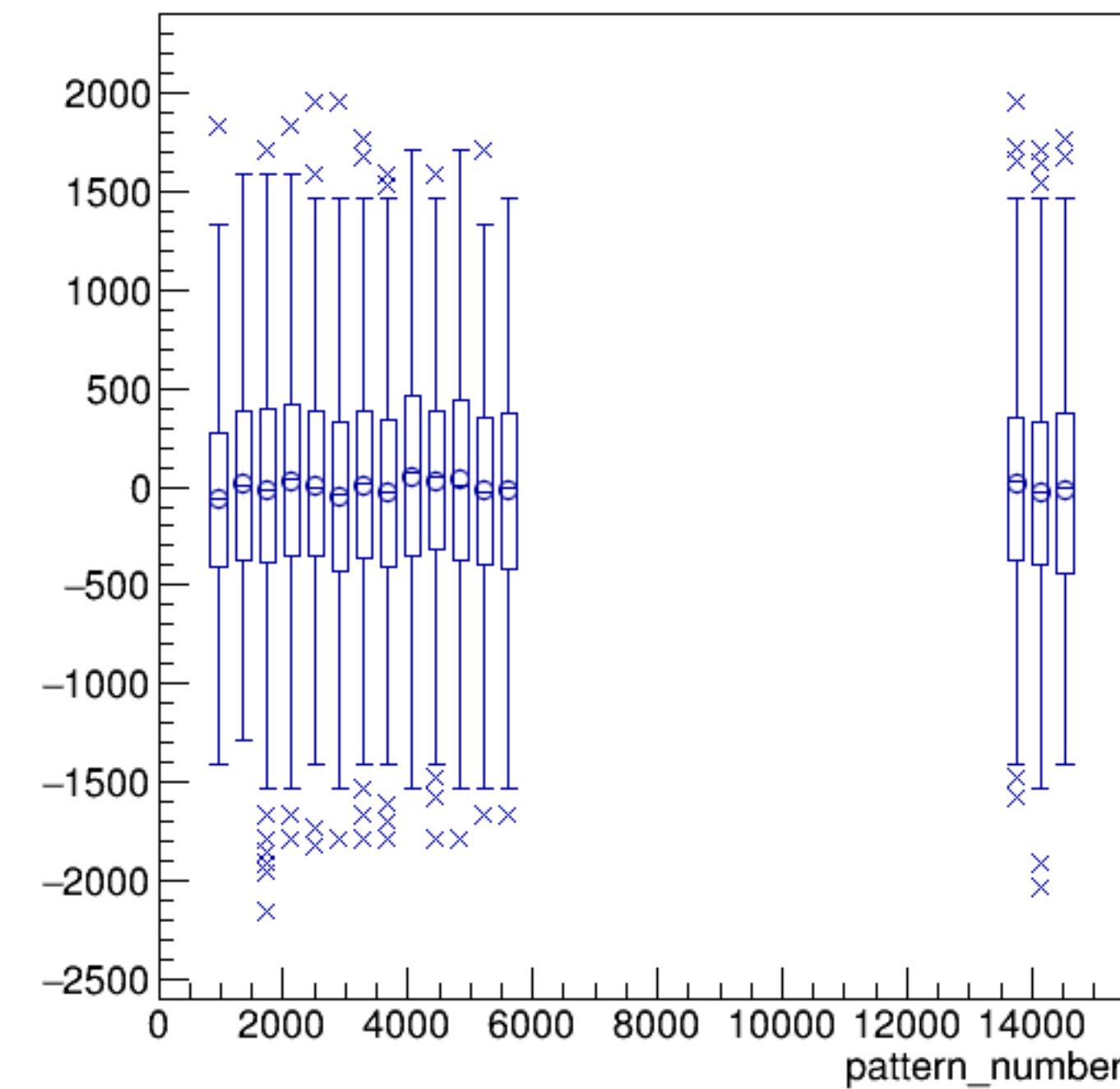


run7700_000_pairTree_dsr.png

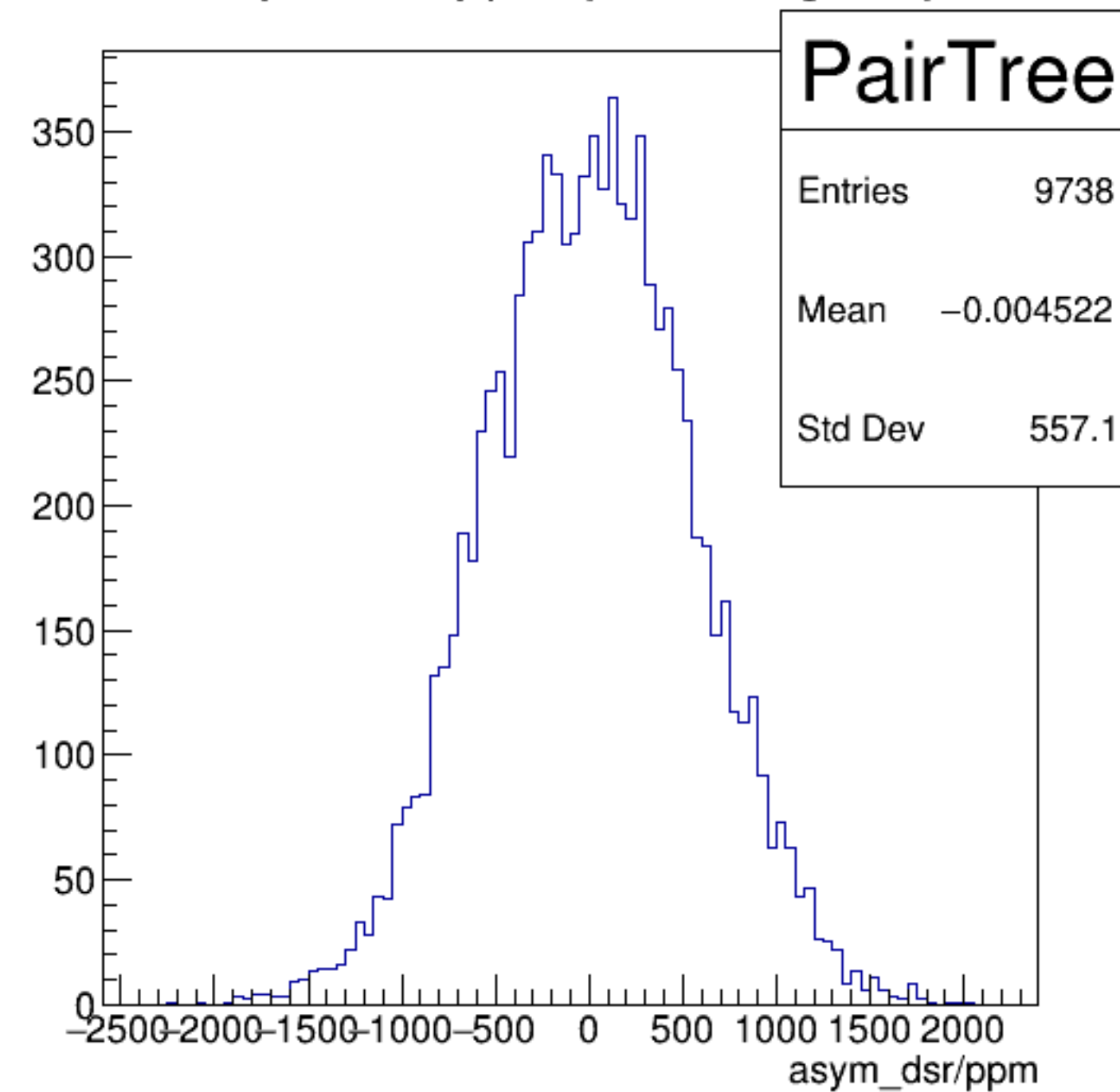
yield_dsr {ErrorFlag==0}



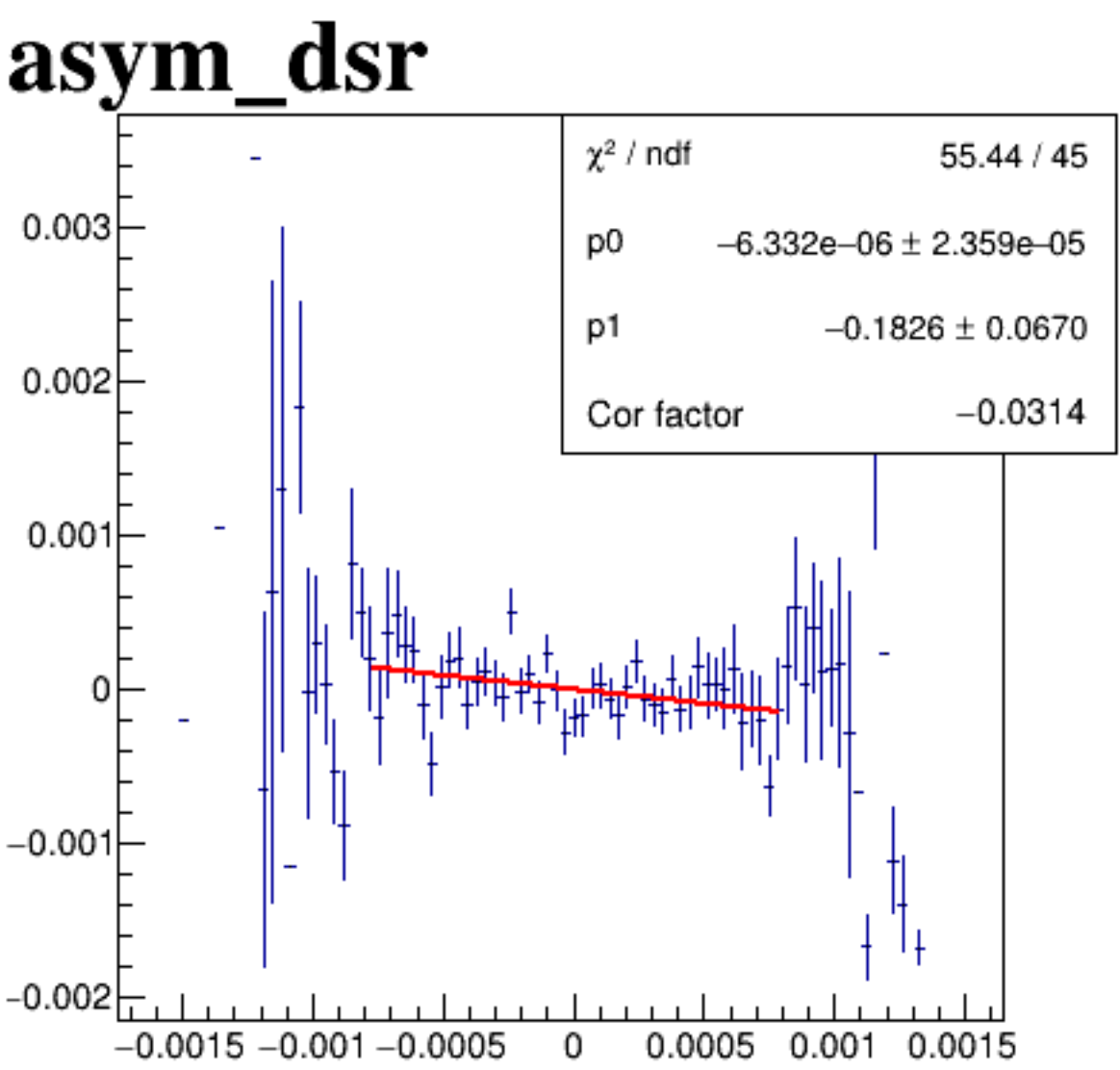
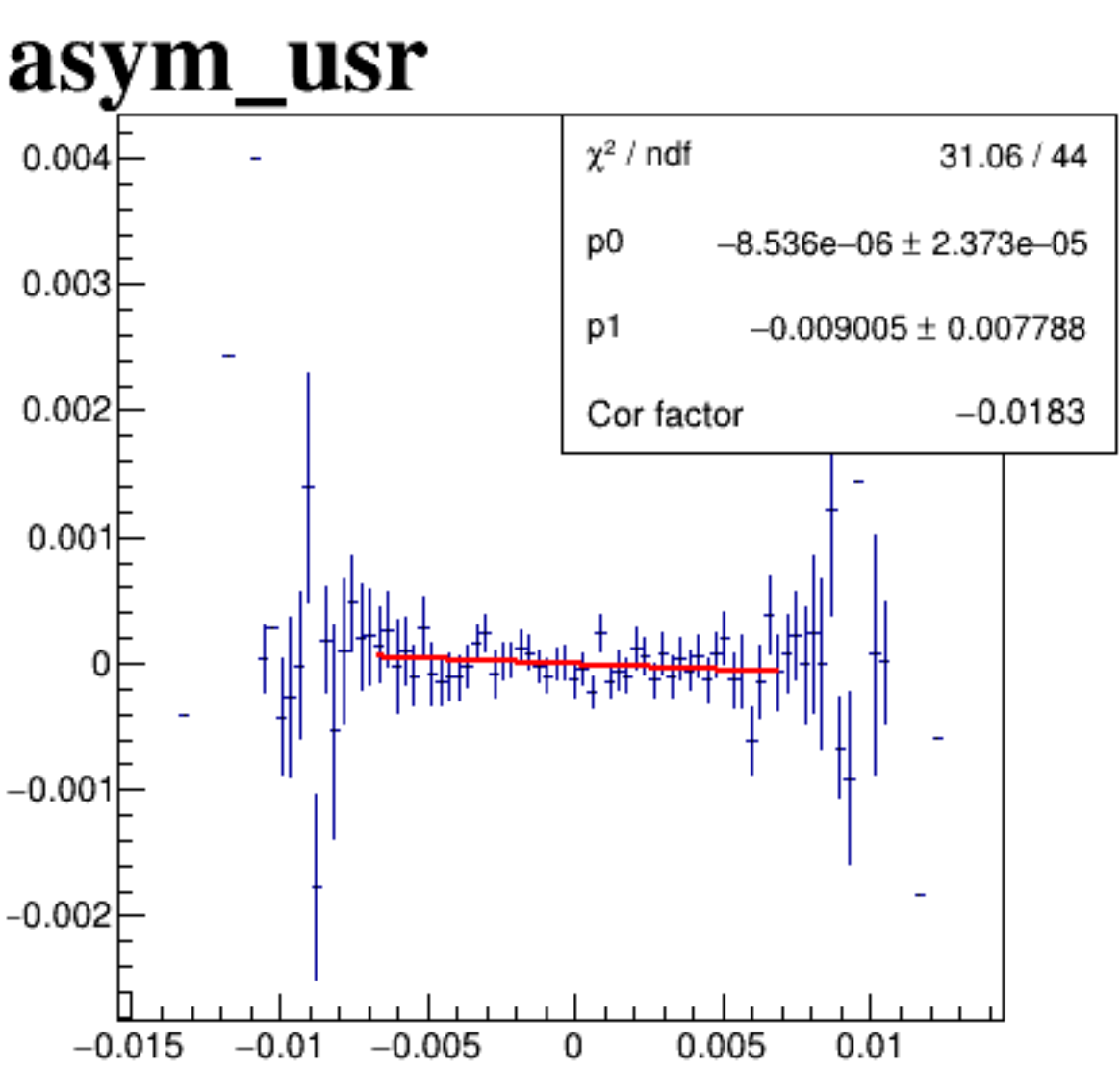
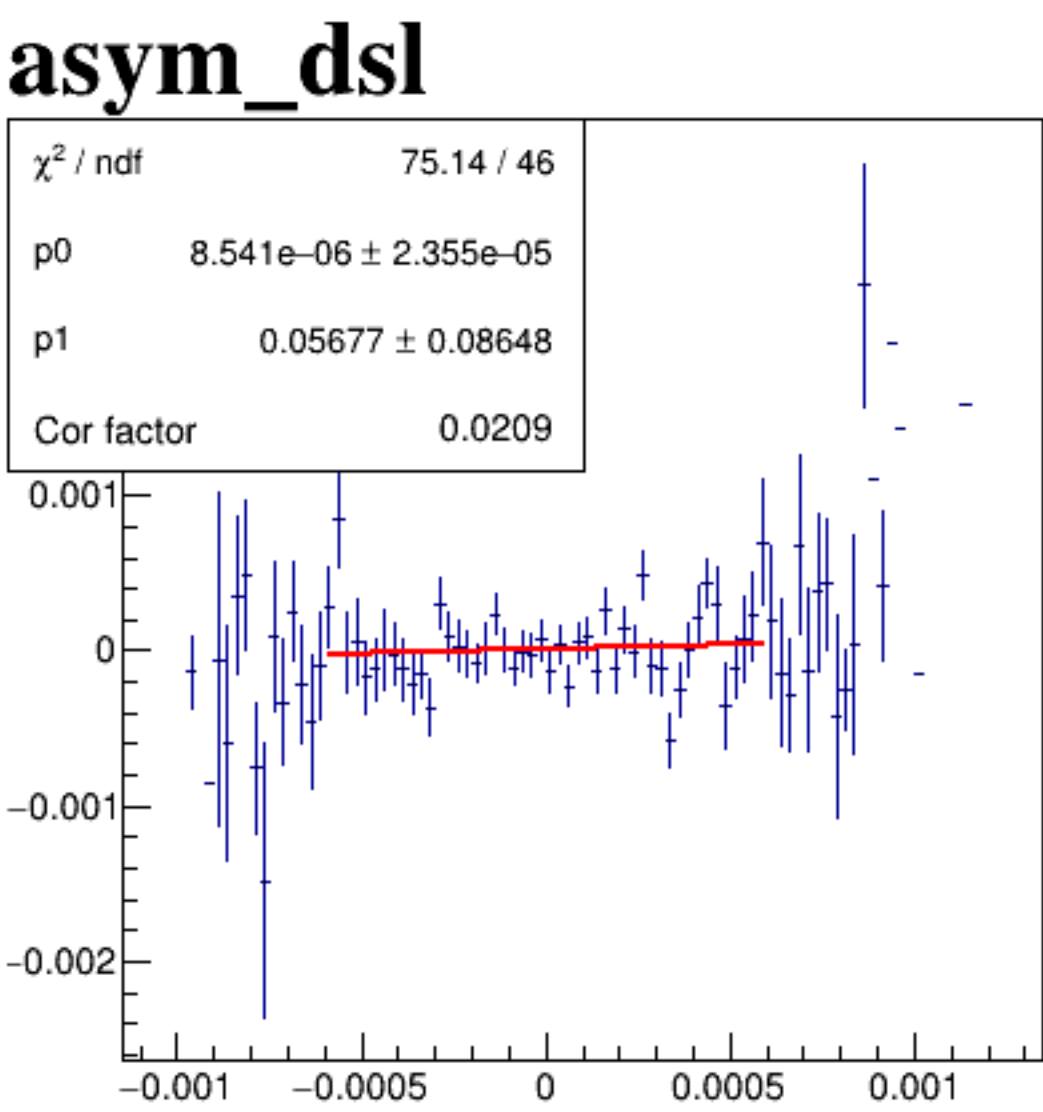
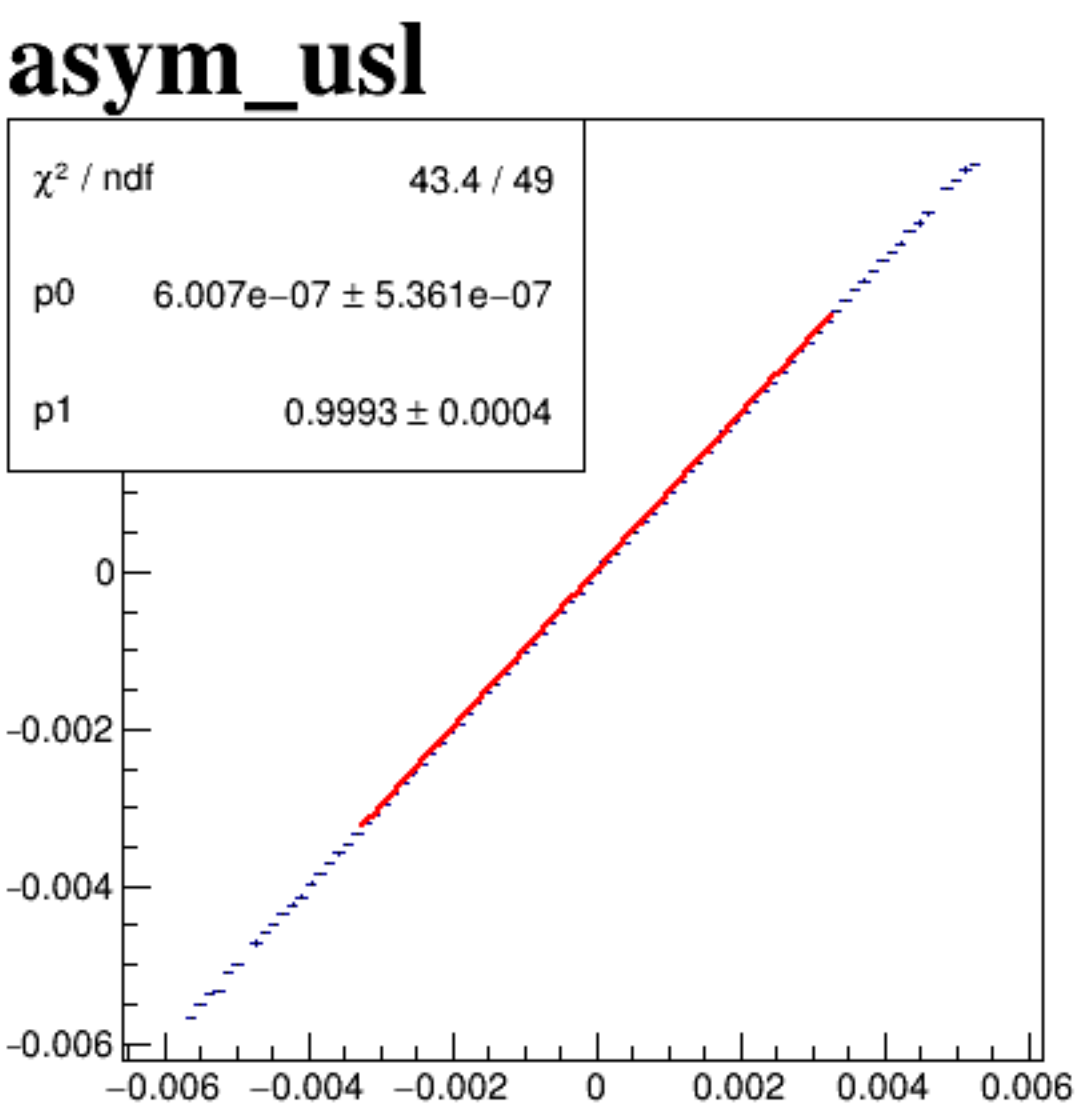
asym_dsr/ppm:pattern_number {ErrorFlag==0}



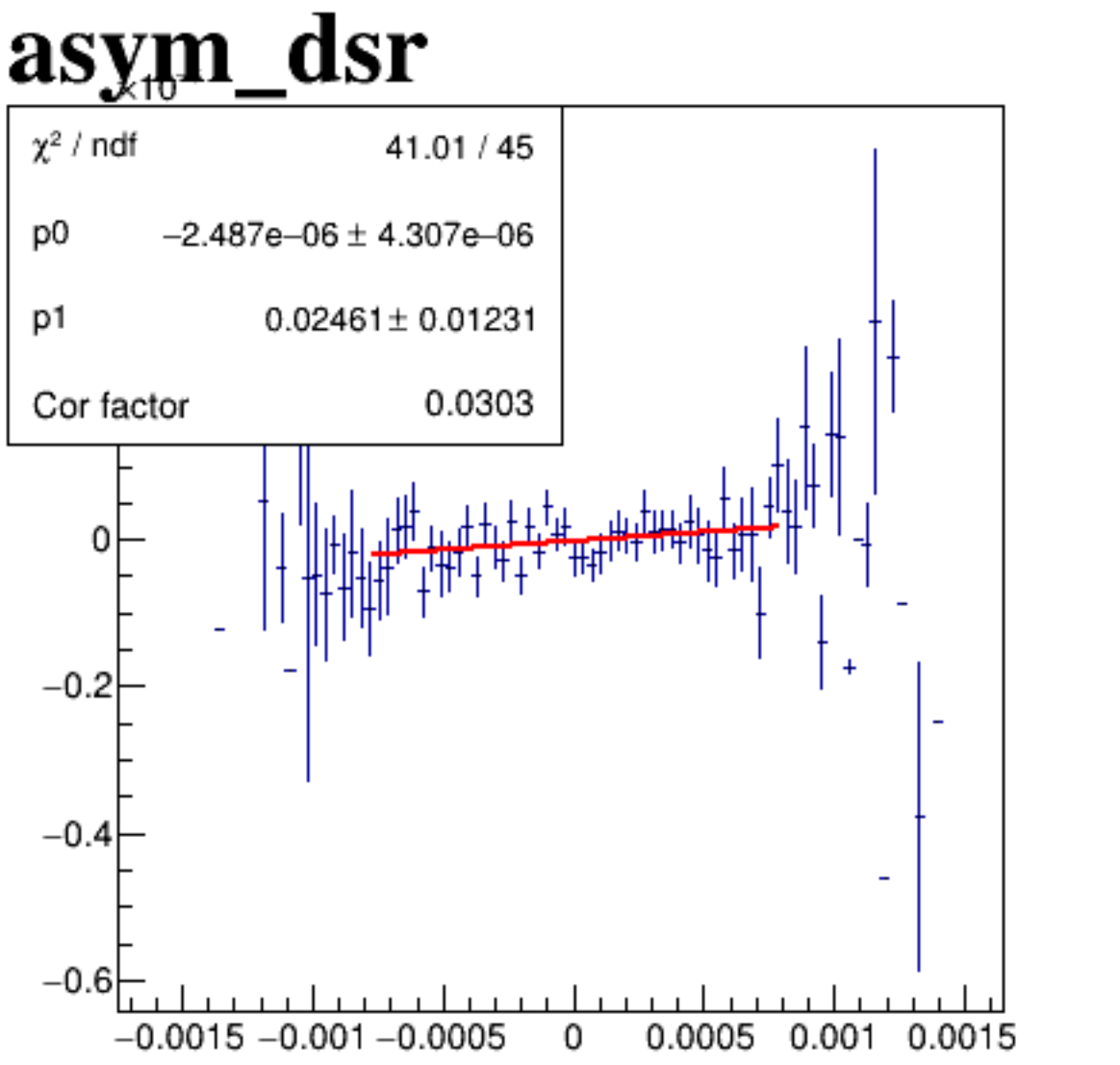
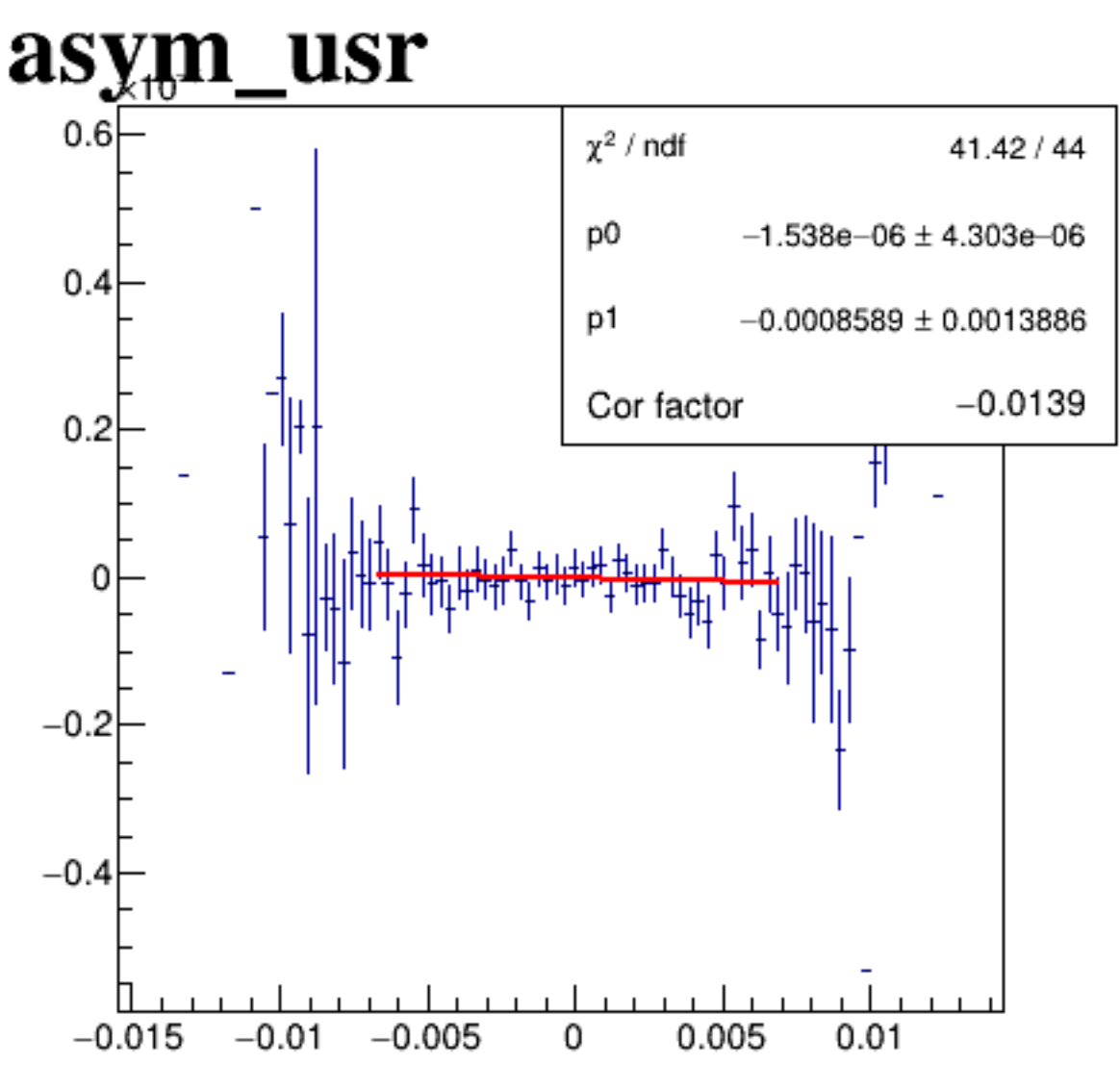
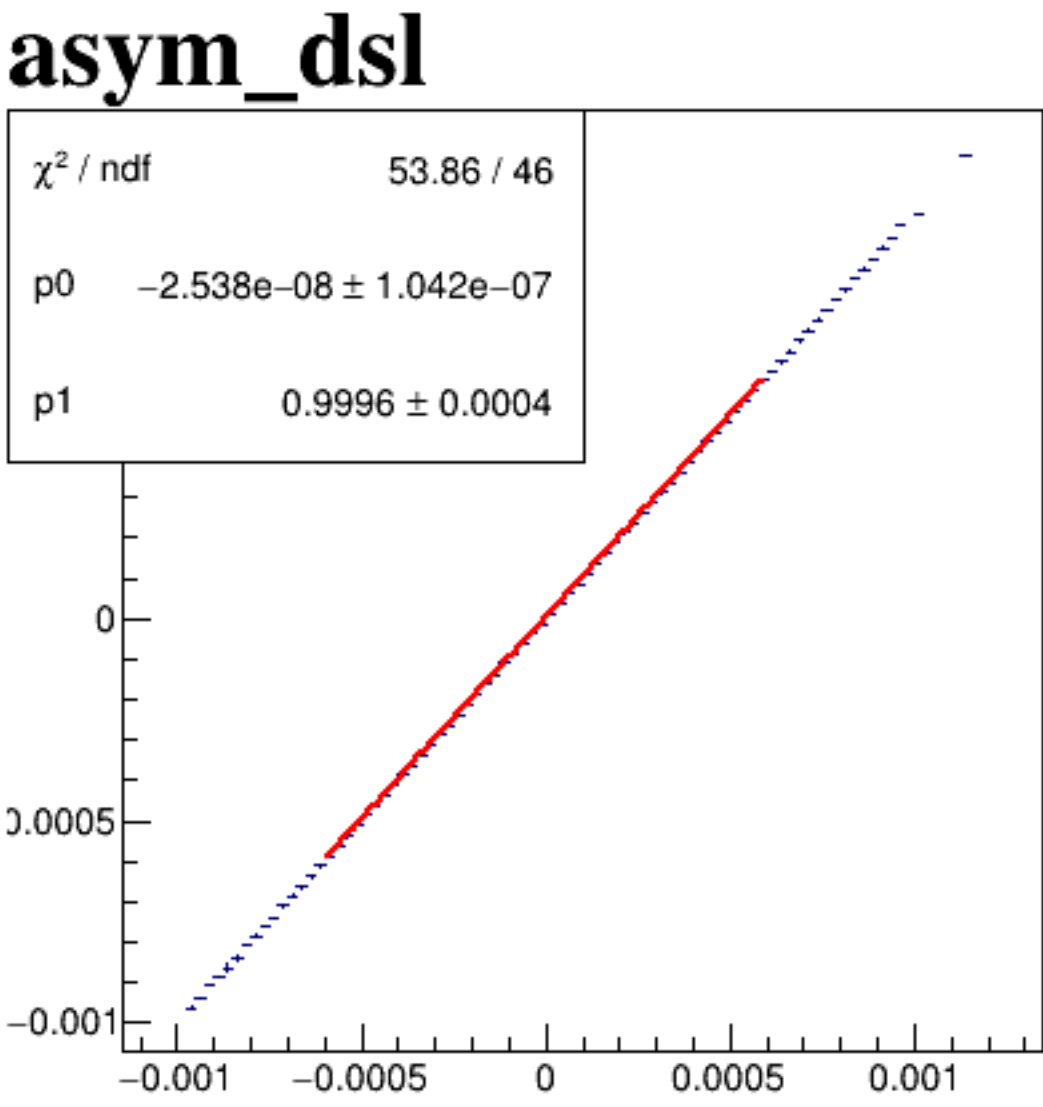
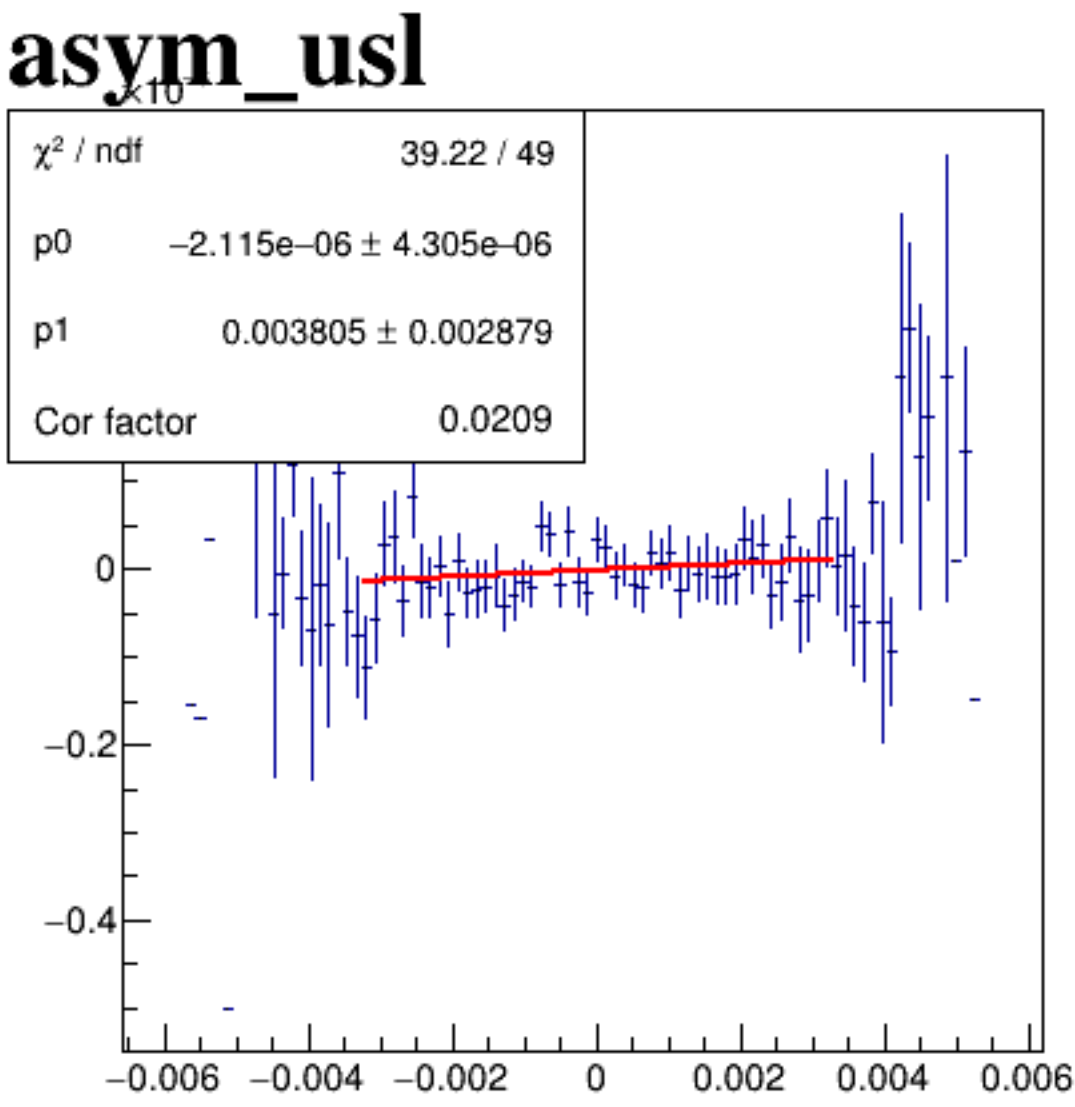
asym_dsr/ppm {ErrorFlag==0}



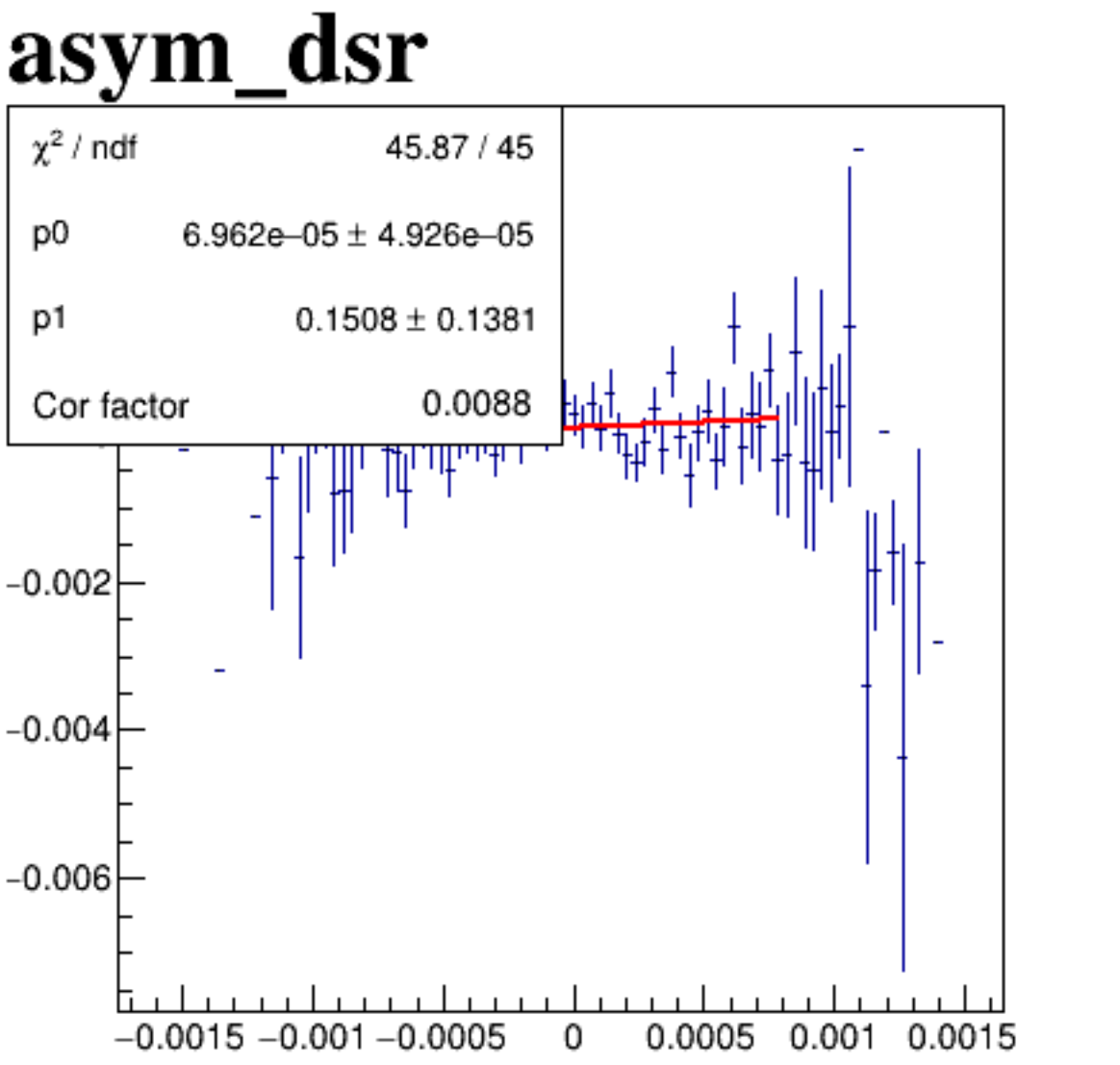
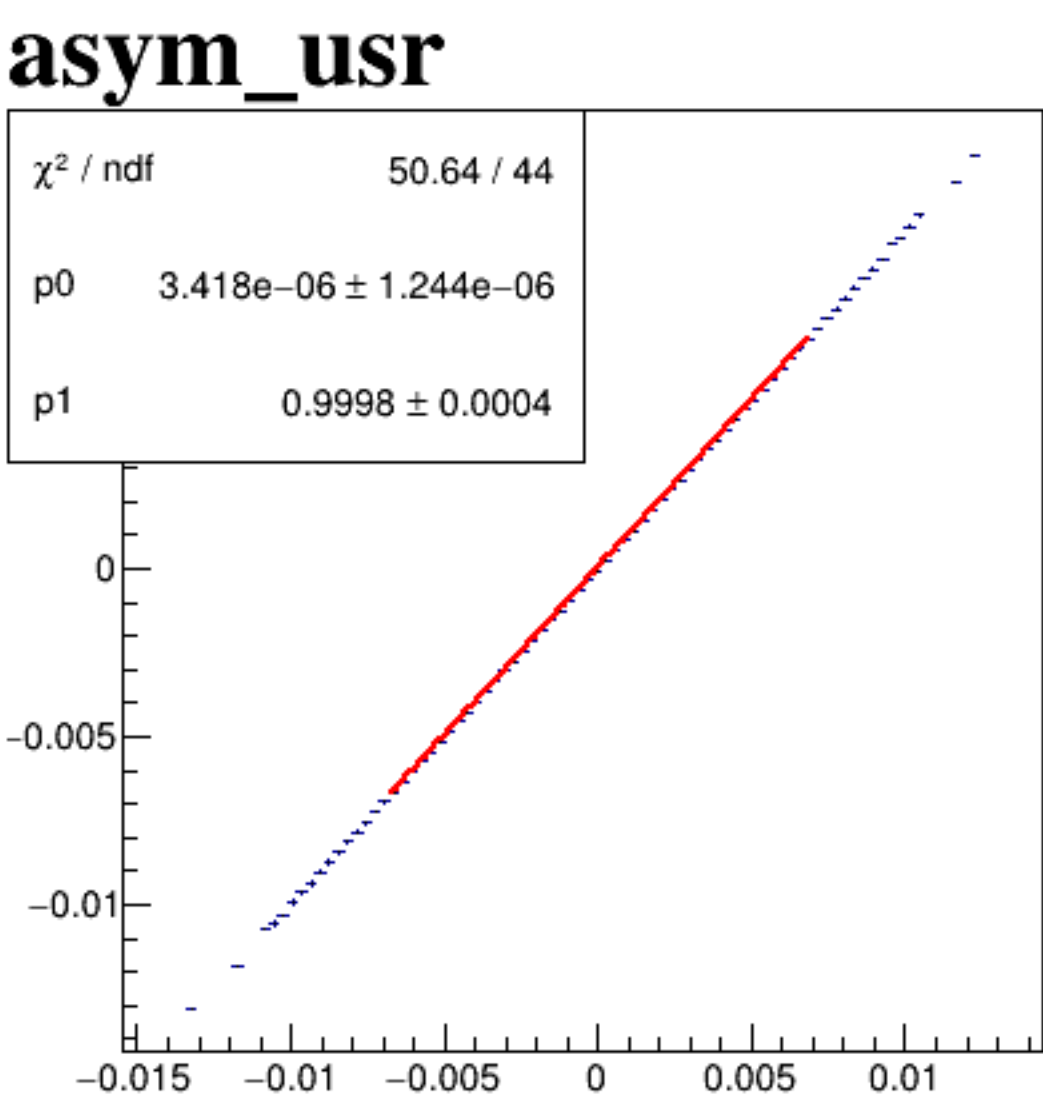
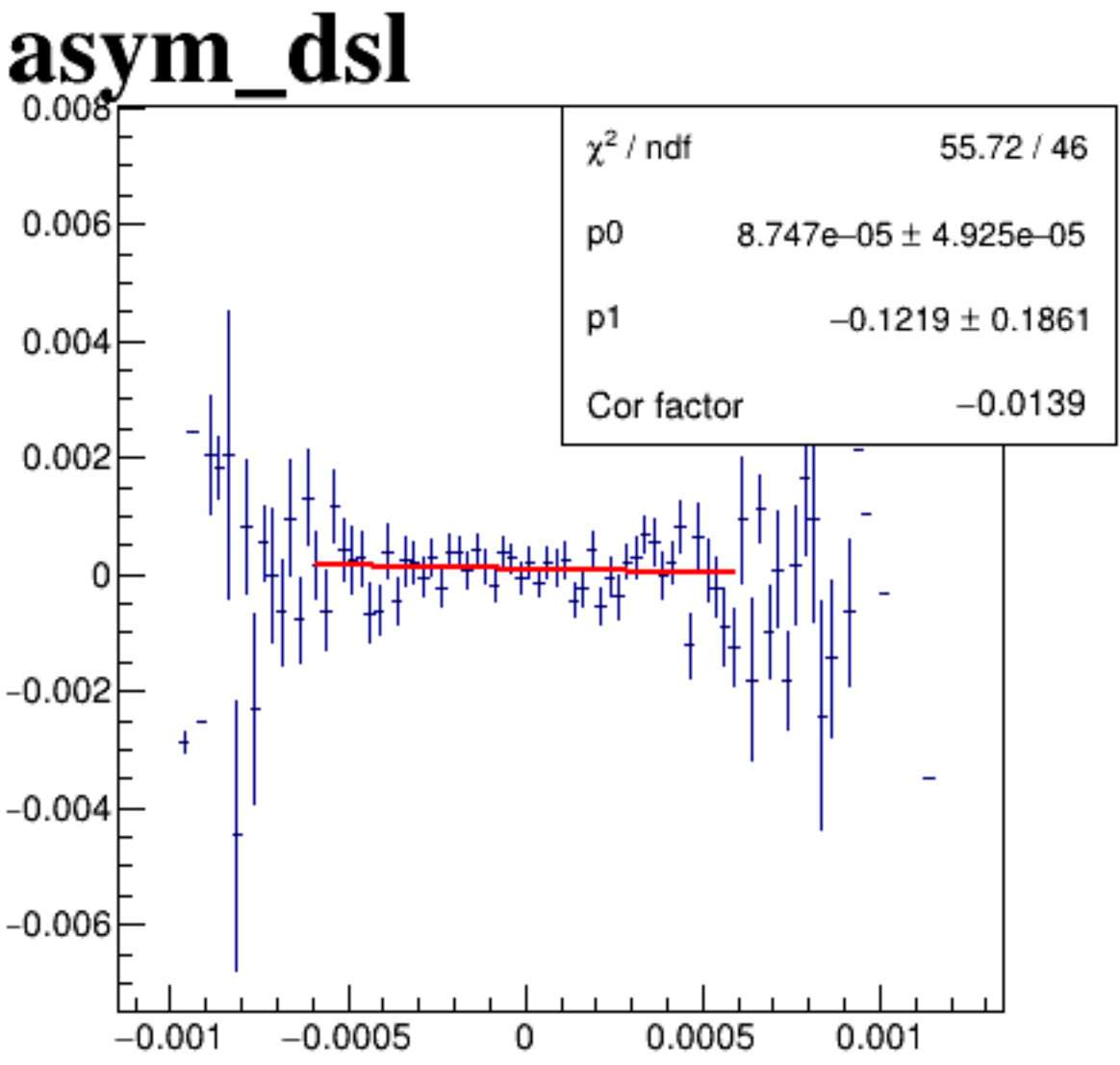
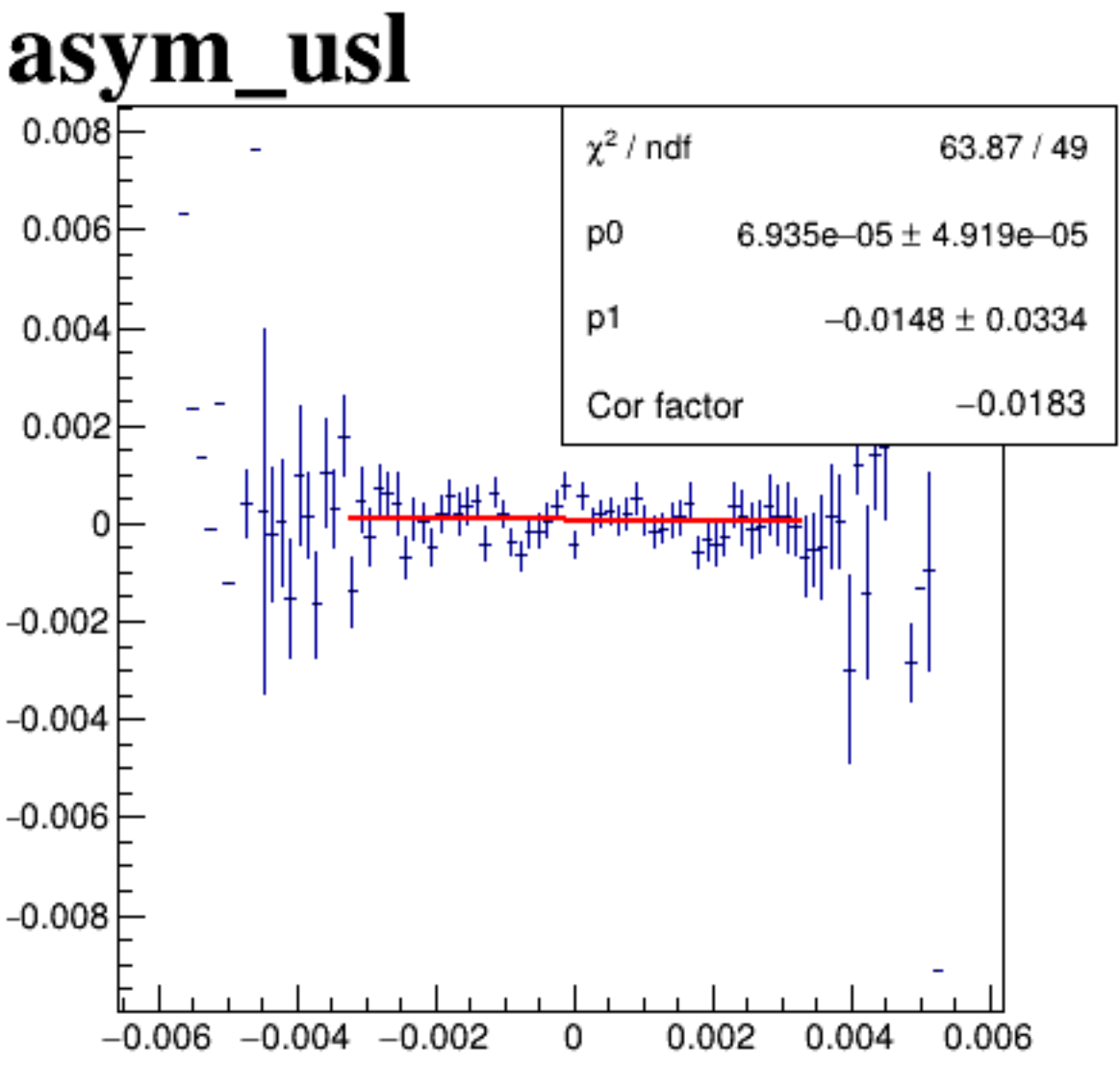
asym_usl



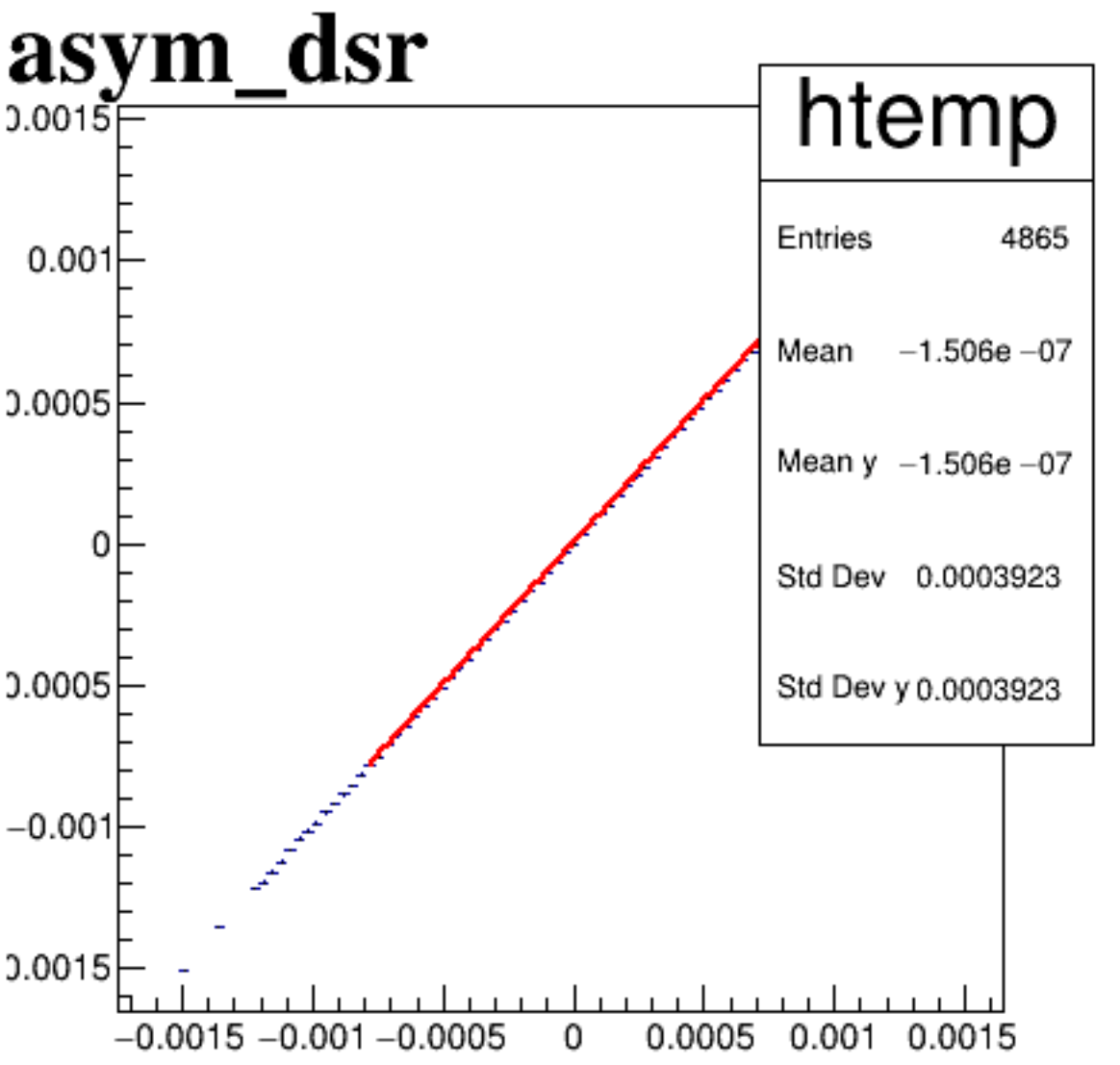
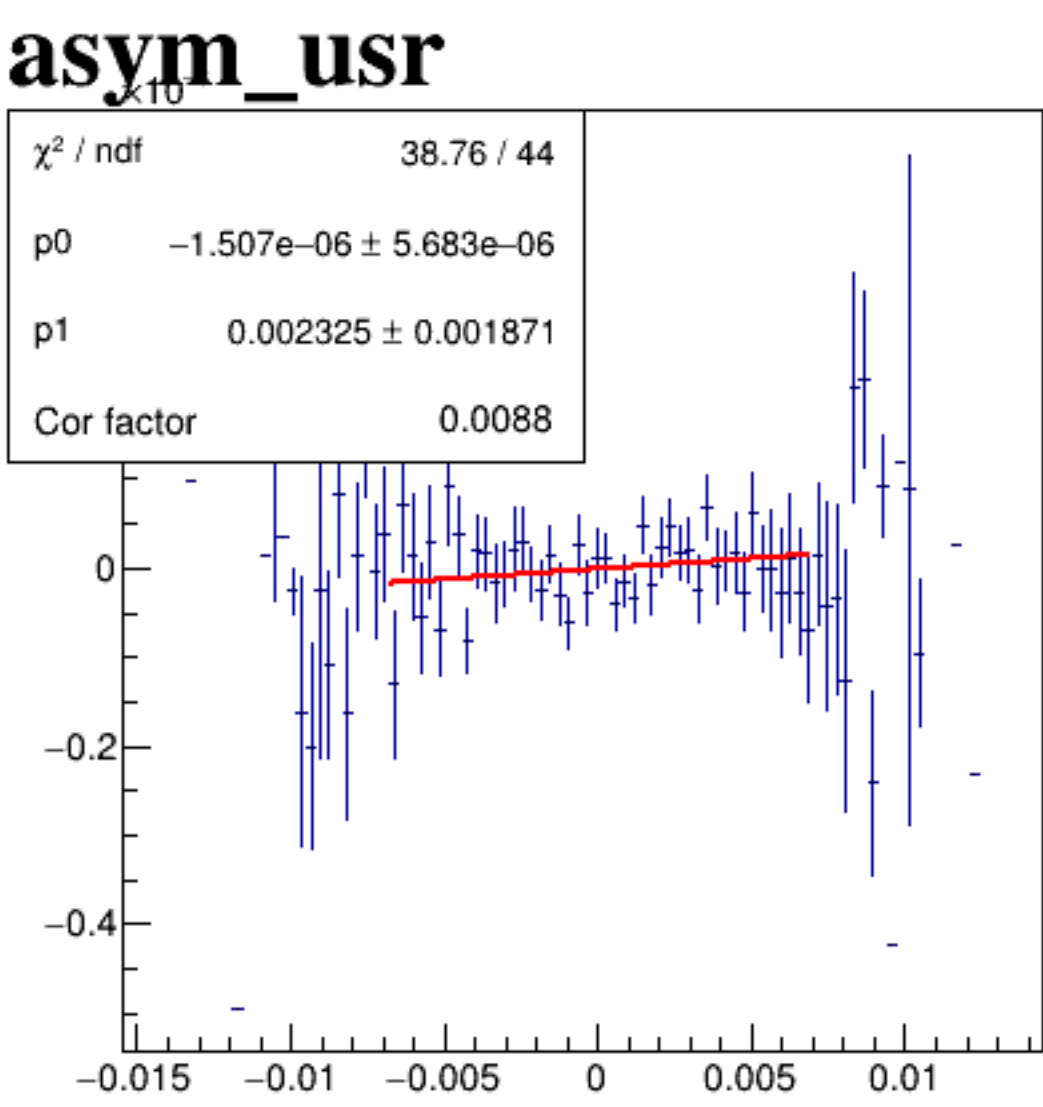
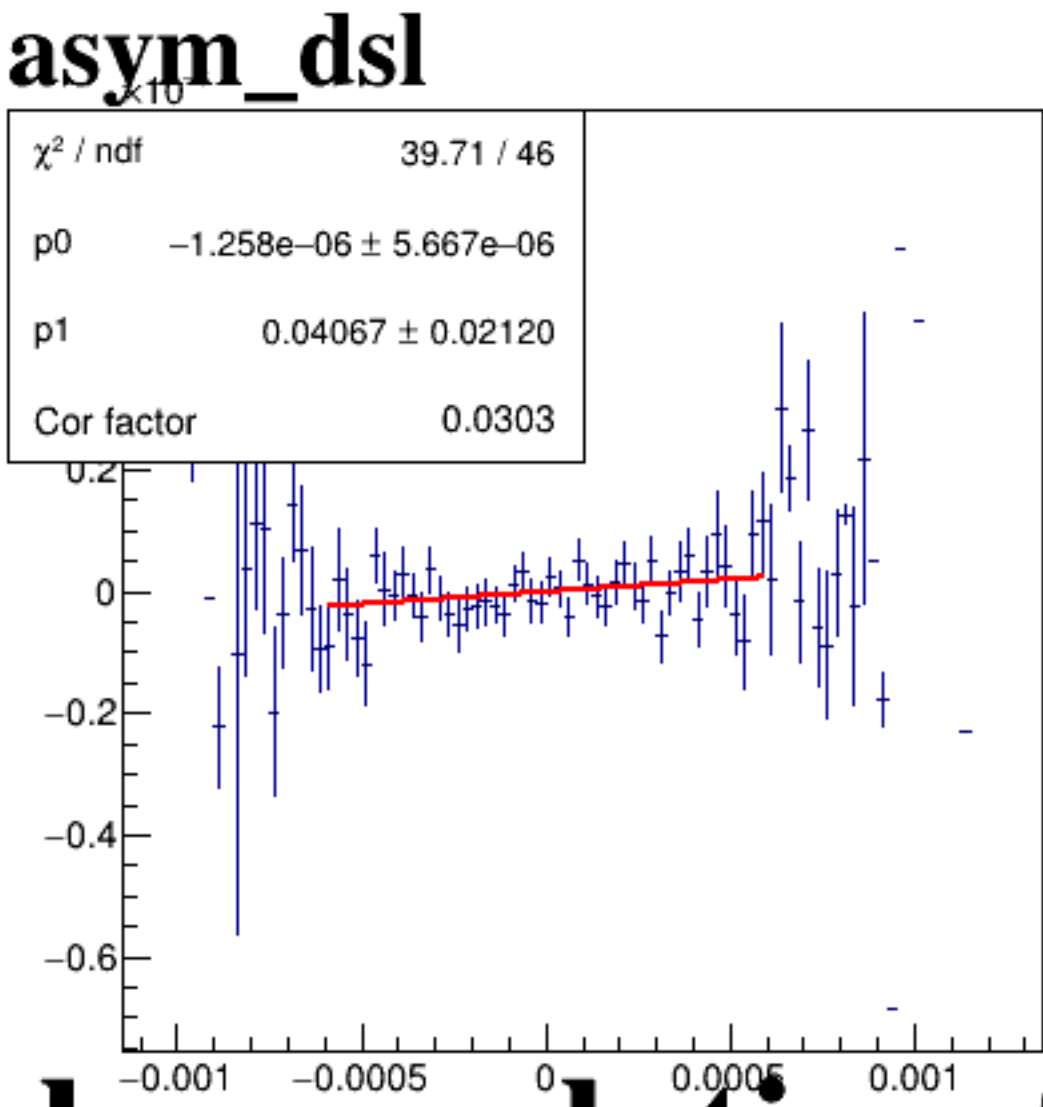
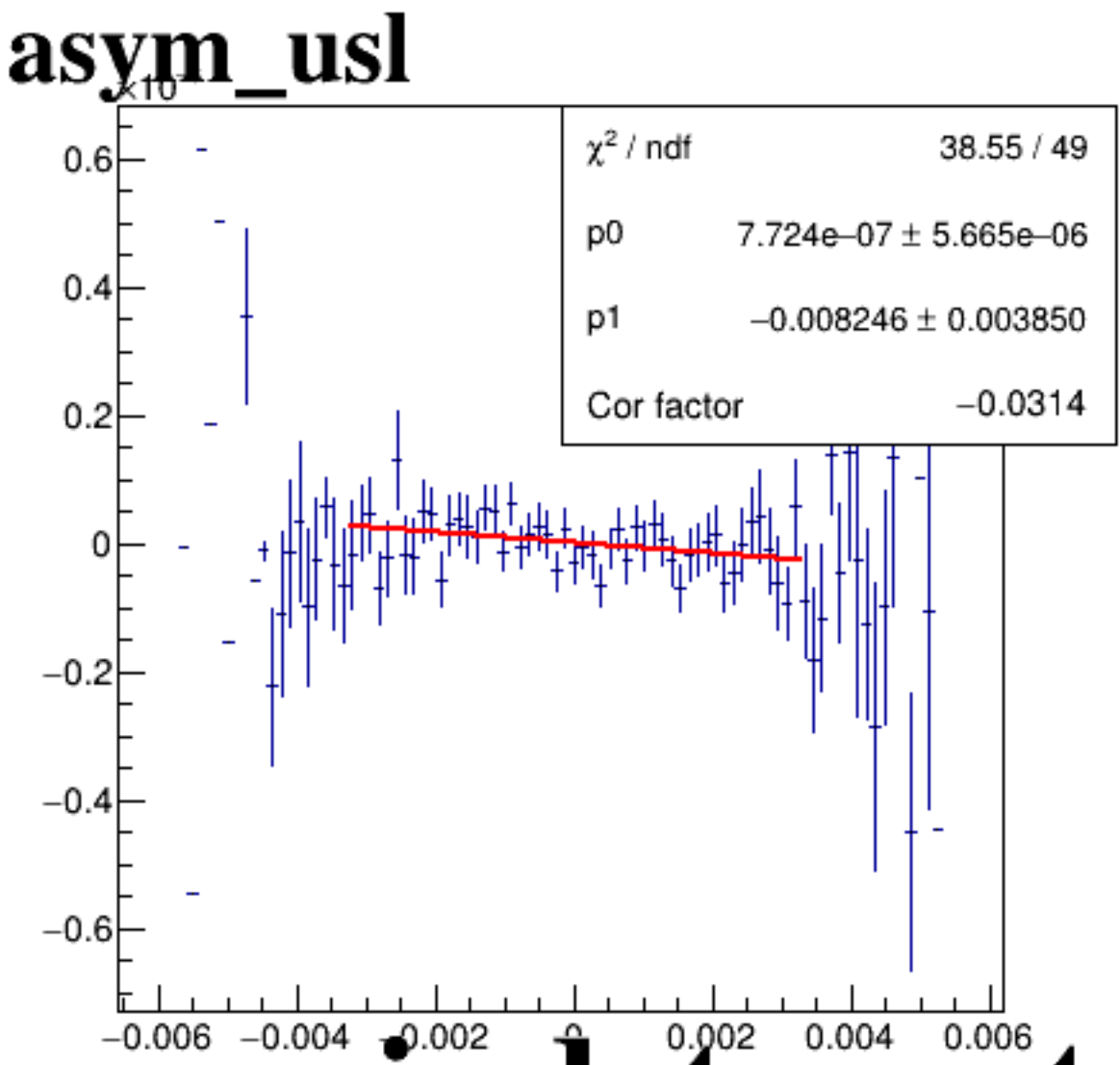
asym_dsl



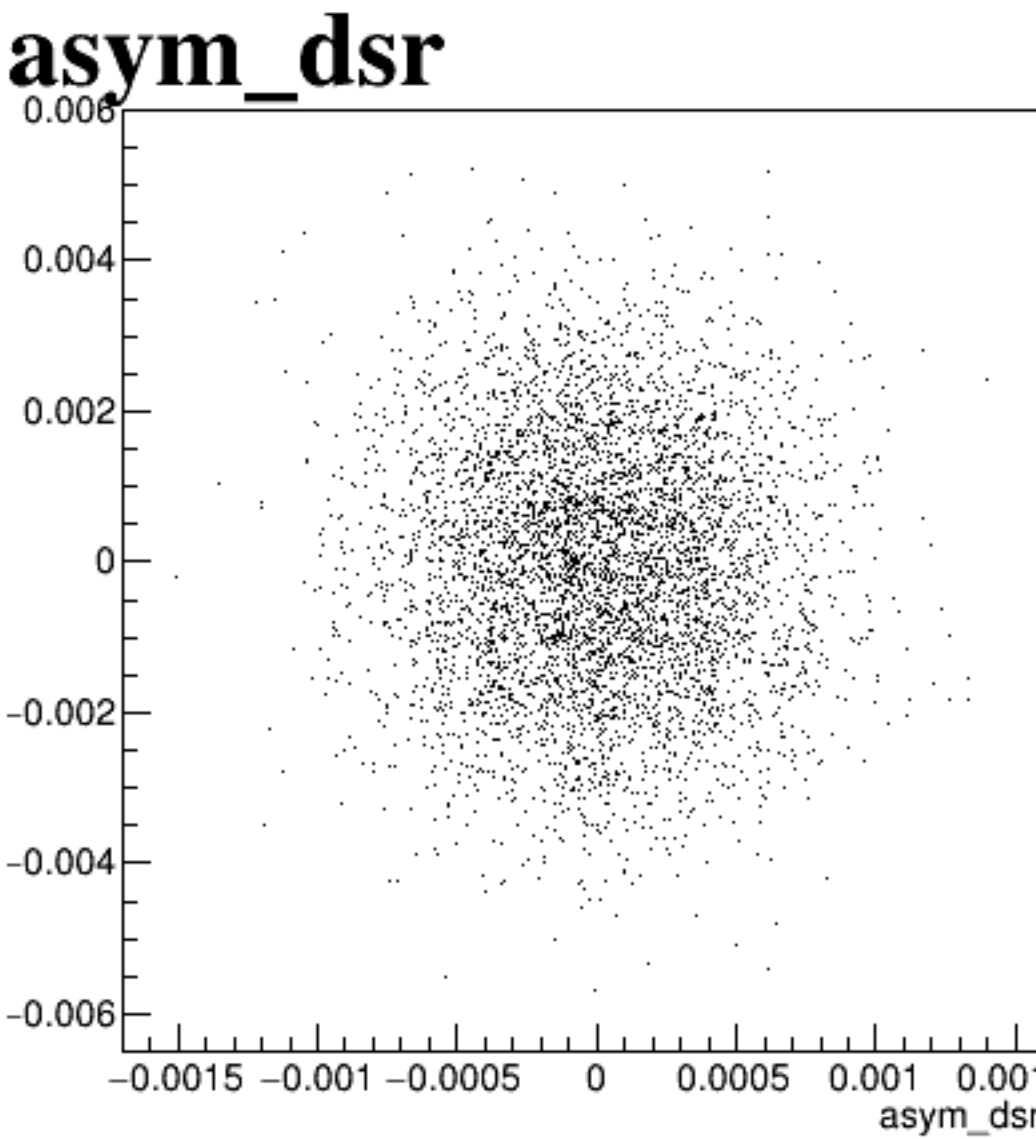
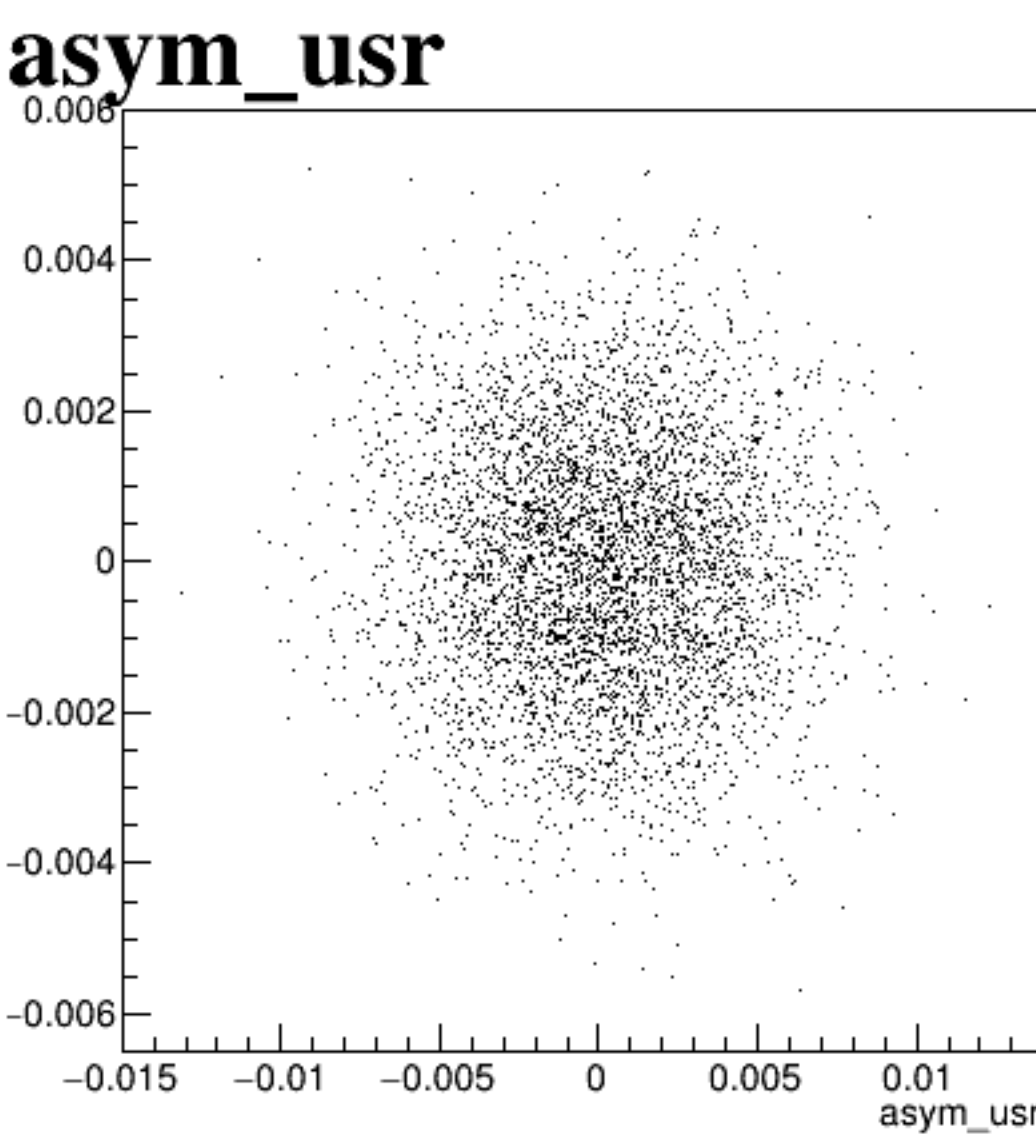
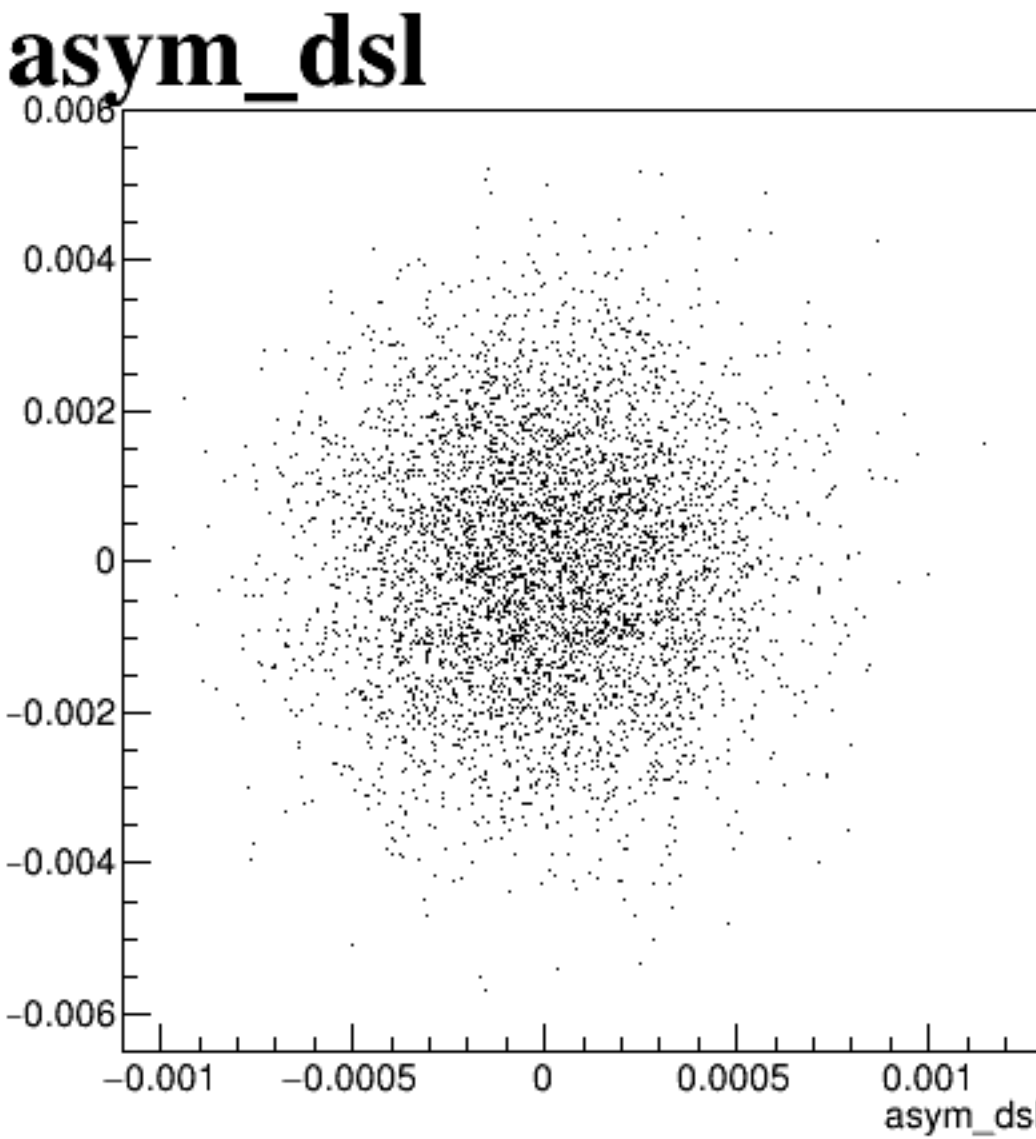
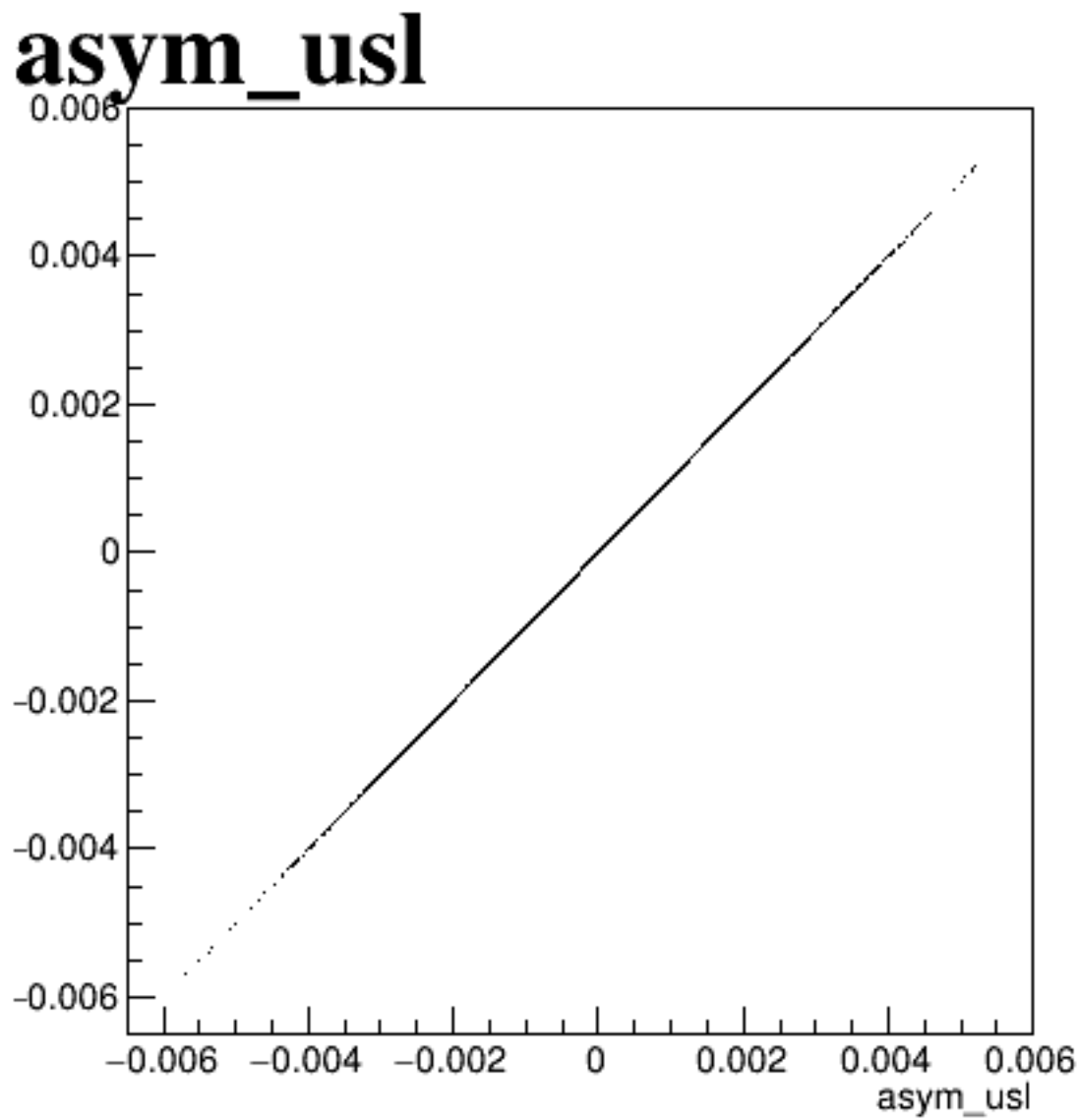
asym_usr



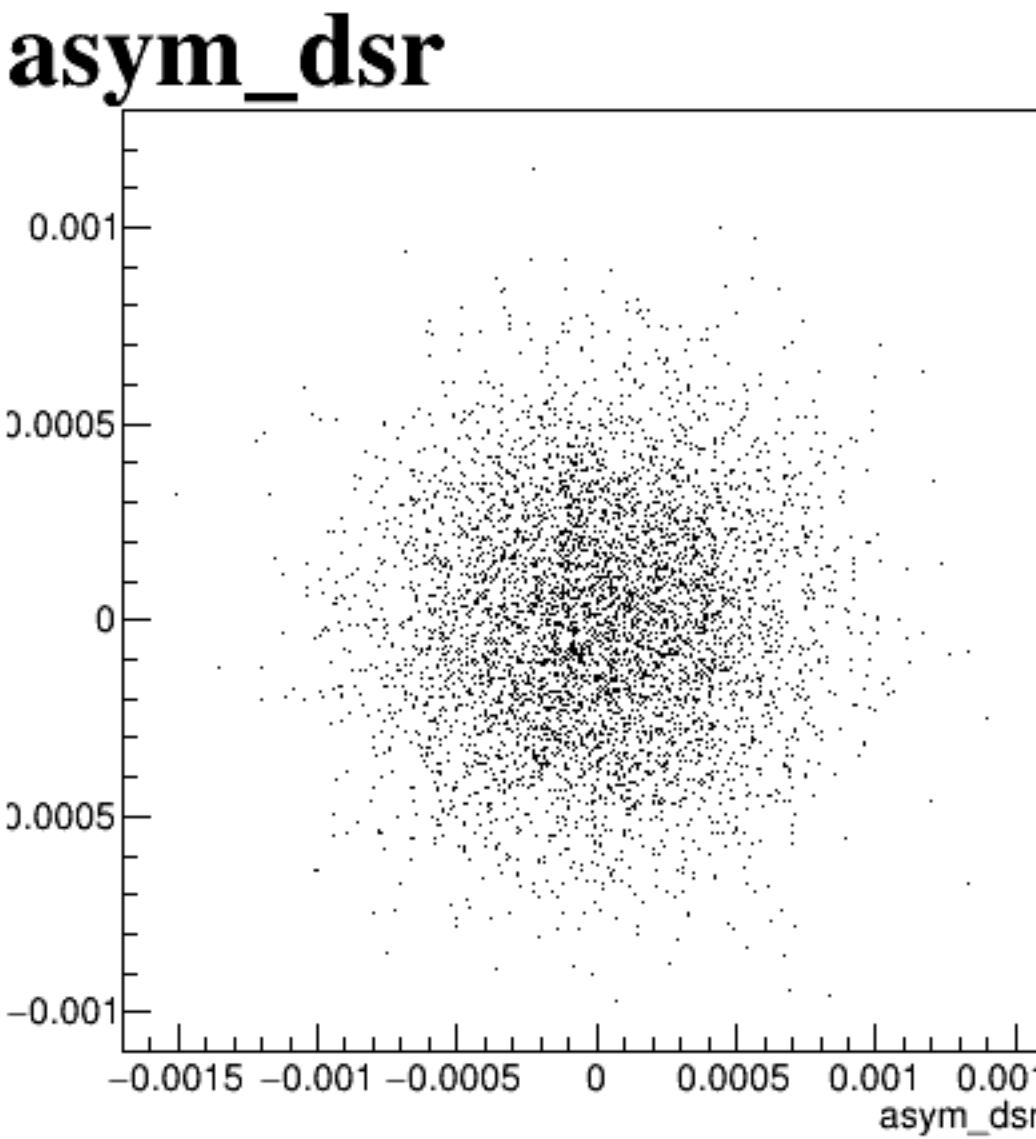
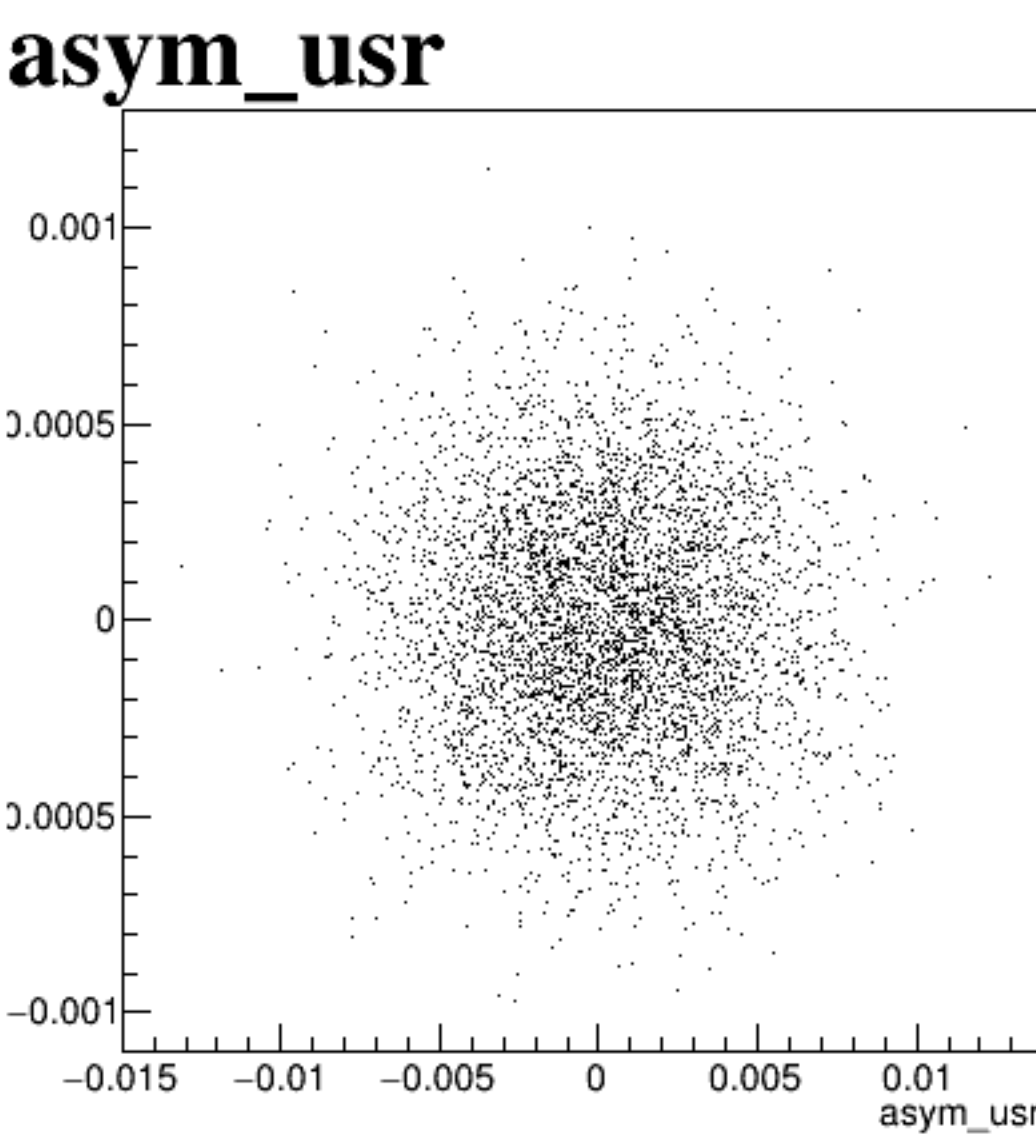
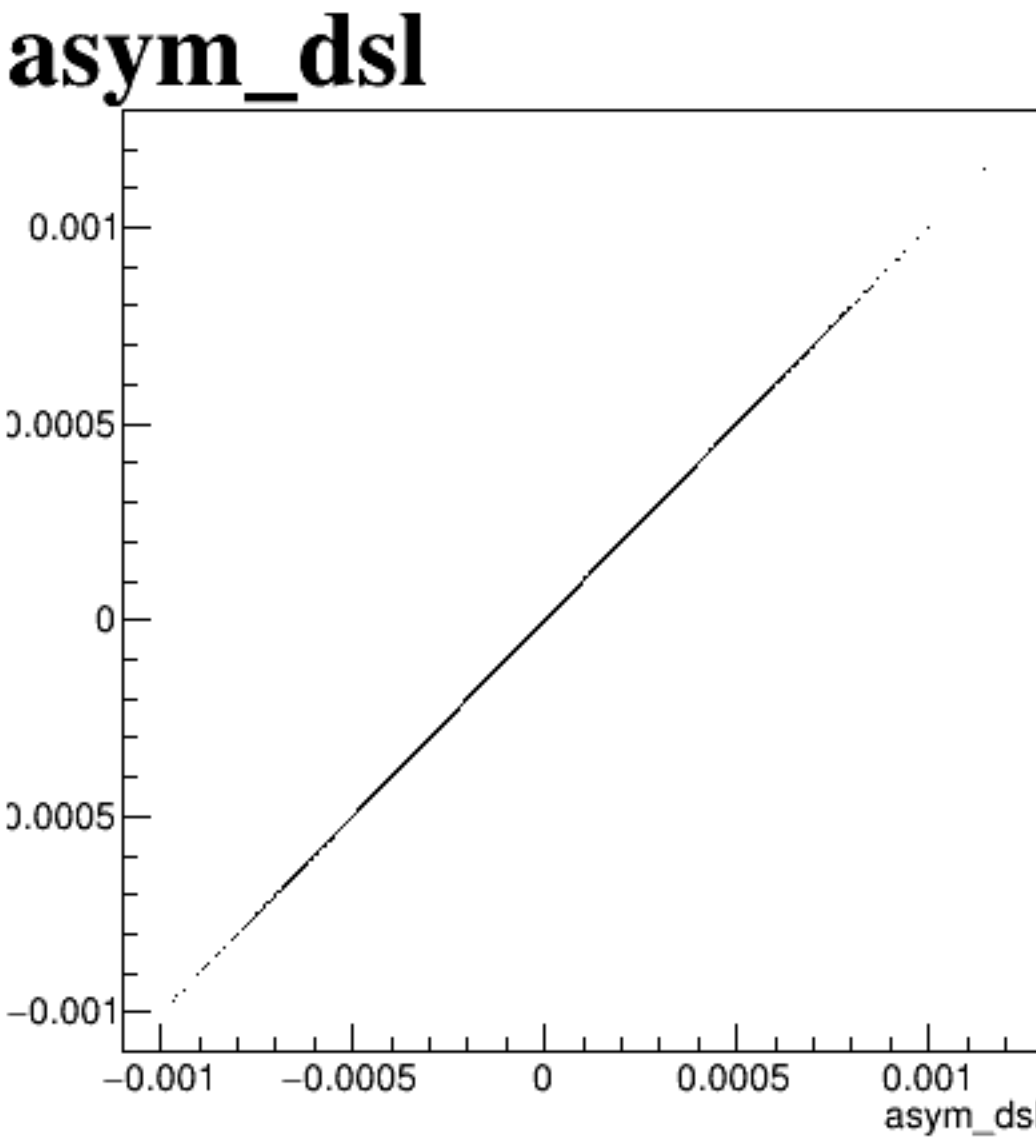
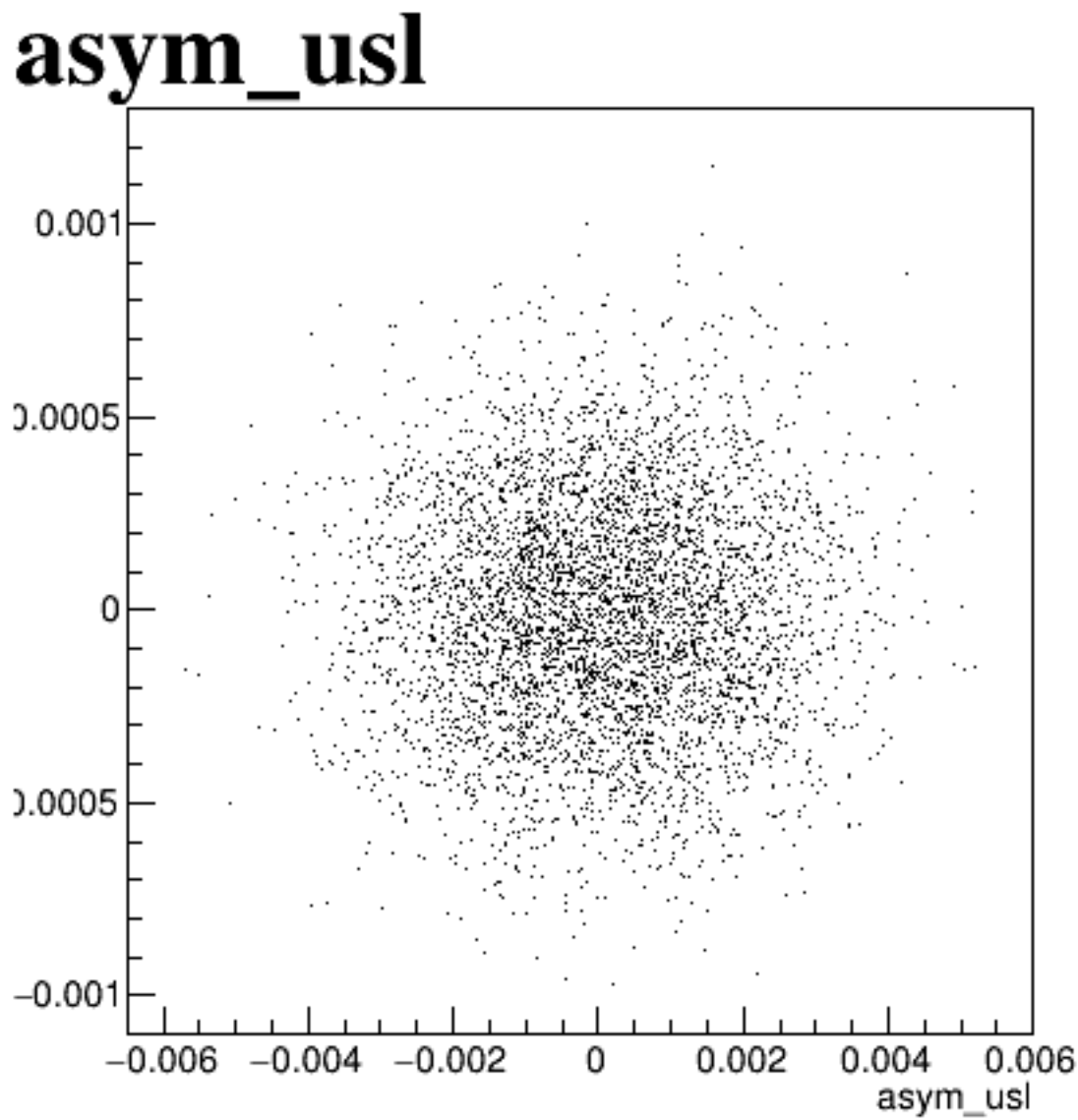
asym_dsr



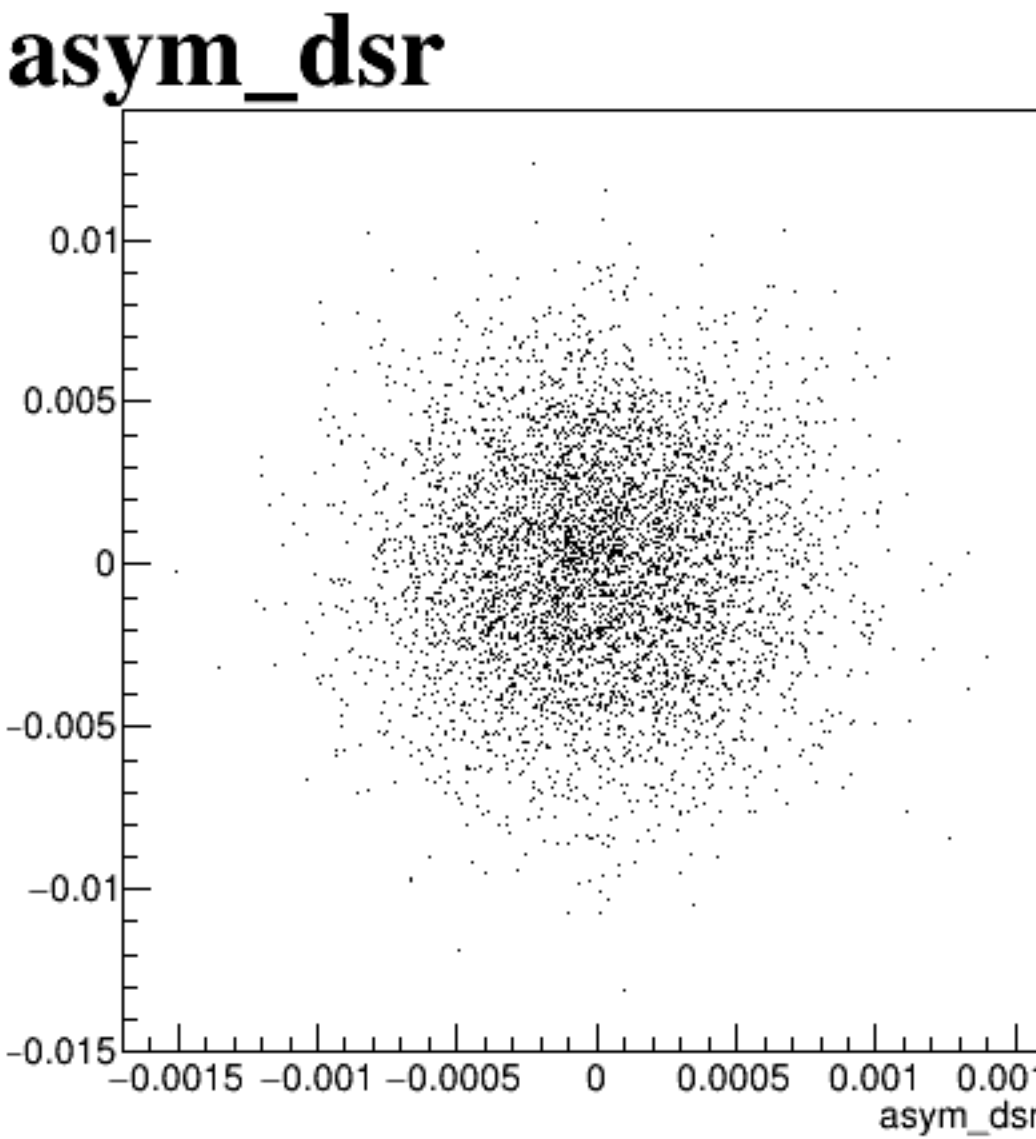
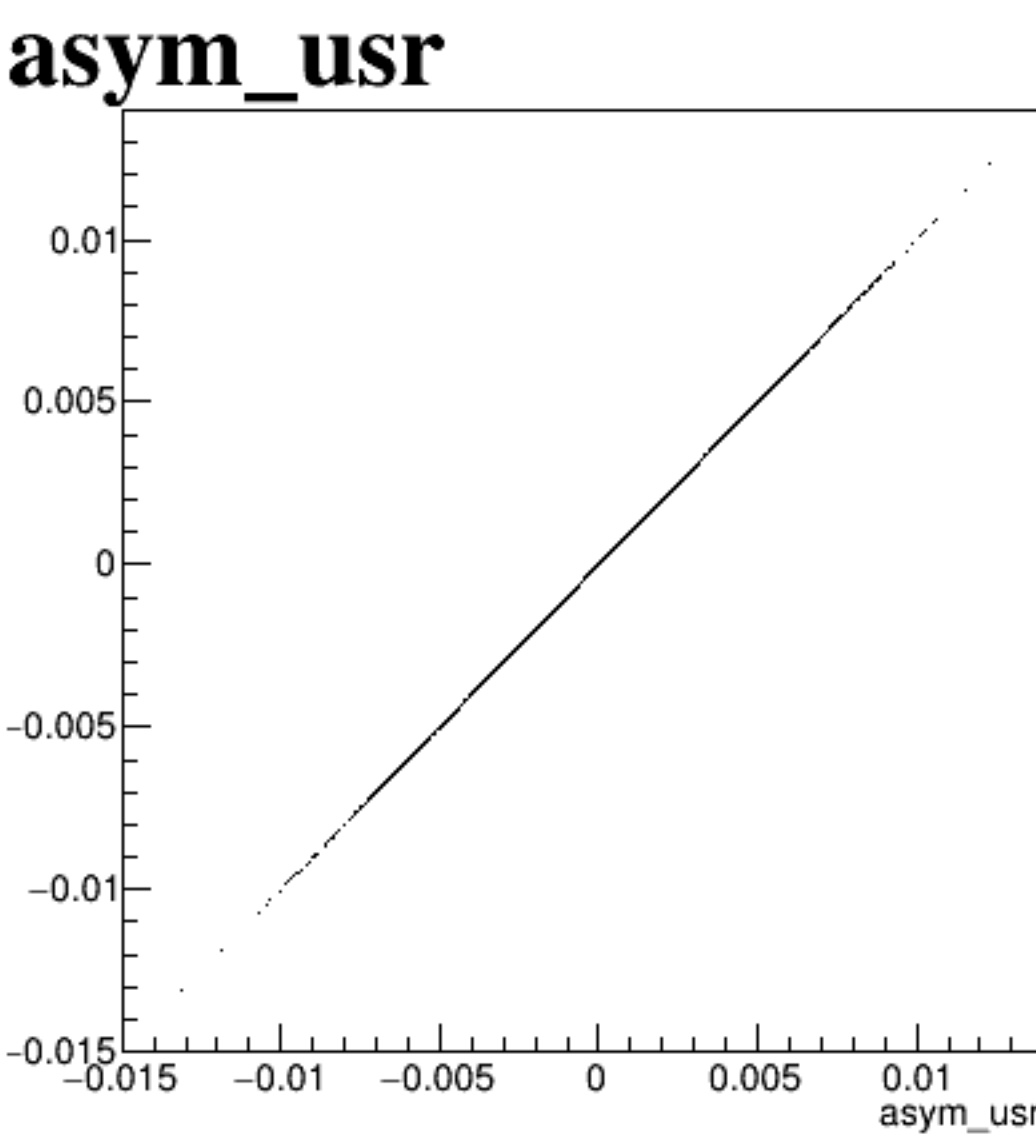
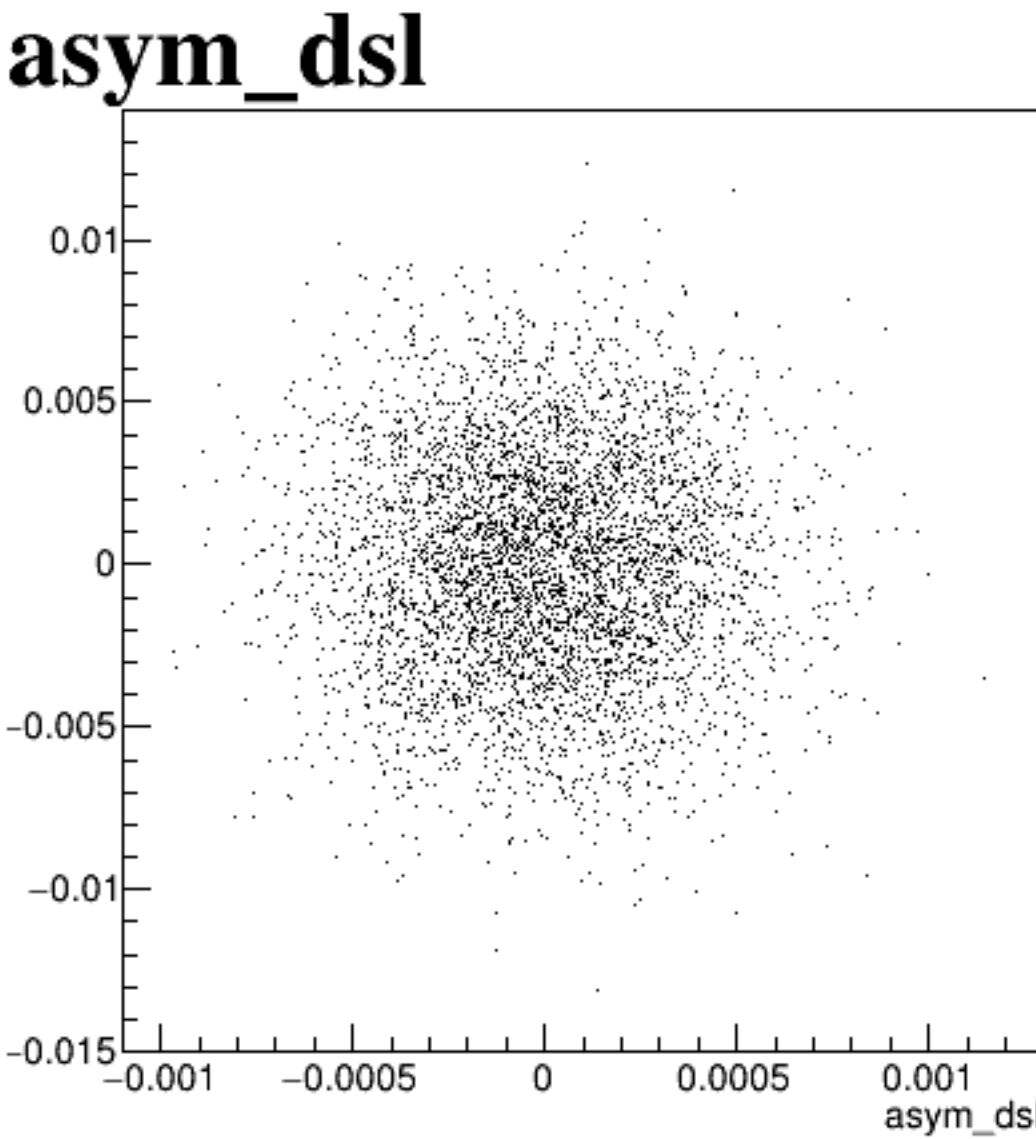
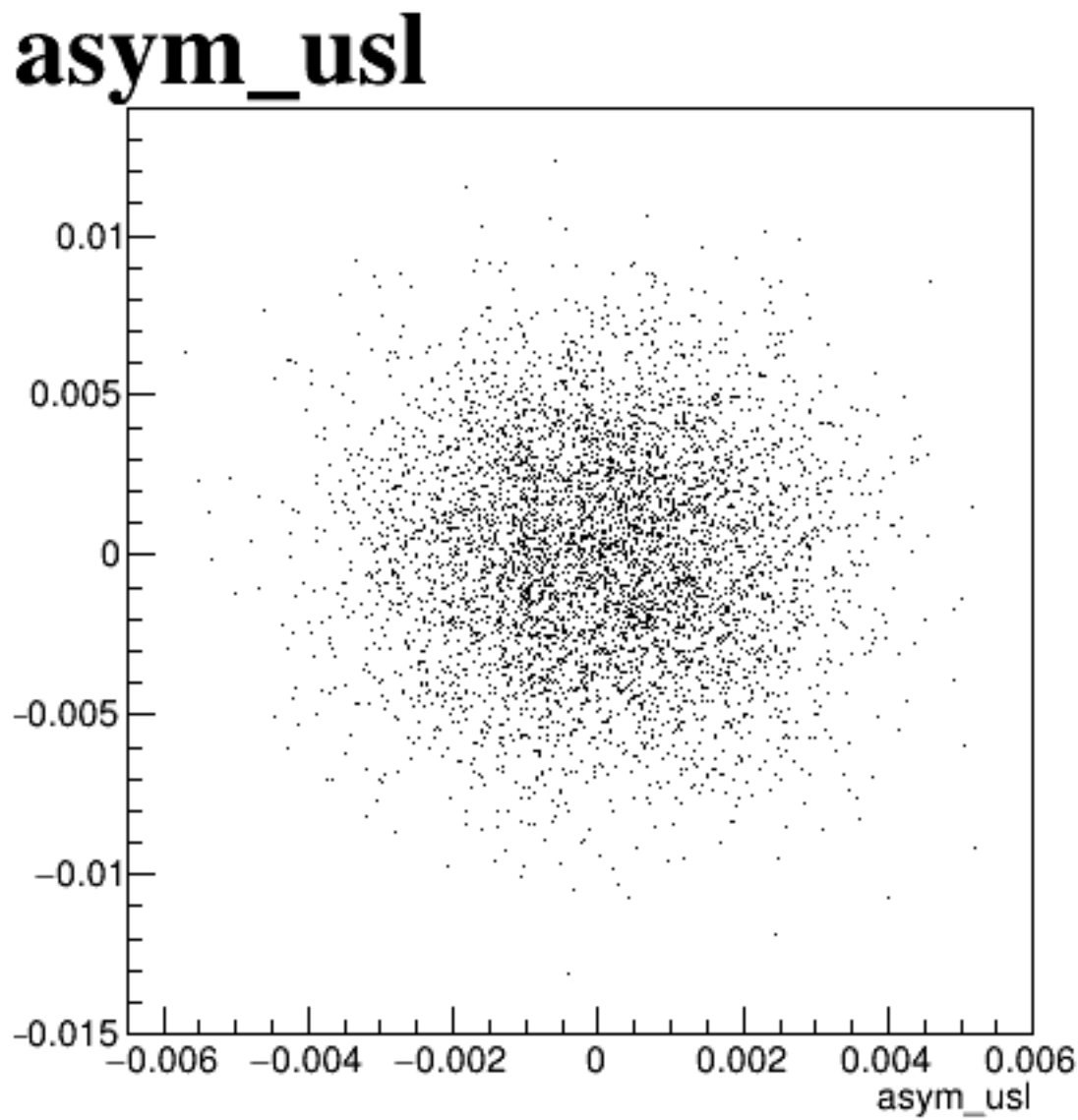
asym_usl



asym_dsl



asym_usr



asym_dsr

