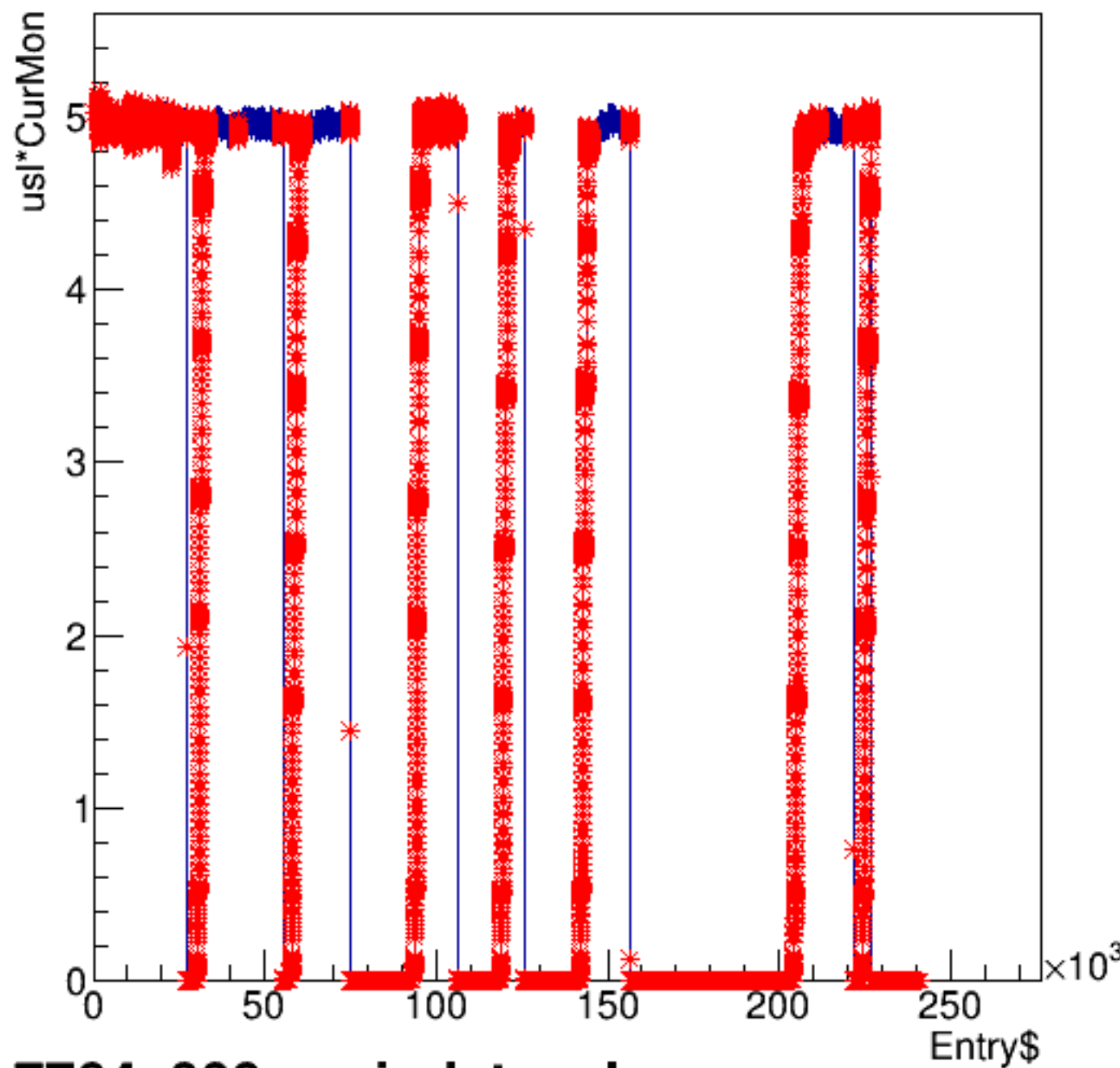
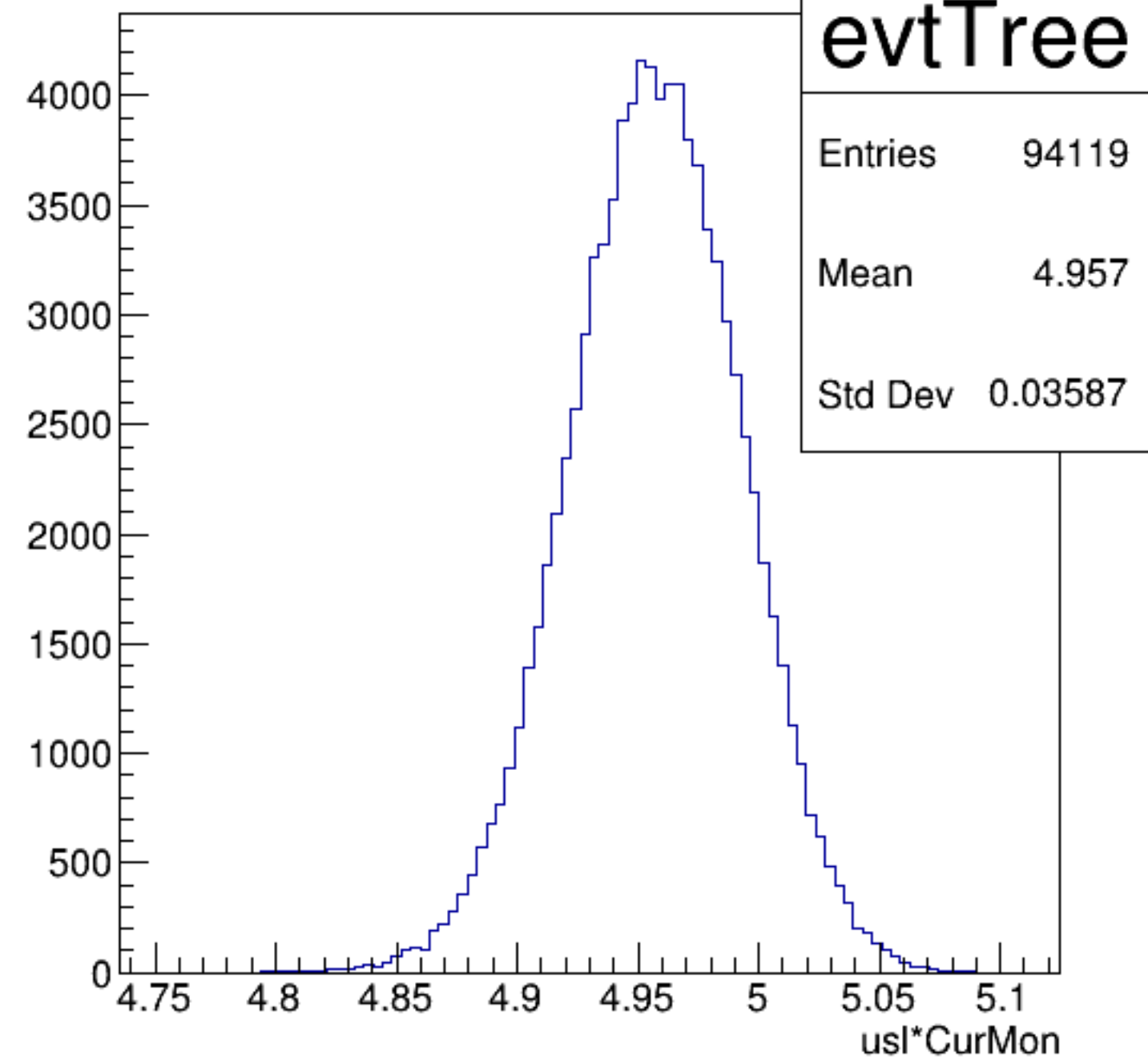


usl*CurMon:Entry\$

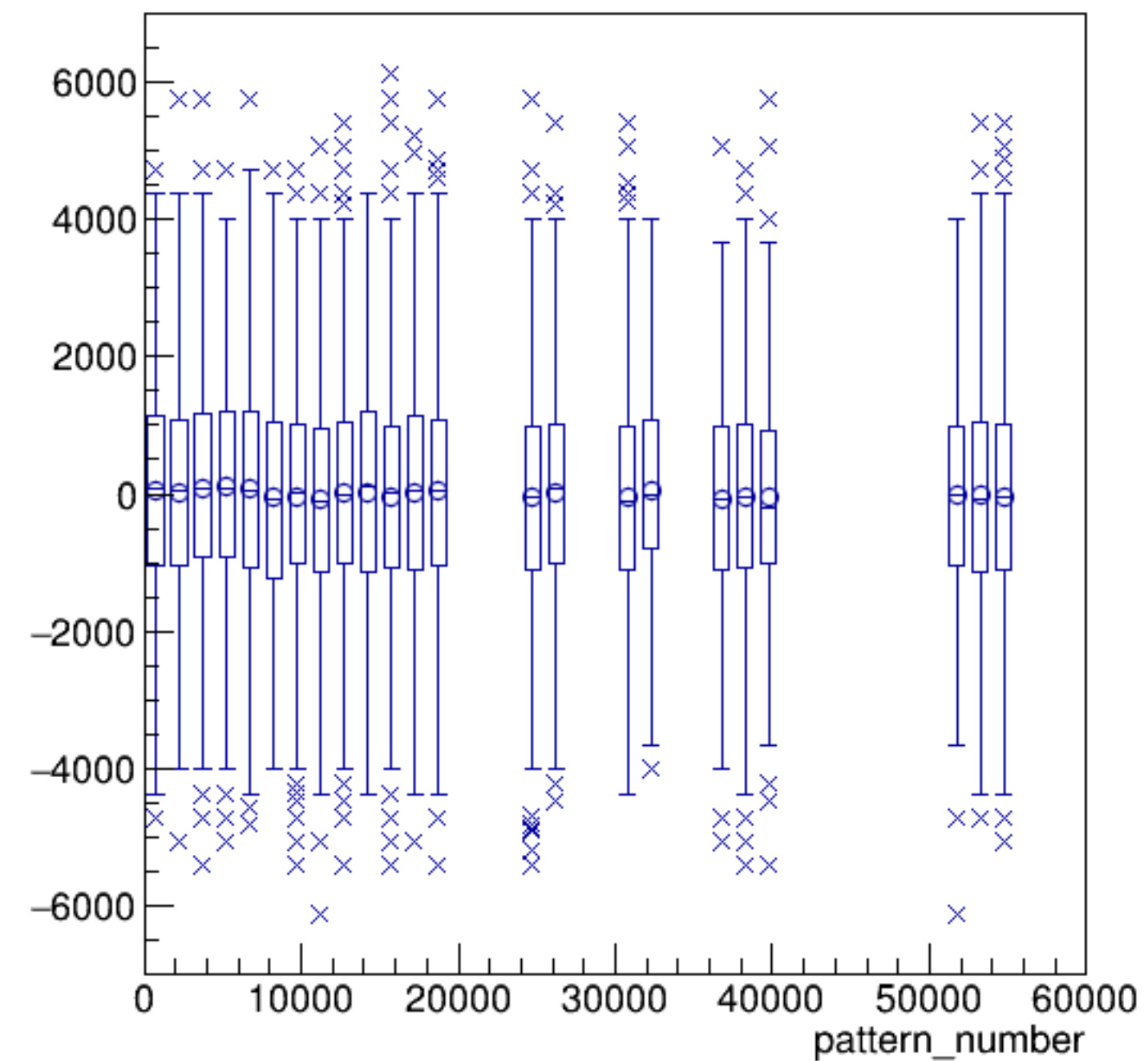


run7764_000_maintet_usl.png

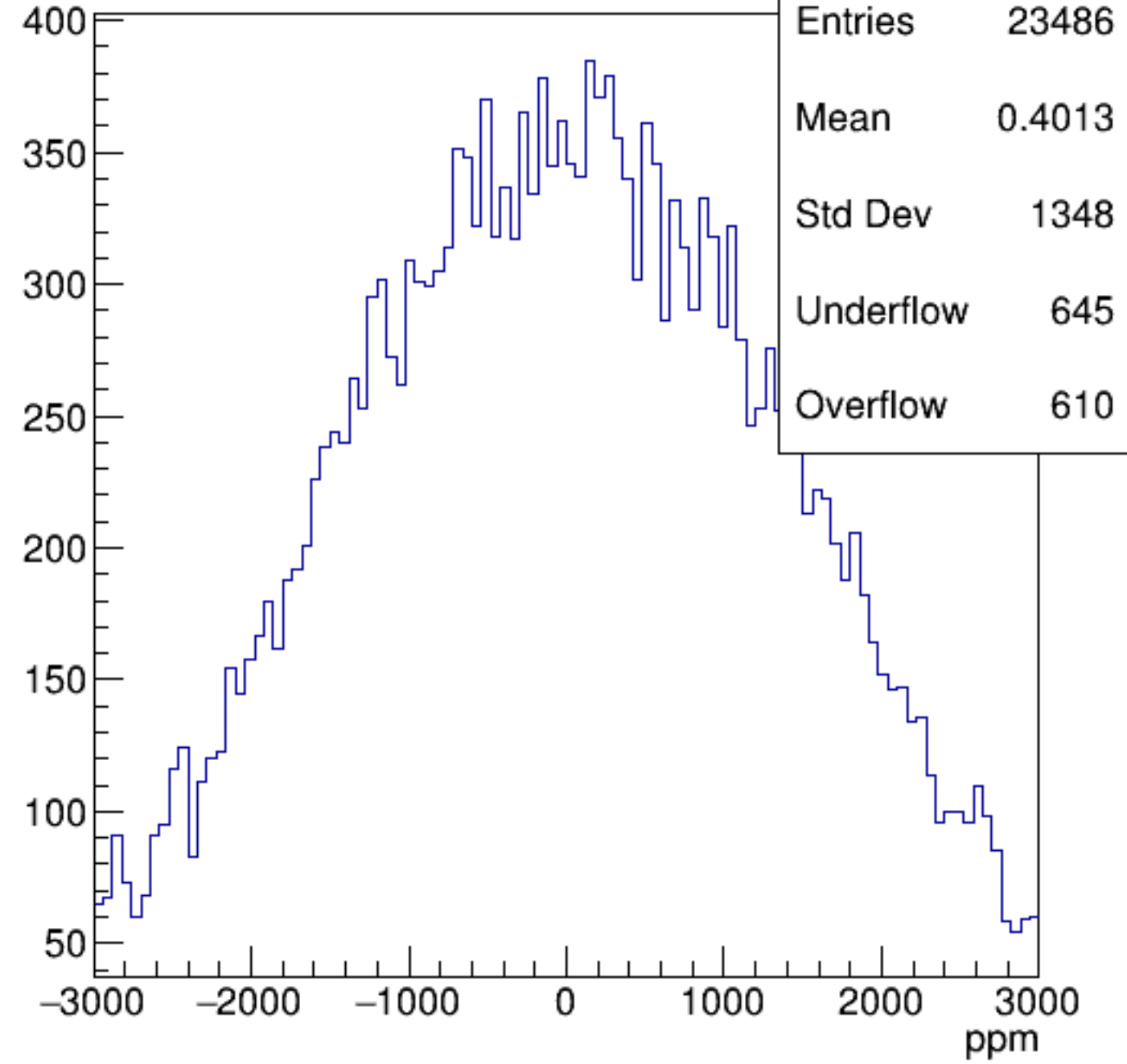
usl*CurMon {ErrorFlag==0}



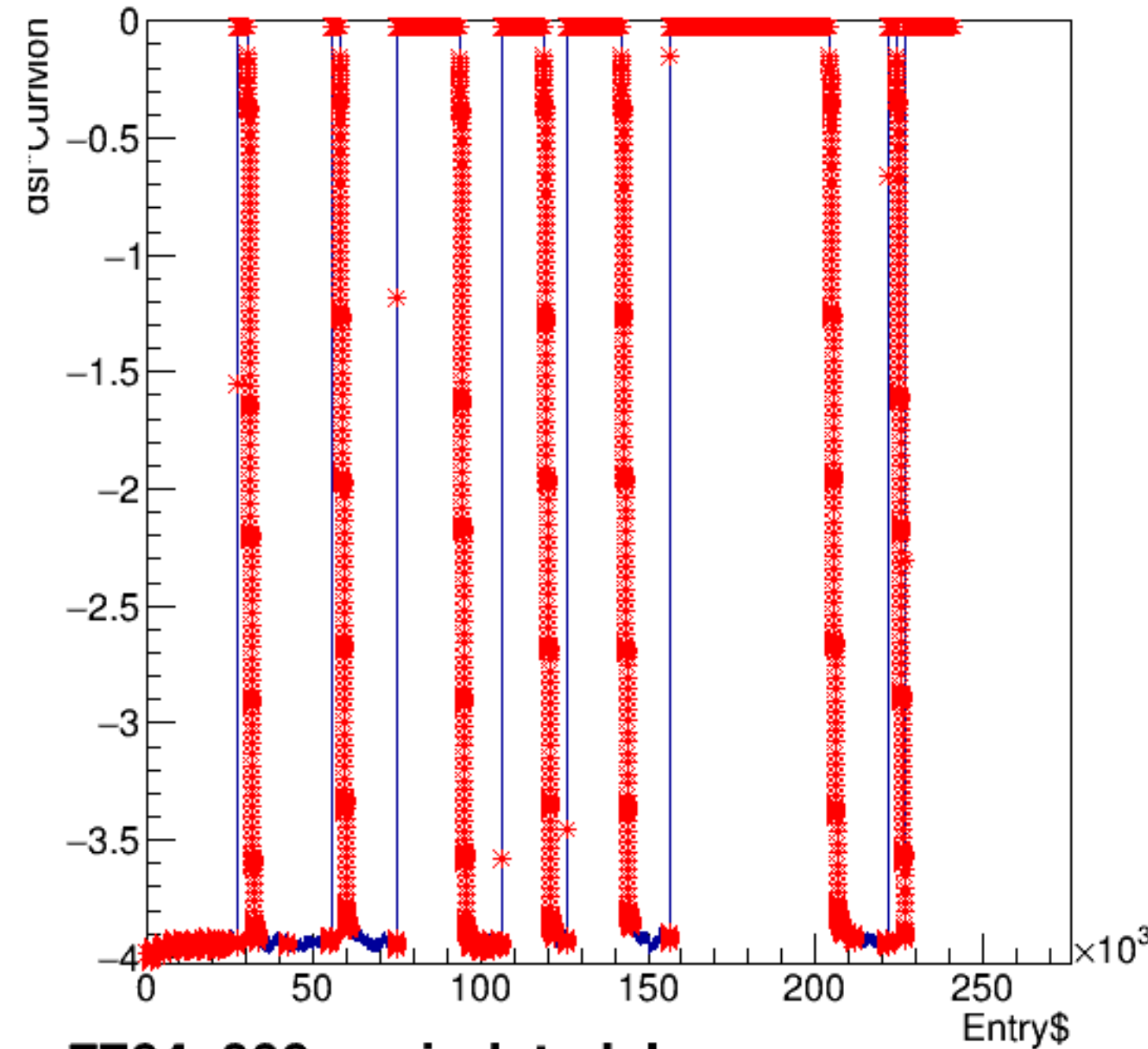
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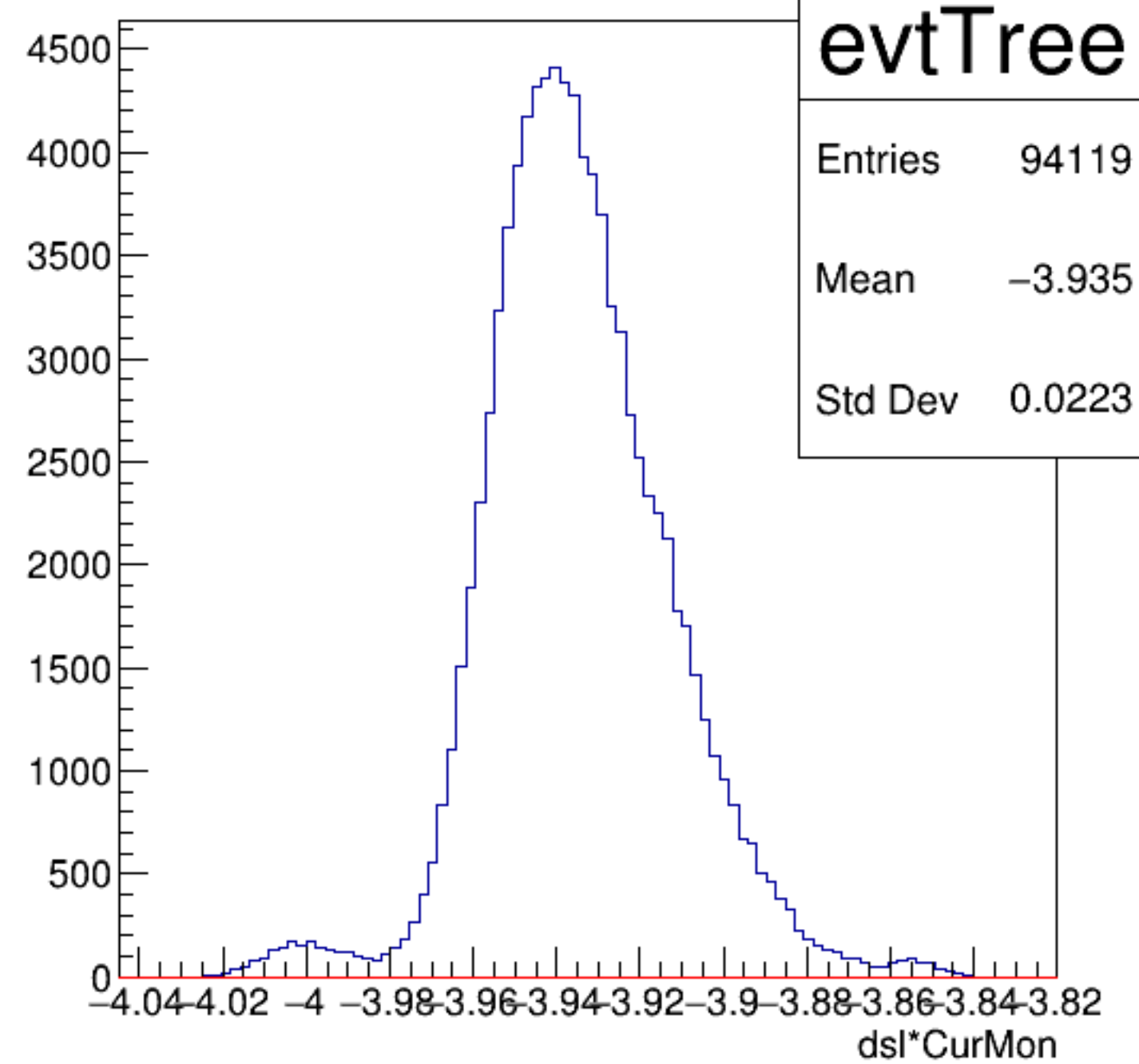
asym_usl



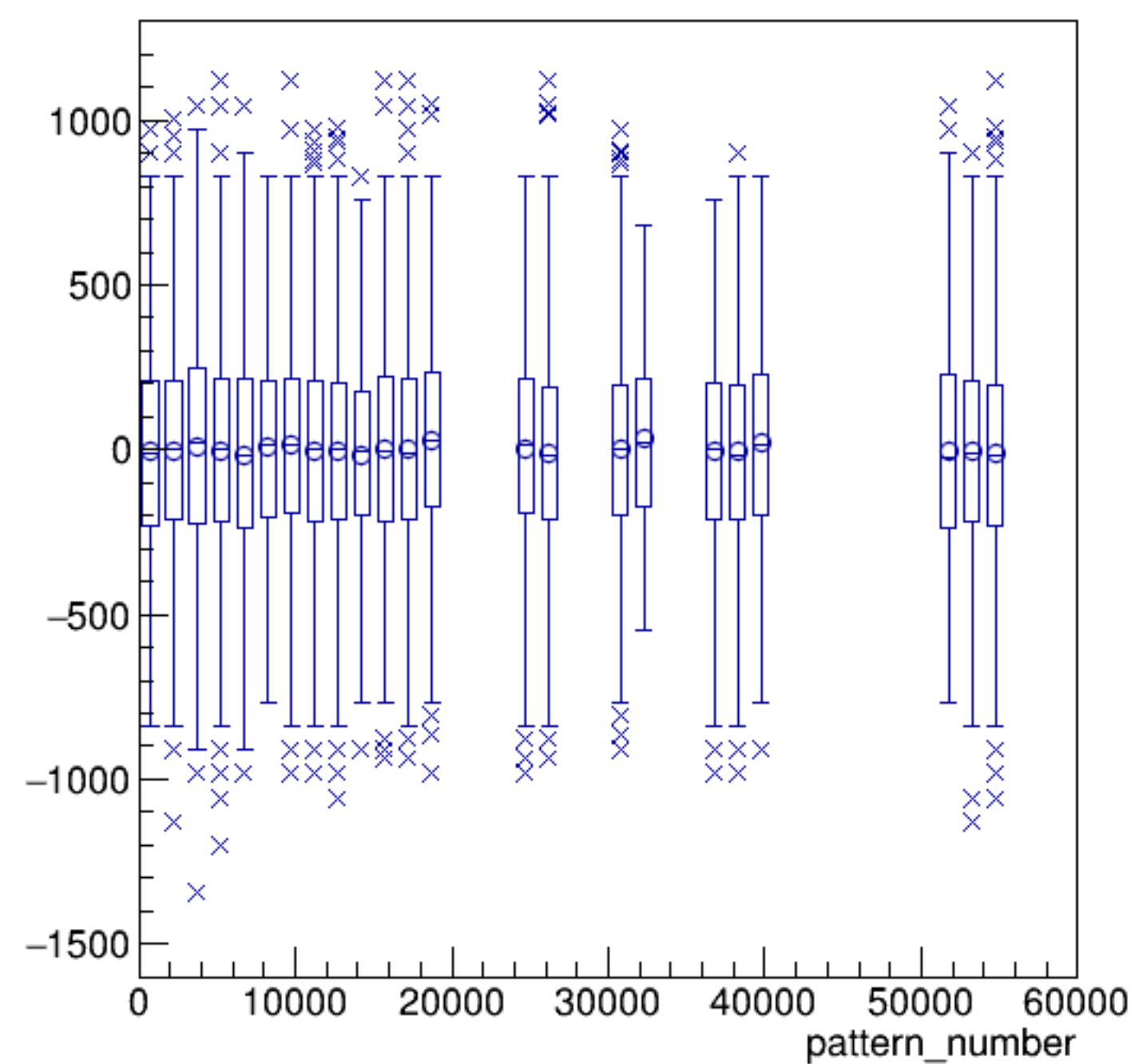
dsl*CurMon:Entry\$



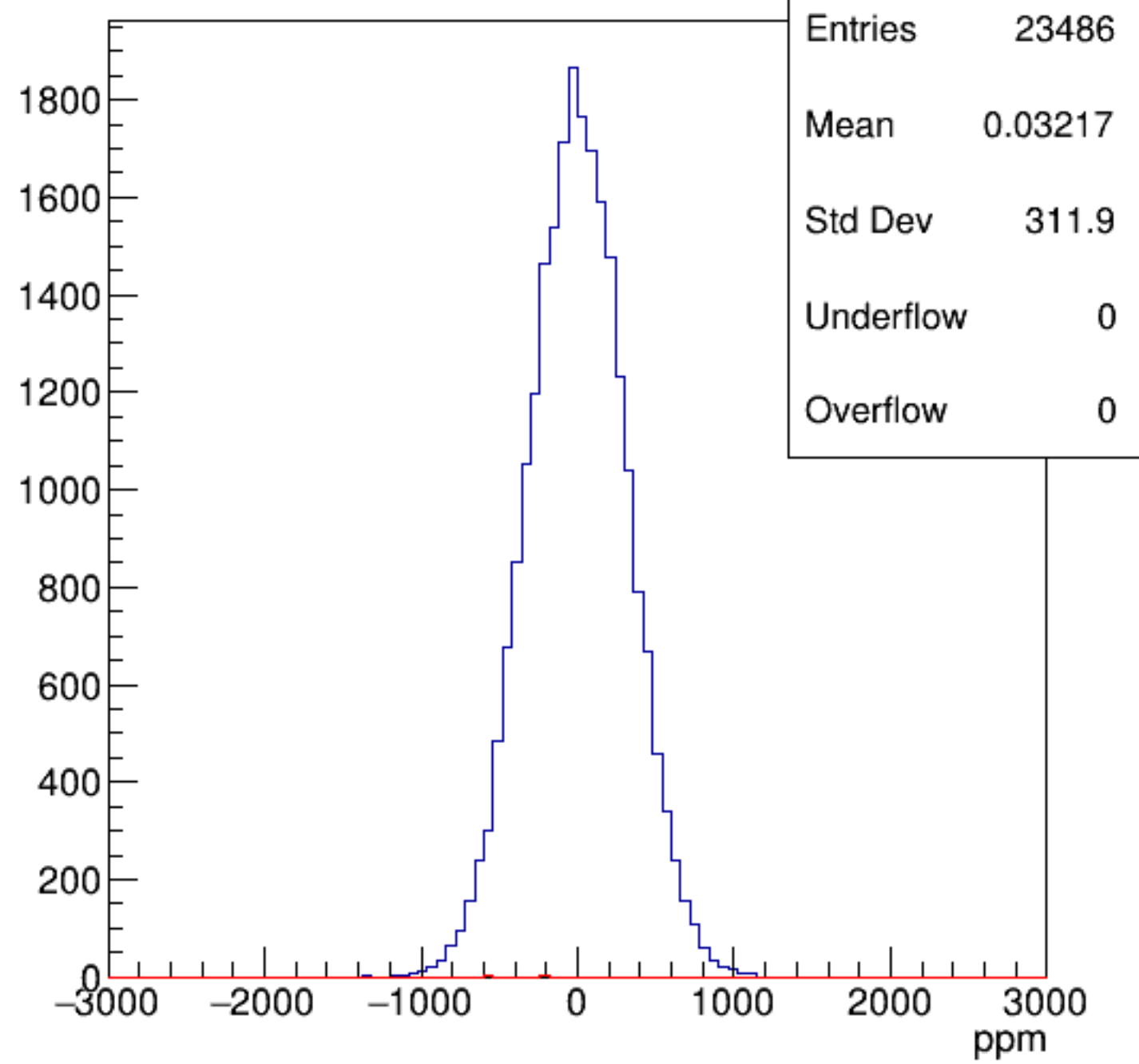
dsl*CurMon {ErrorFlag==0}



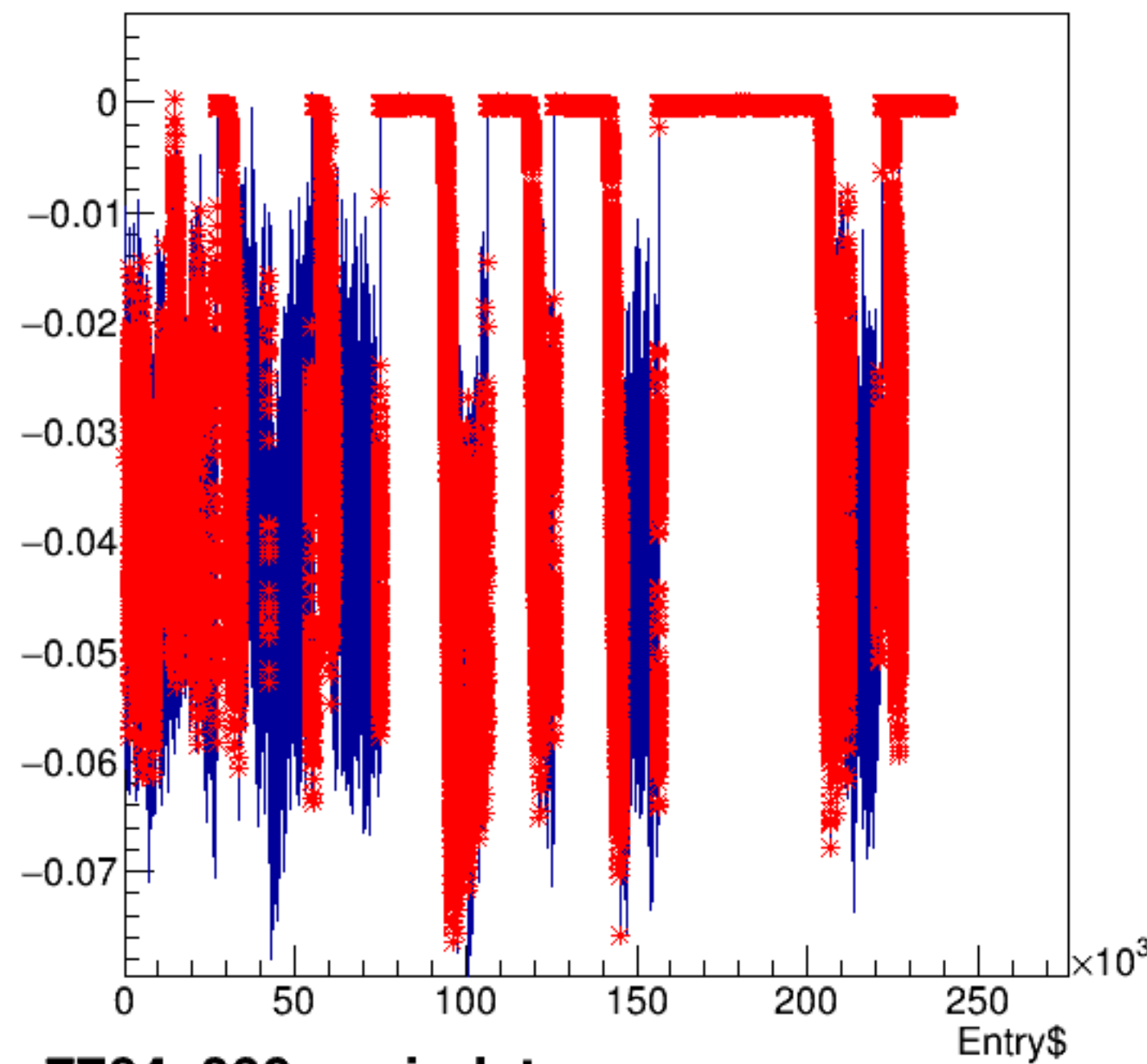
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asym_dsl

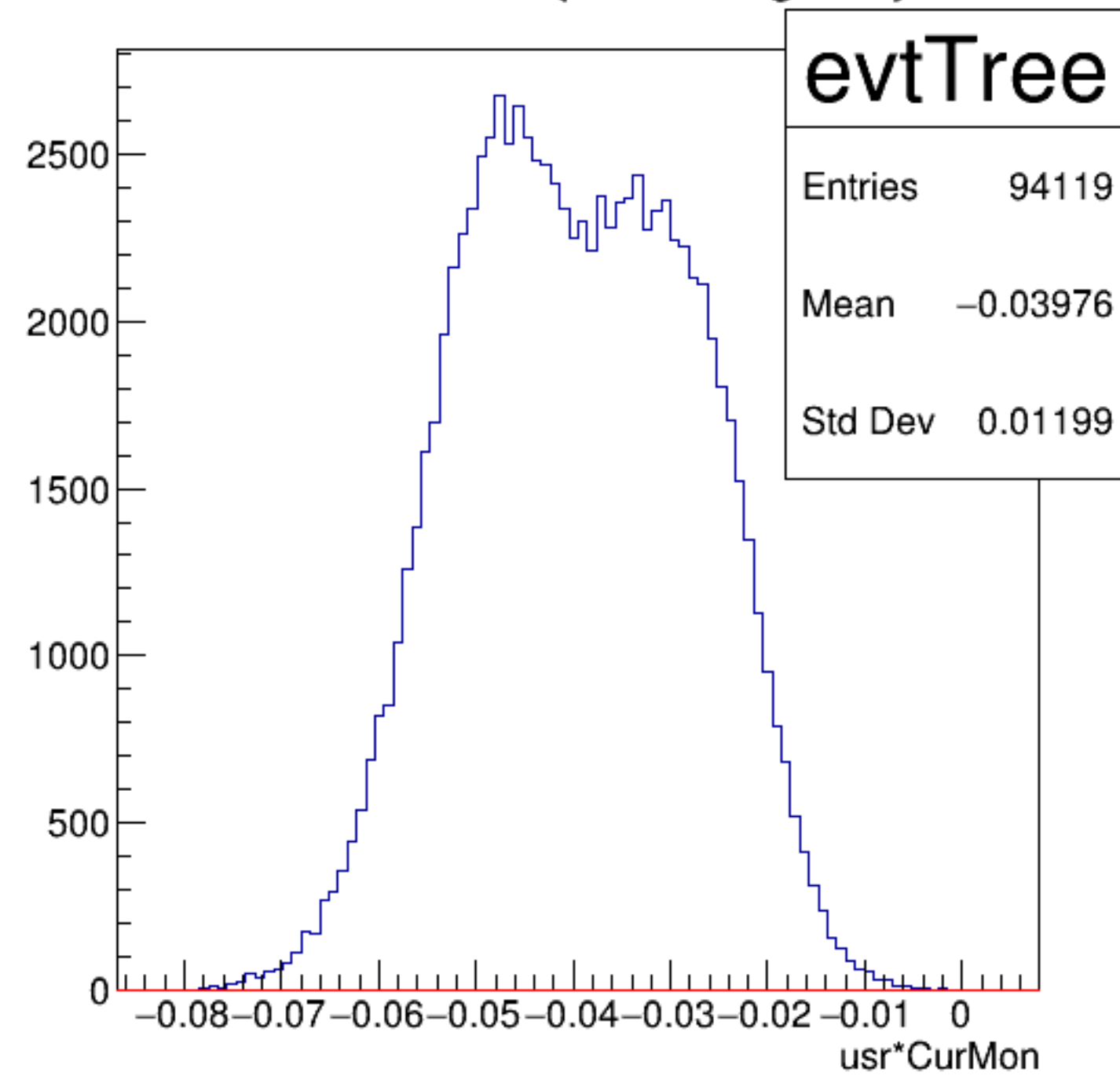


usr*CurMon:Entry\$

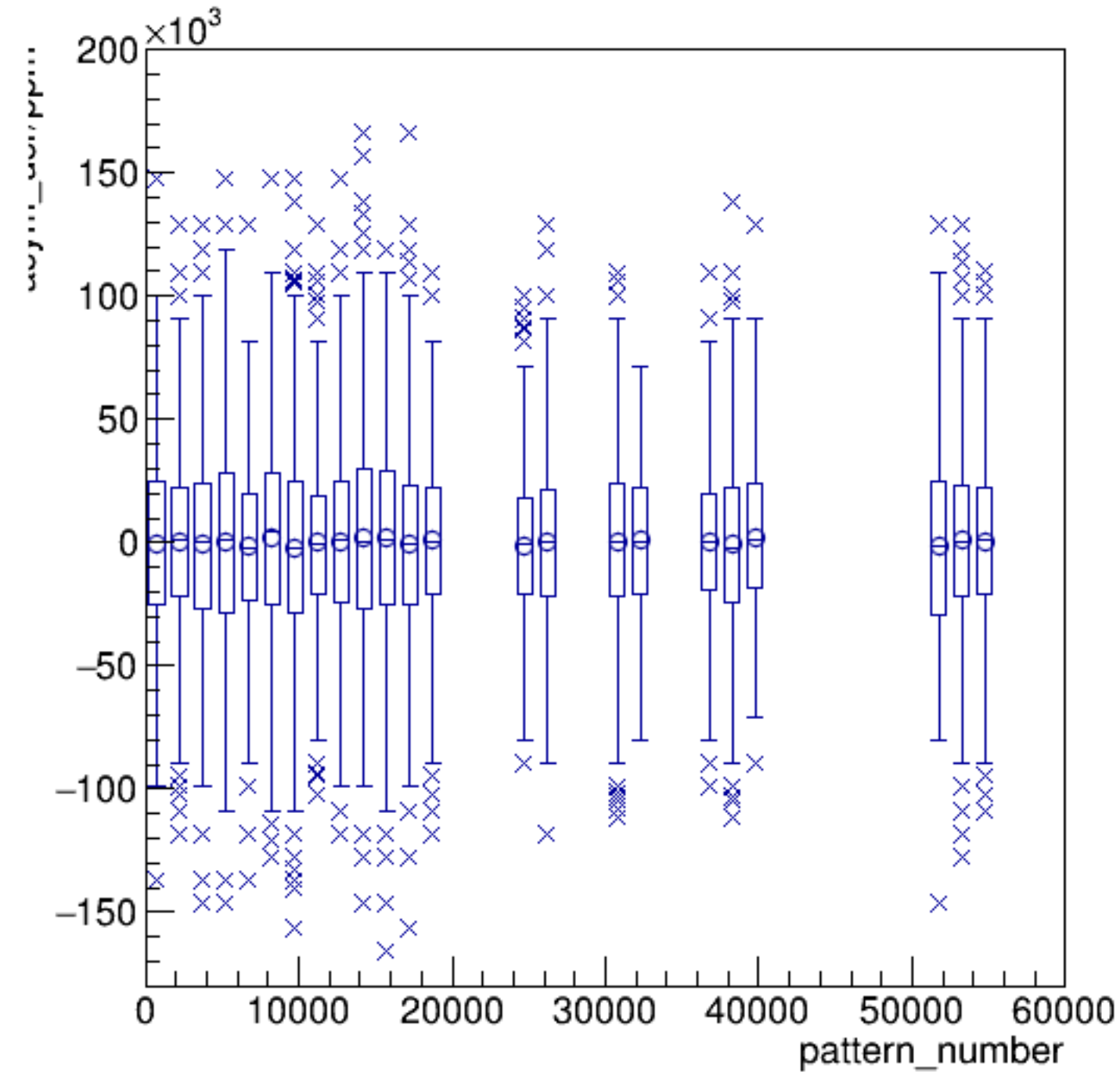


run7764_000_maintet_usr.png

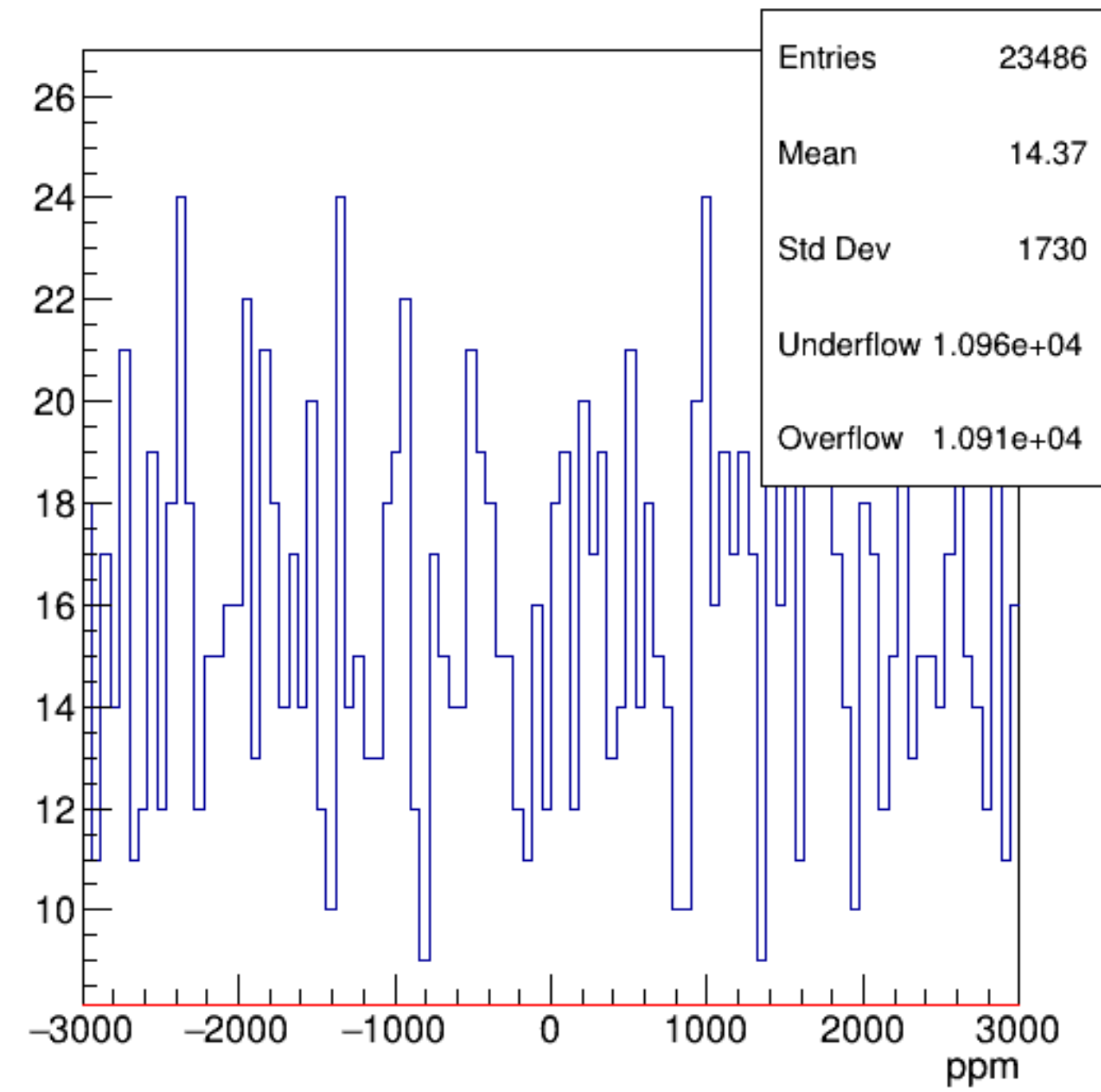
usr*CurMon {ErrorFlag==0}



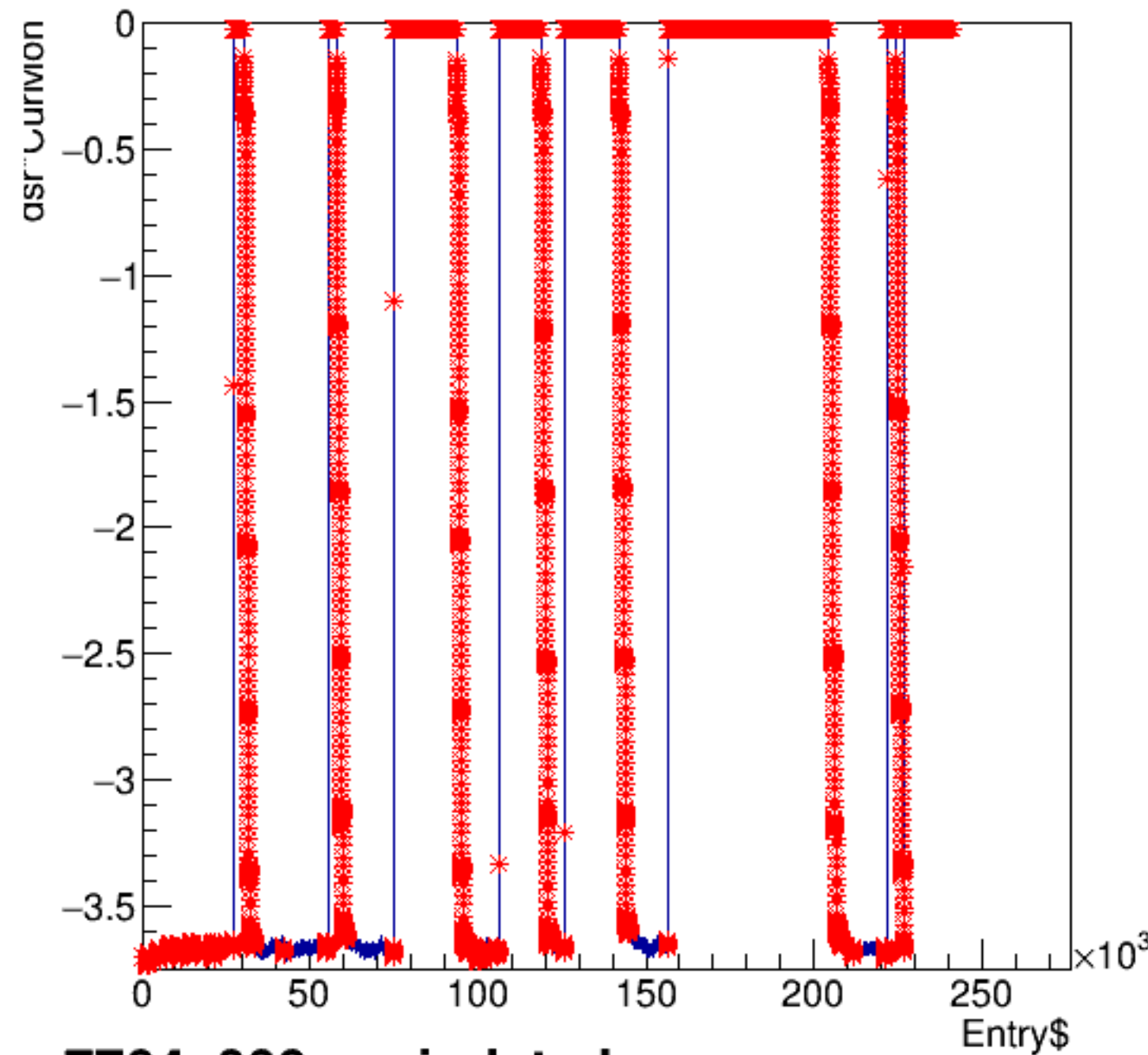
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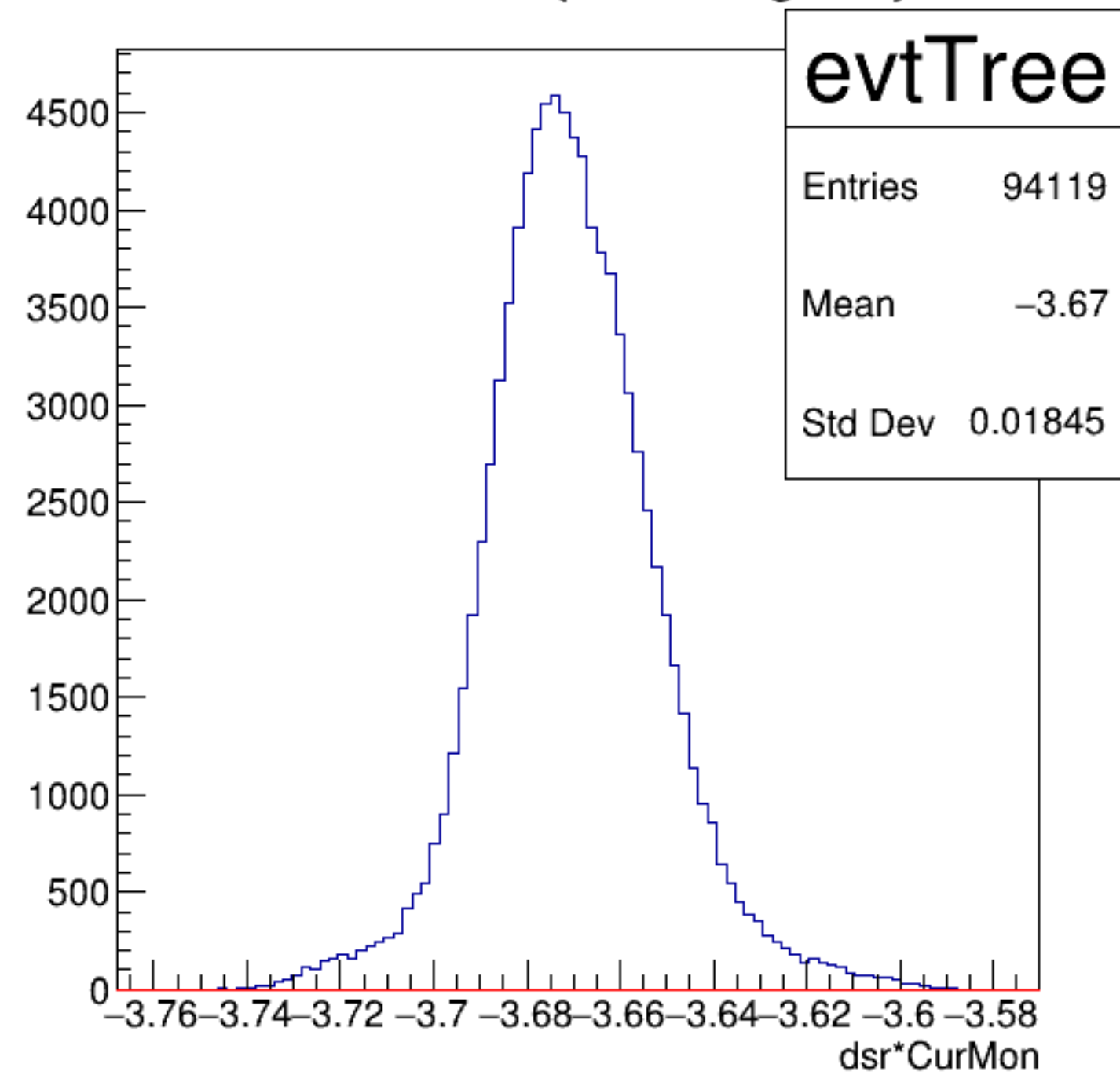
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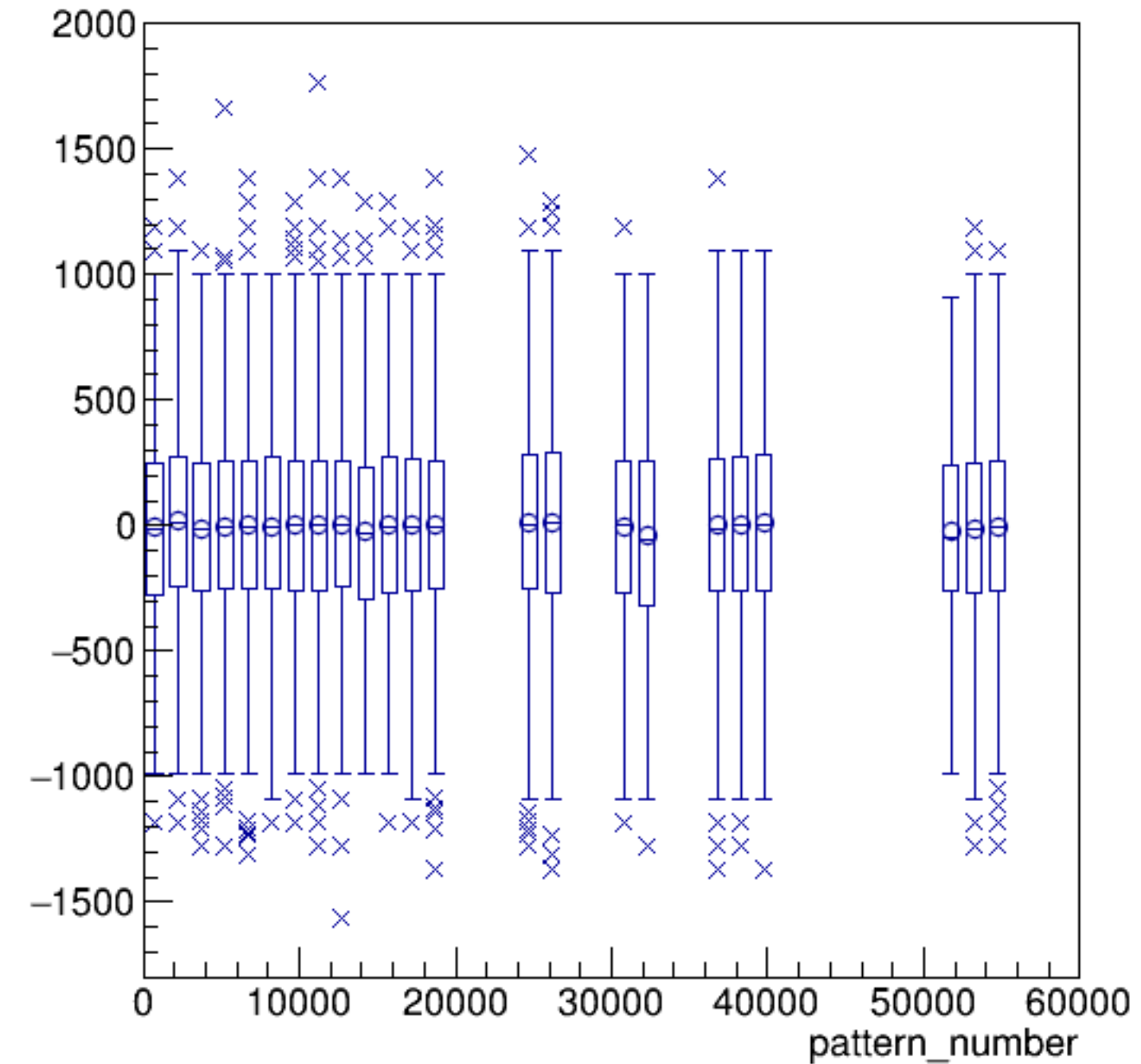
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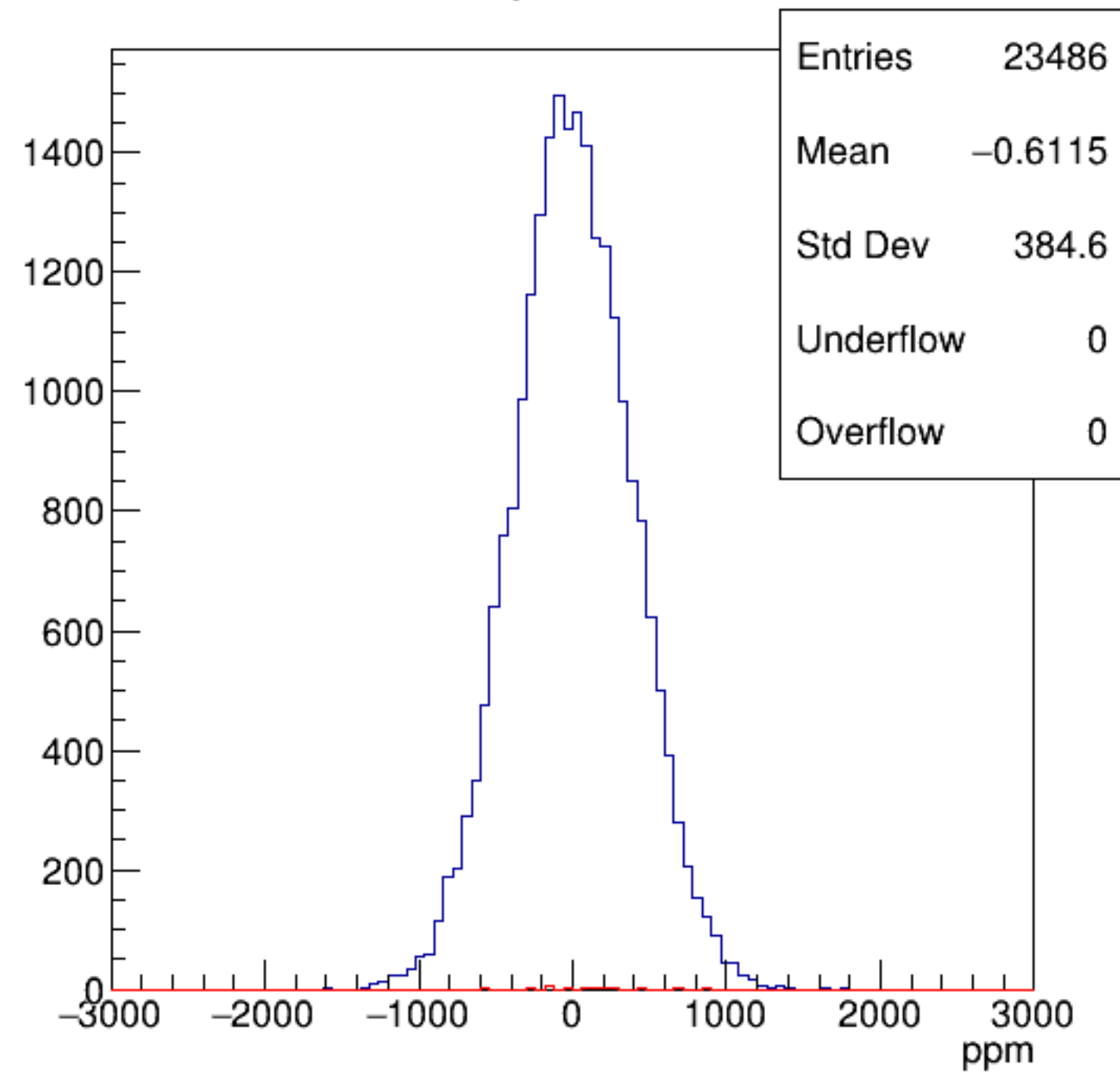
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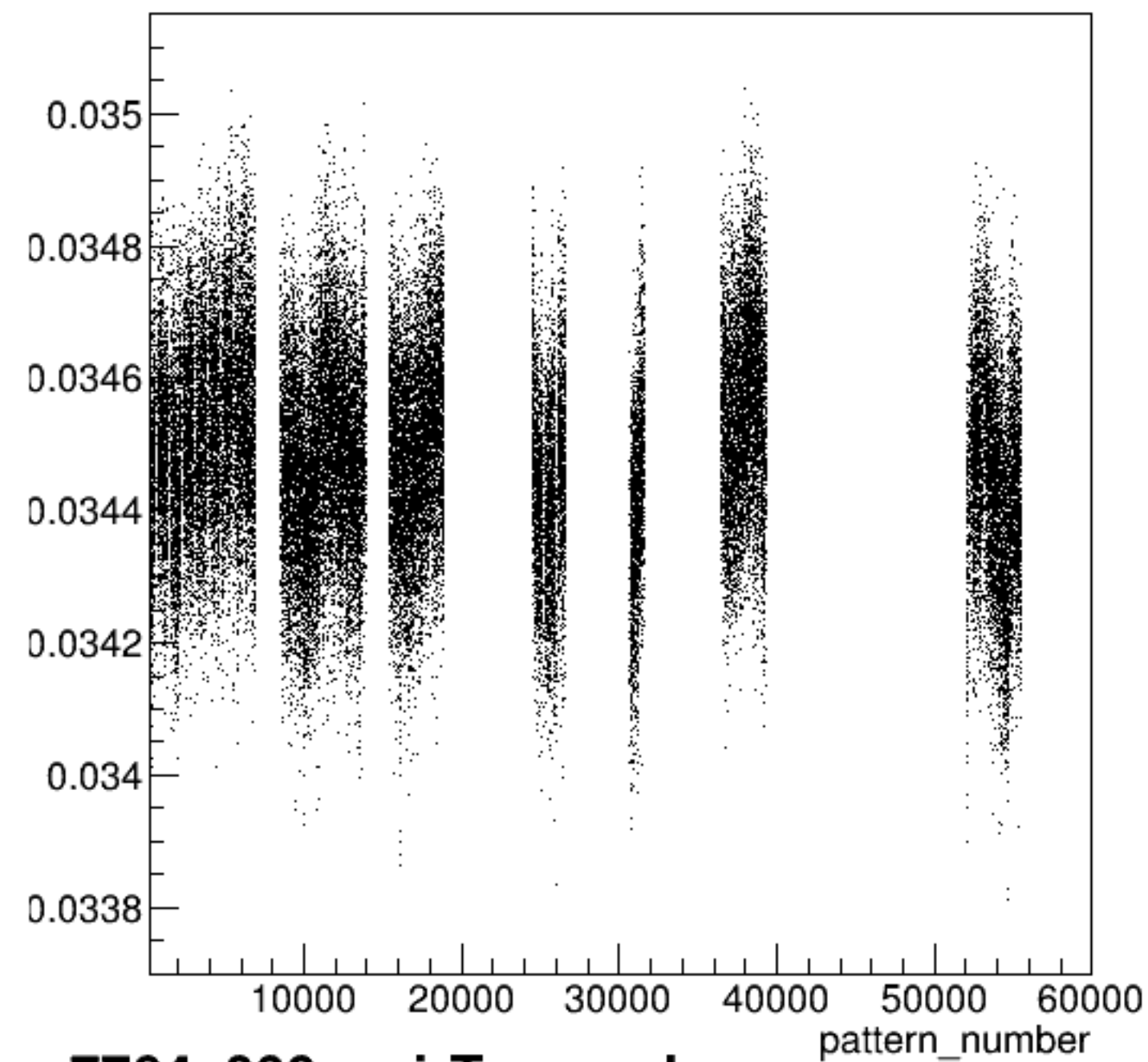
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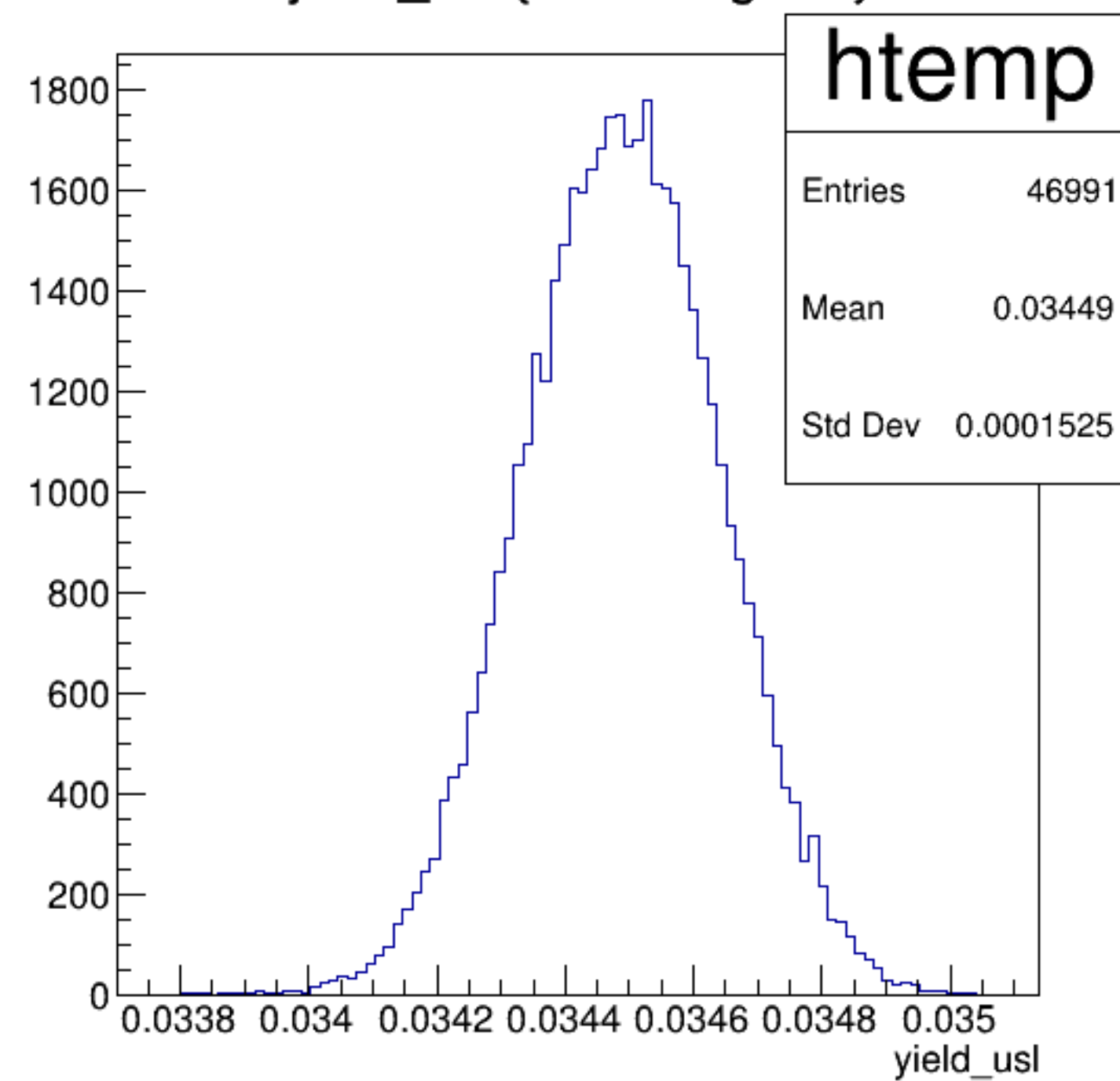
asym_dsr



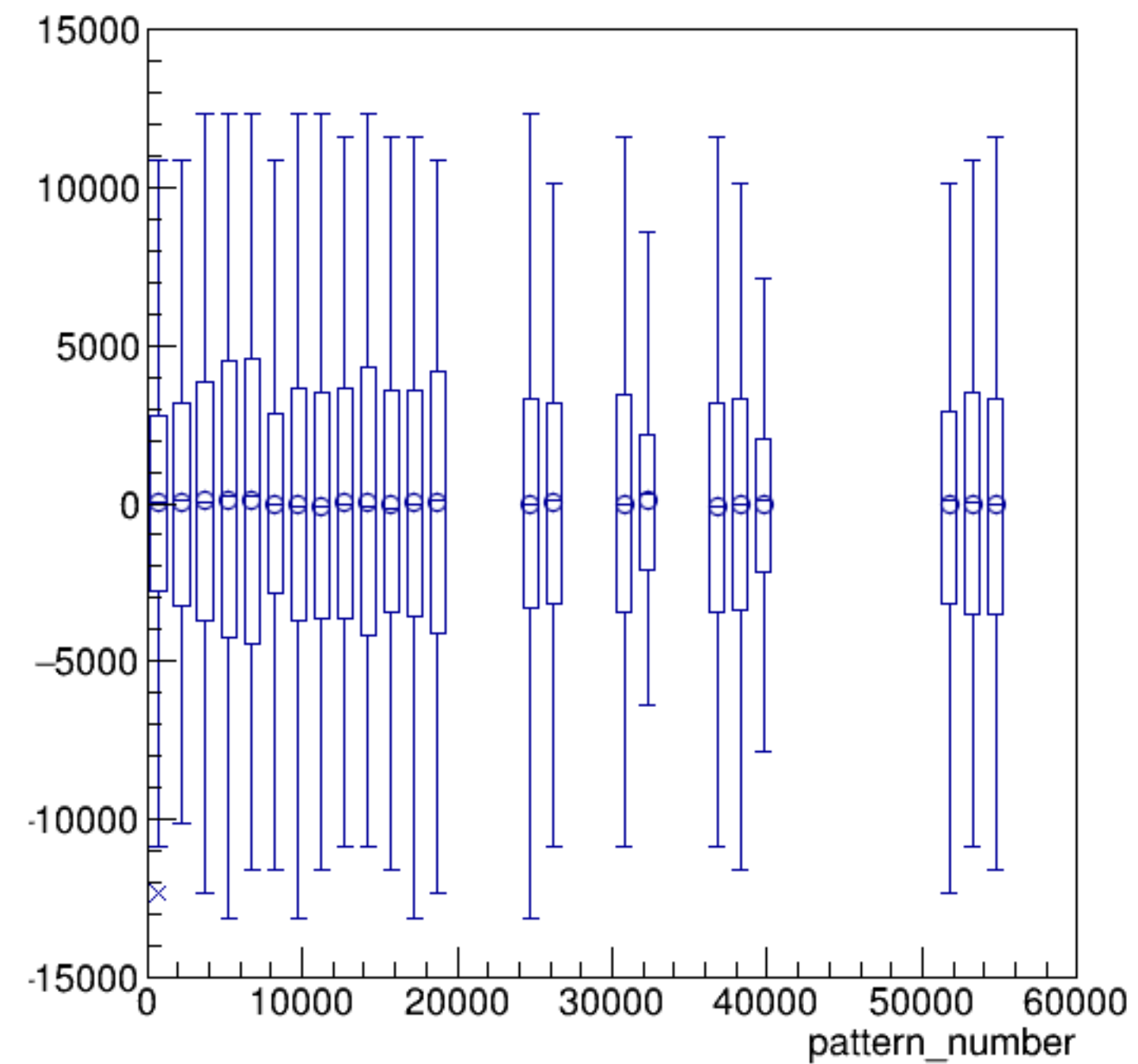
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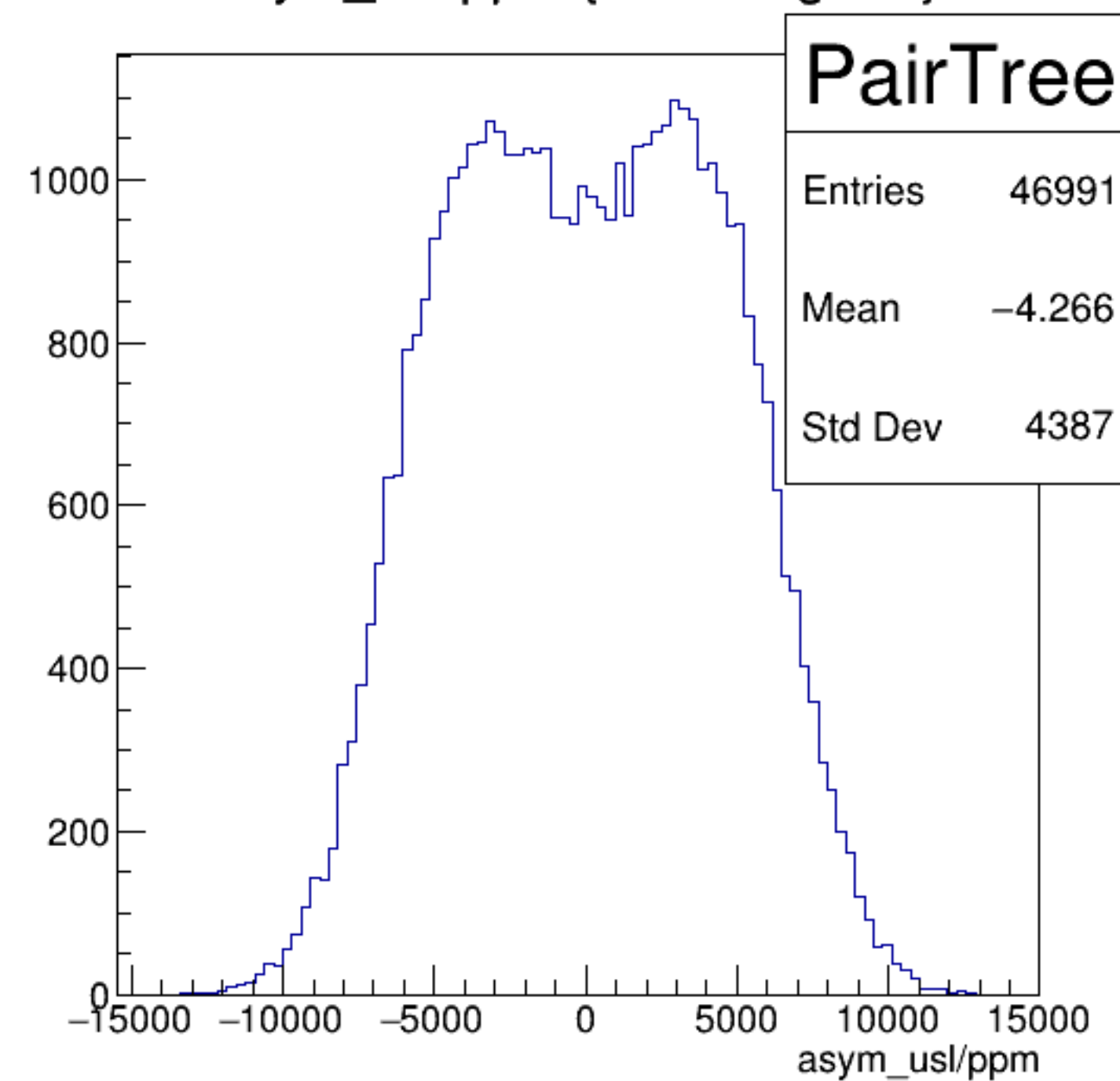
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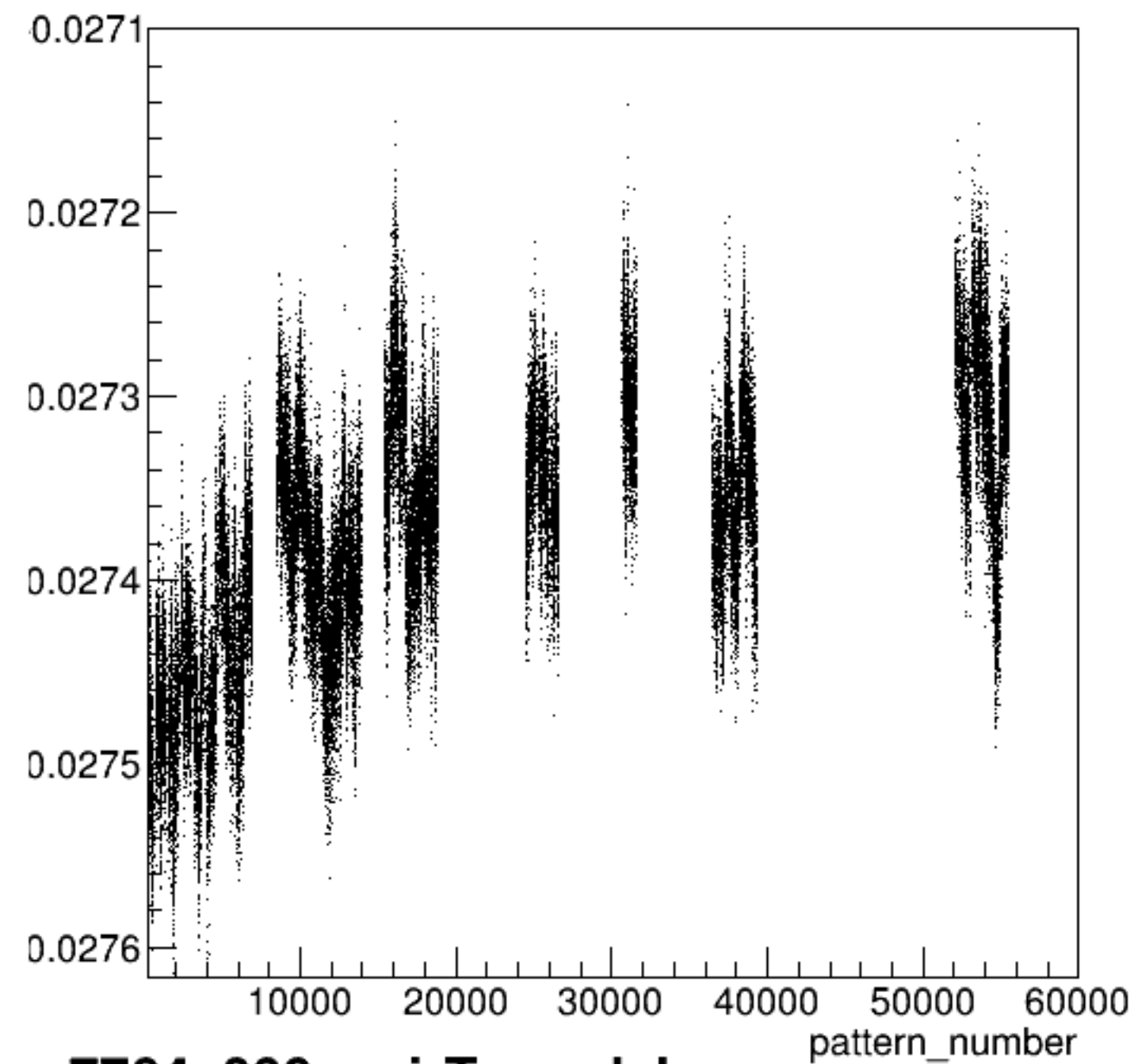
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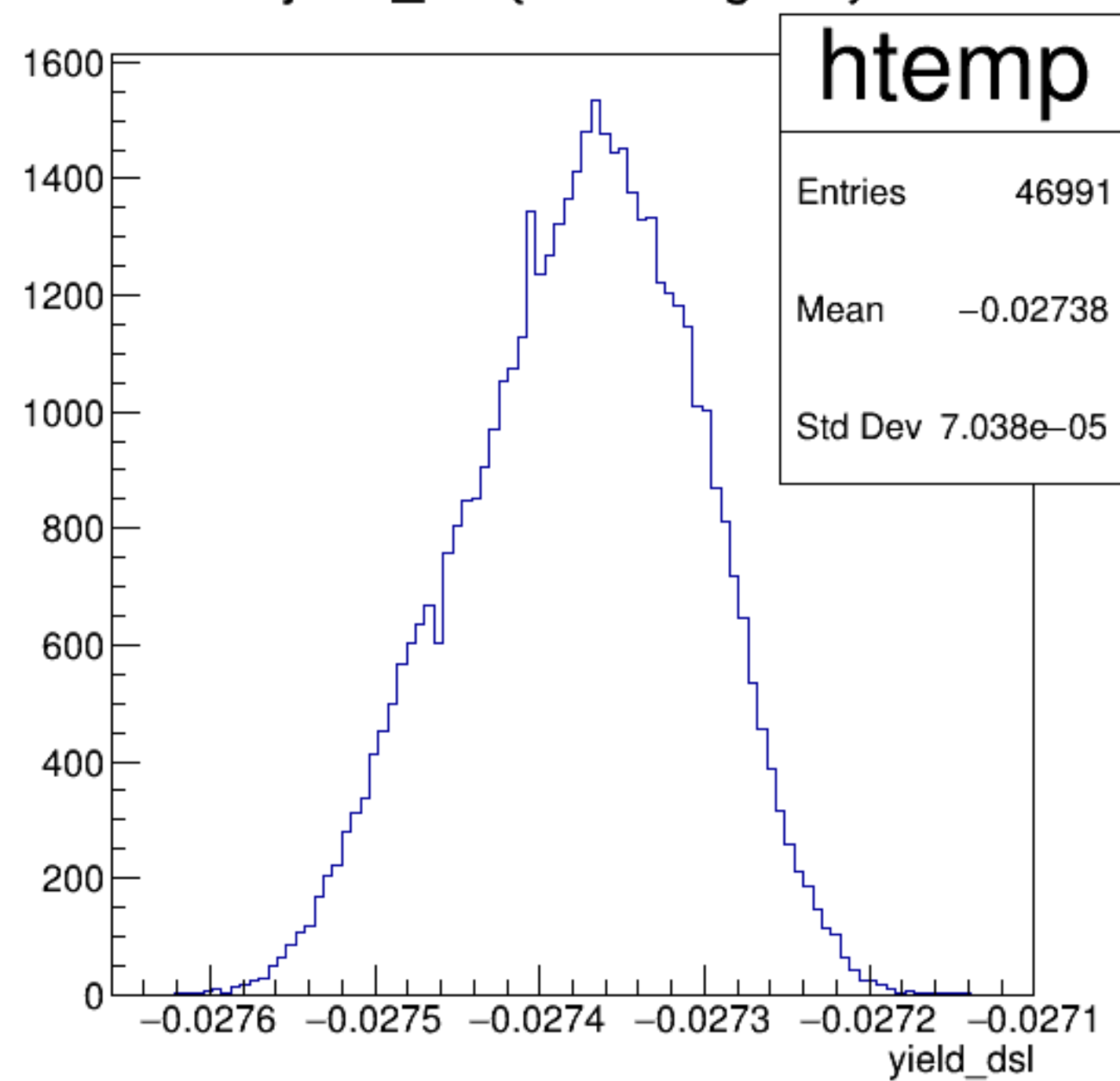
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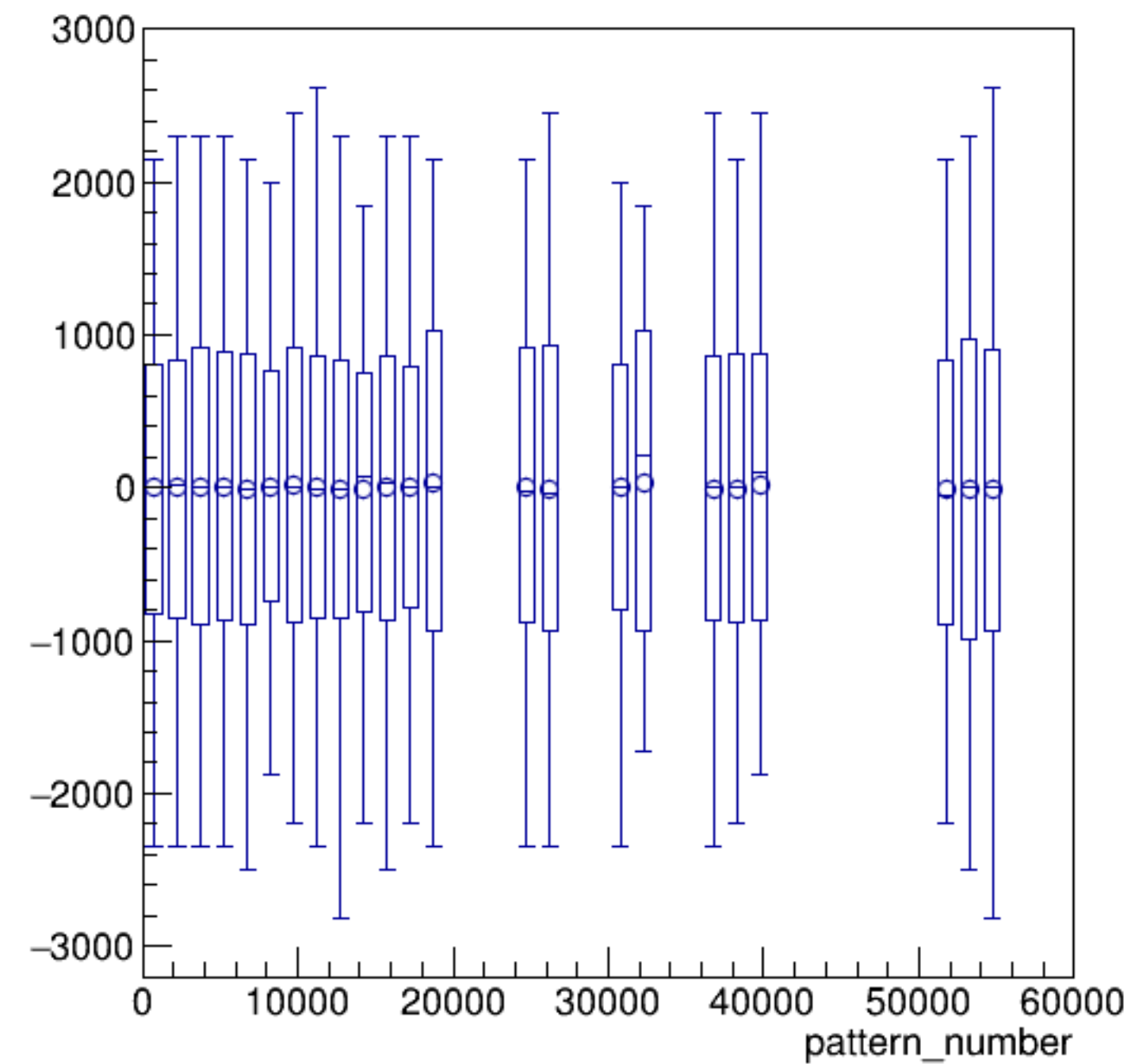
yield_dsl:pattern_number {ErrorFlag==0}



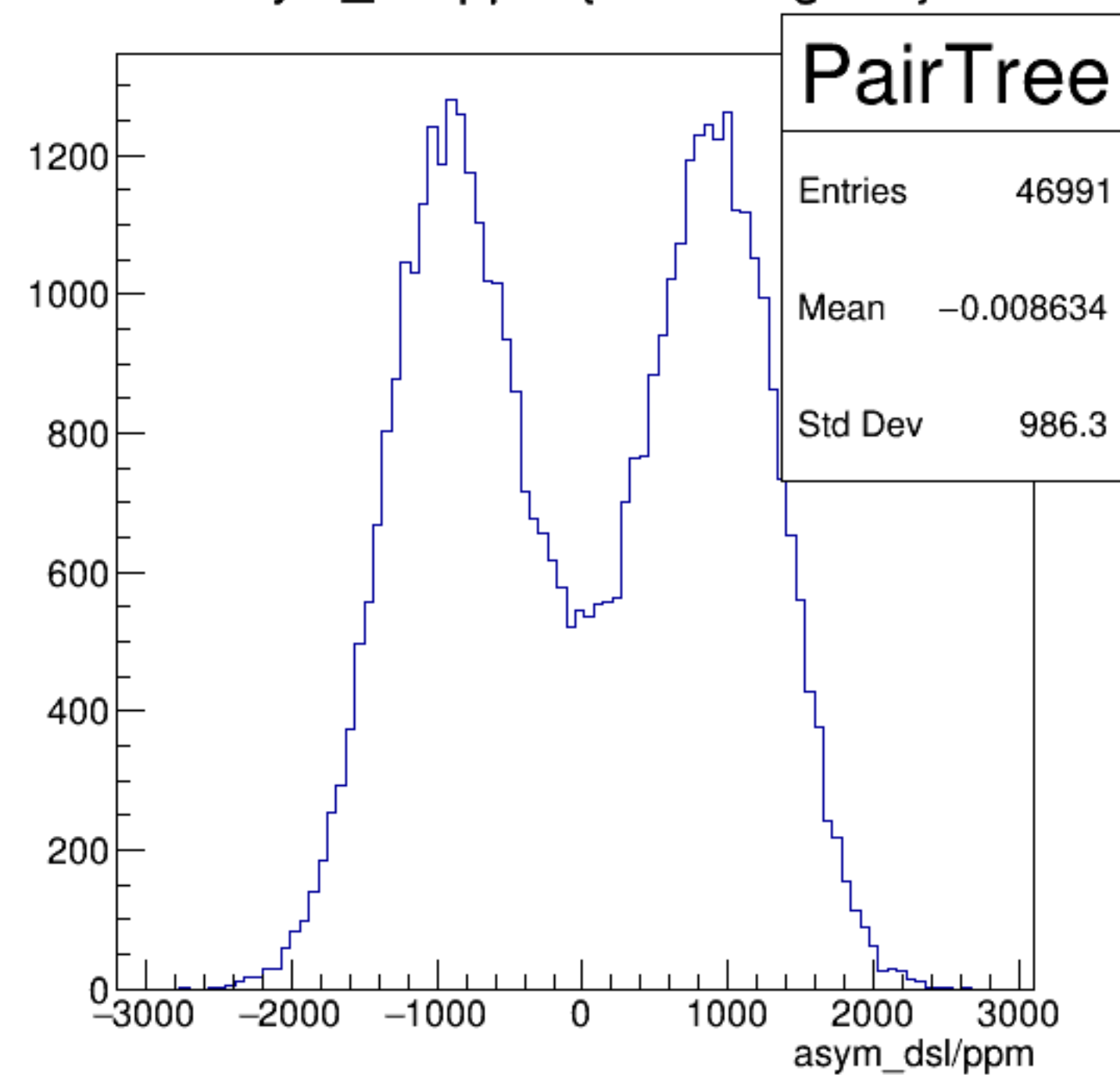
yield_dsl {ErrorFlag==0}

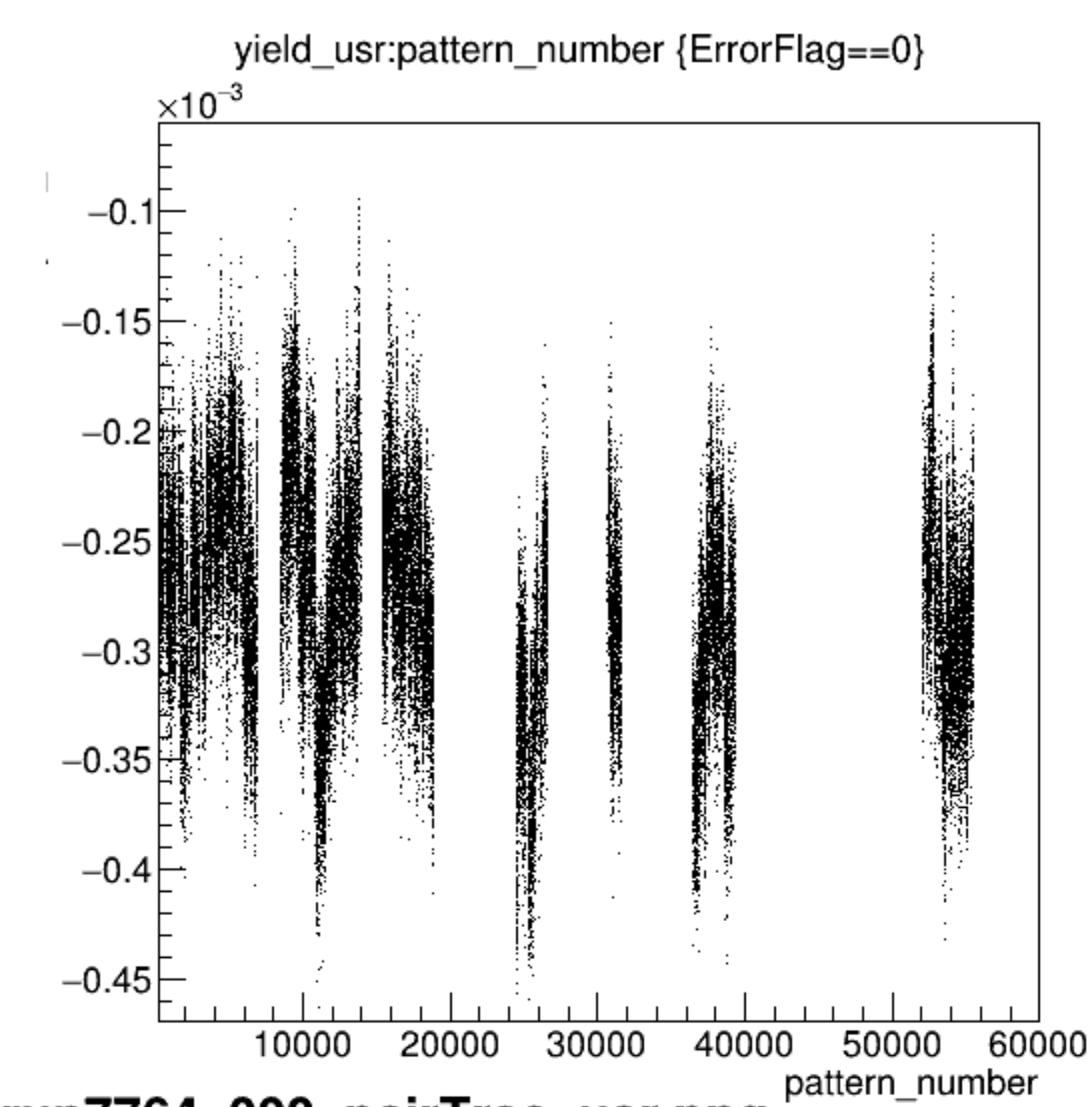


asym_dsl/ppm:pattern_number {ErrorFlag==0}

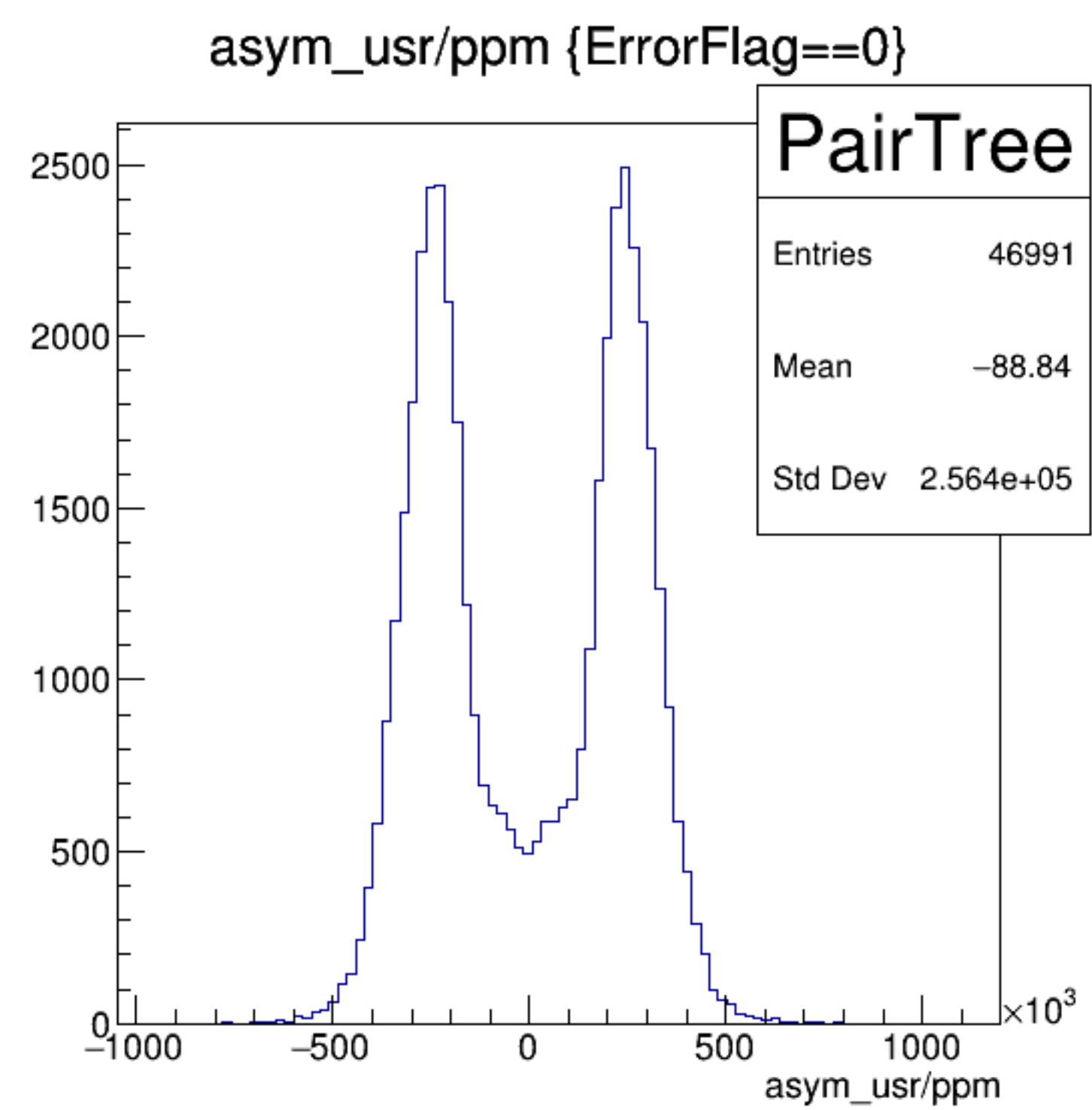
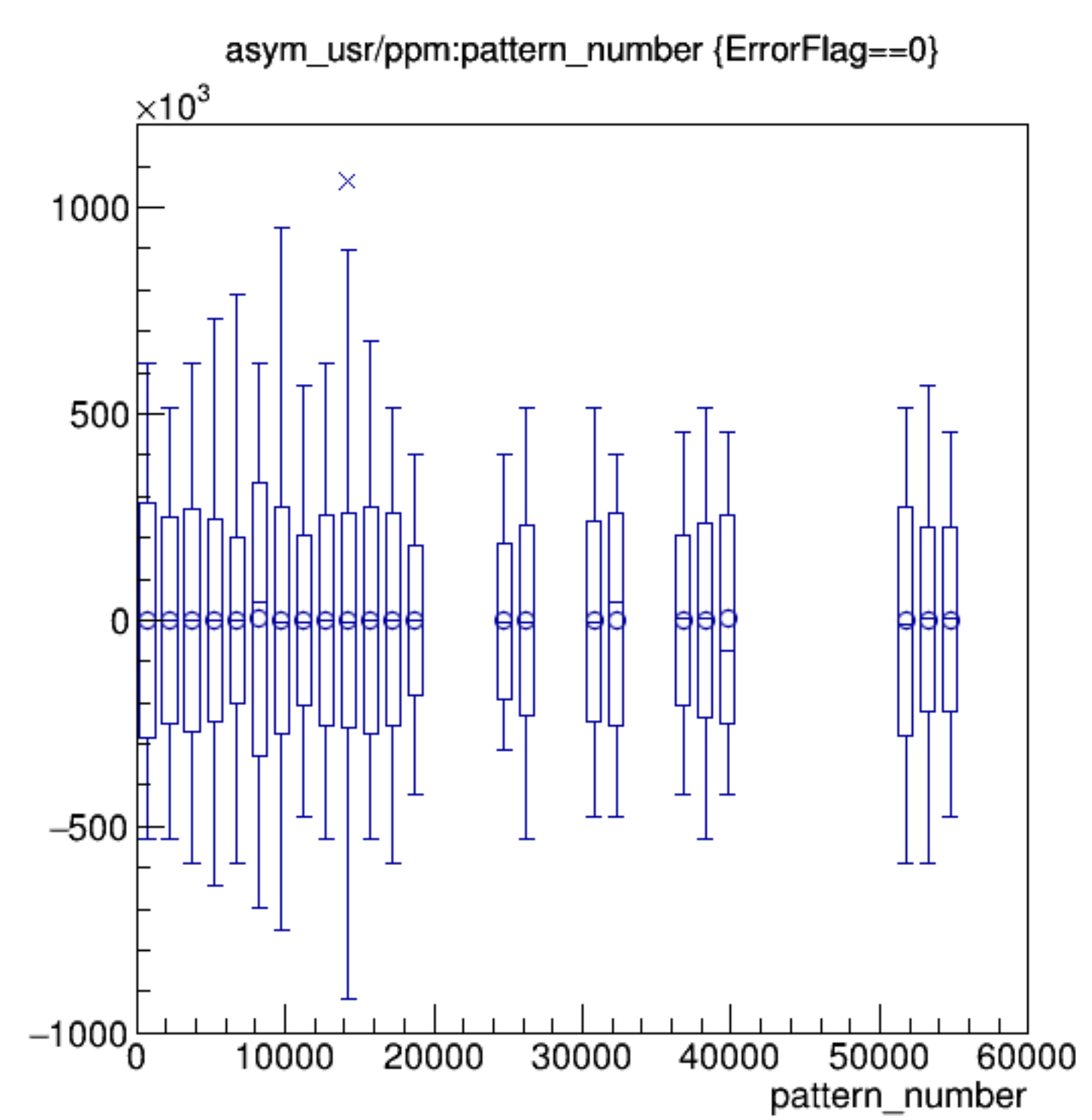
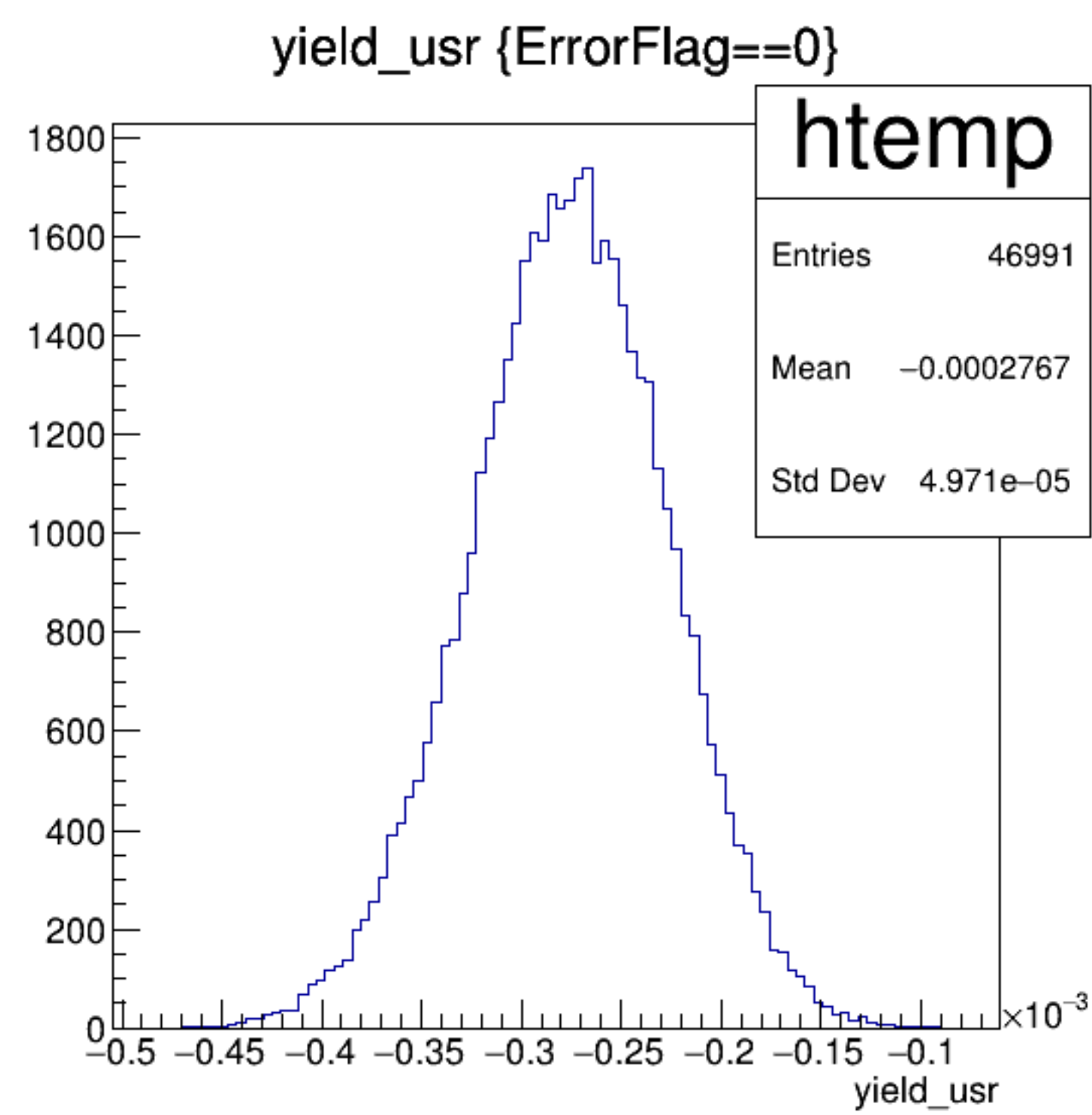


asym_dsl/ppm {ErrorFlag==0}

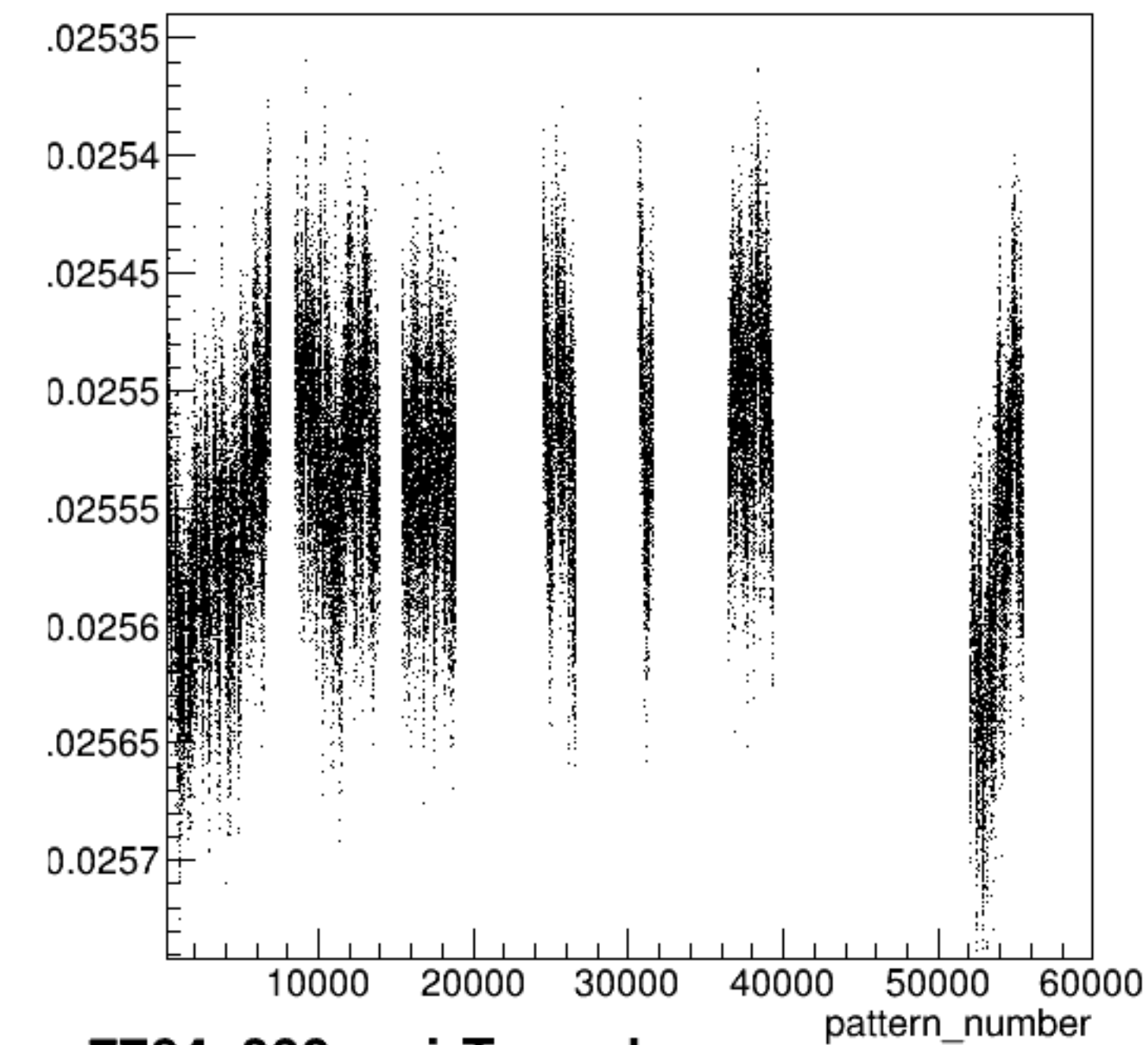




run7764_000_pairTree_usr.png

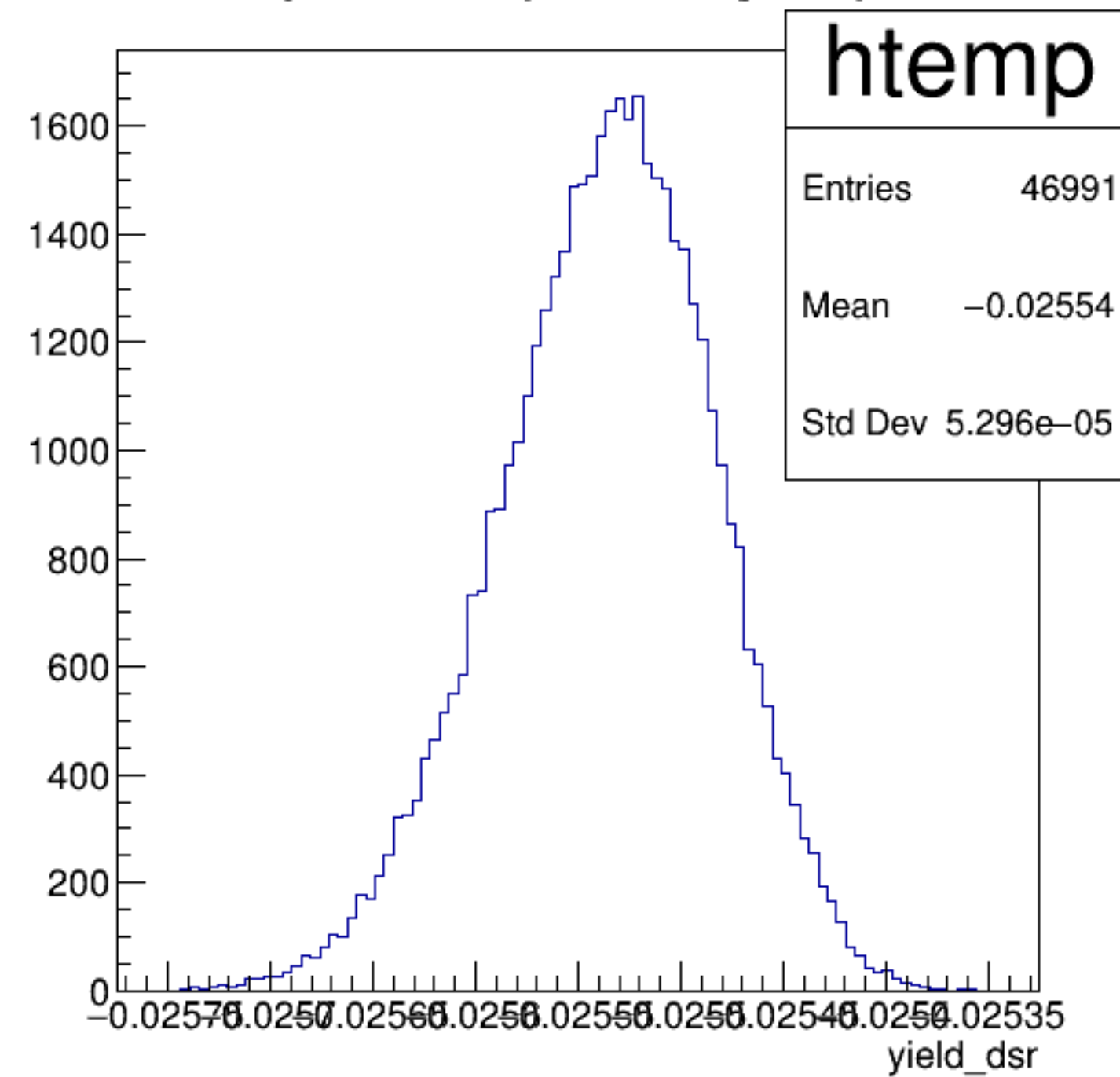


yield_dsr:pattern_number {ErrorFlag==0}

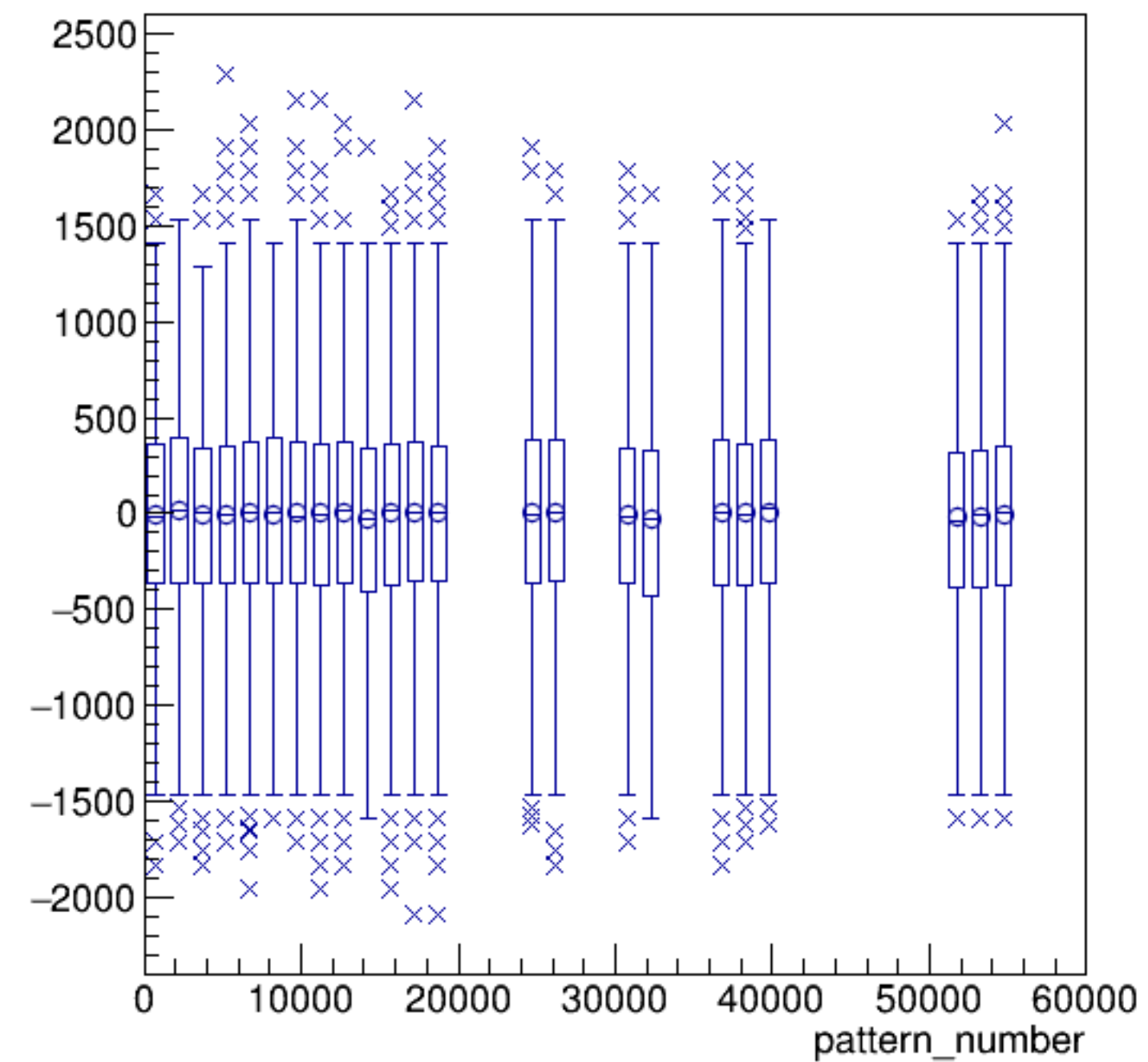


run7764_000_pairTree_dsr.png

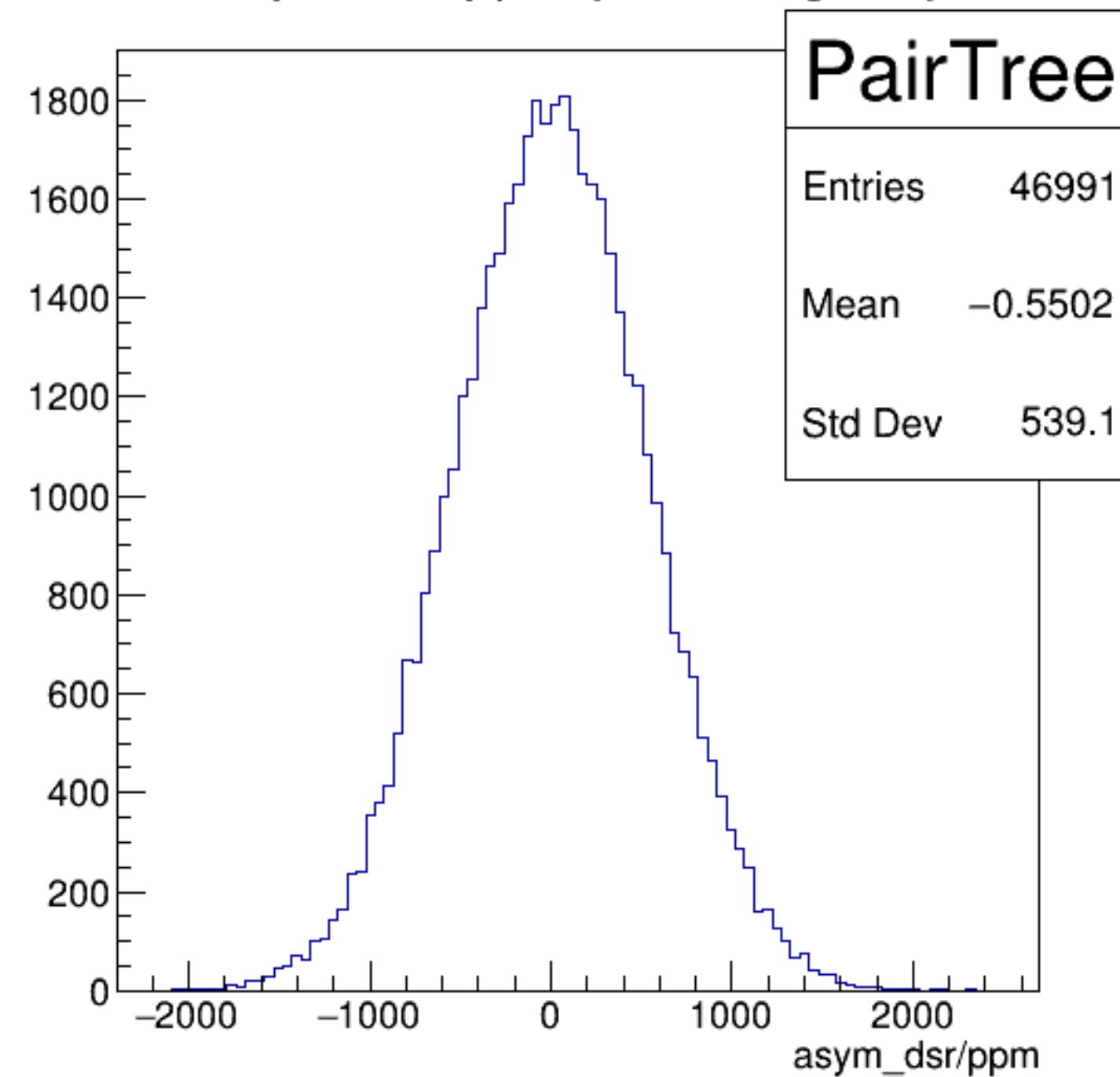
yield_dsr {ErrorFlag==0}



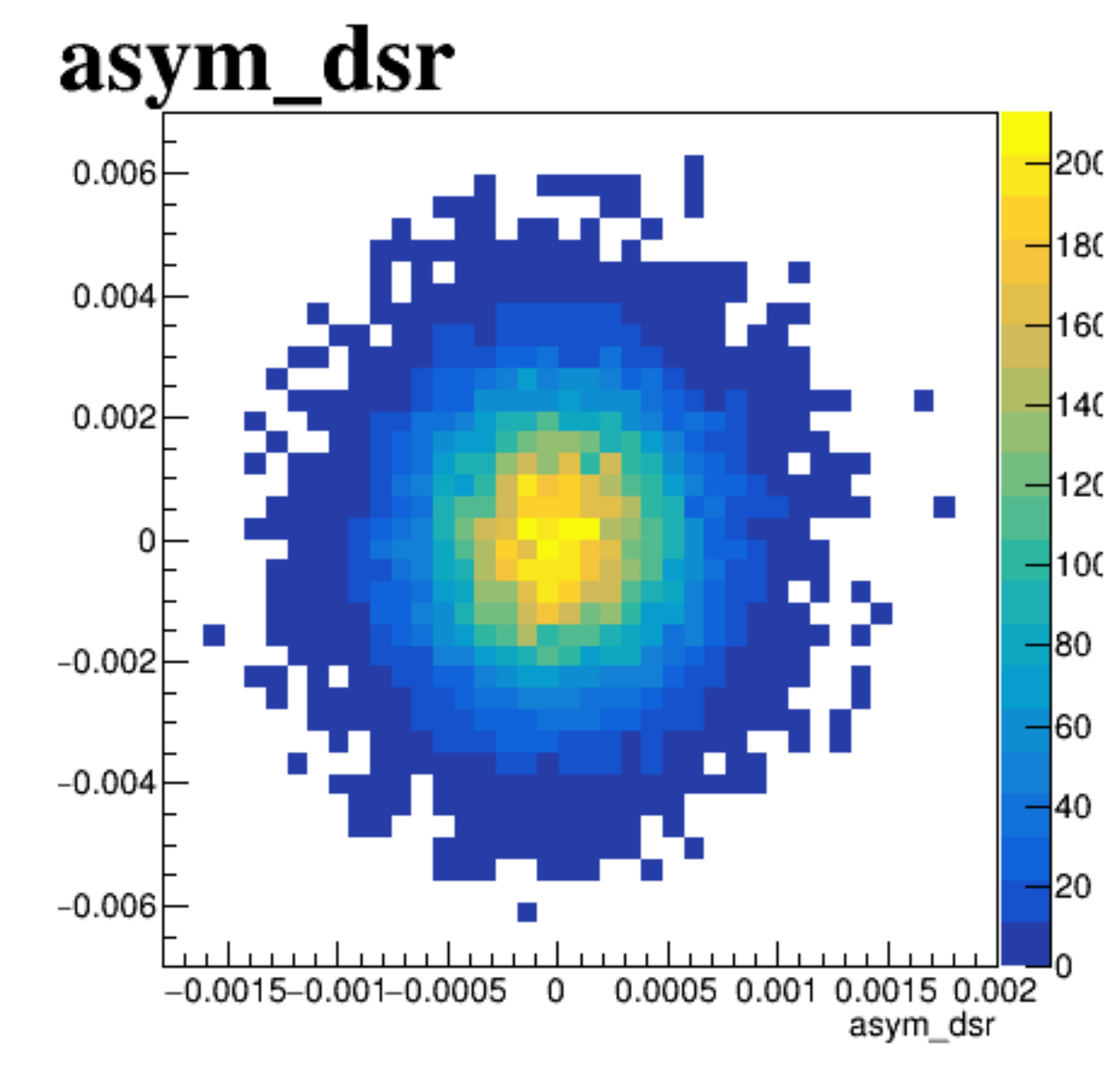
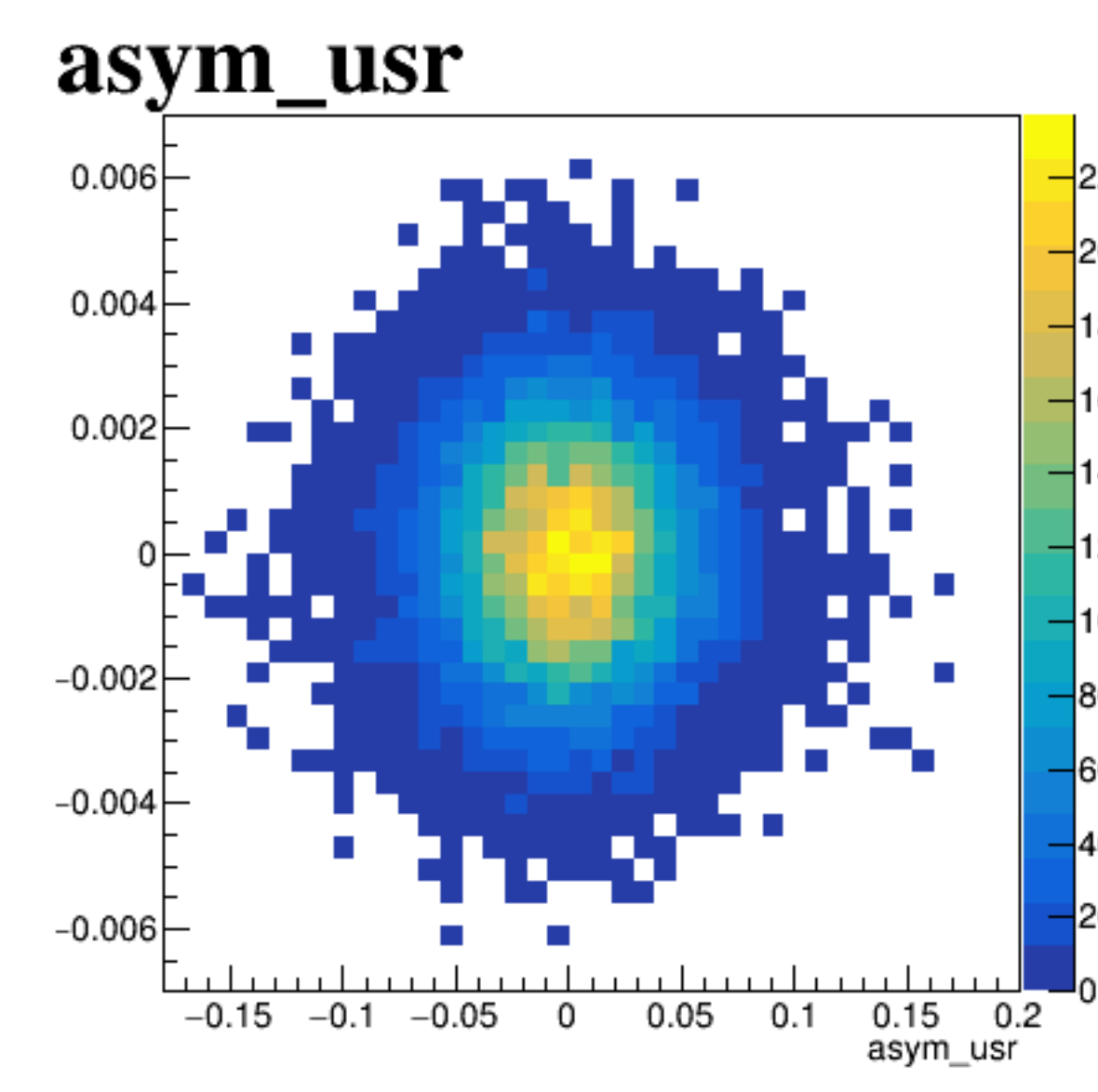
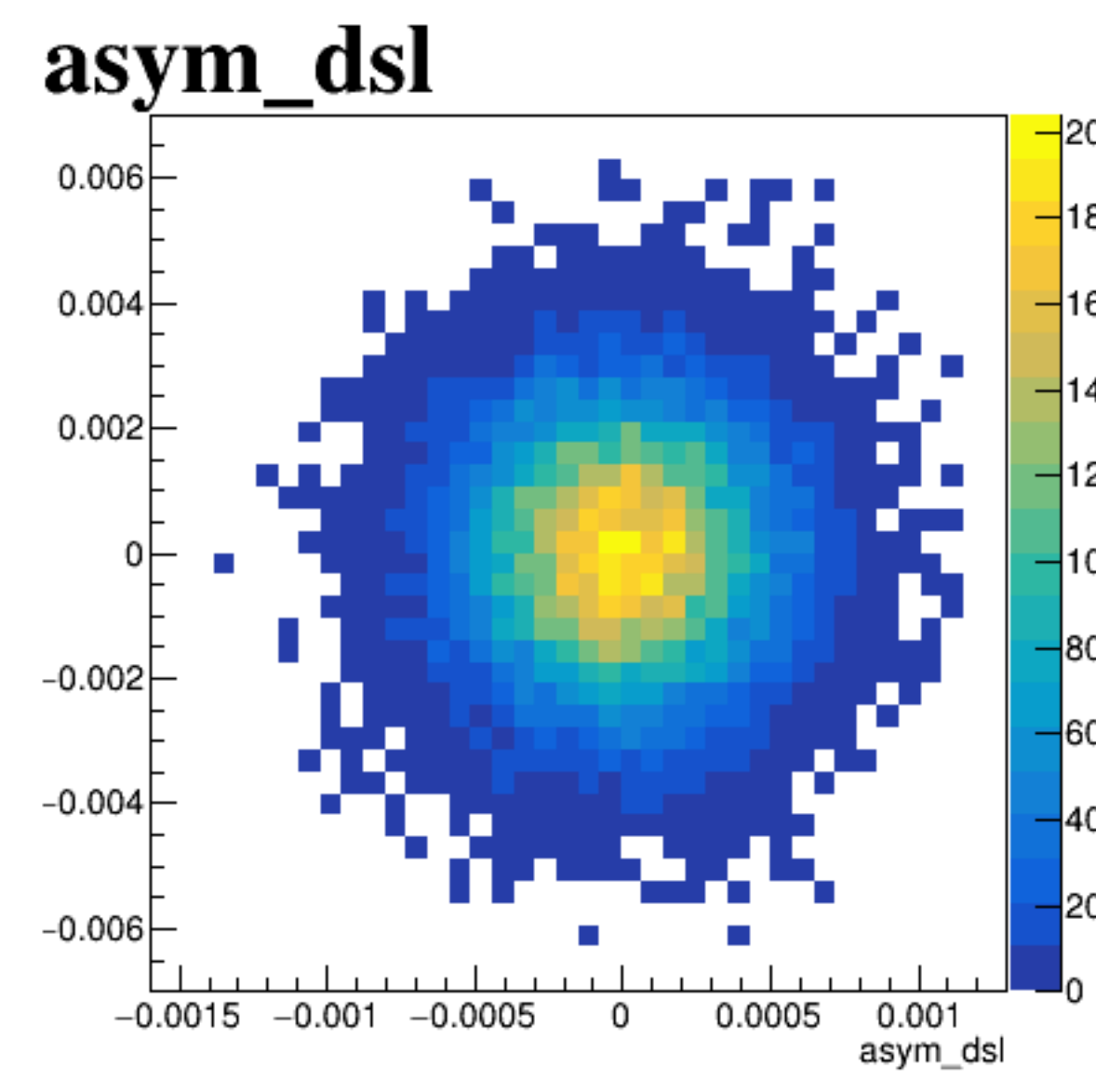
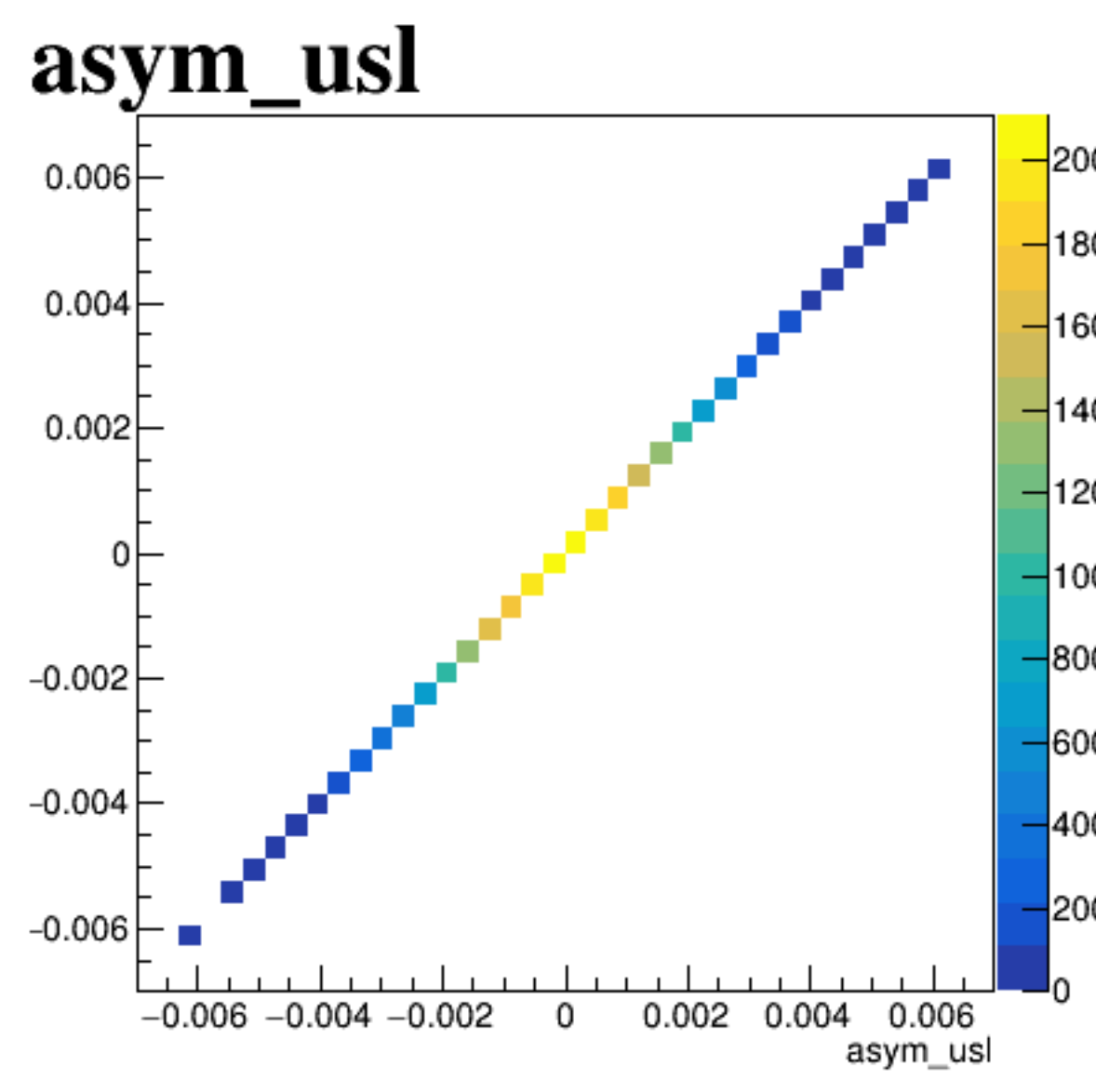
asym_dsr/ppm:pattern_number {ErrorFlag==0}



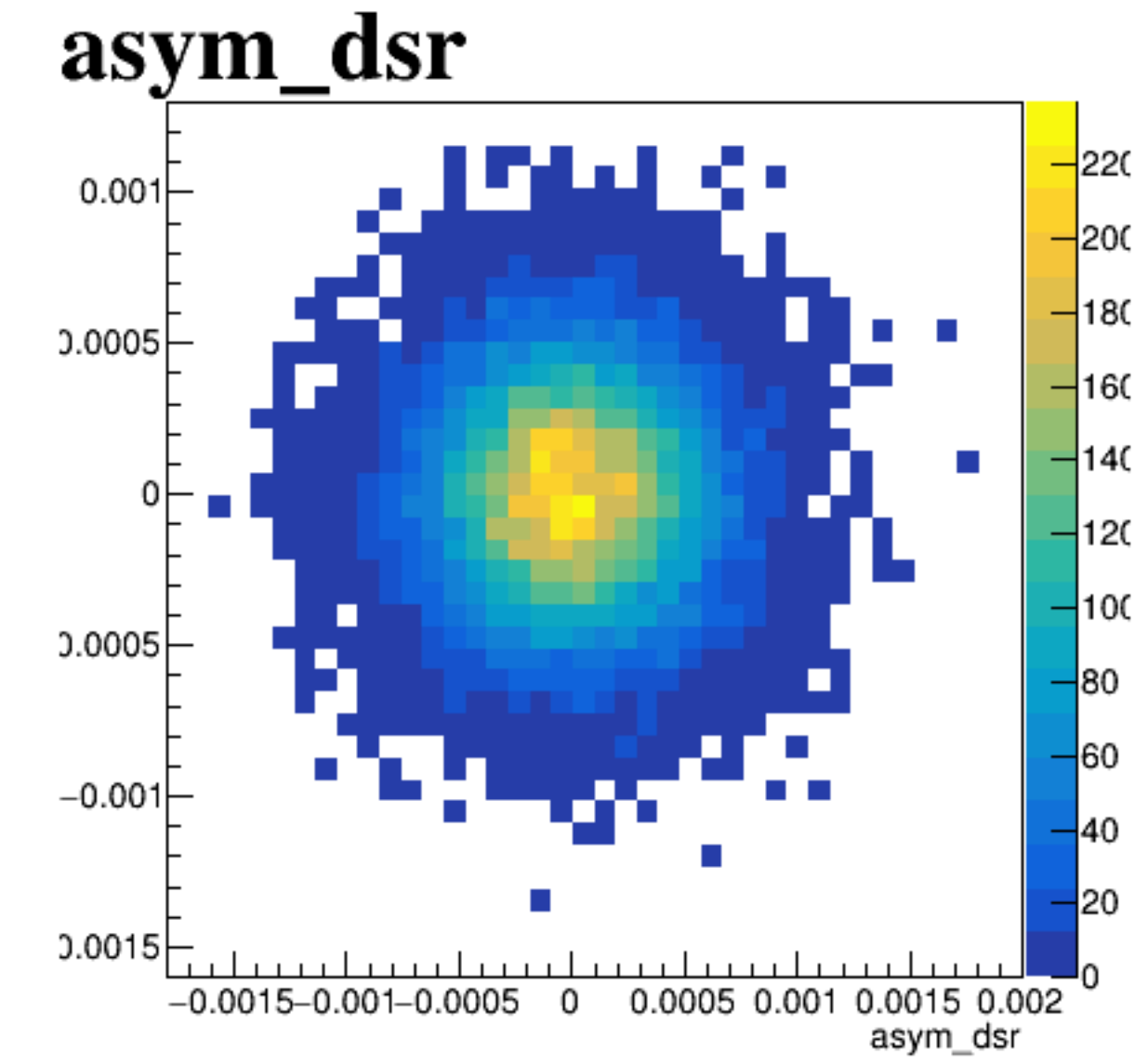
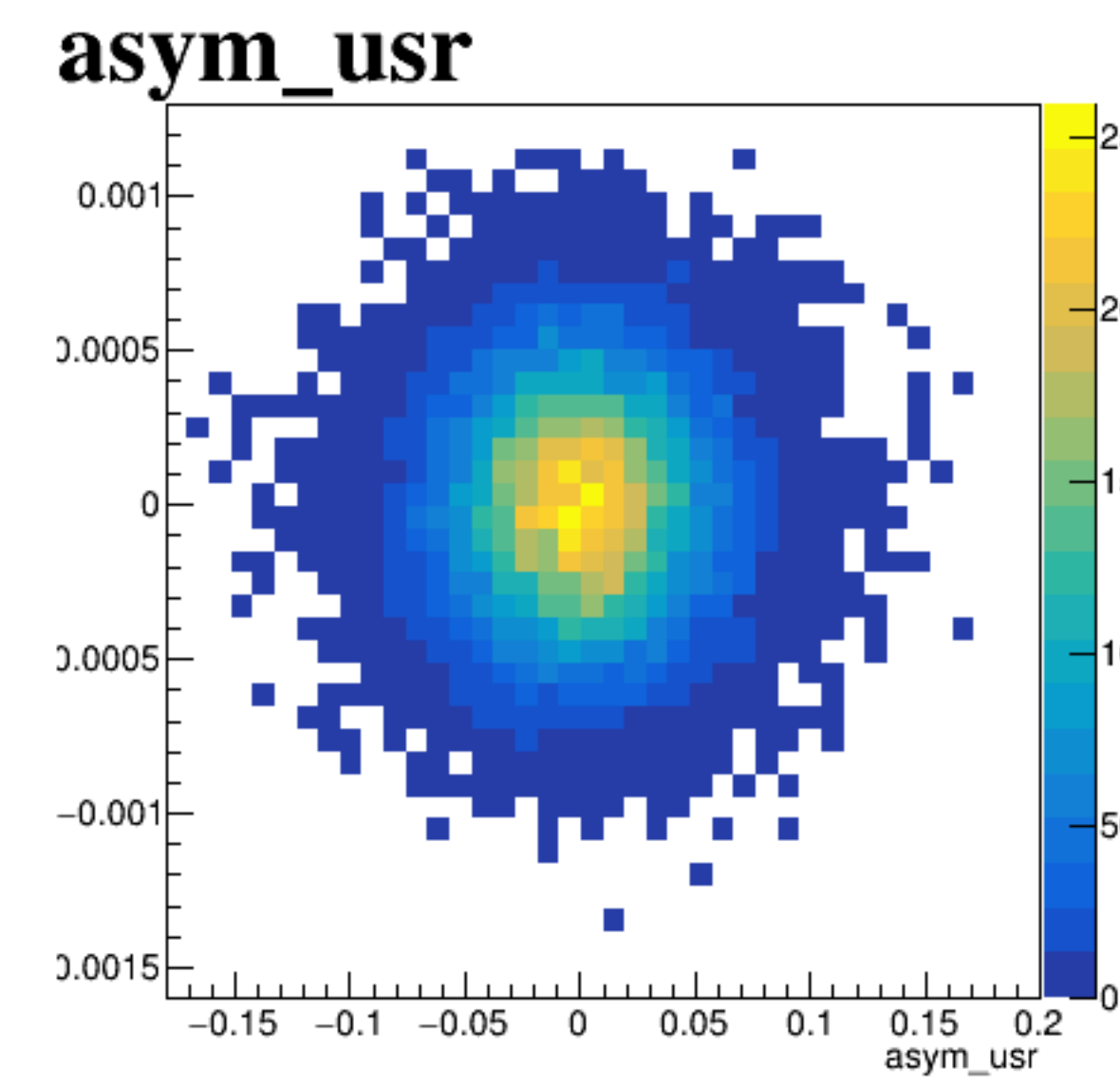
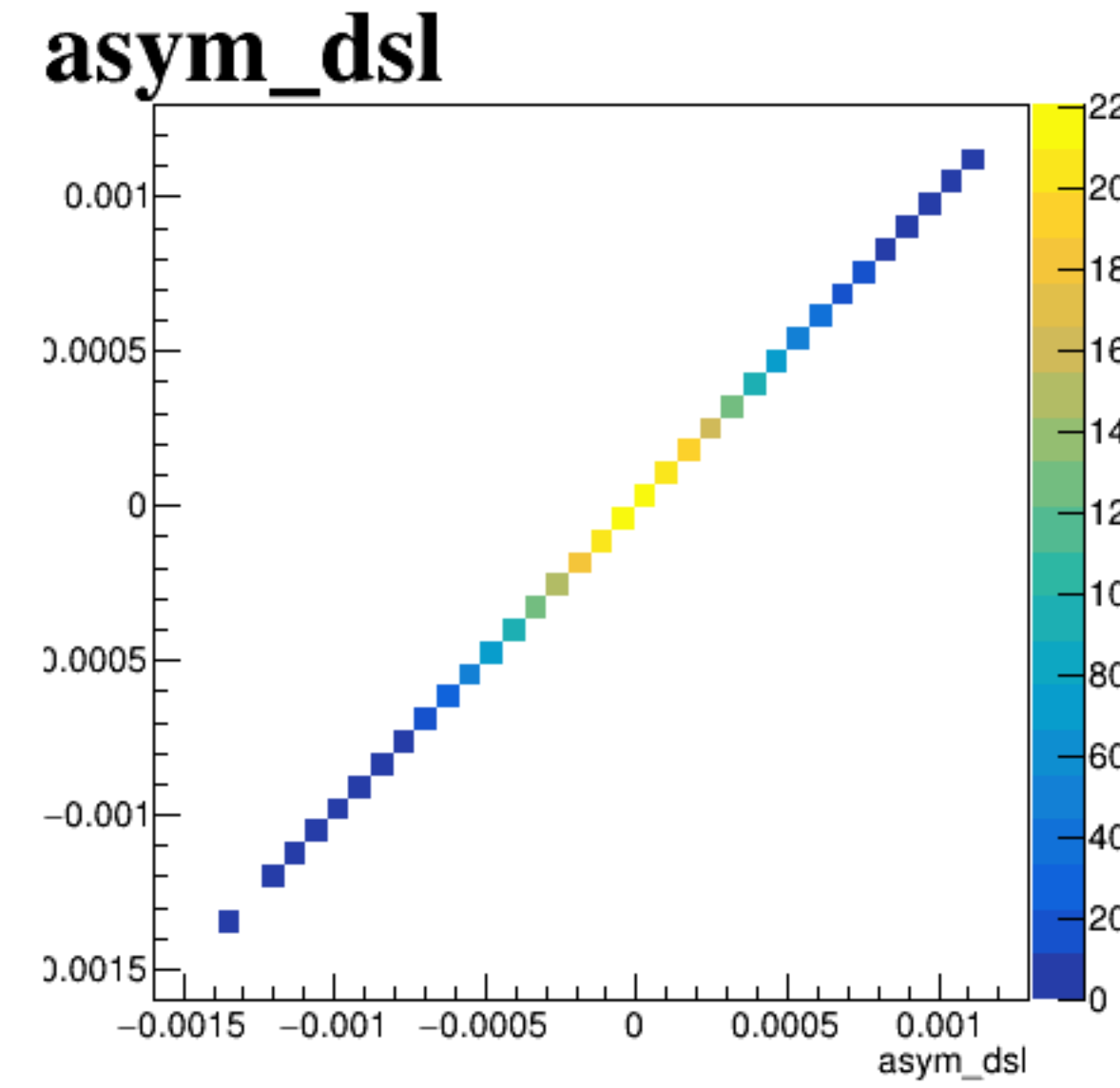
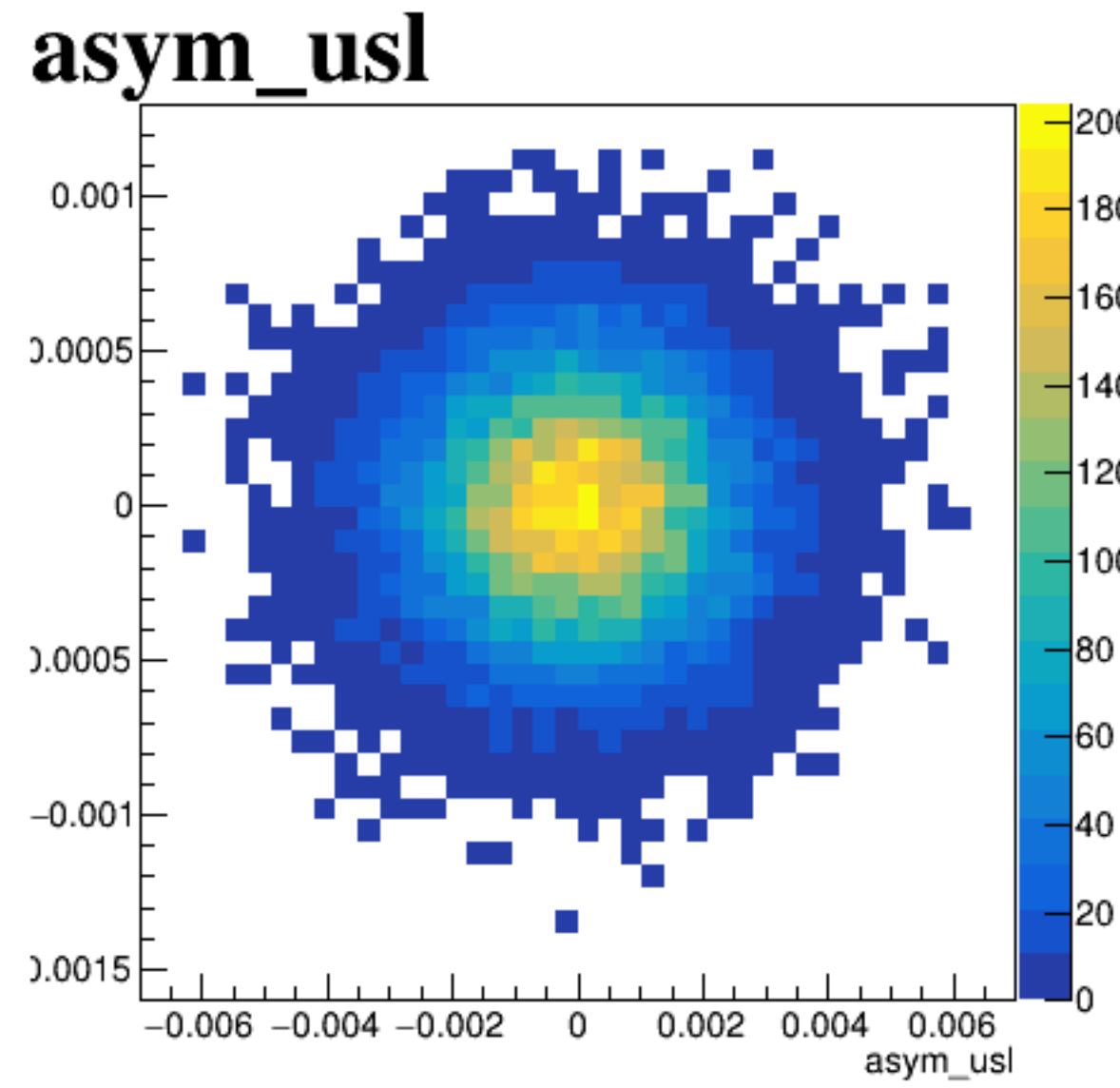
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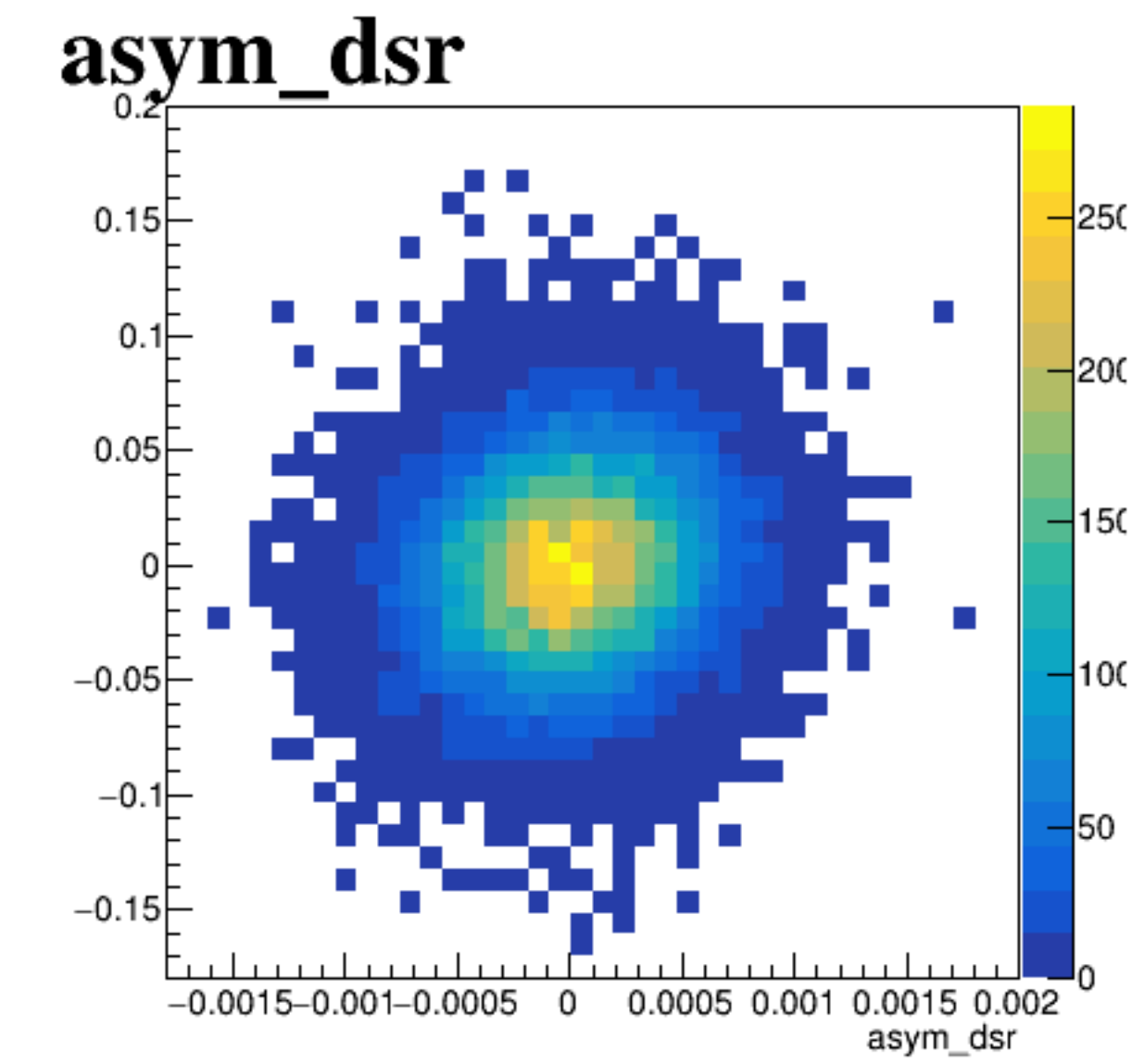
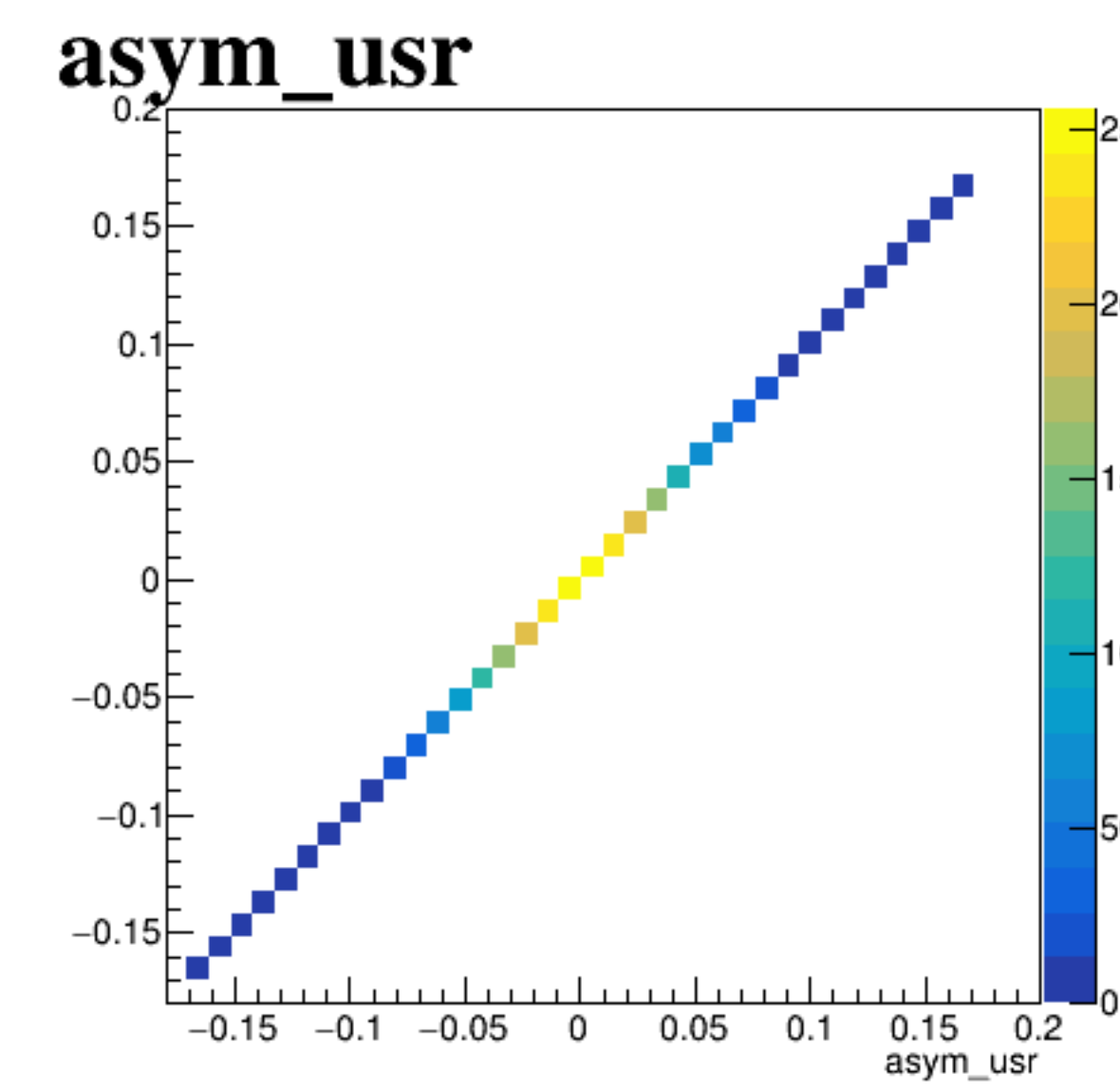
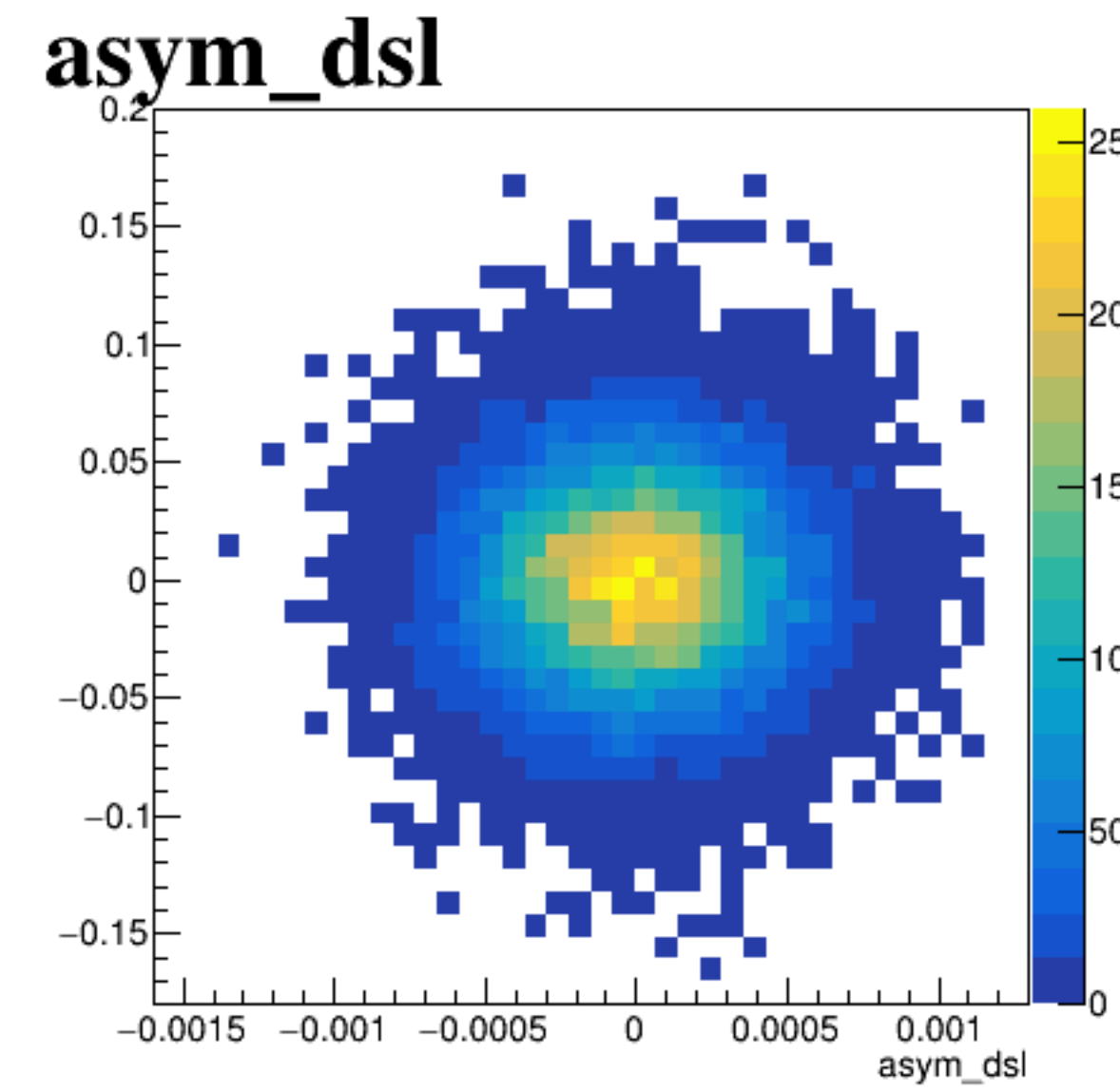
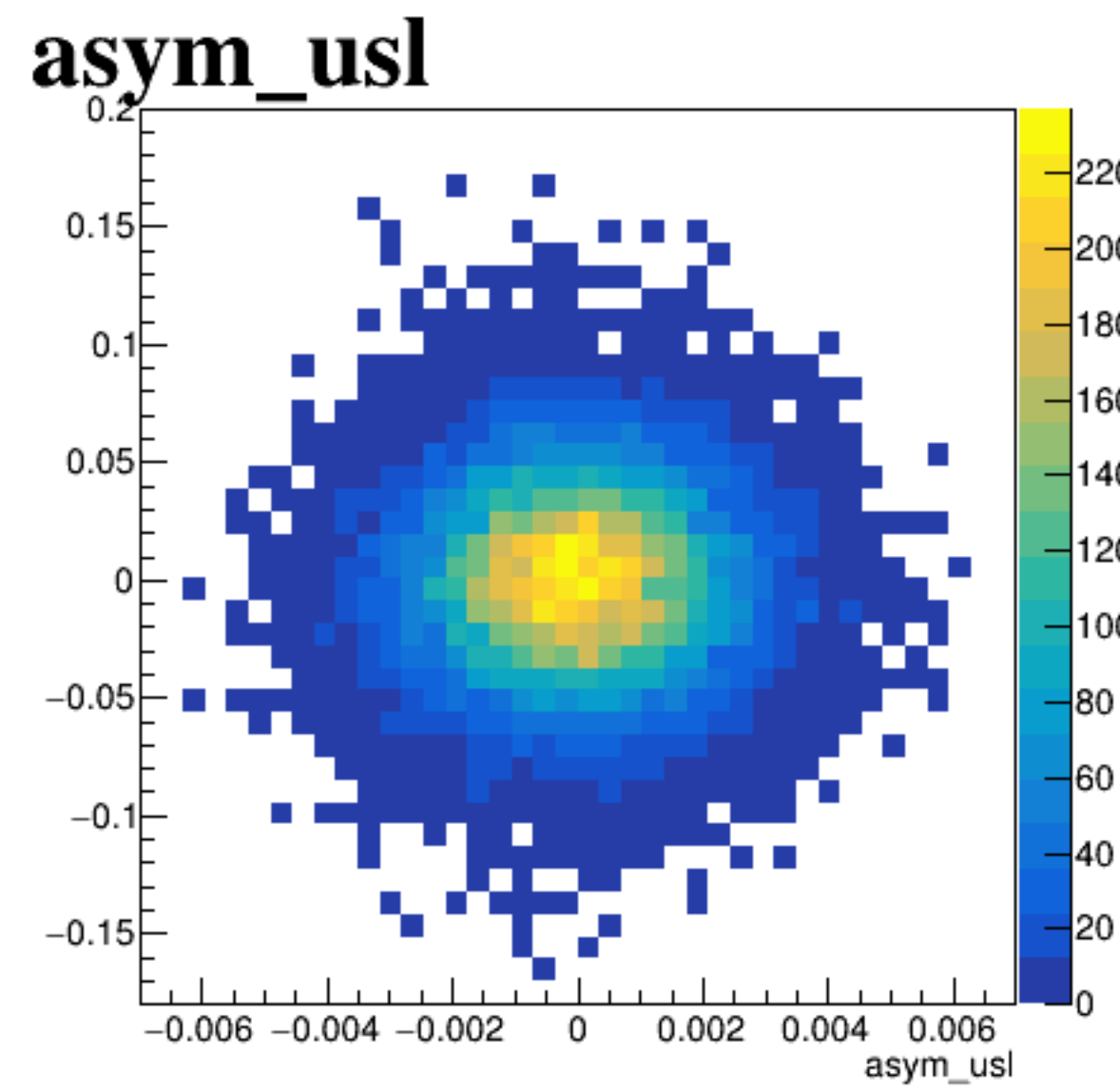
asym_usl



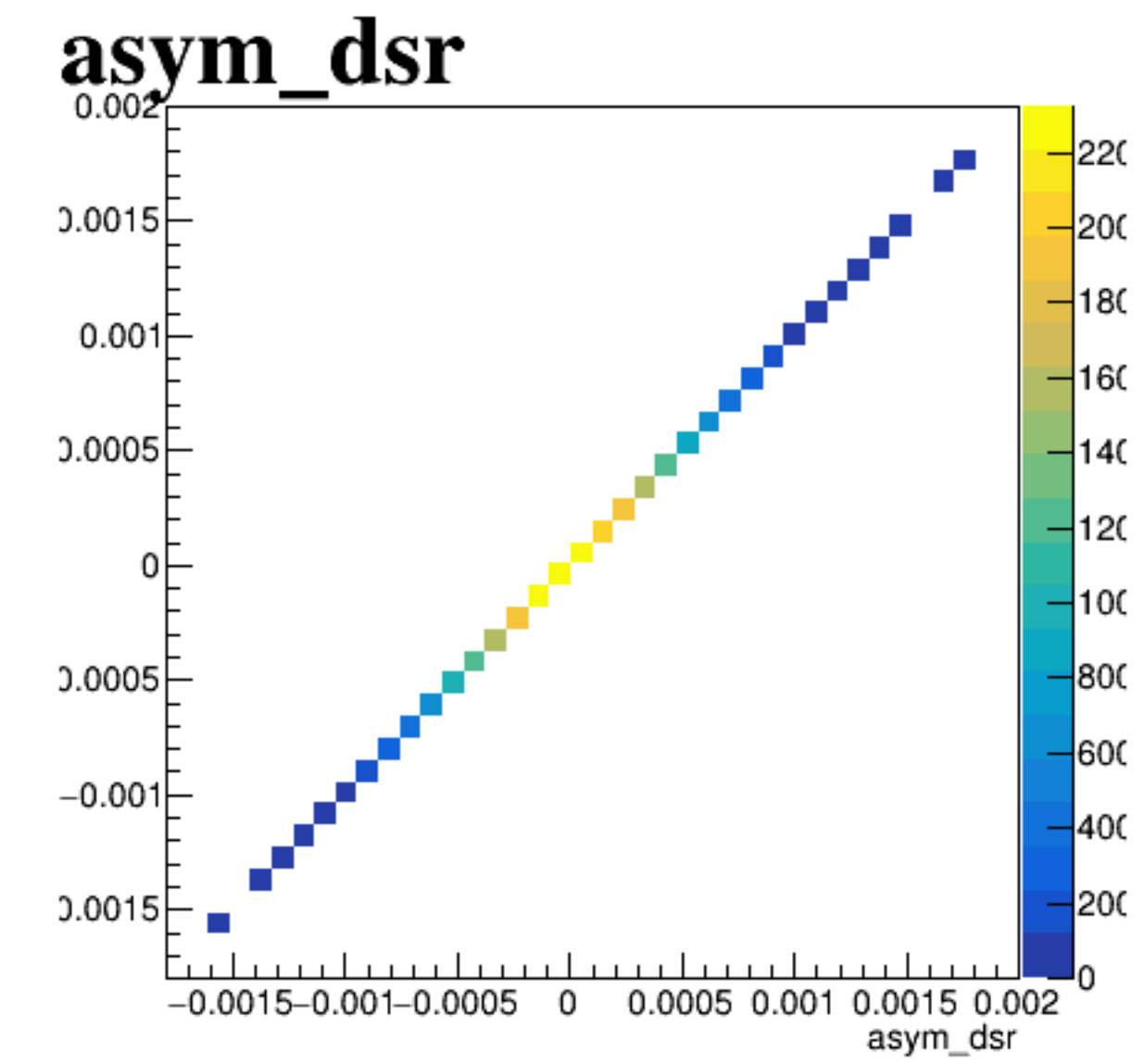
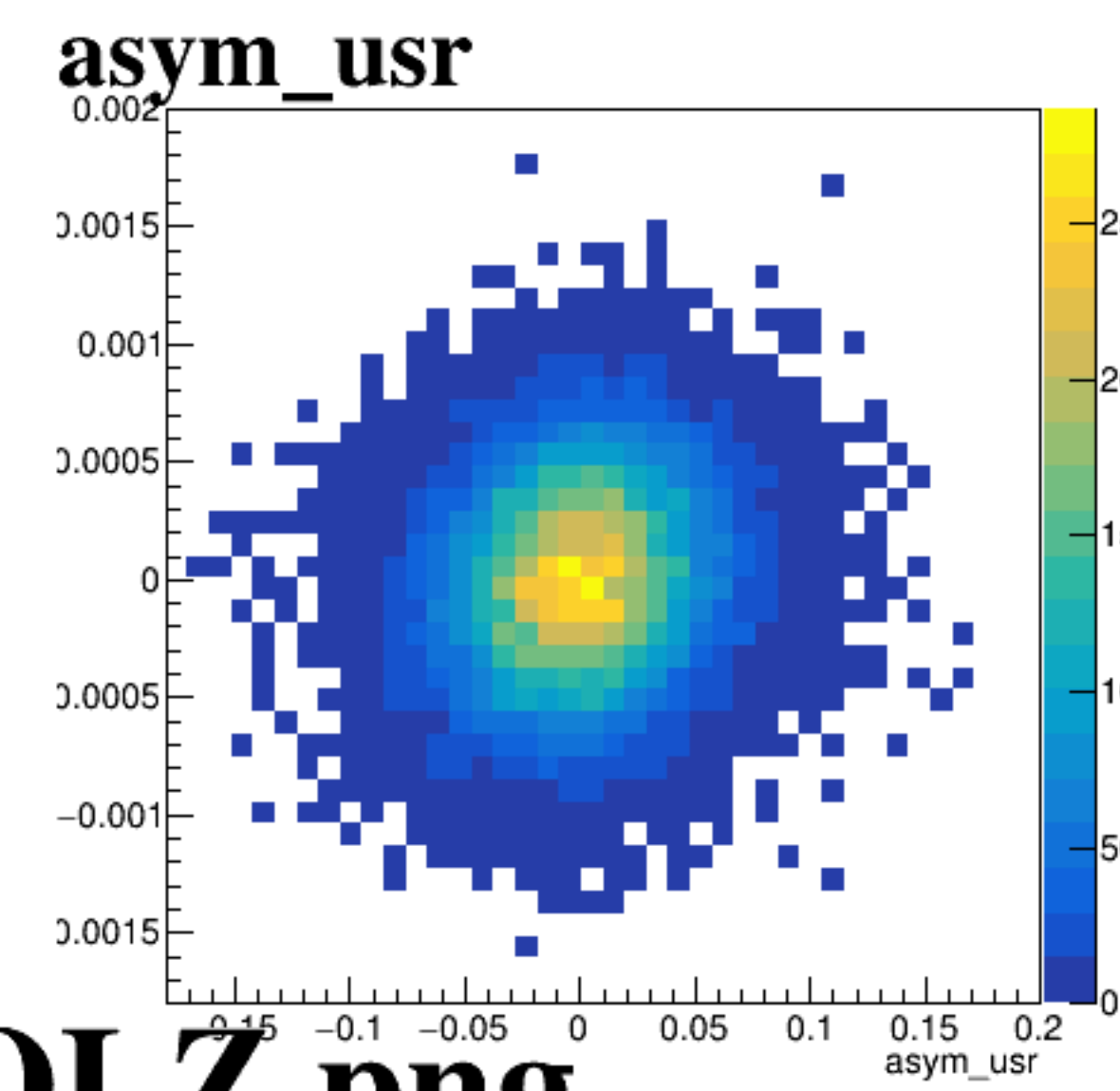
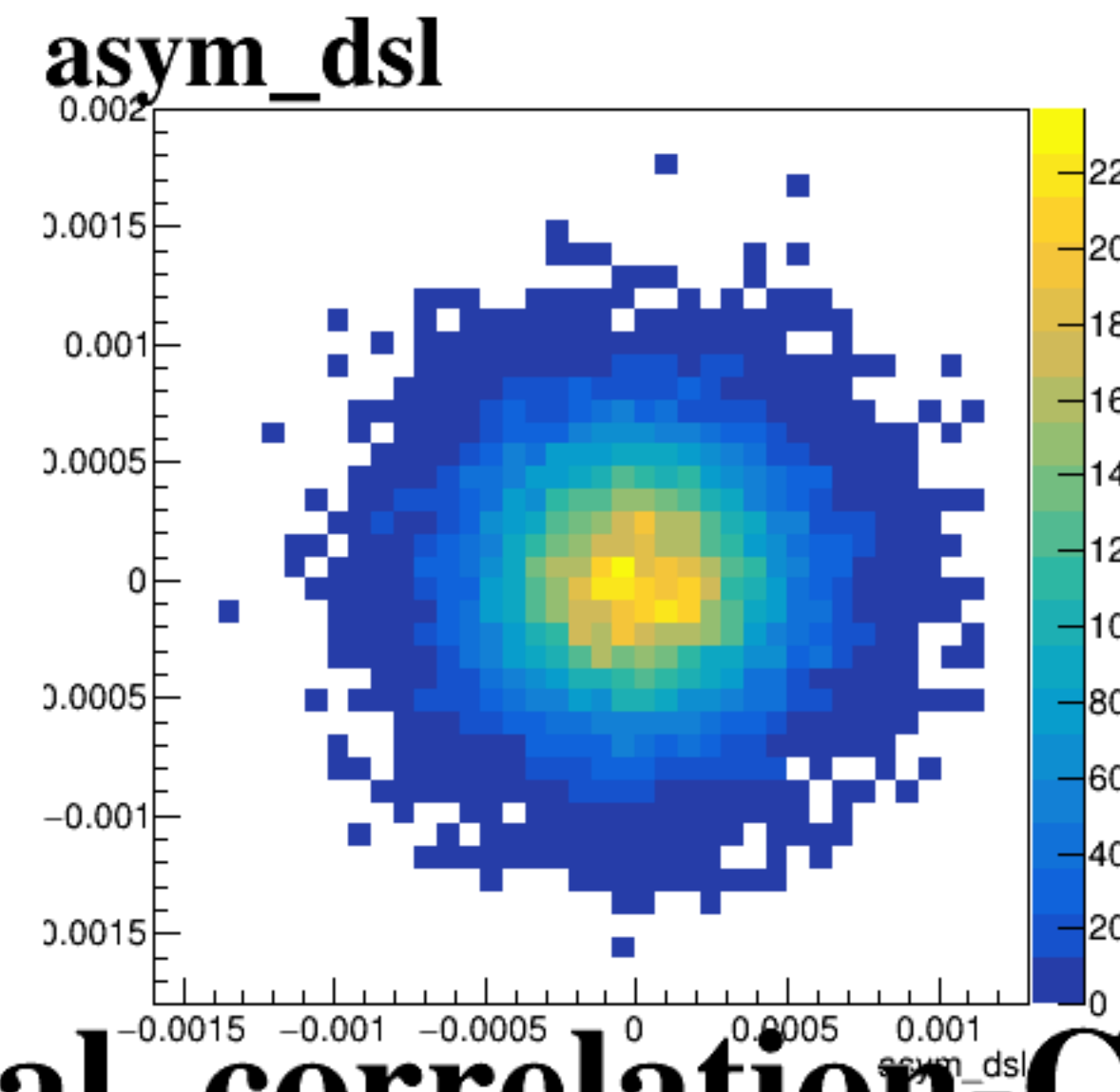
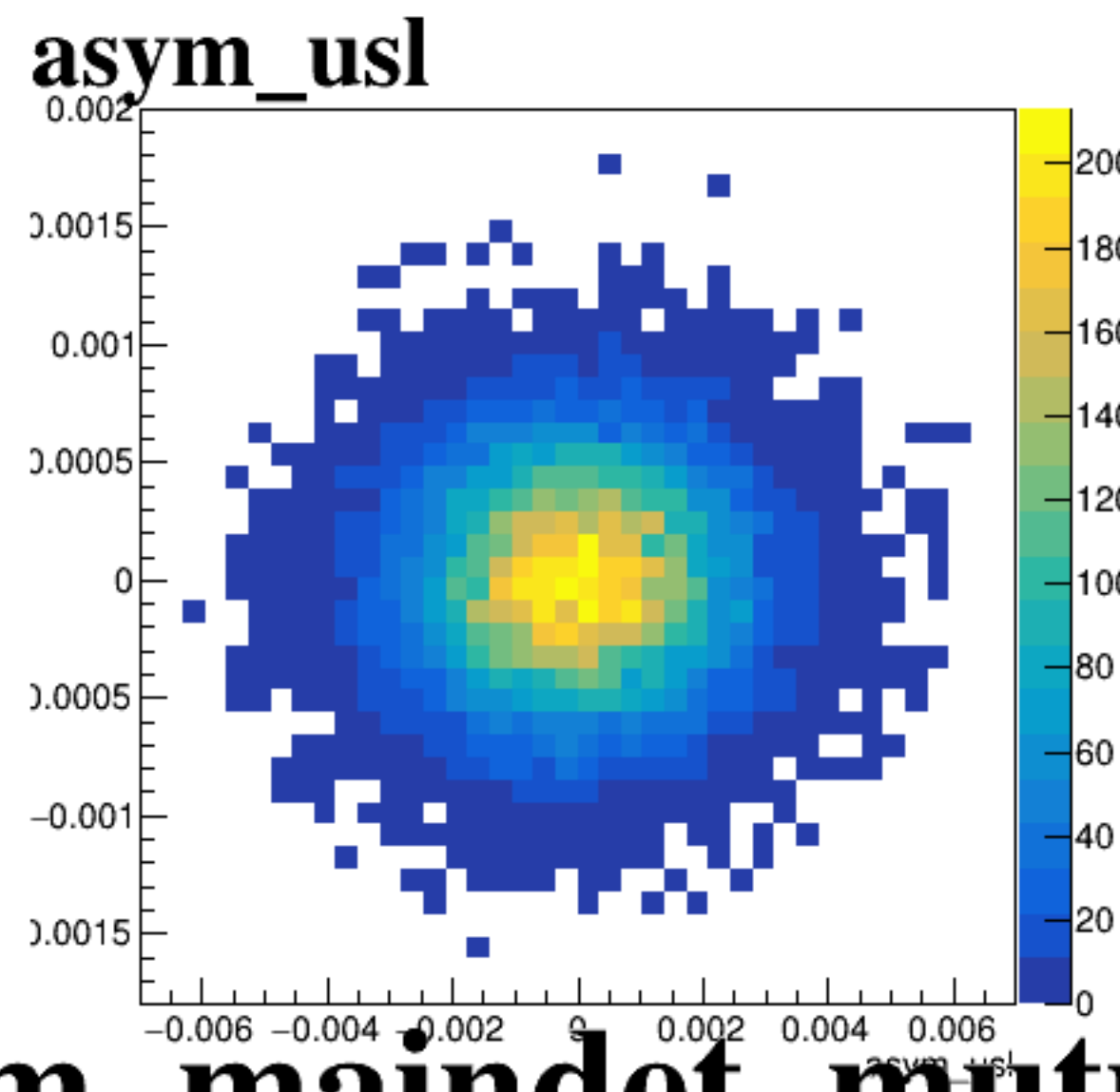
asym_dsl



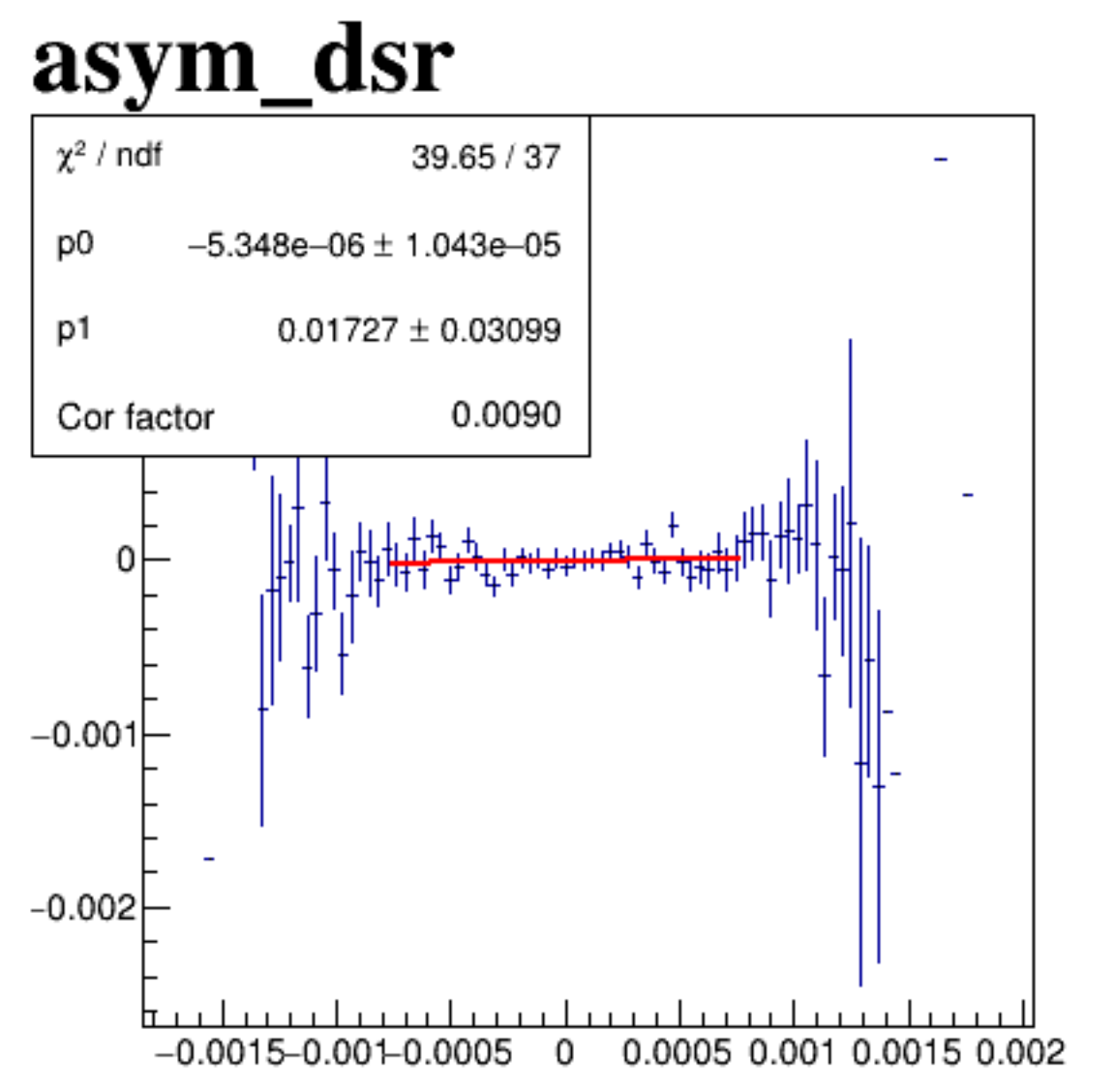
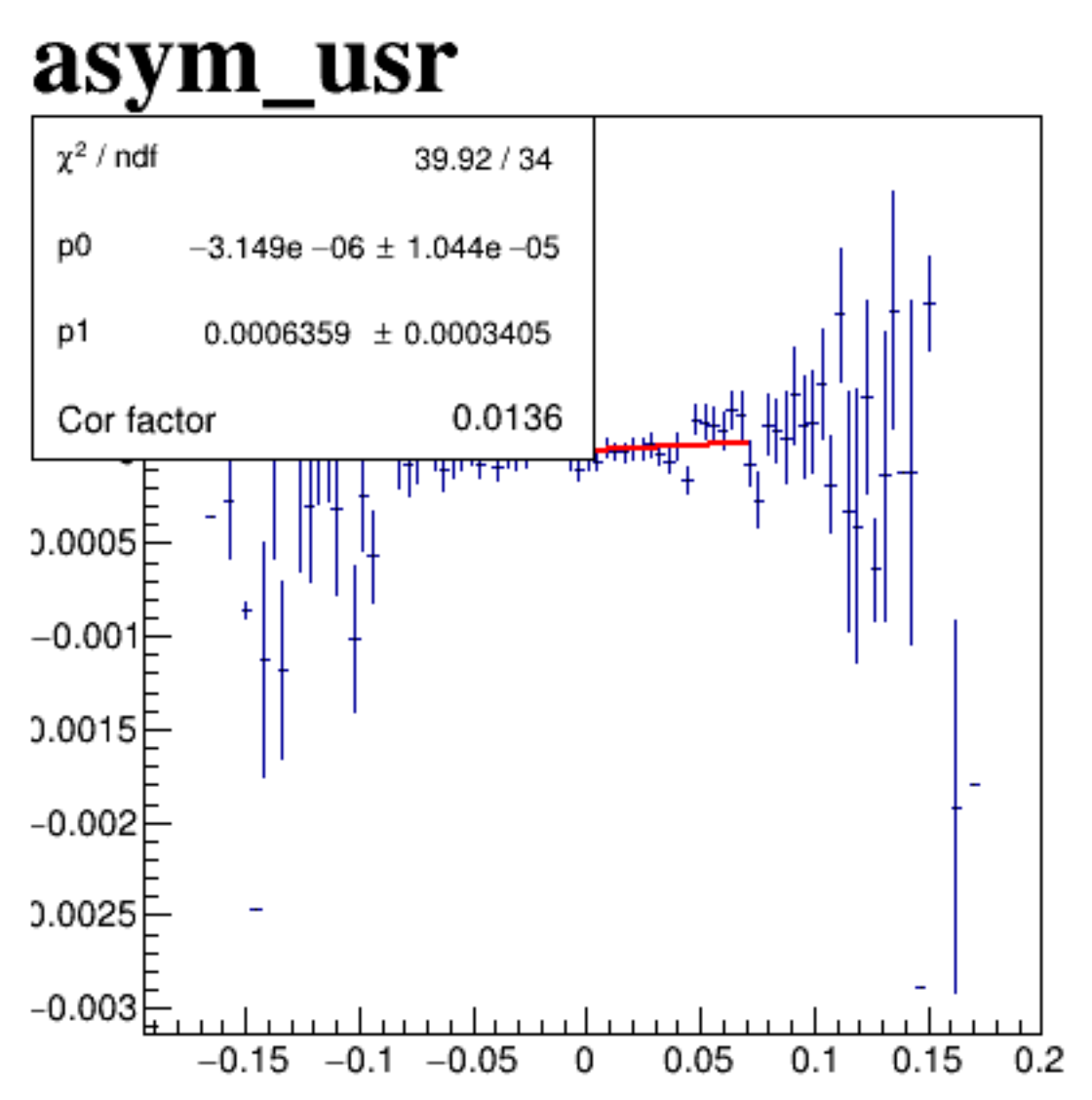
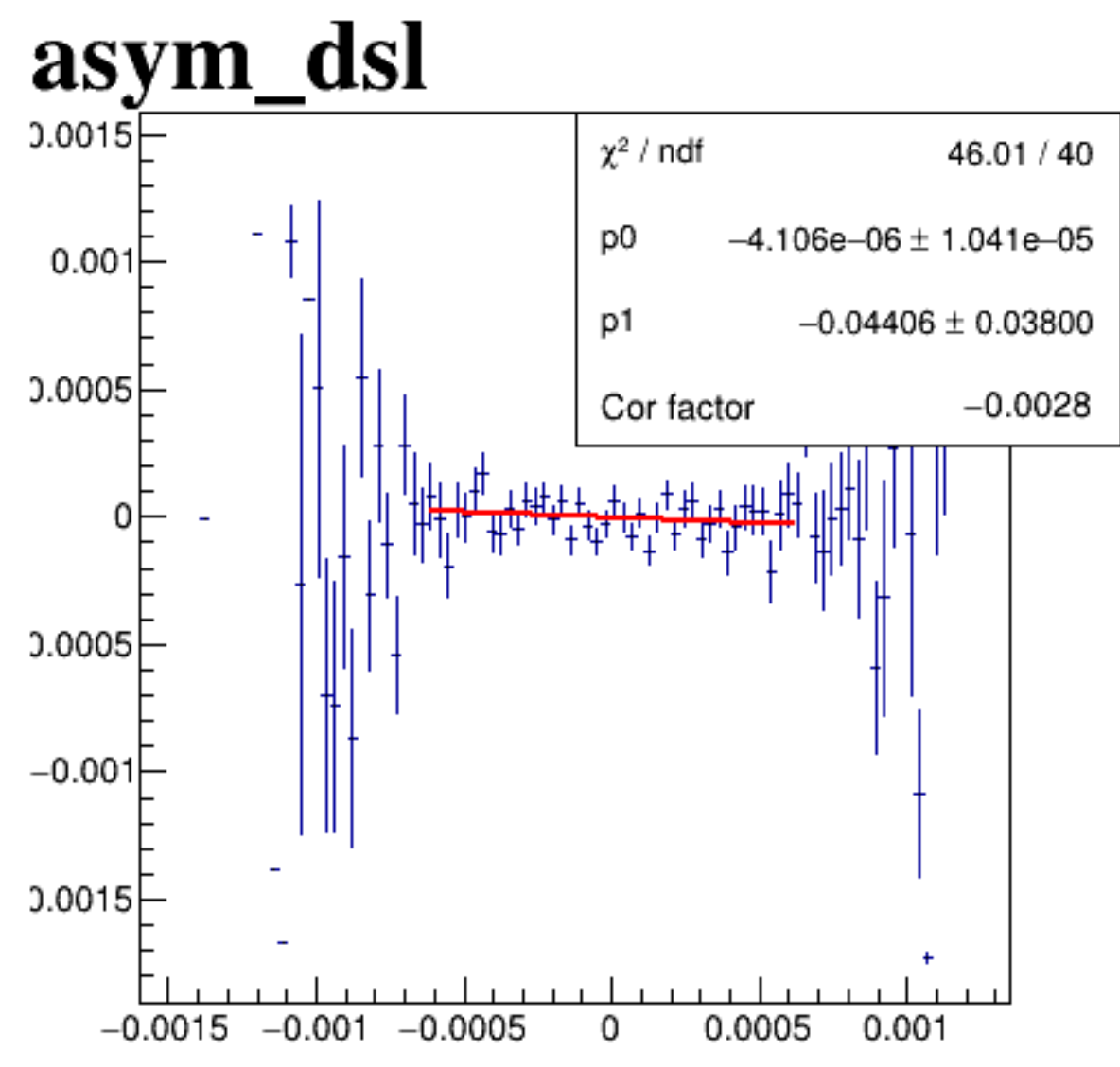
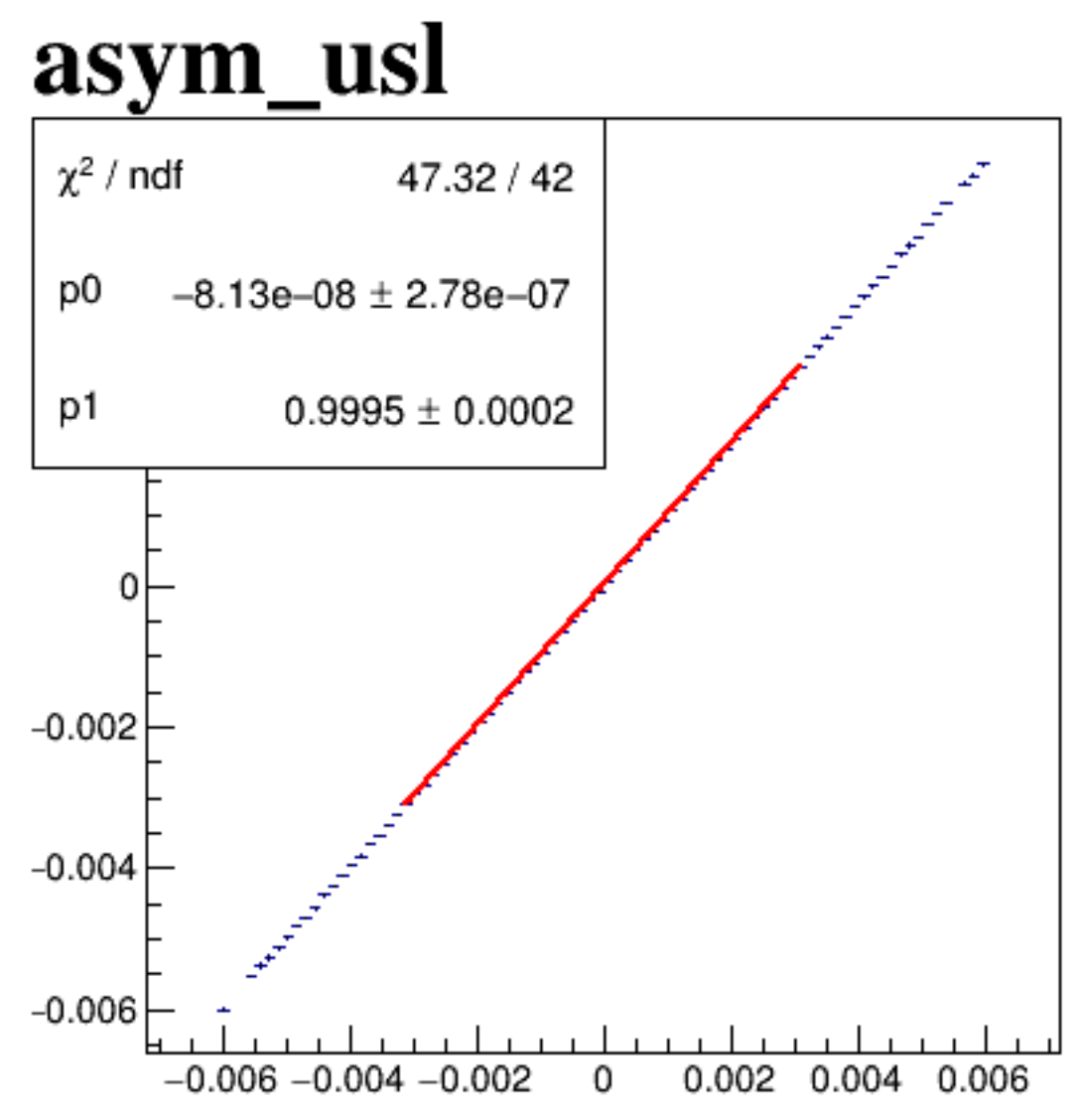
asym_usr



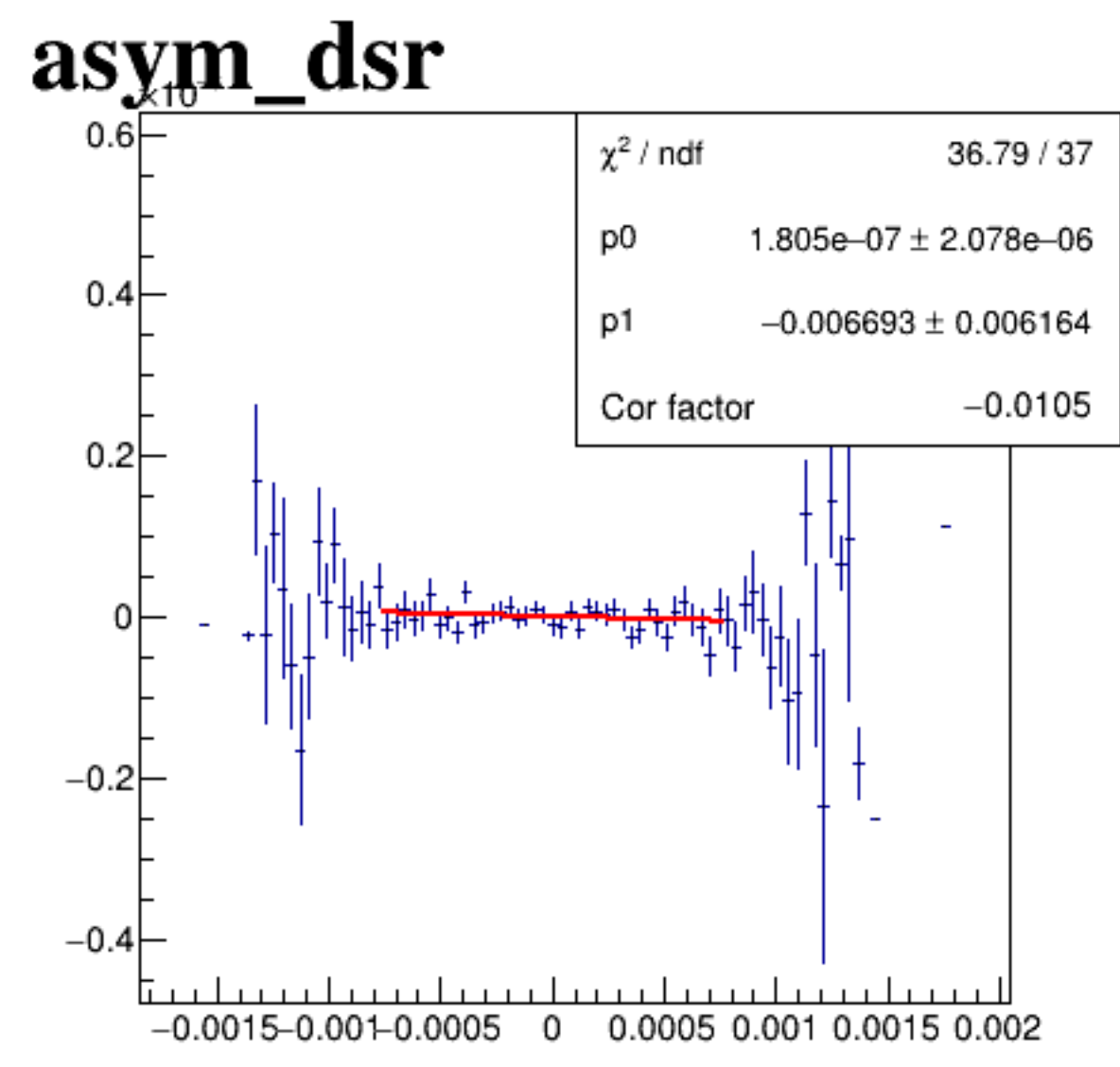
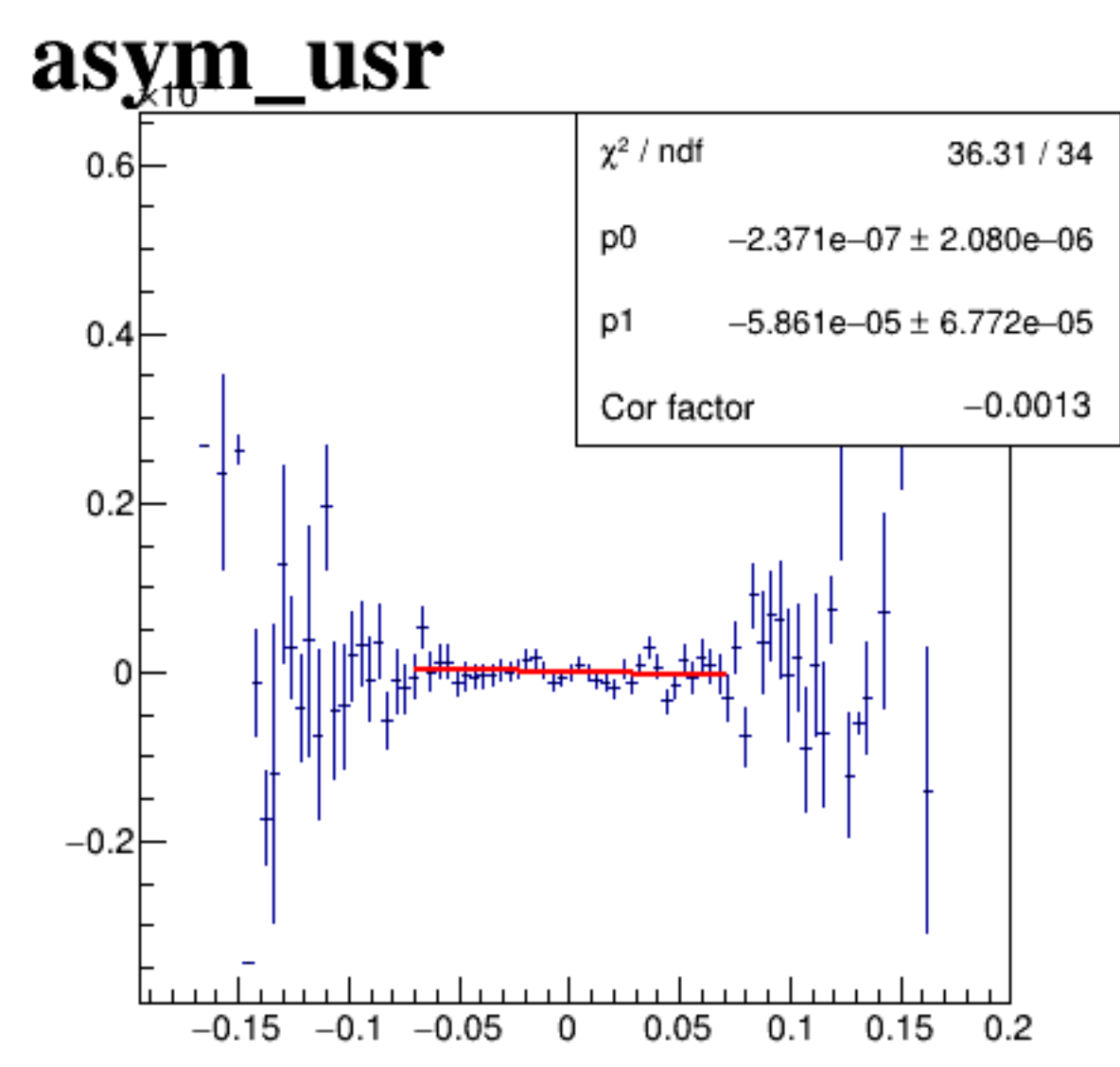
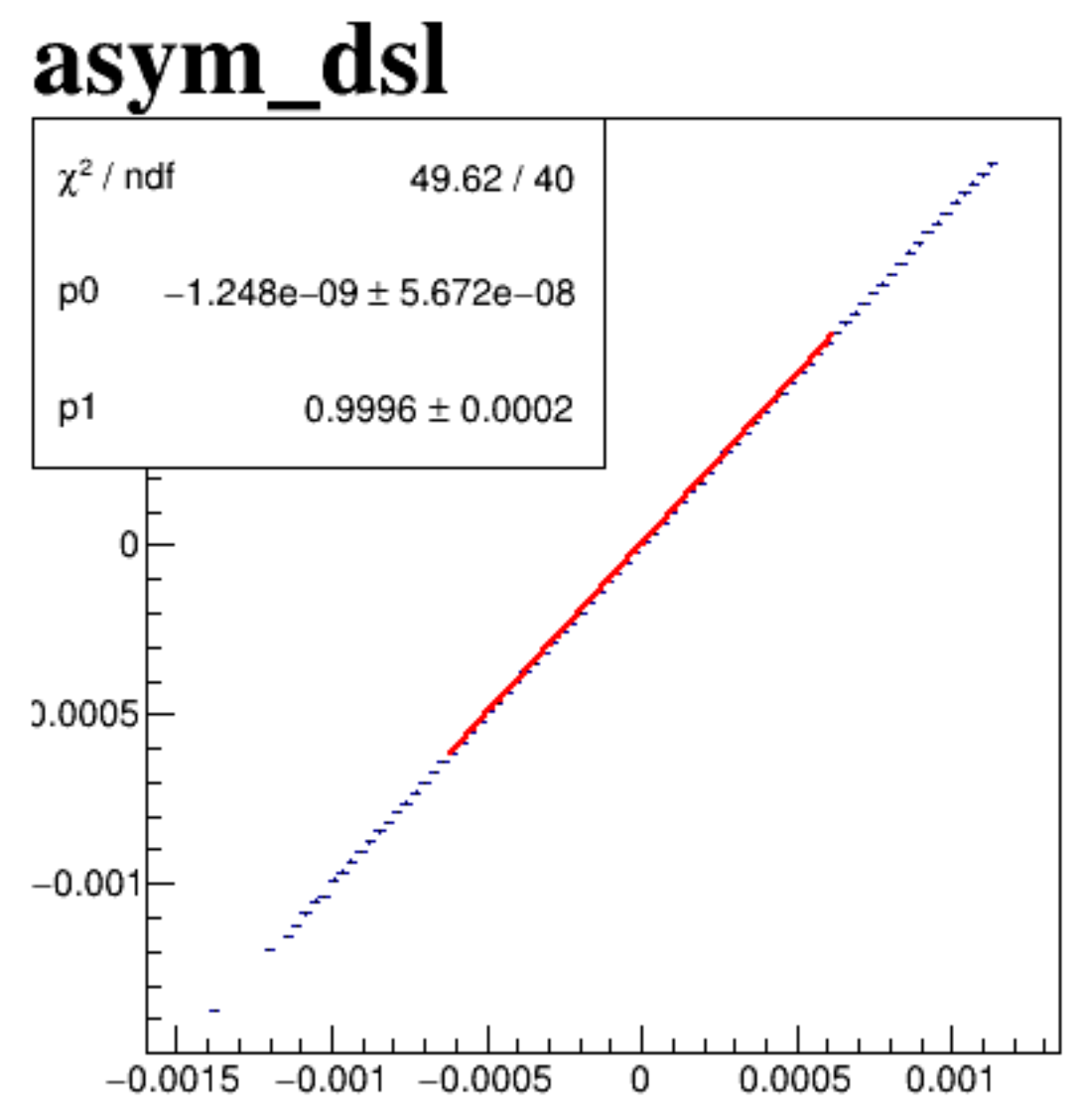
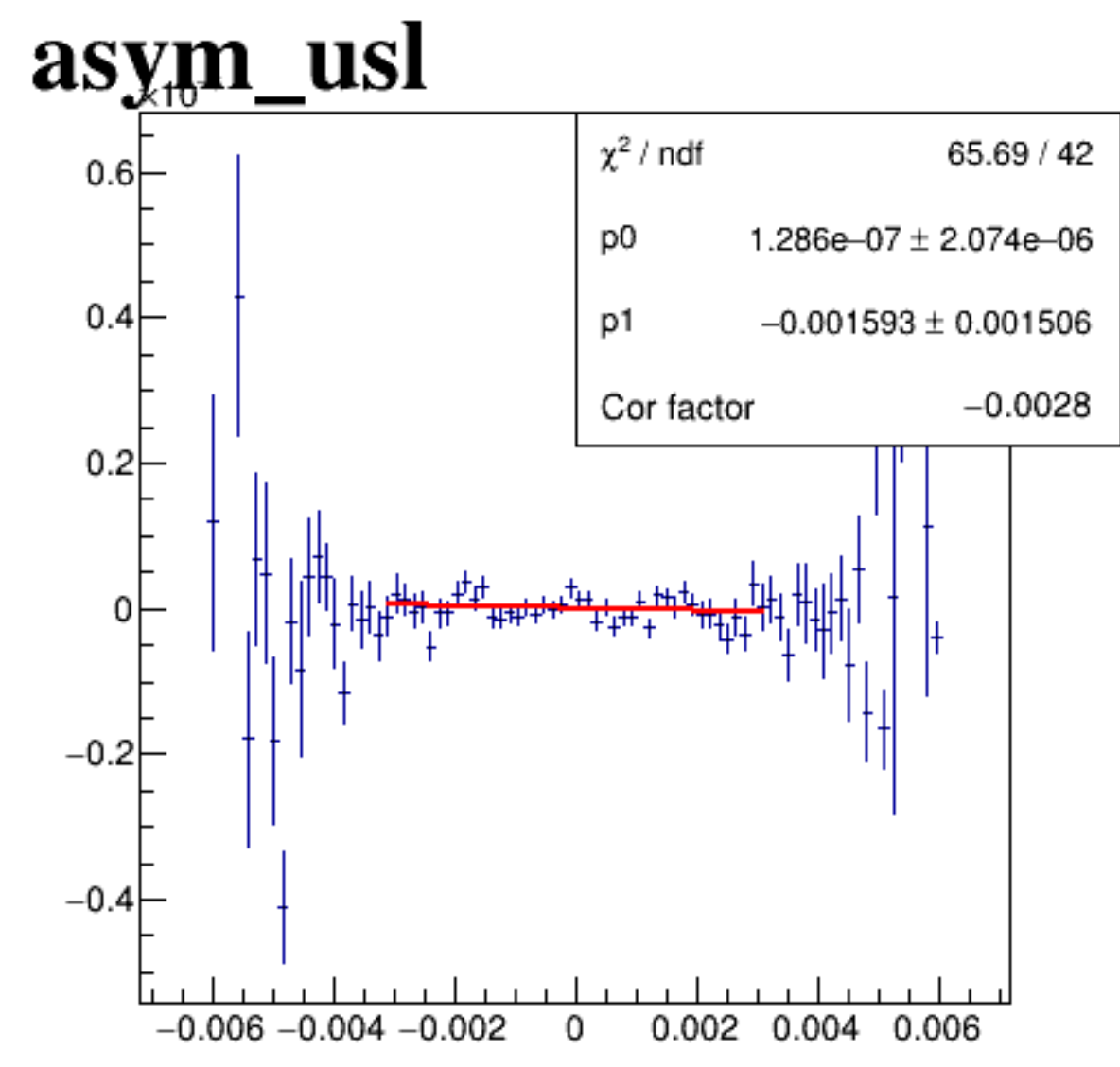
asym_dsr



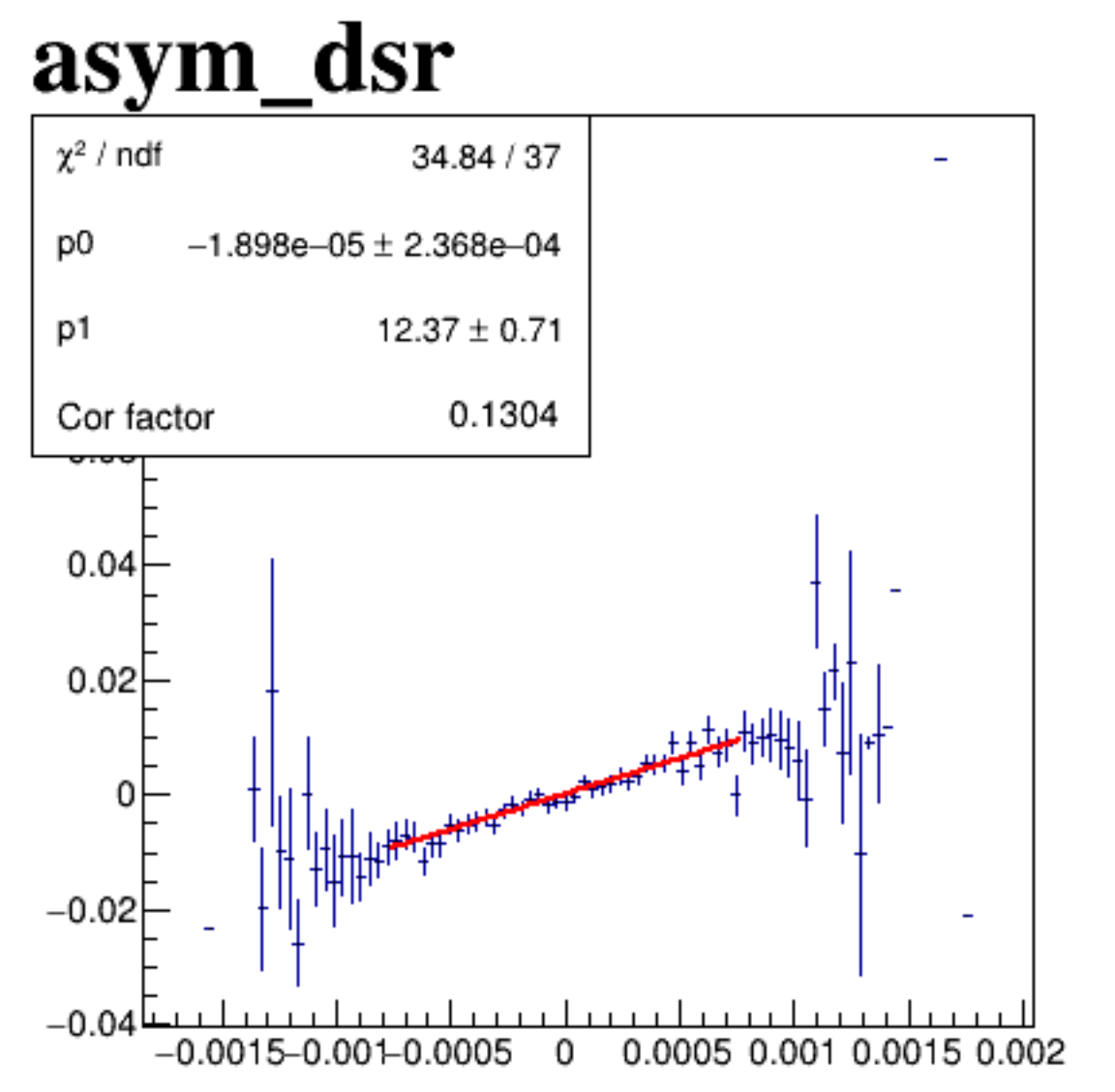
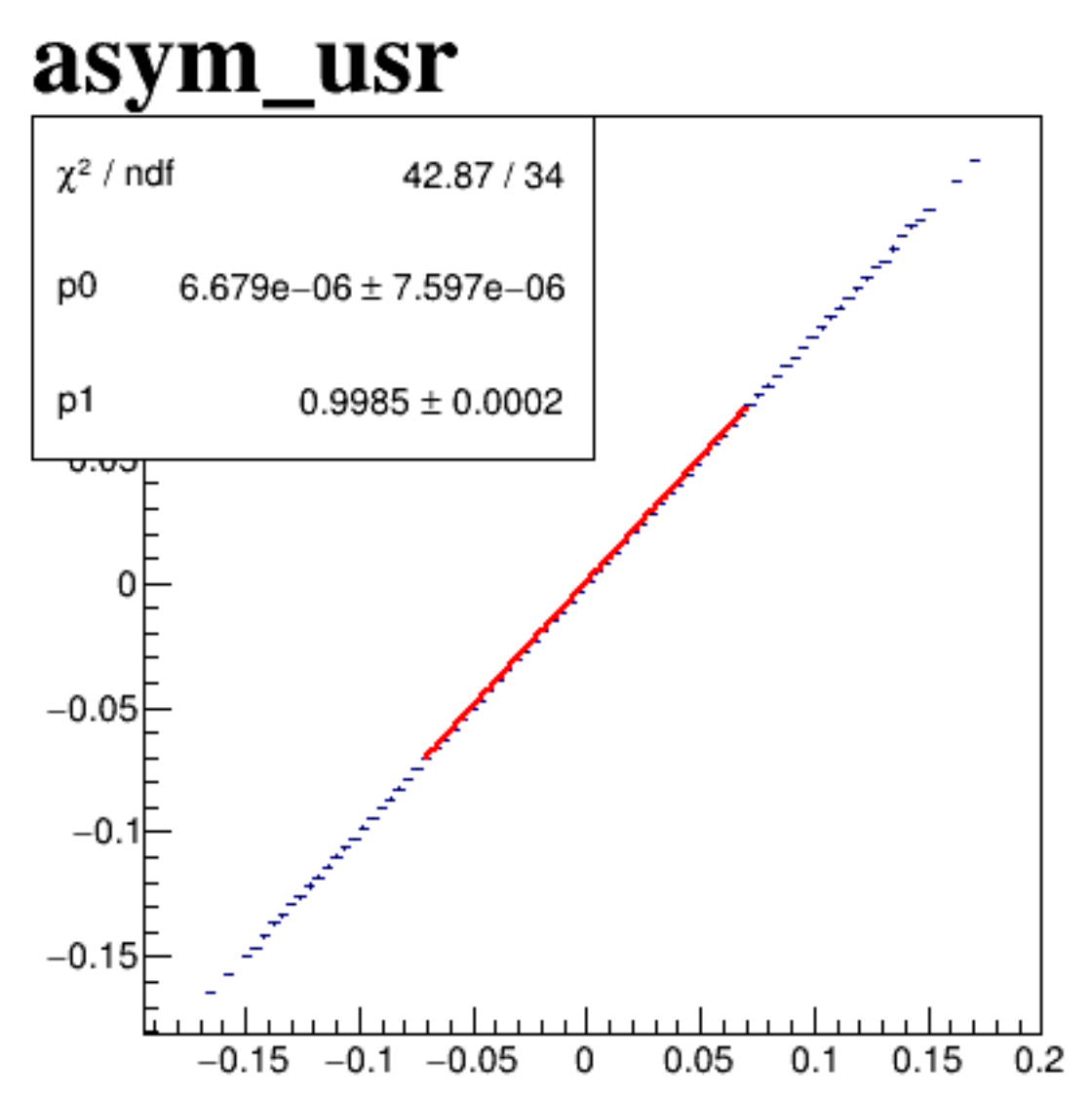
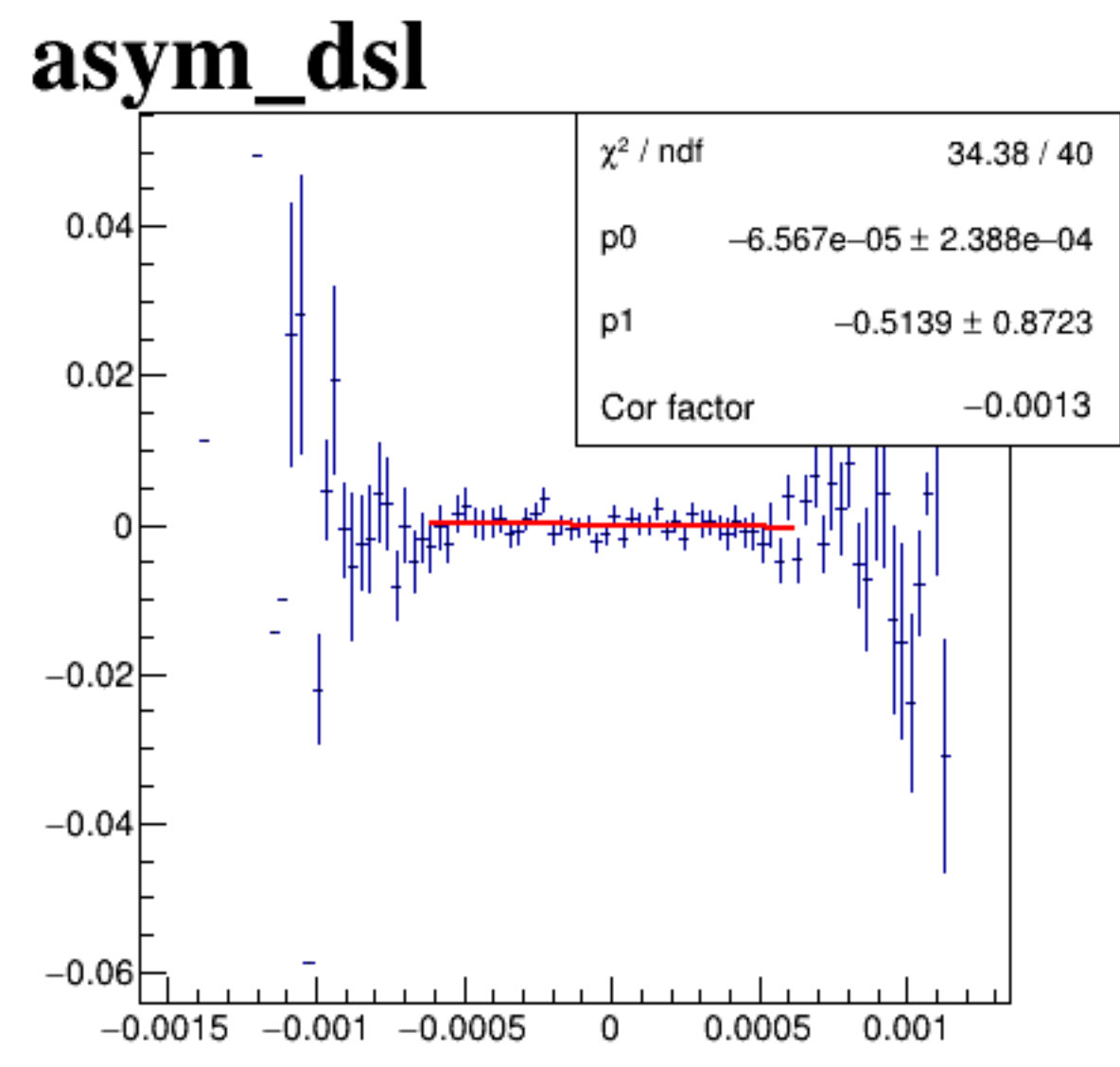
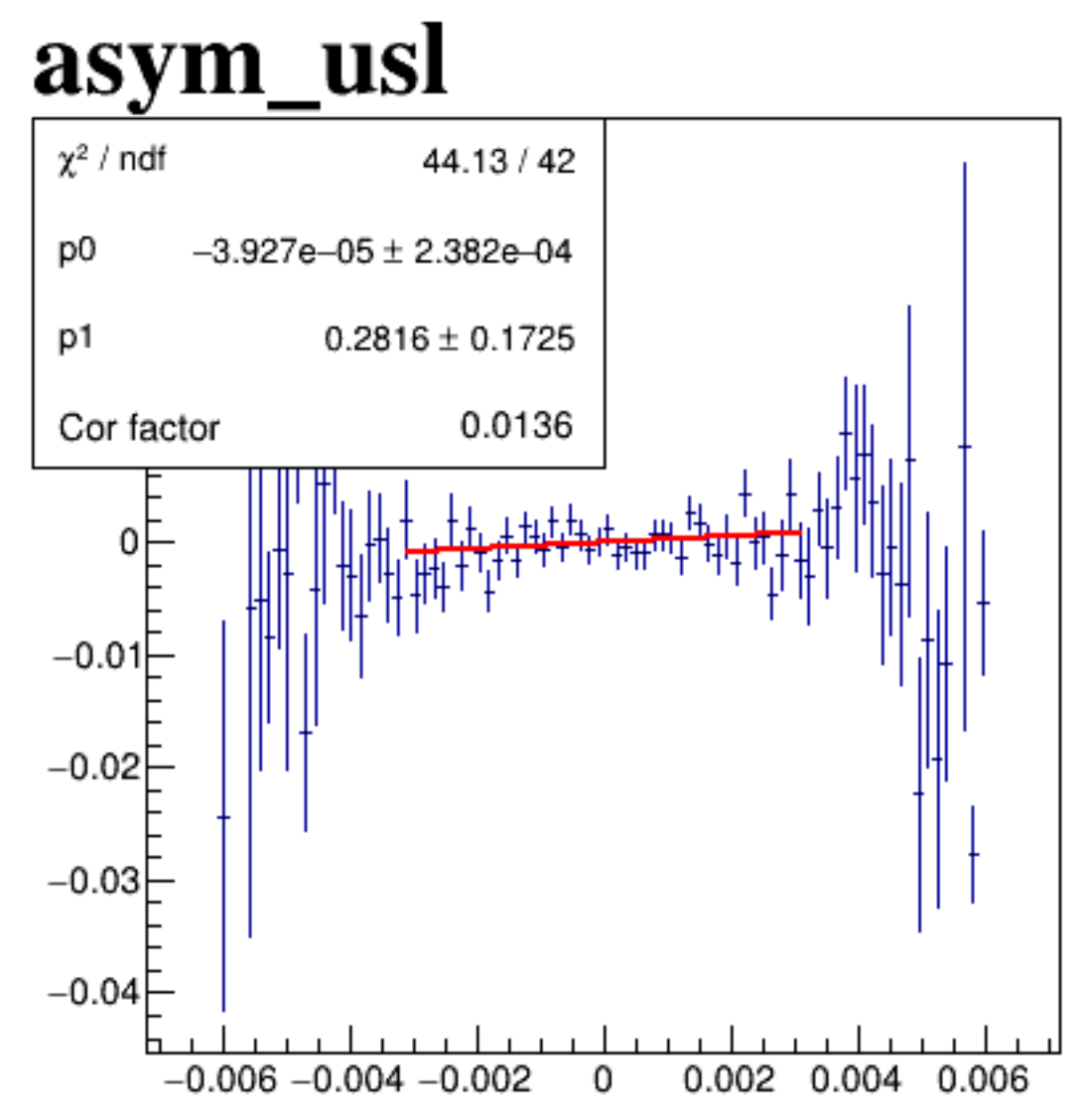
asym_usl



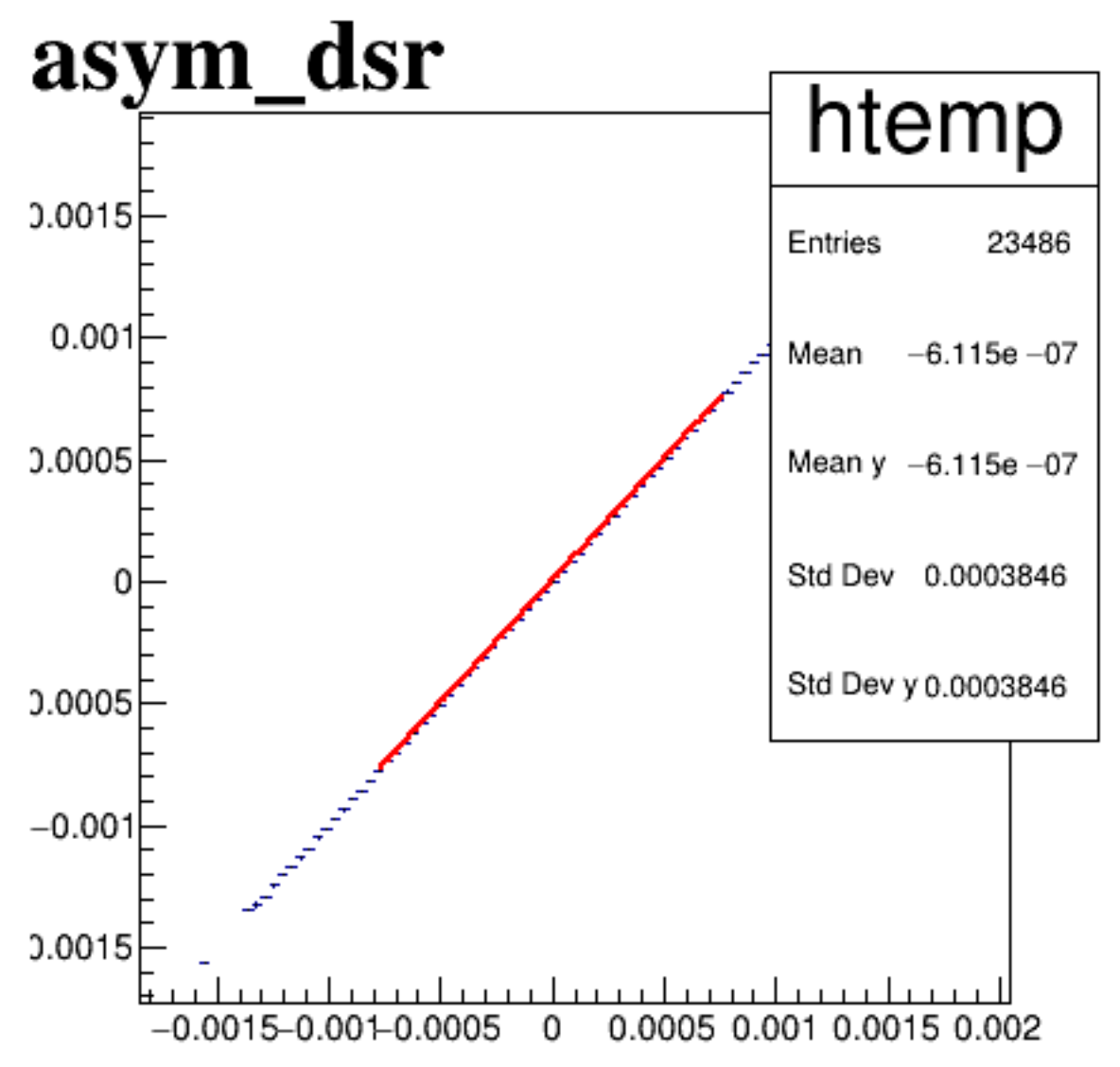
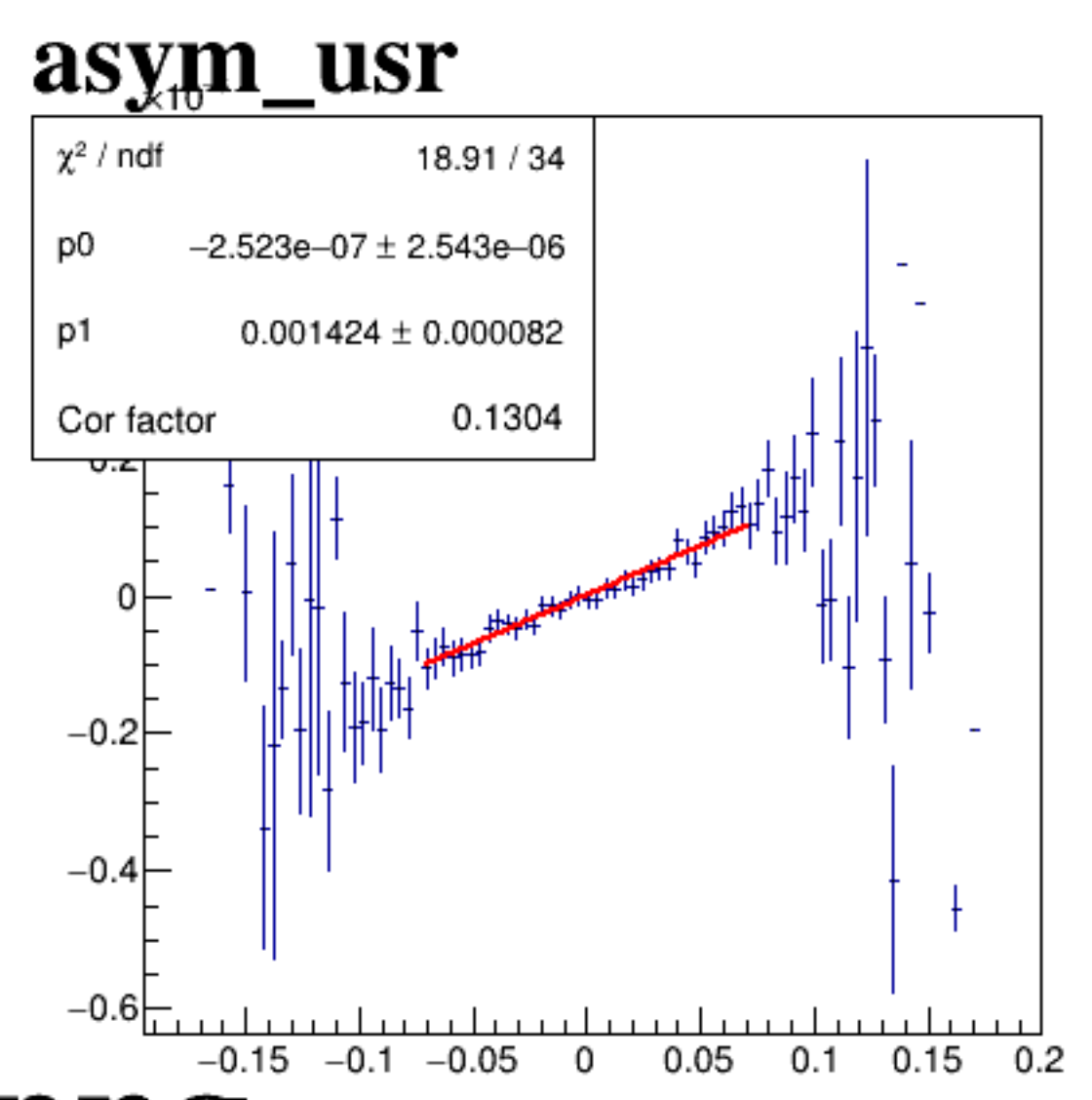
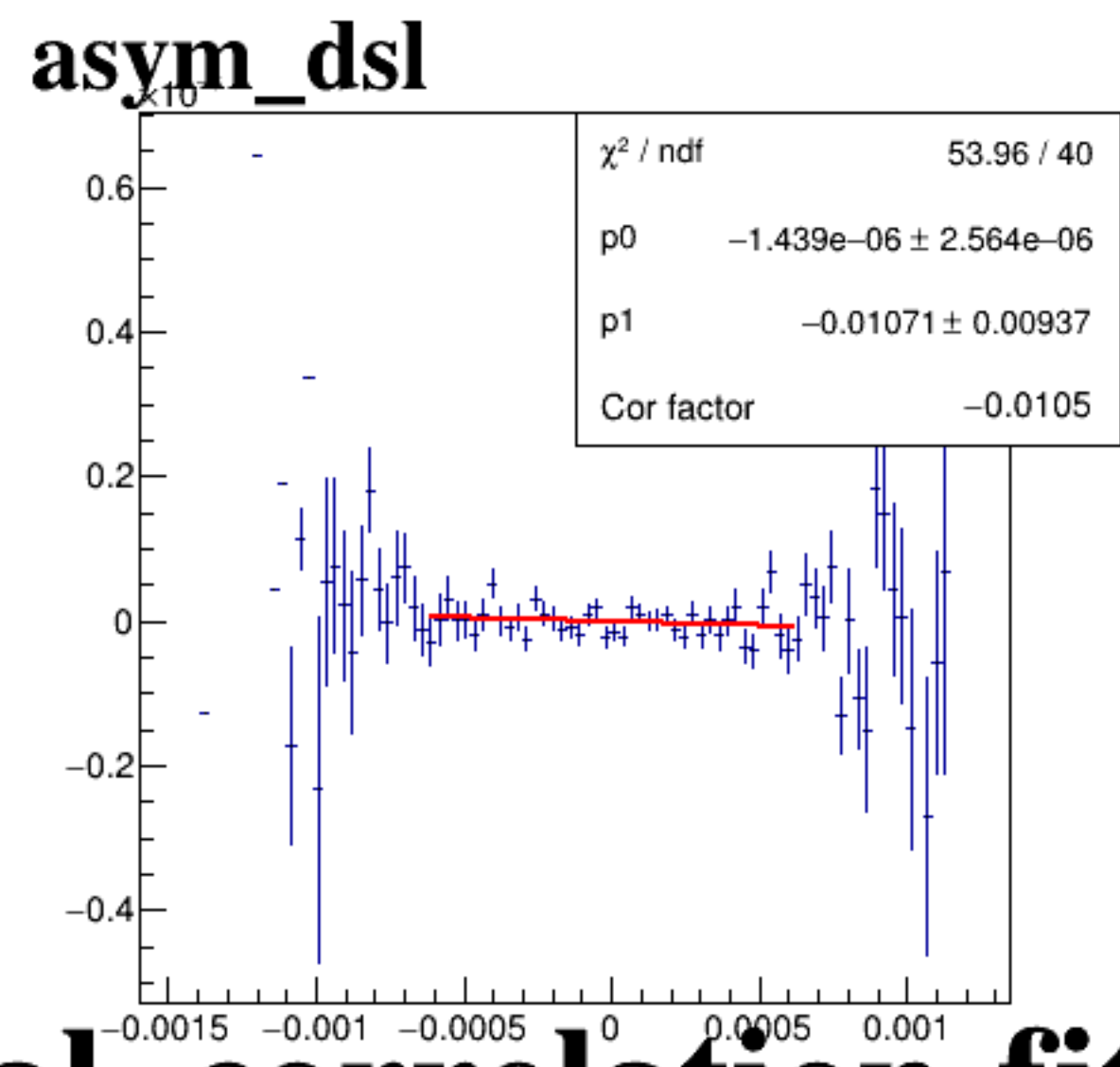
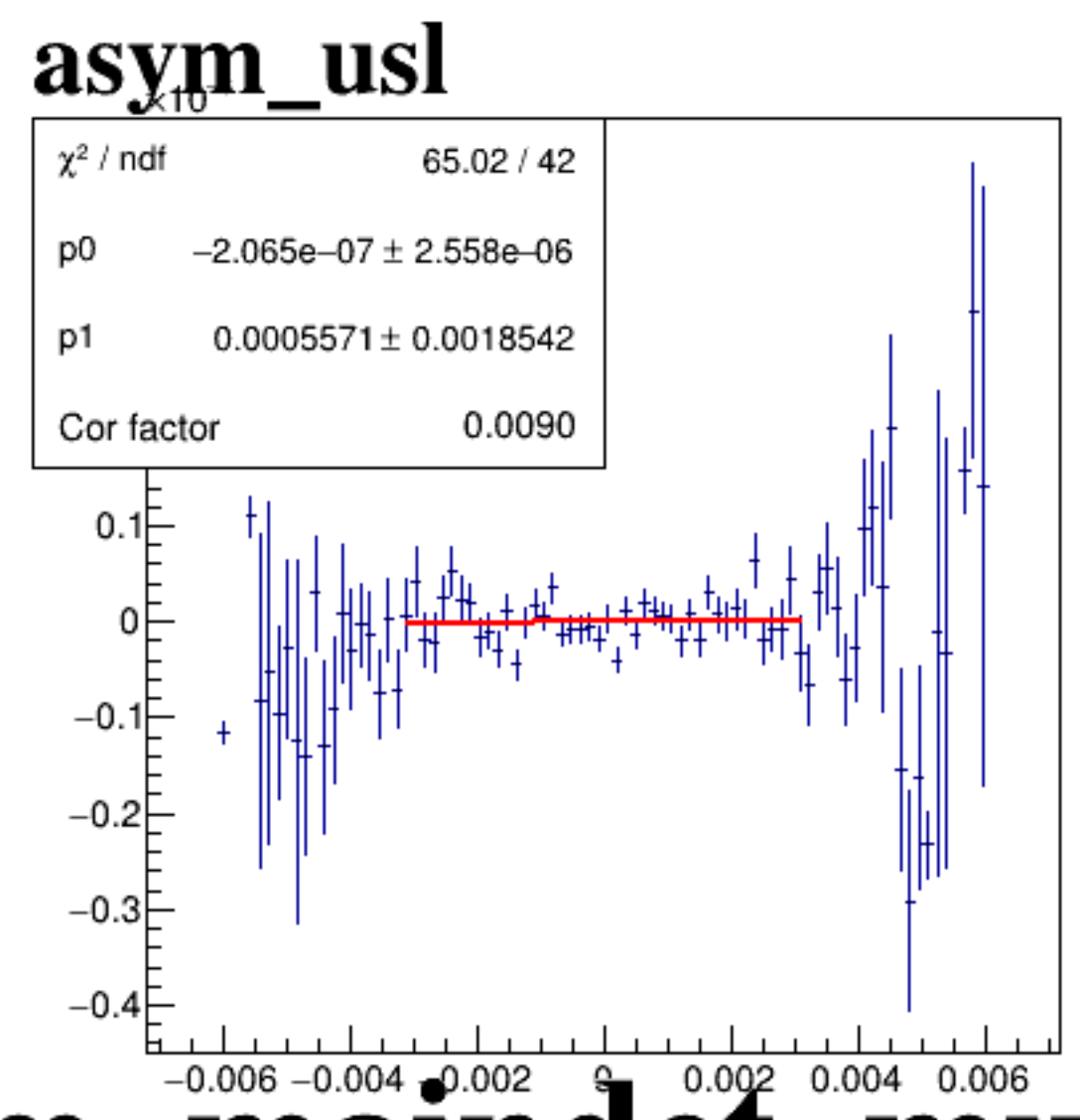
asym_dsl



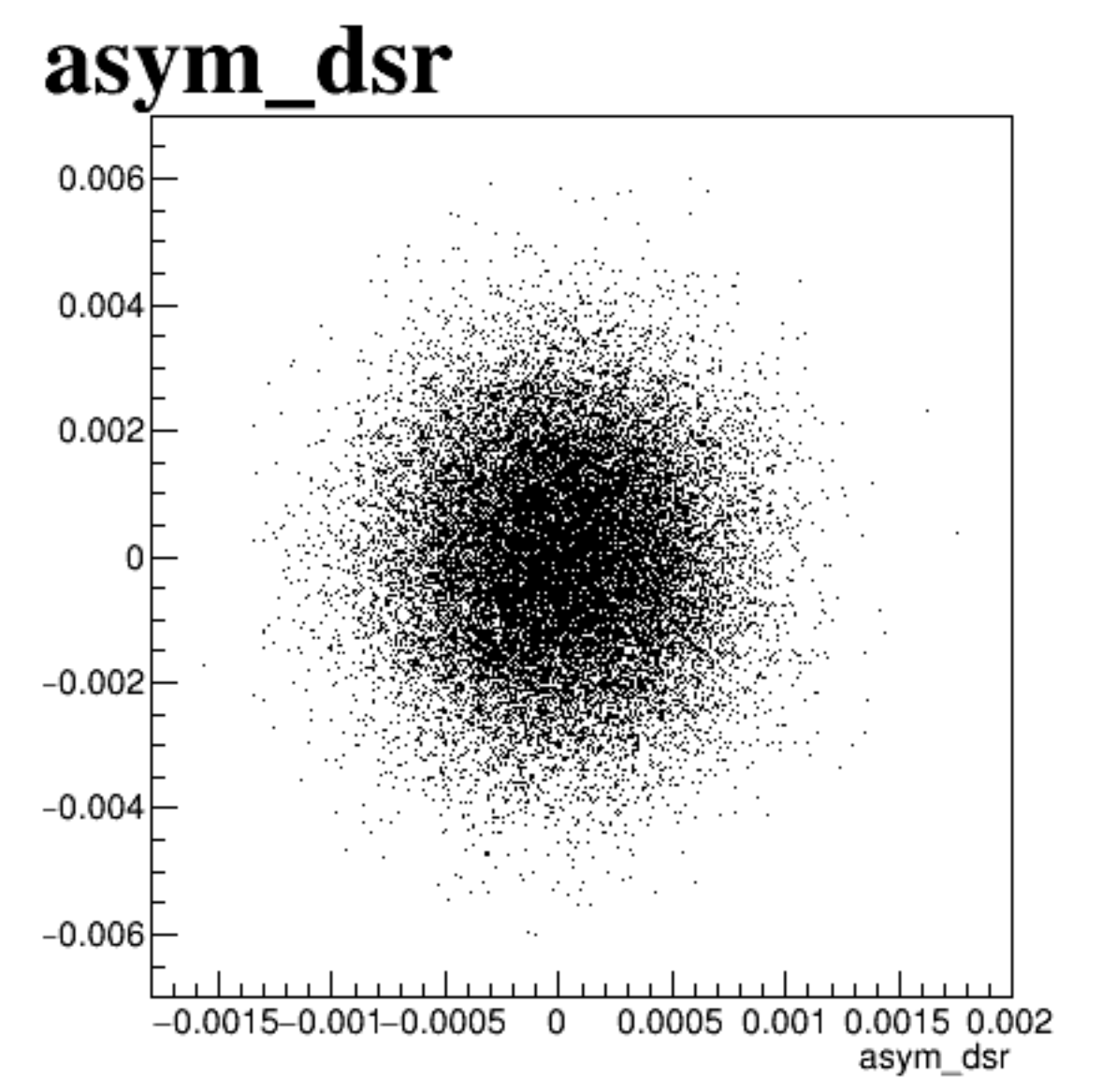
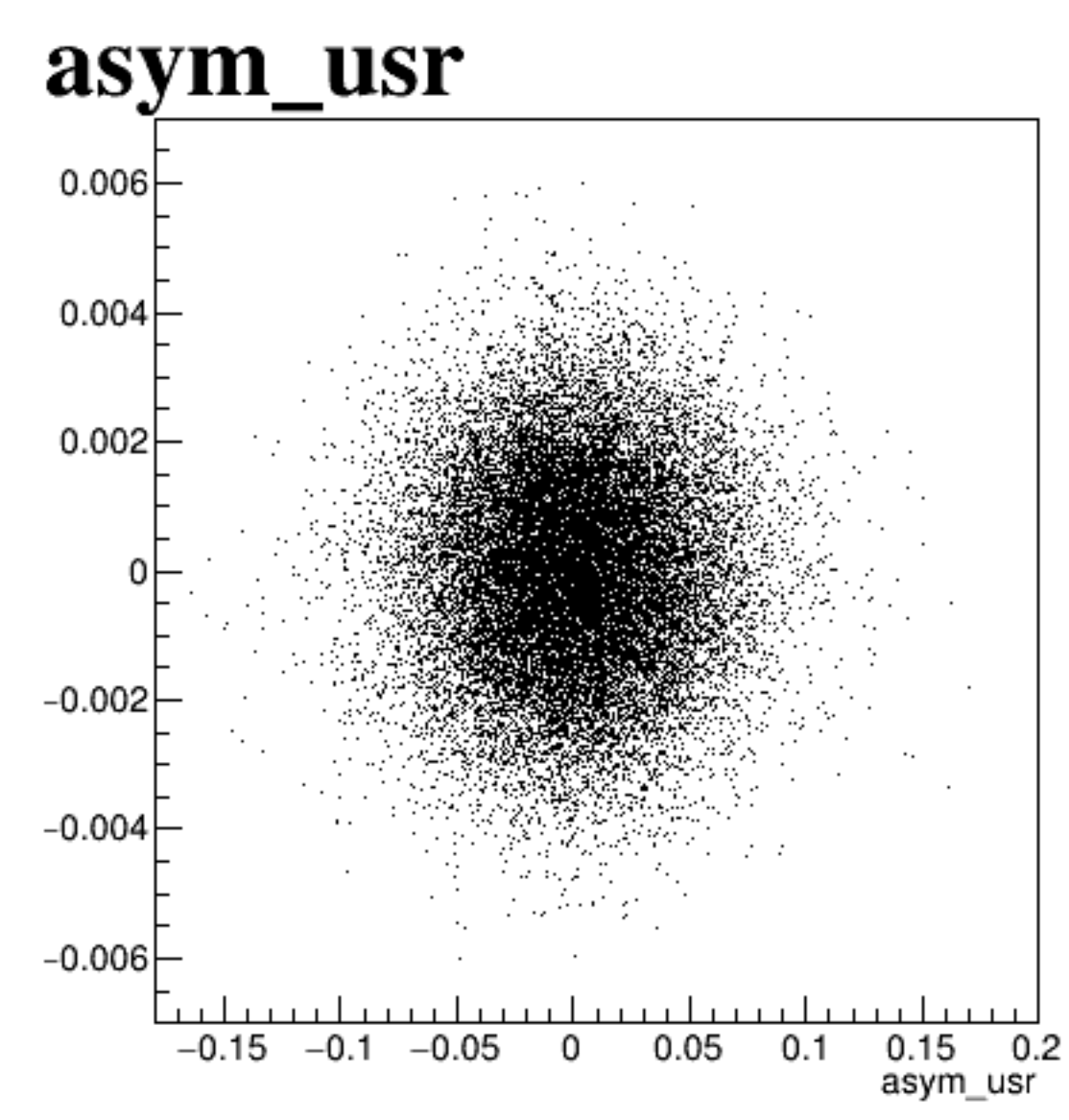
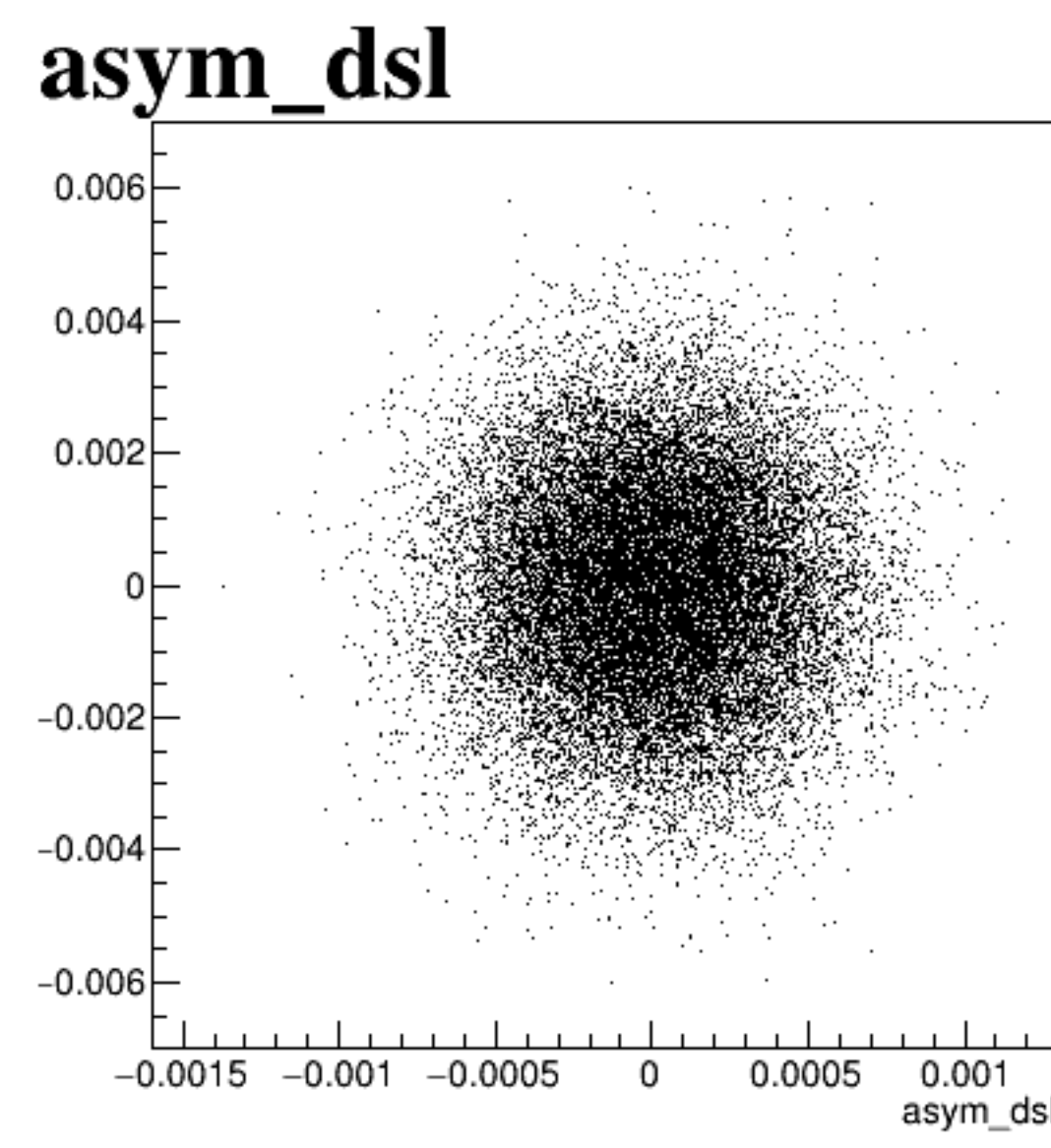
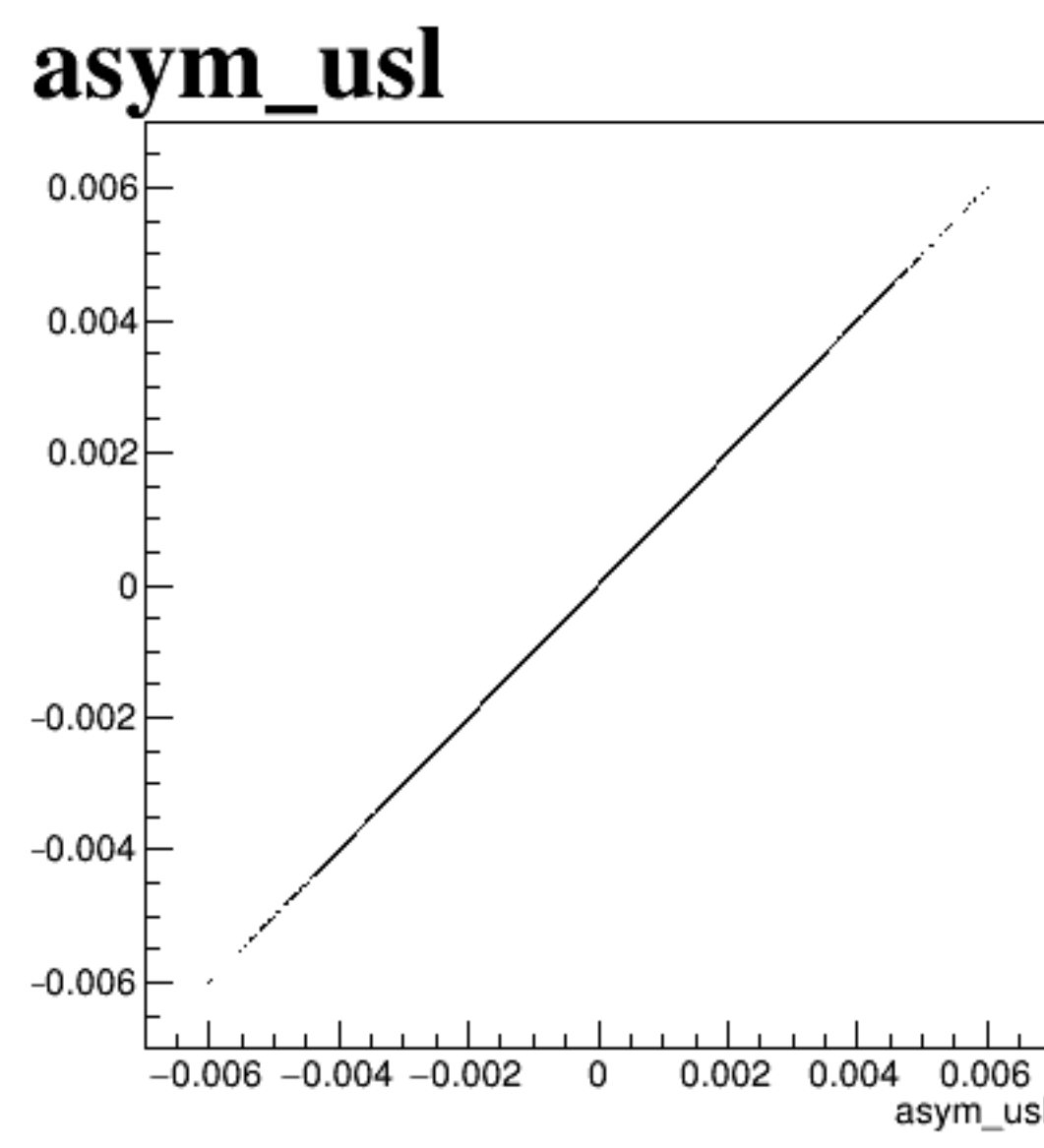
asym_usr



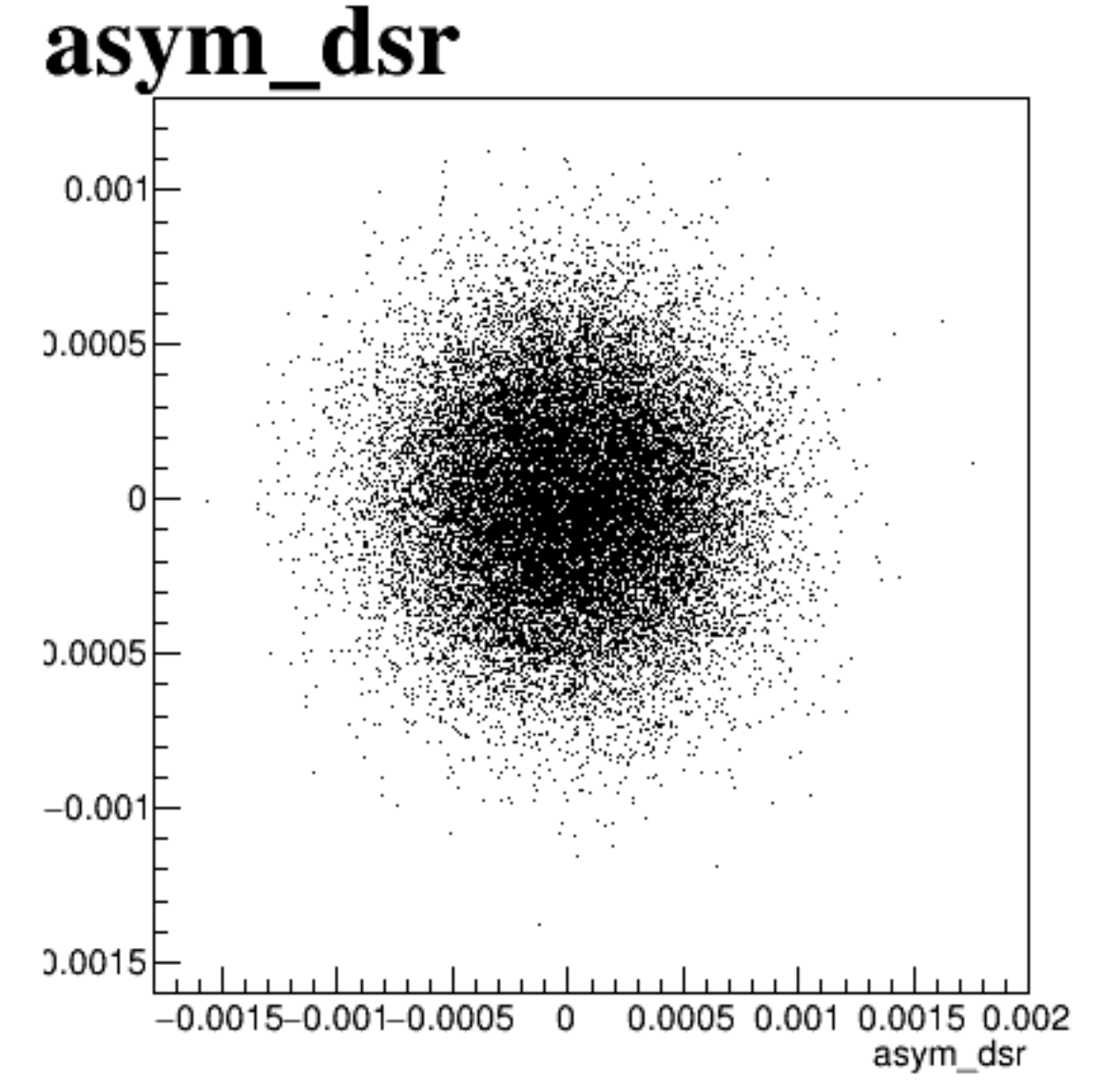
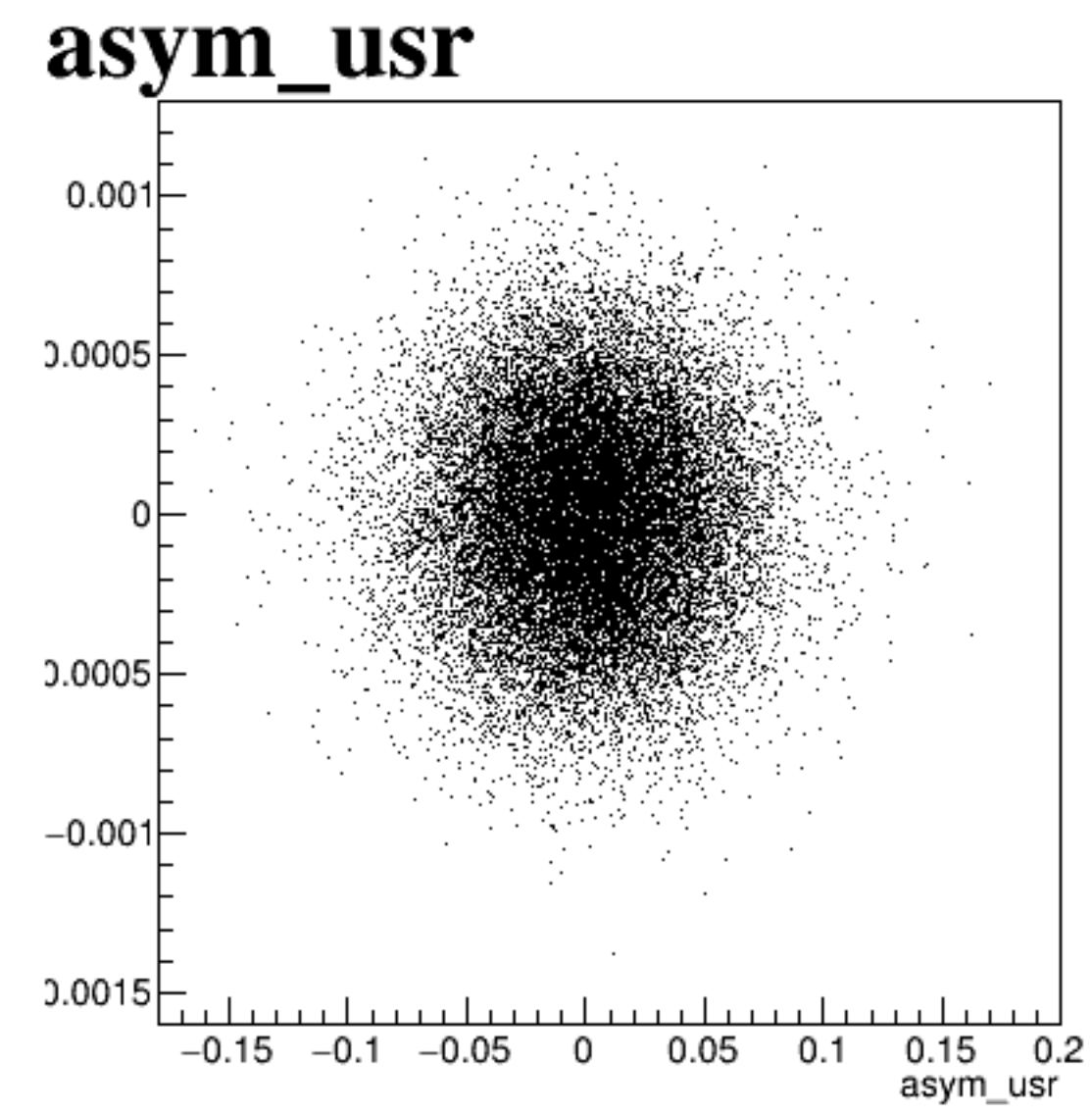
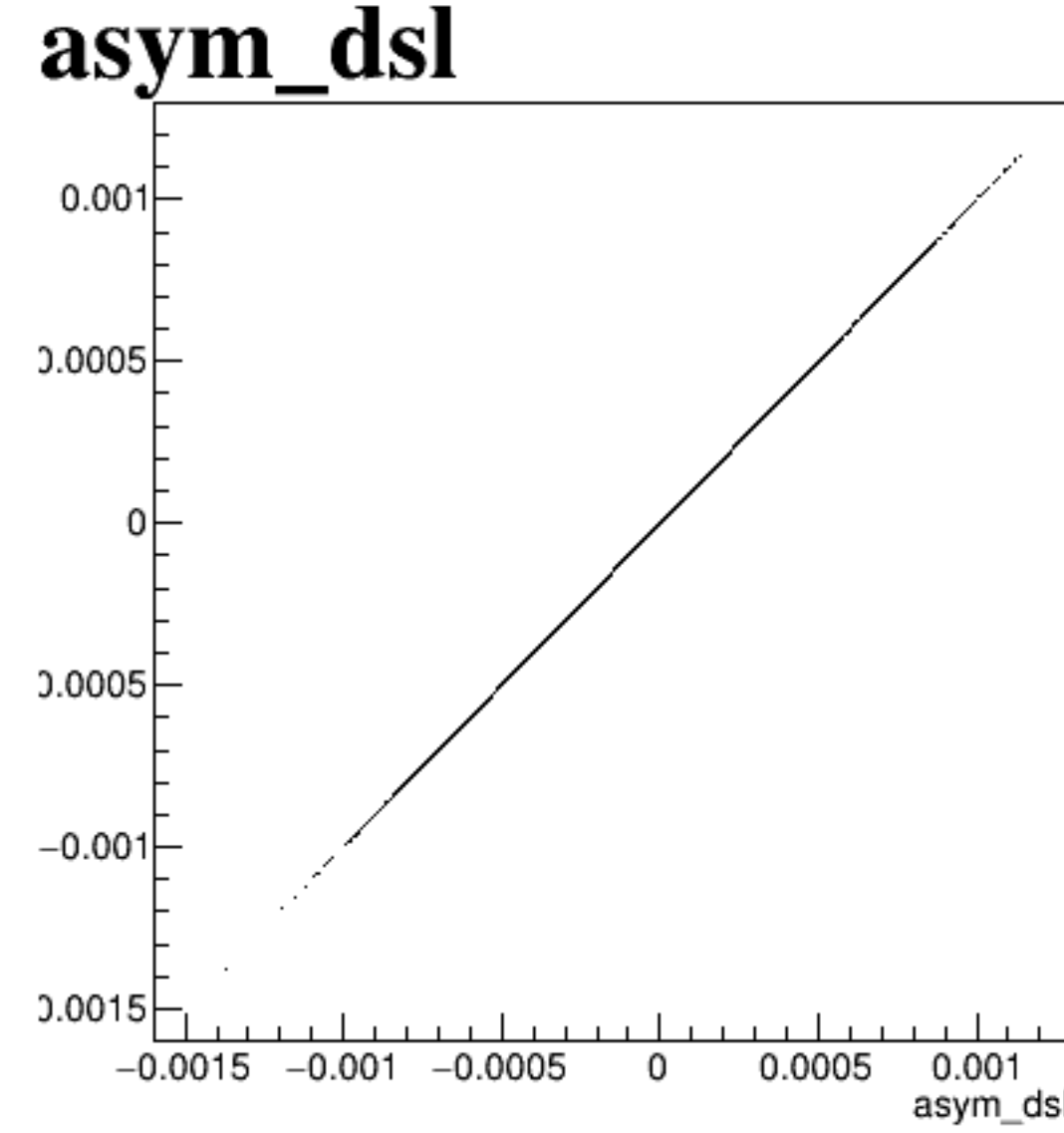
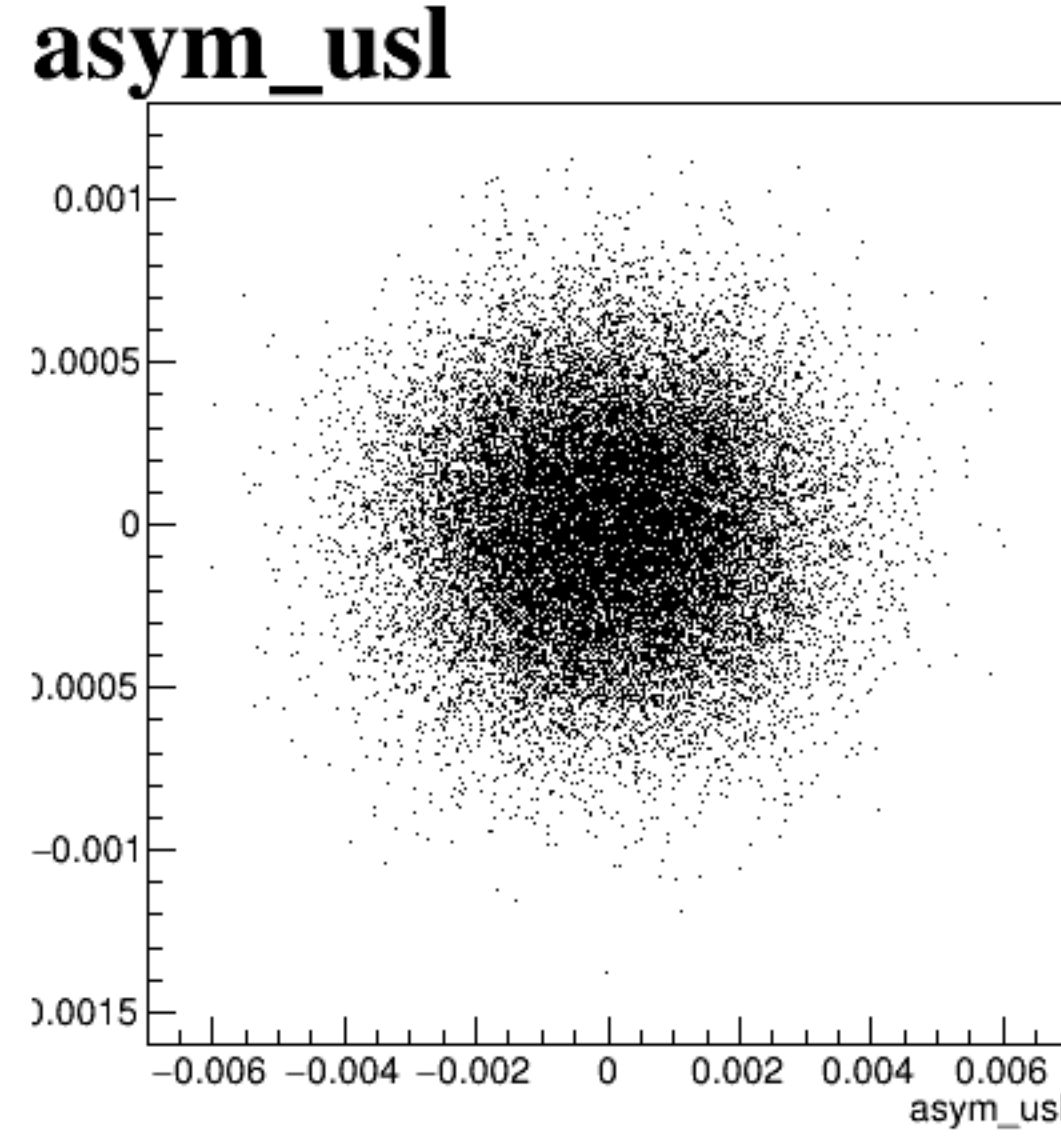
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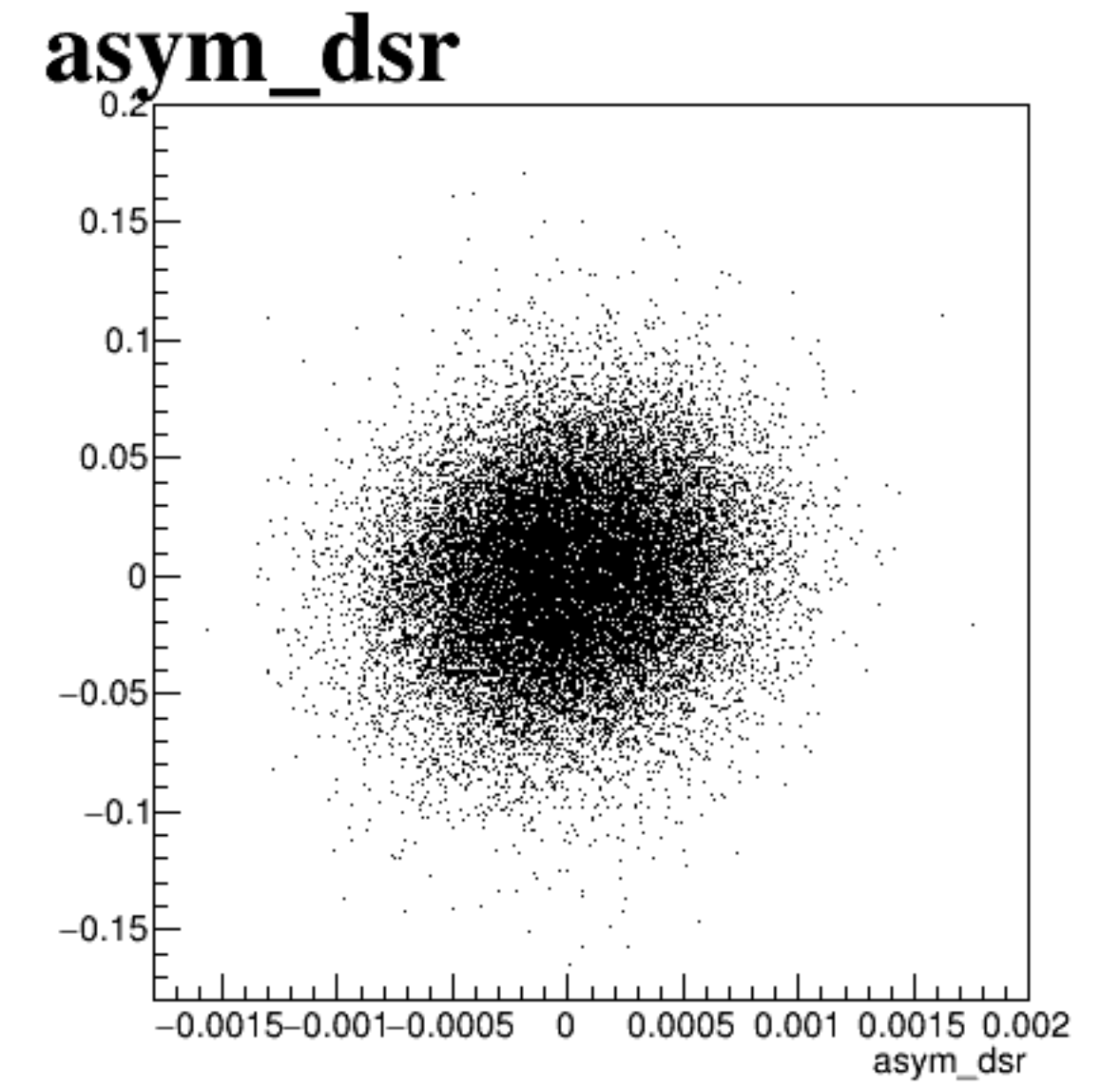
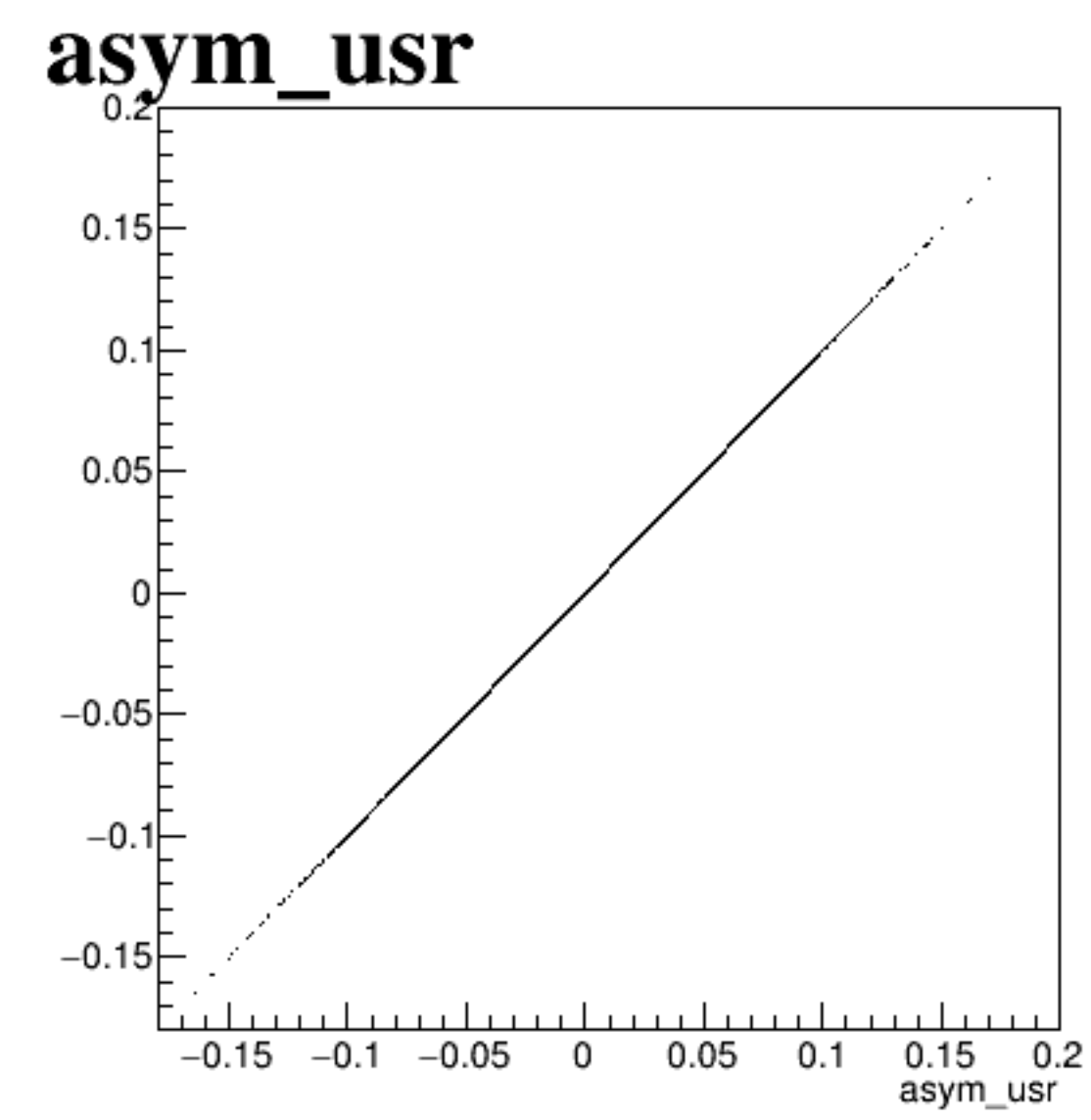
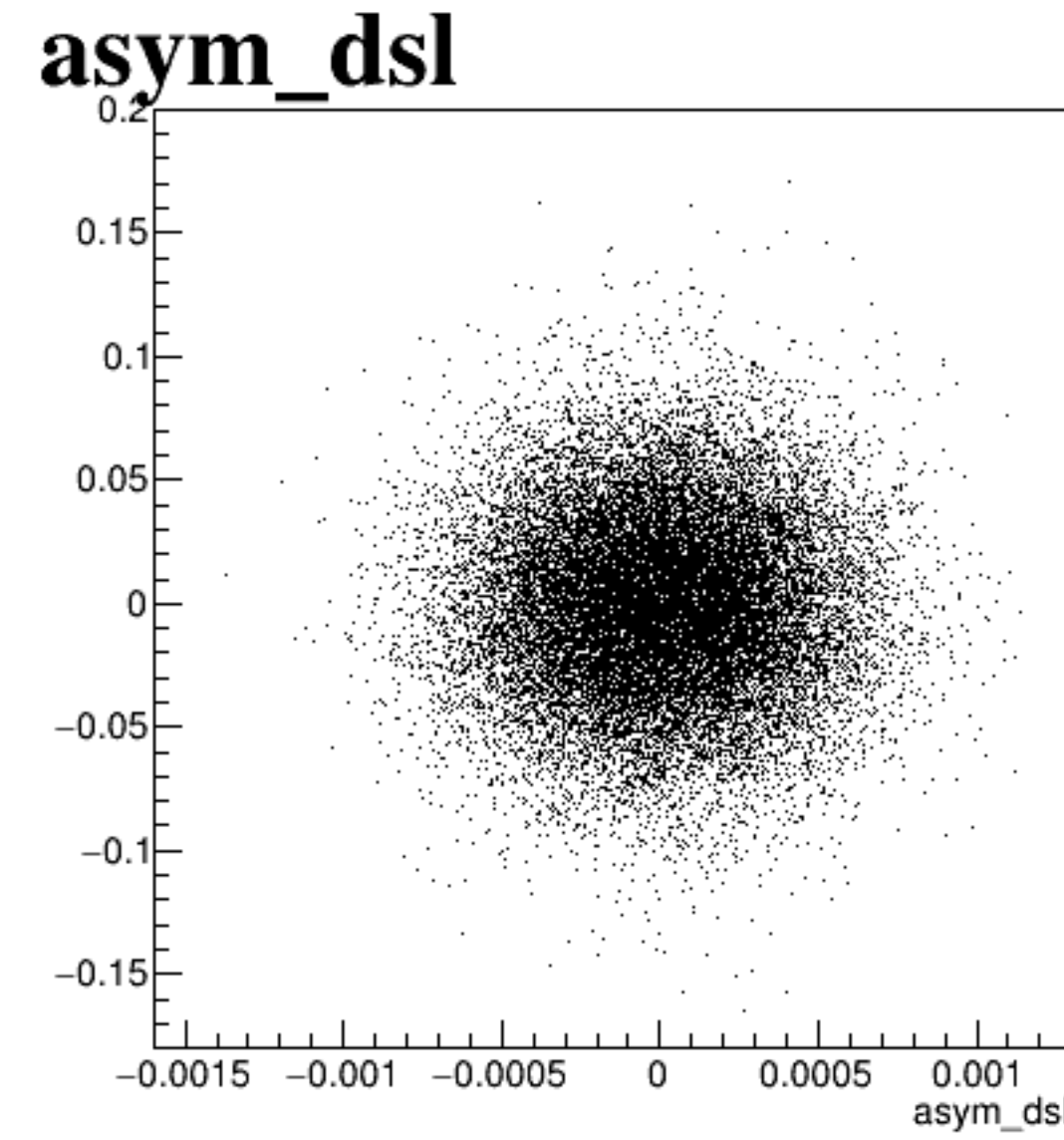
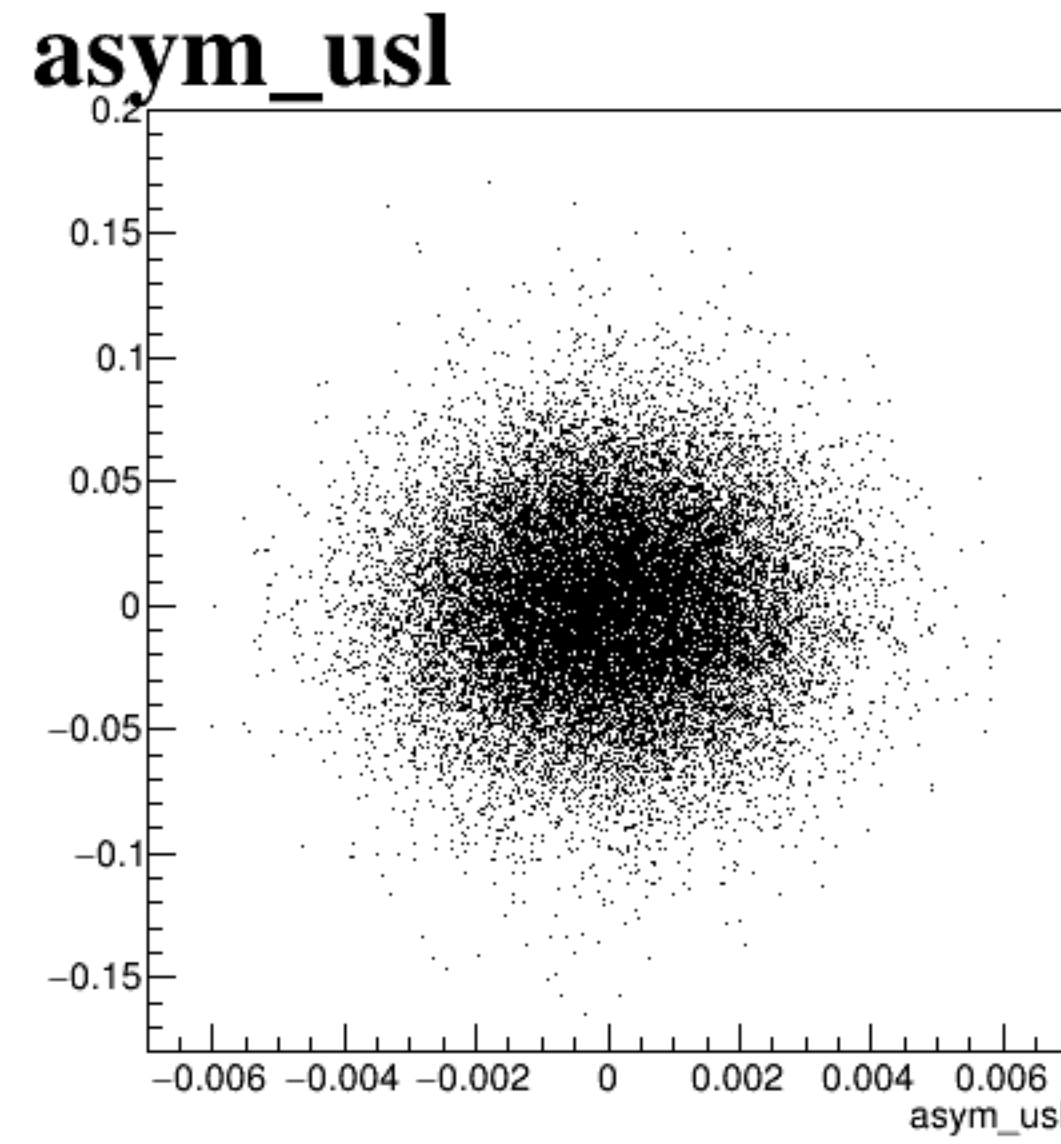
asym_usl



asym_dsl



asym_usr



asym_dsr

