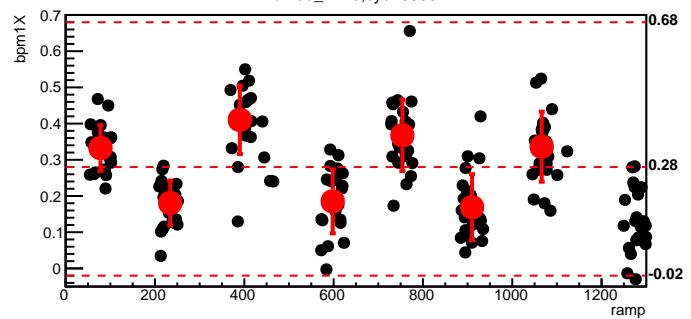


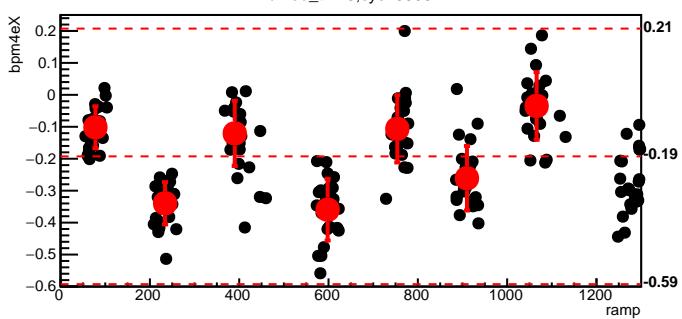




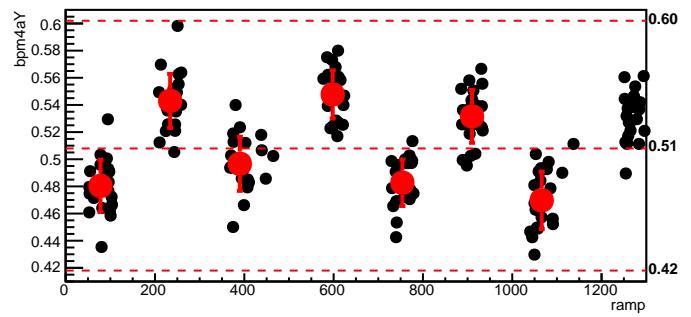
bmod Trim3,cyc=9508



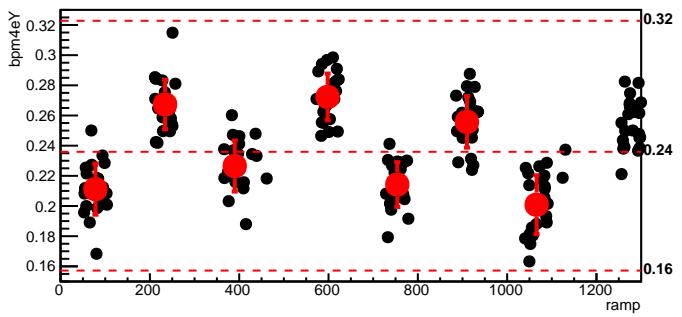
bmod Trim3,cyc=9508



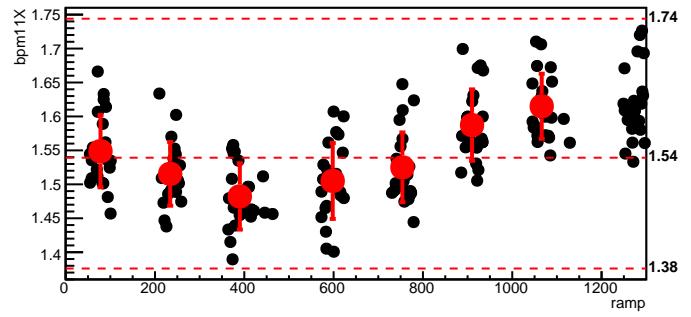
bmod Trim3,cyc=9508



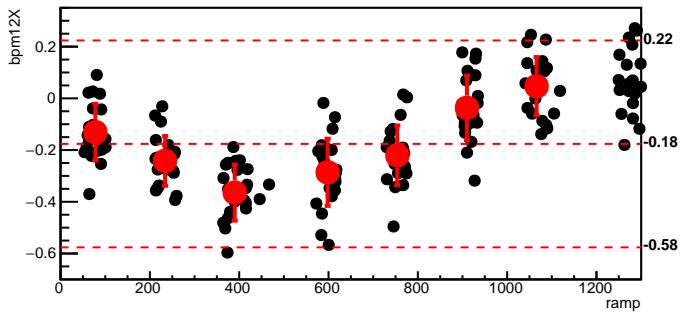
bmod Trim3,cyc=9508



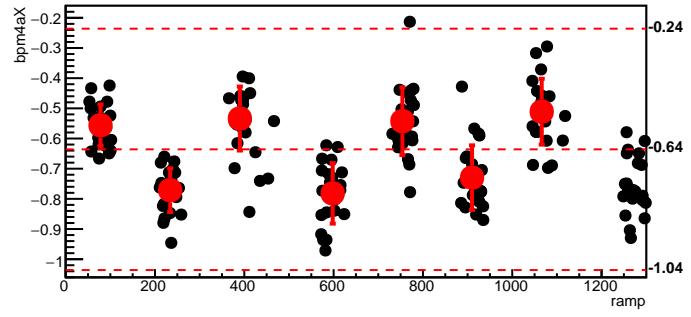
bmod Trim3,cyc=9508



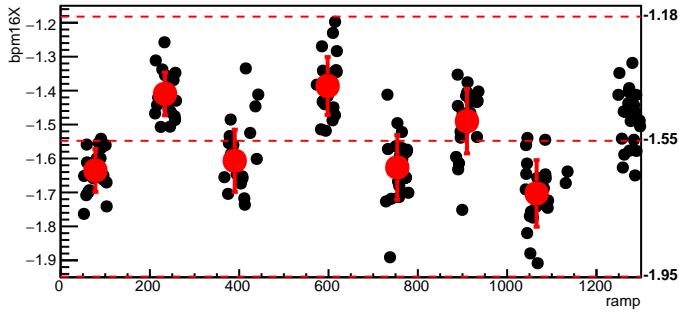
bmod Trim3,cyc=9508



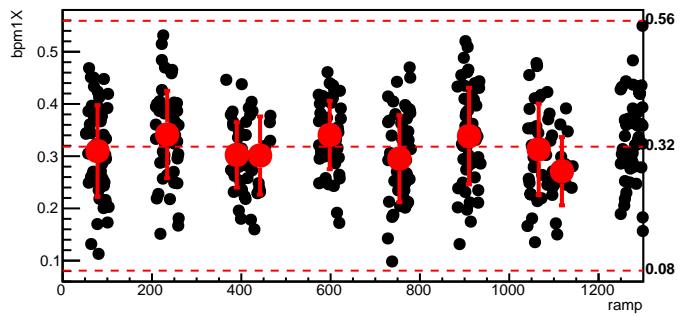
bmod Trim3,cyc=9508



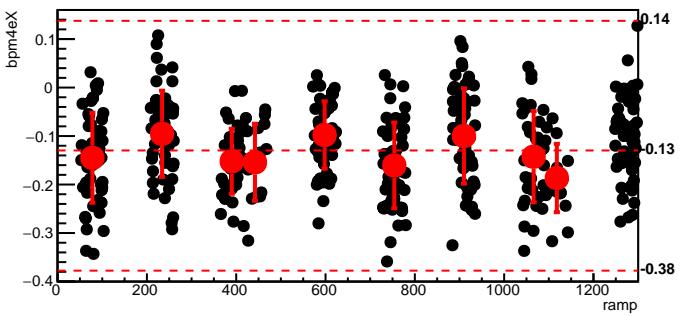
bmod Trim3,cyc=9508



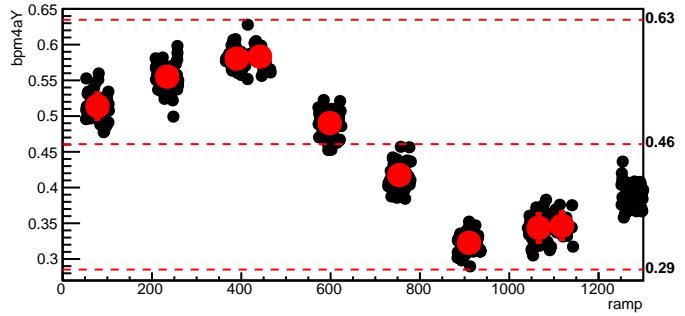
bmod Trim4,cyc=9508



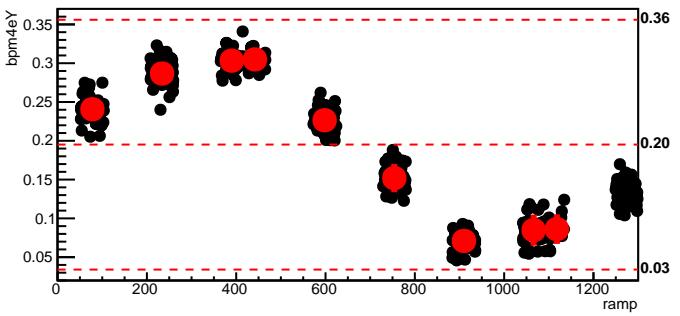
bmod Trim4,cyc=9508



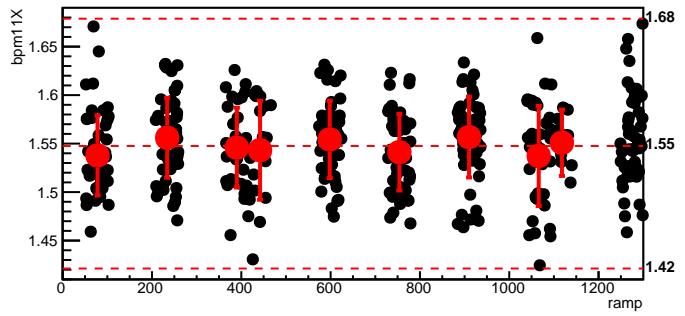
bmod Trim4,cyc=9508



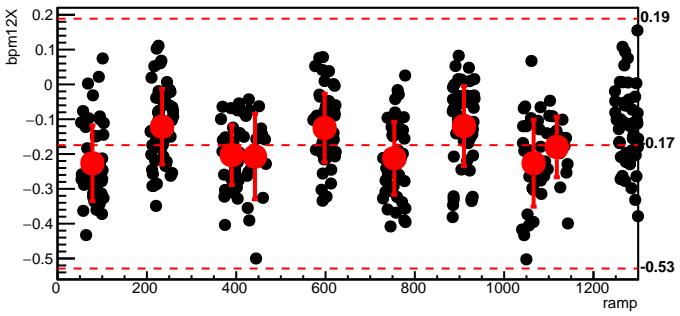
bmod Trim4,cyc=9508



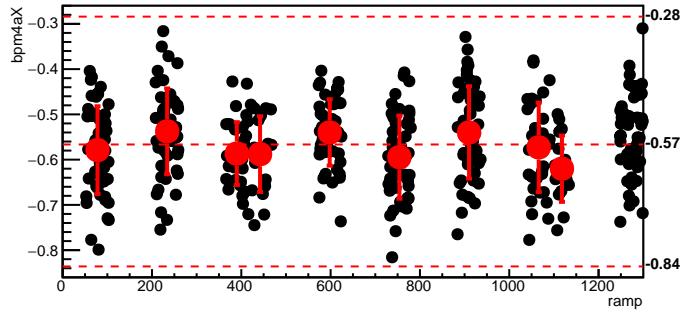
bmod Trim4,cyc=9508



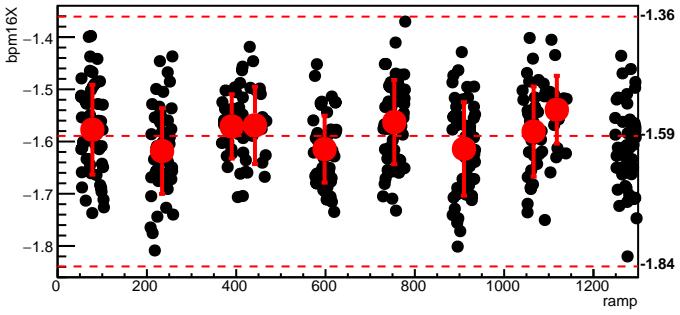
bmod Trim4,cyc=9508



bmod Trim4,cyc=9508



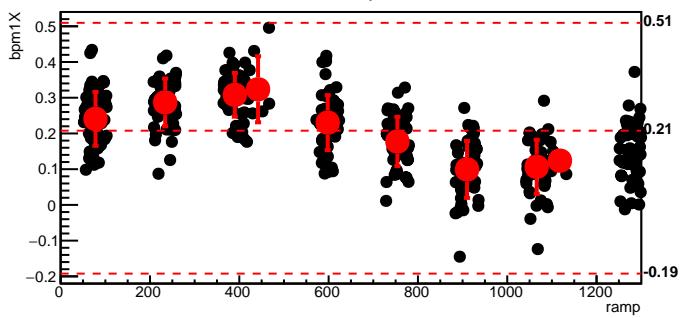
bmod Trim4,cyc=9508



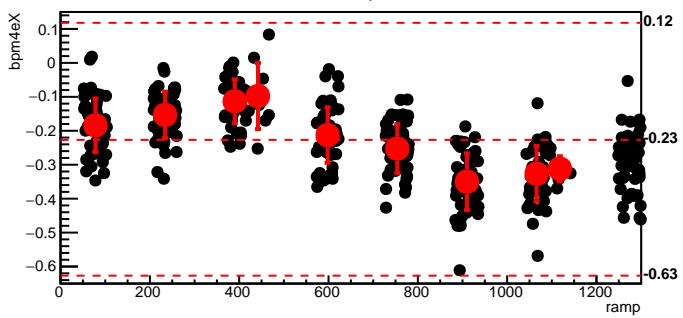




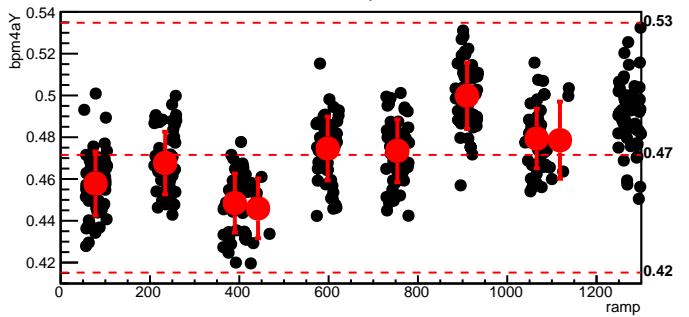
bmod Trim7,cyc=9508



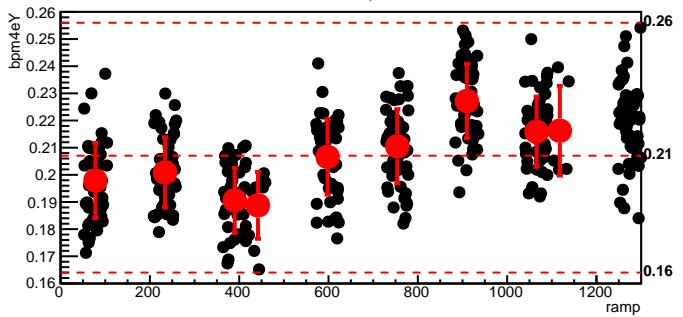
bmod Trim7,cyc=9508



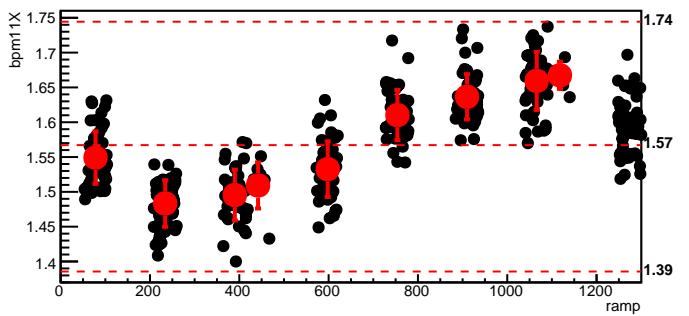
bmod Trim7,cyc=9508



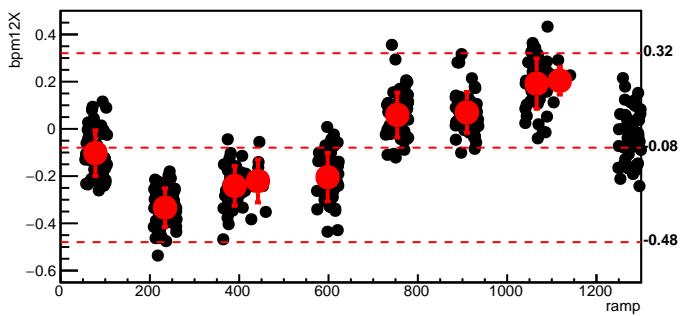
bmod Trim7,cyc=9508



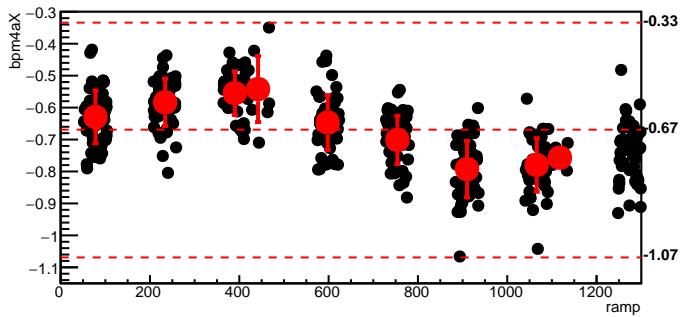
bmod Trim7,cyc=9508



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bmod Trim7,cyc=9508

