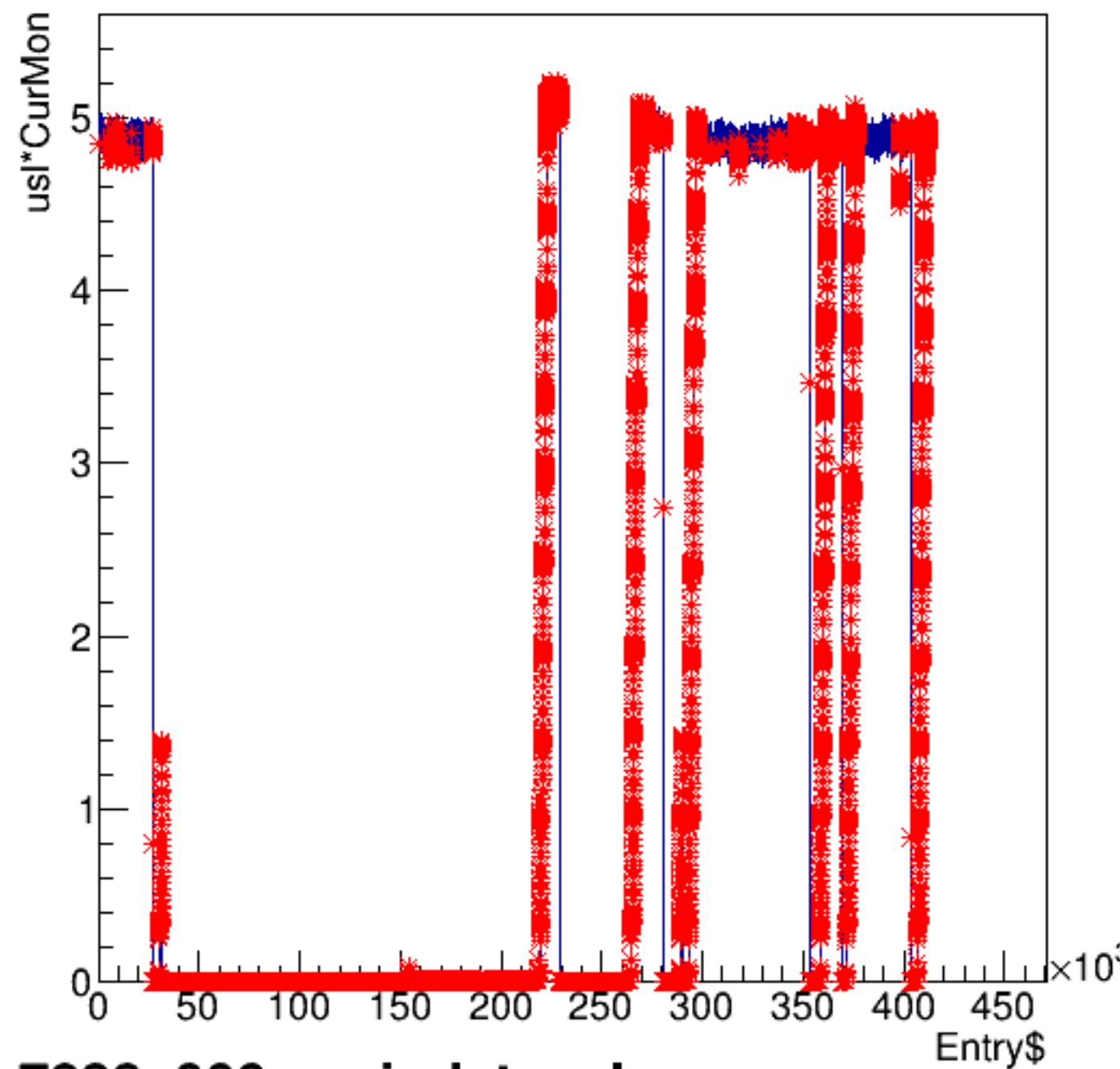
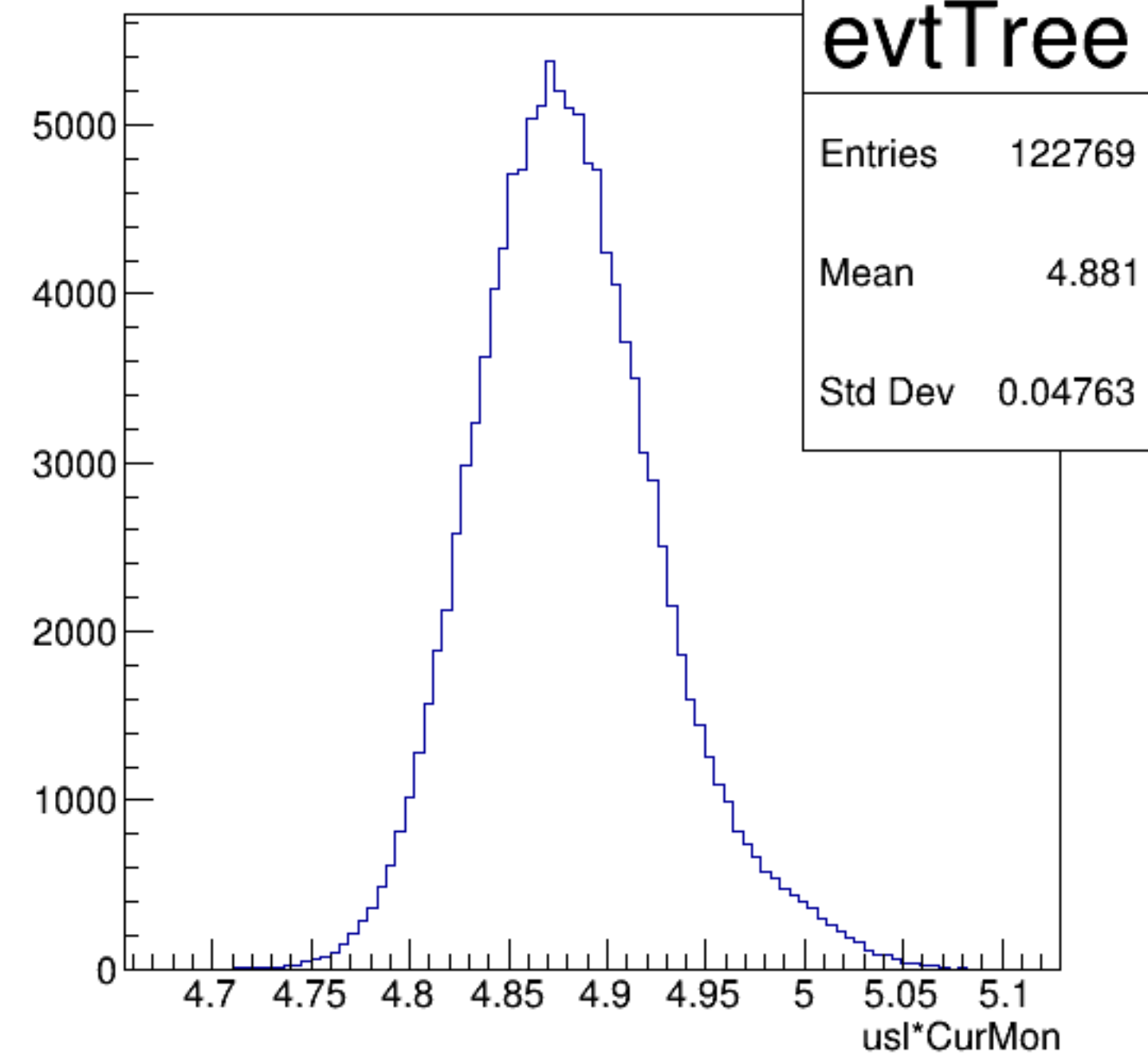


usl*CurMon:Entry\$



run7838_000_maindet_usl.png

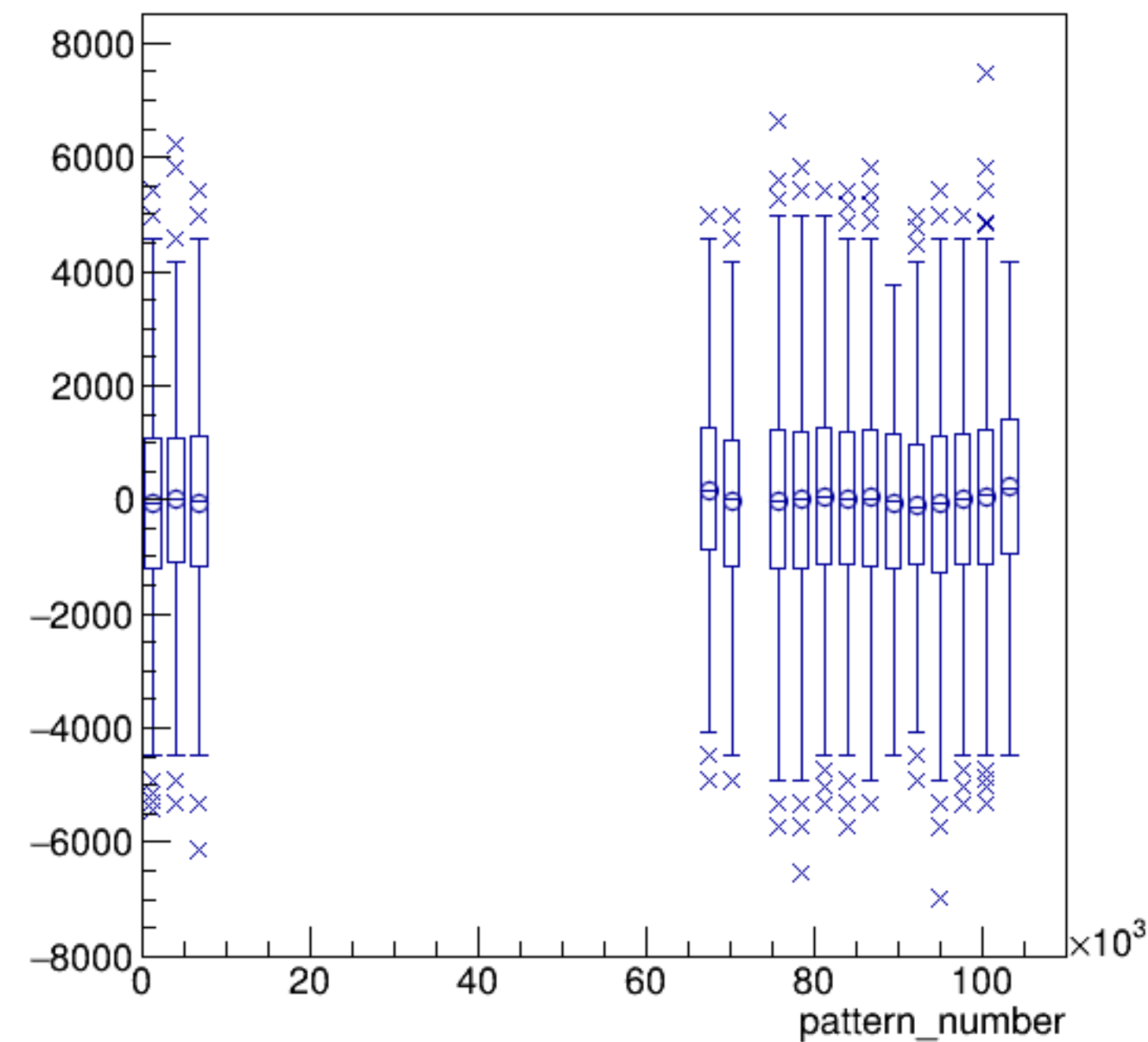
usl*CurMon {ErrorFlag==0}



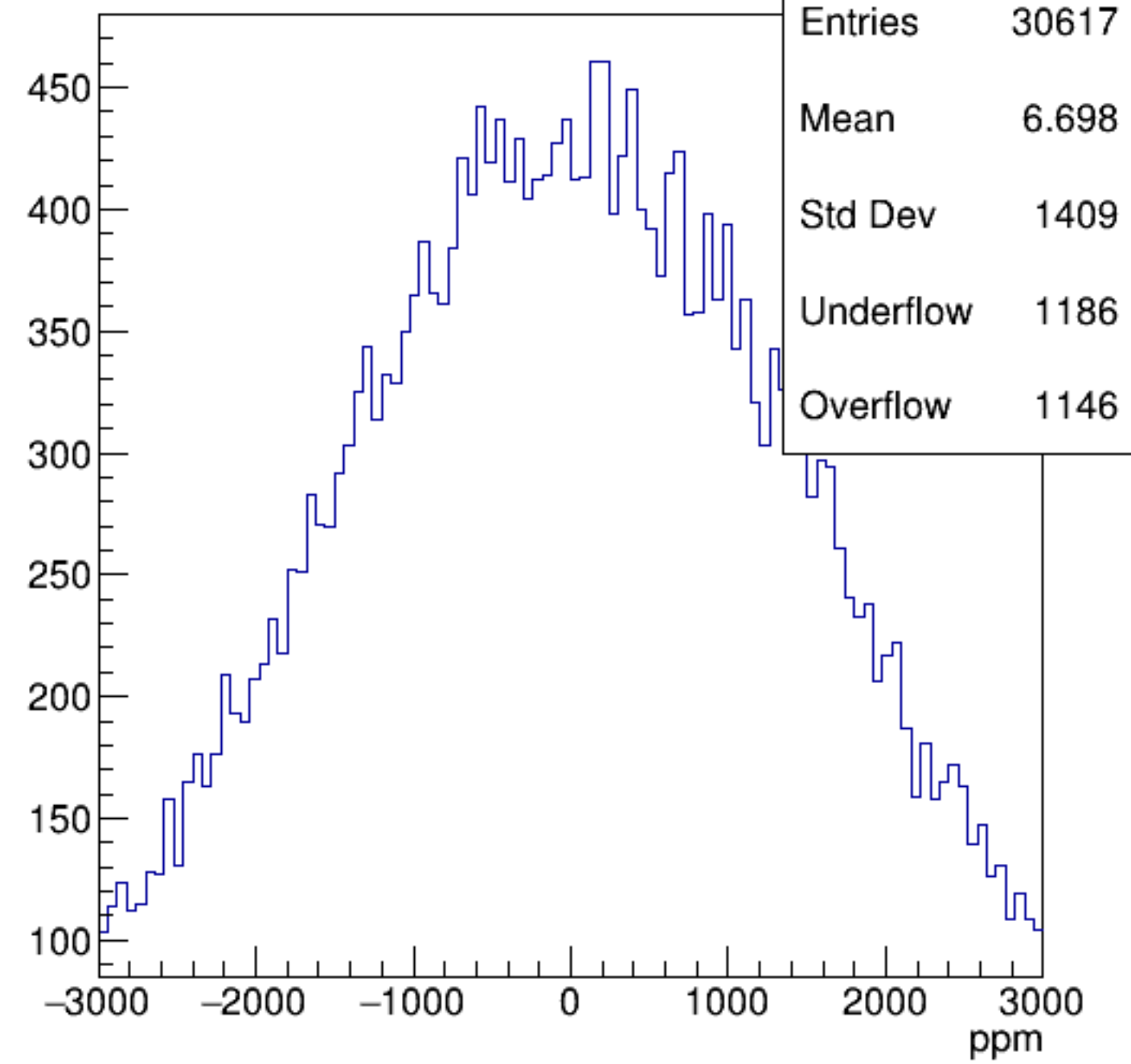
evtTree

Entries	122769
Mean	4.881
Std Dev	0.04763

asym_usl/ppm:pattern_number {ErrorFlag==0}

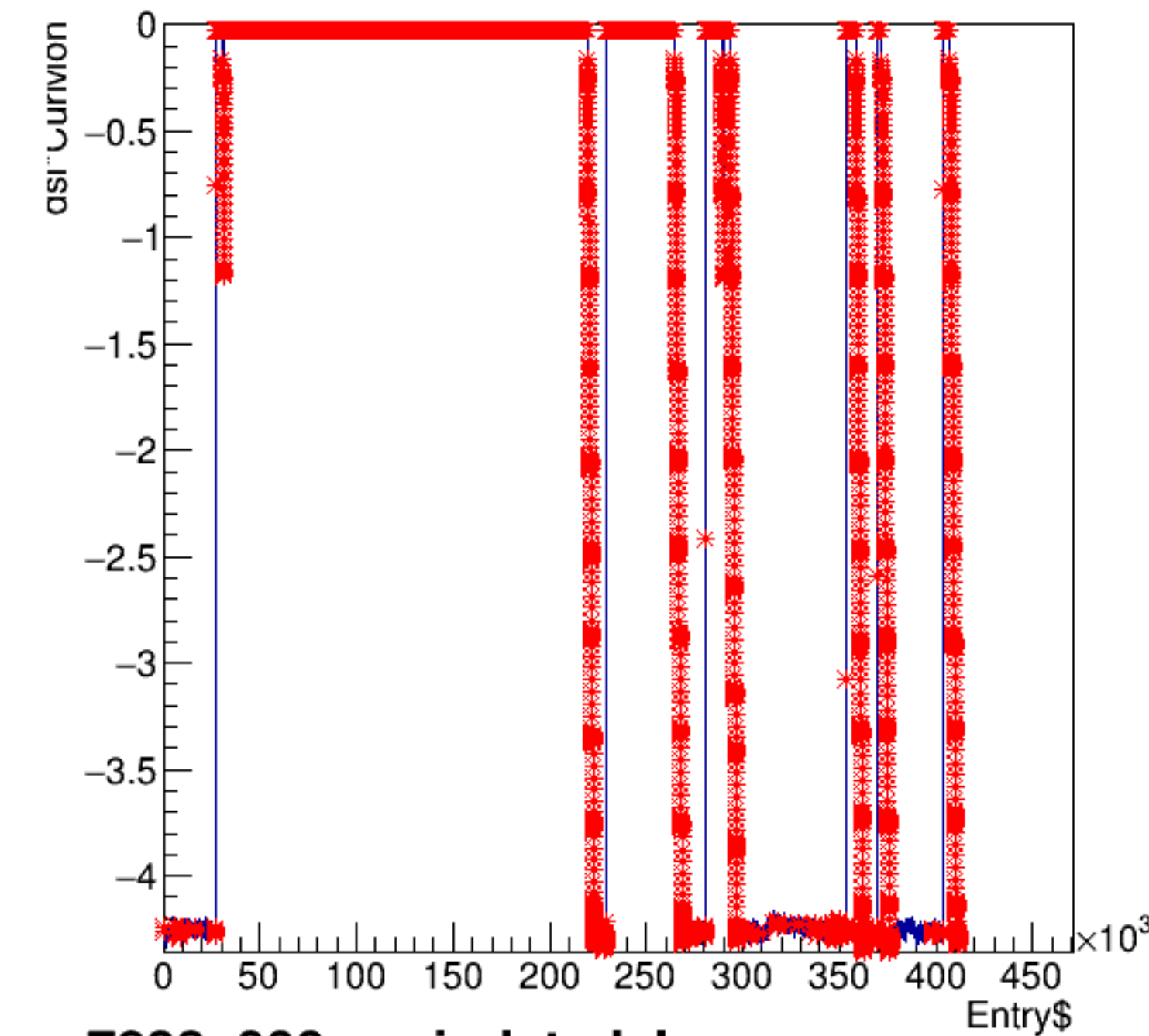


asym_usl



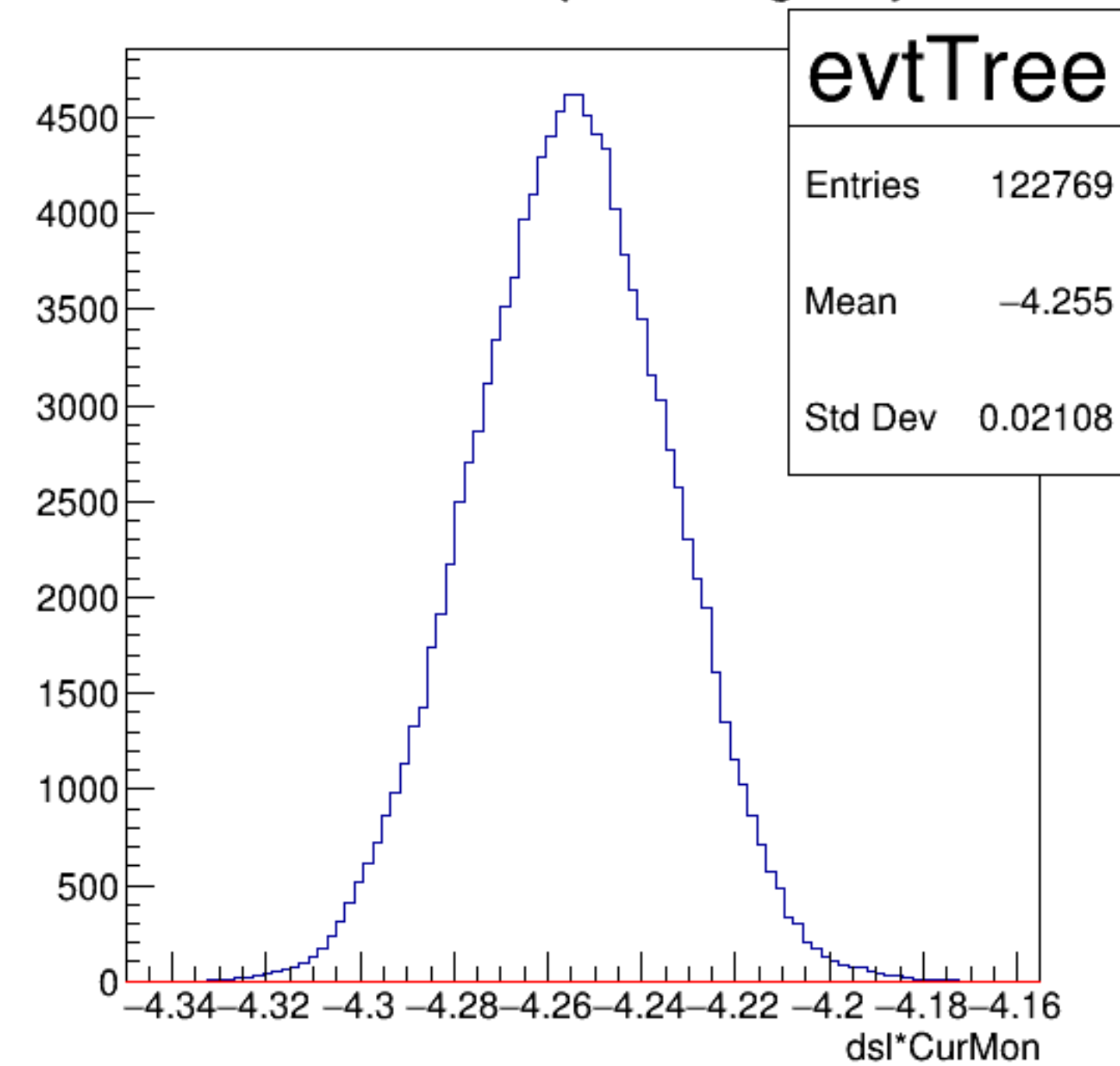
Entries	30617
Mean	6.698
Std Dev	1409
Underflow	1186
Overflow	1146

dsl*CurMon:Entry\$

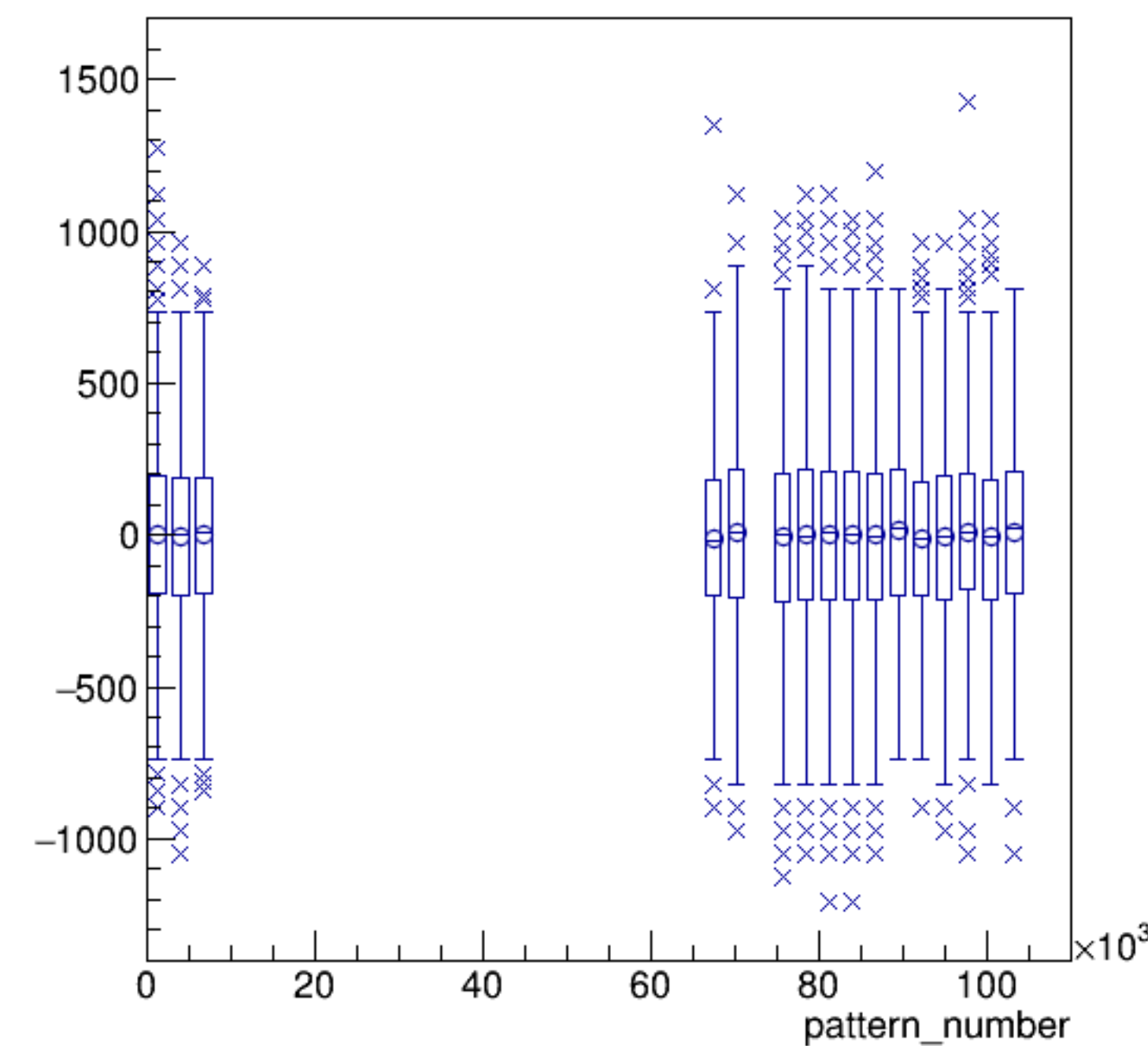


run7838_000_maindet_dsl.png

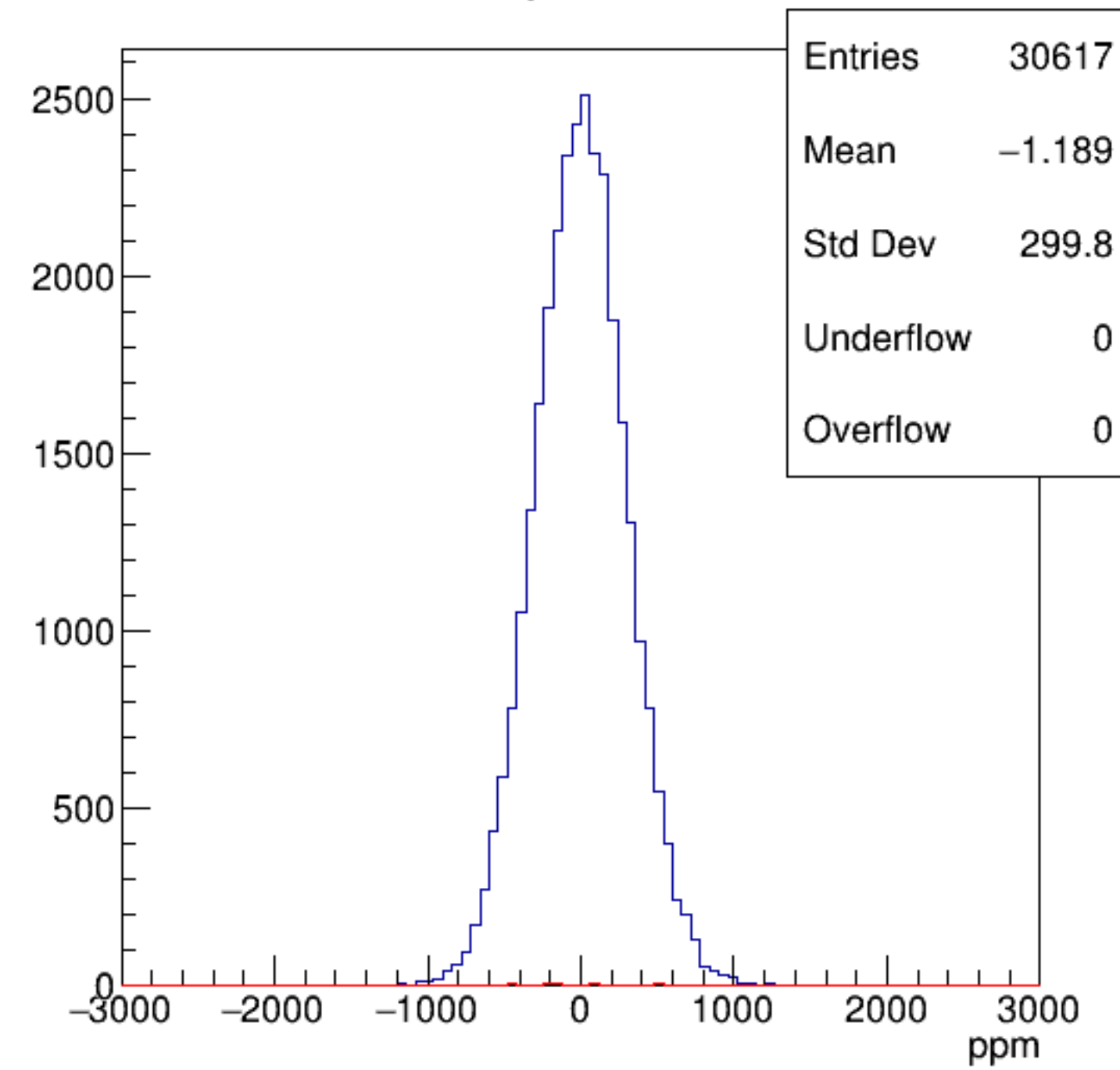
dsl*CurMon {ErrorFlag==0}



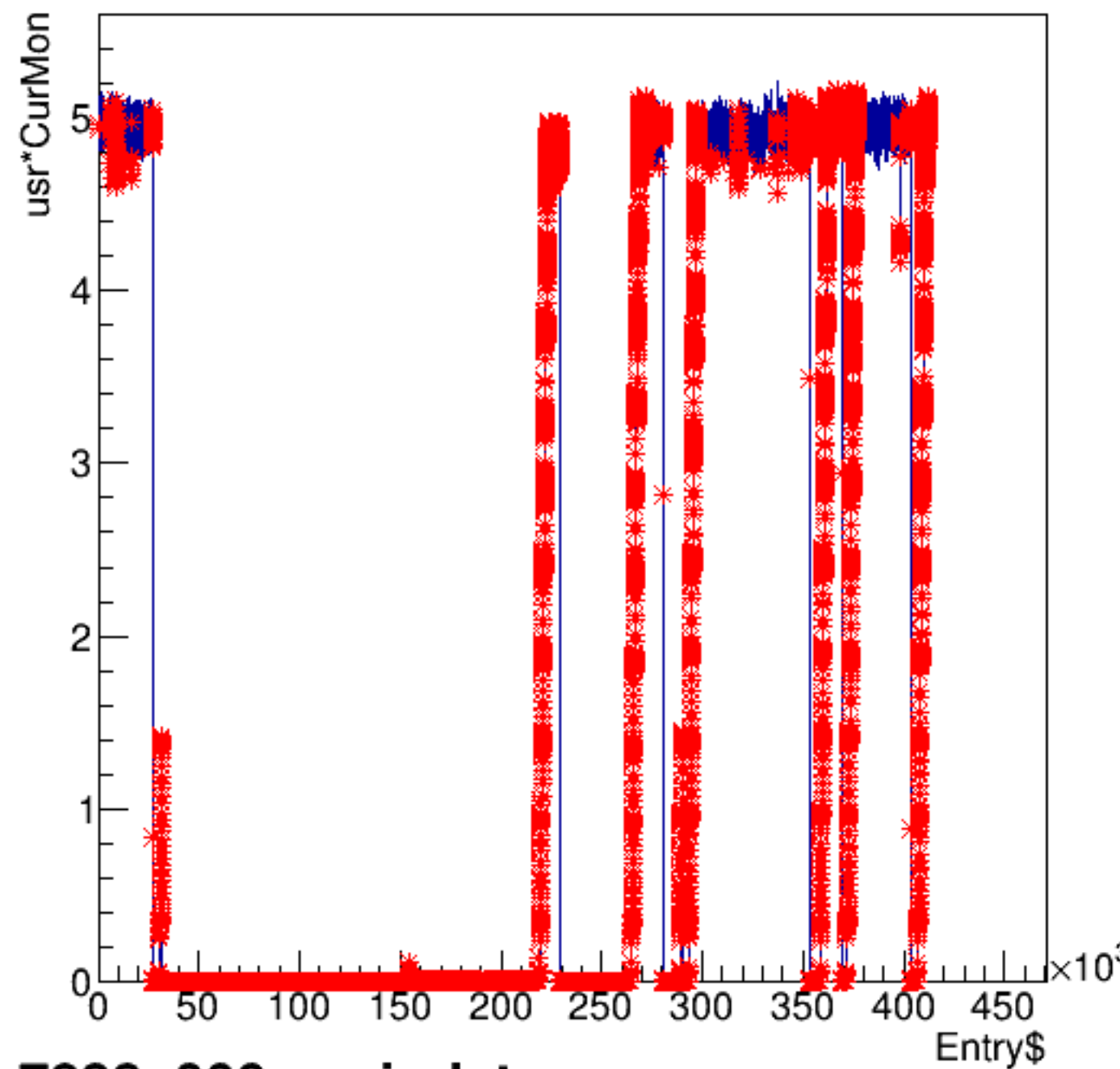
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl

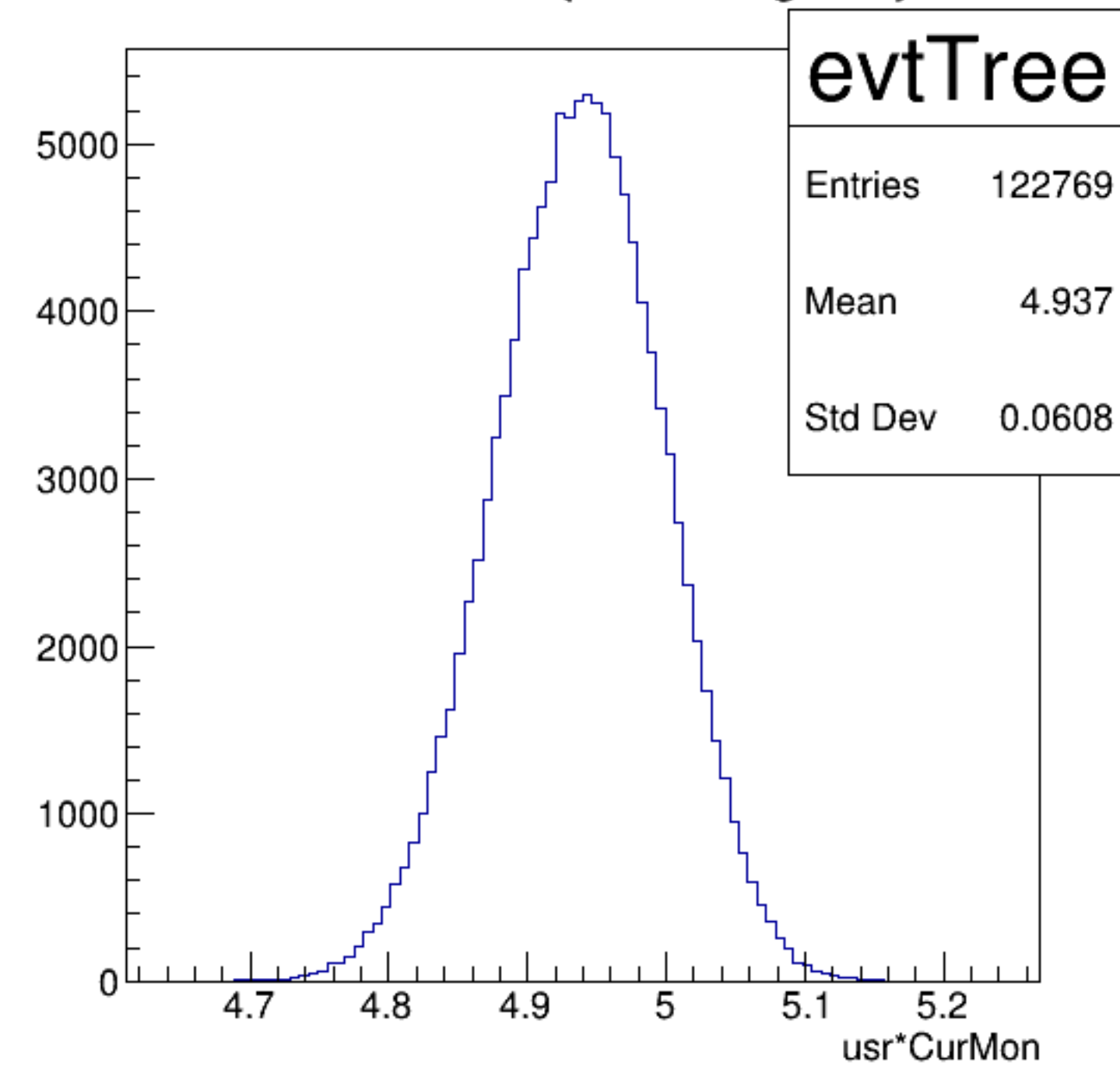


usr*CurMon:Entry\$



run7838_000_maindet_usr.png

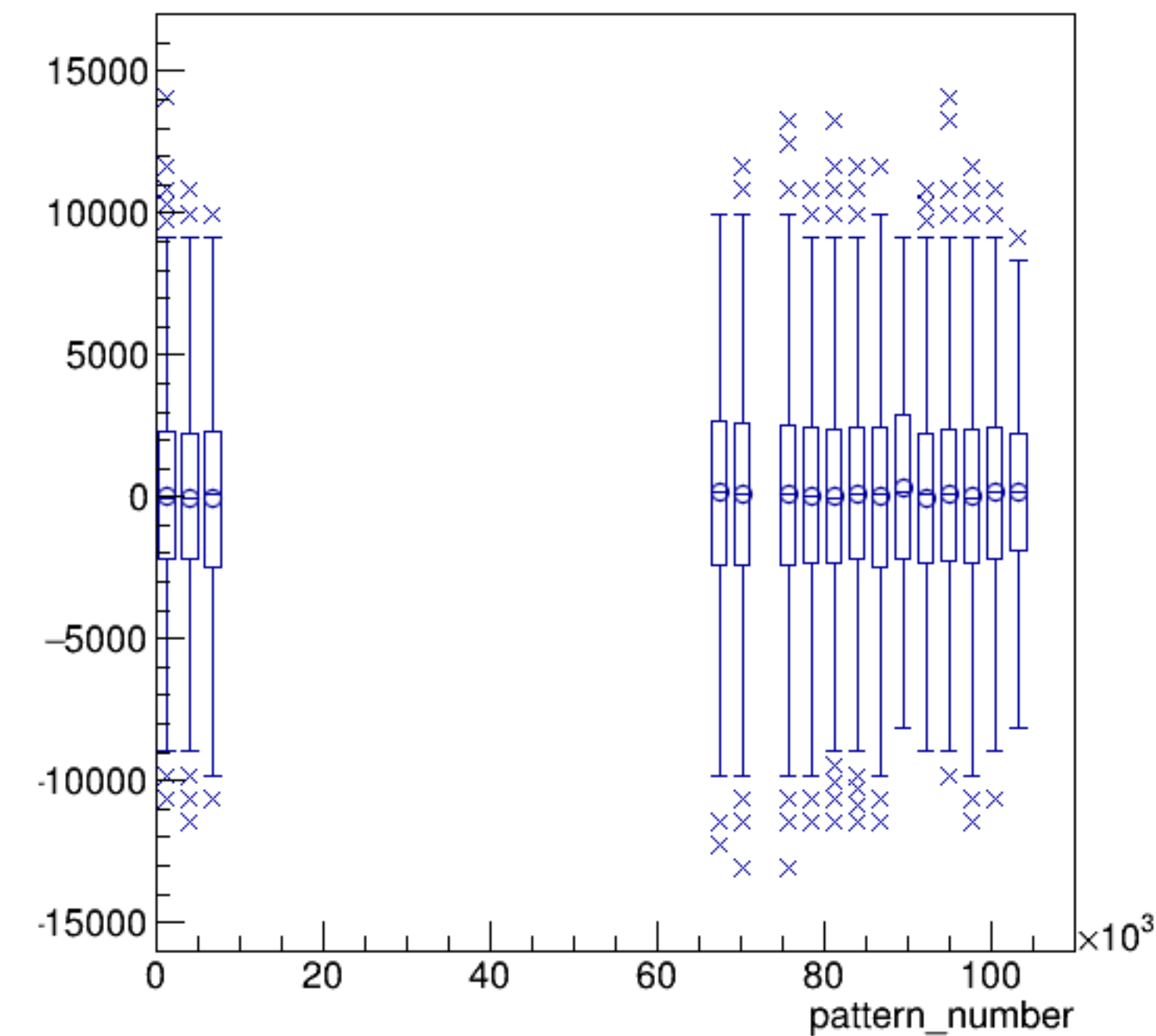
usr*CurMon {ErrorFlag==0}



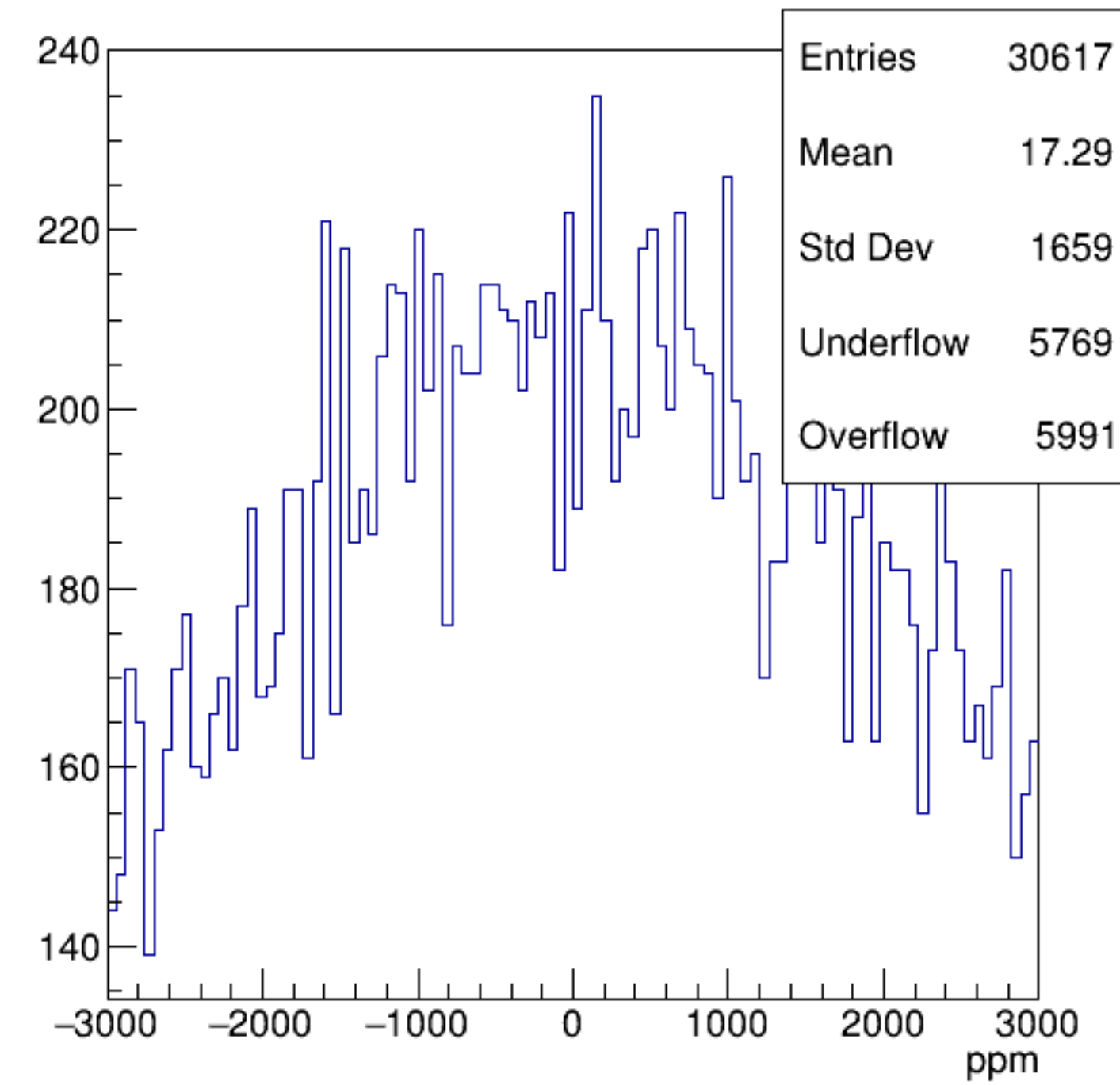
evtTree

Entries	122769
Mean	4.937
Std Dev	0.0608

asym_usr/ppm:pattern_number {ErrorFlag==0}

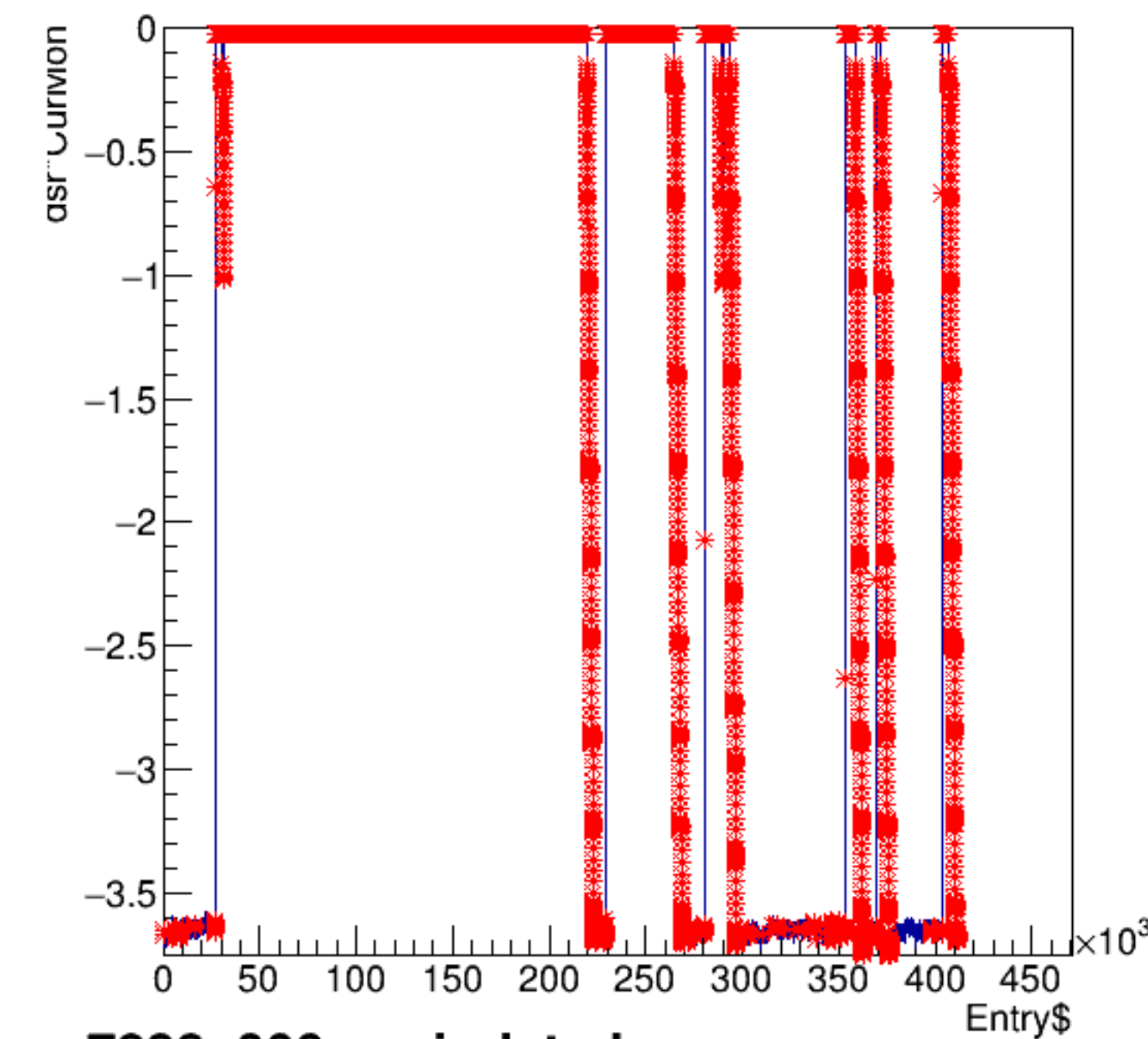


asym_usr



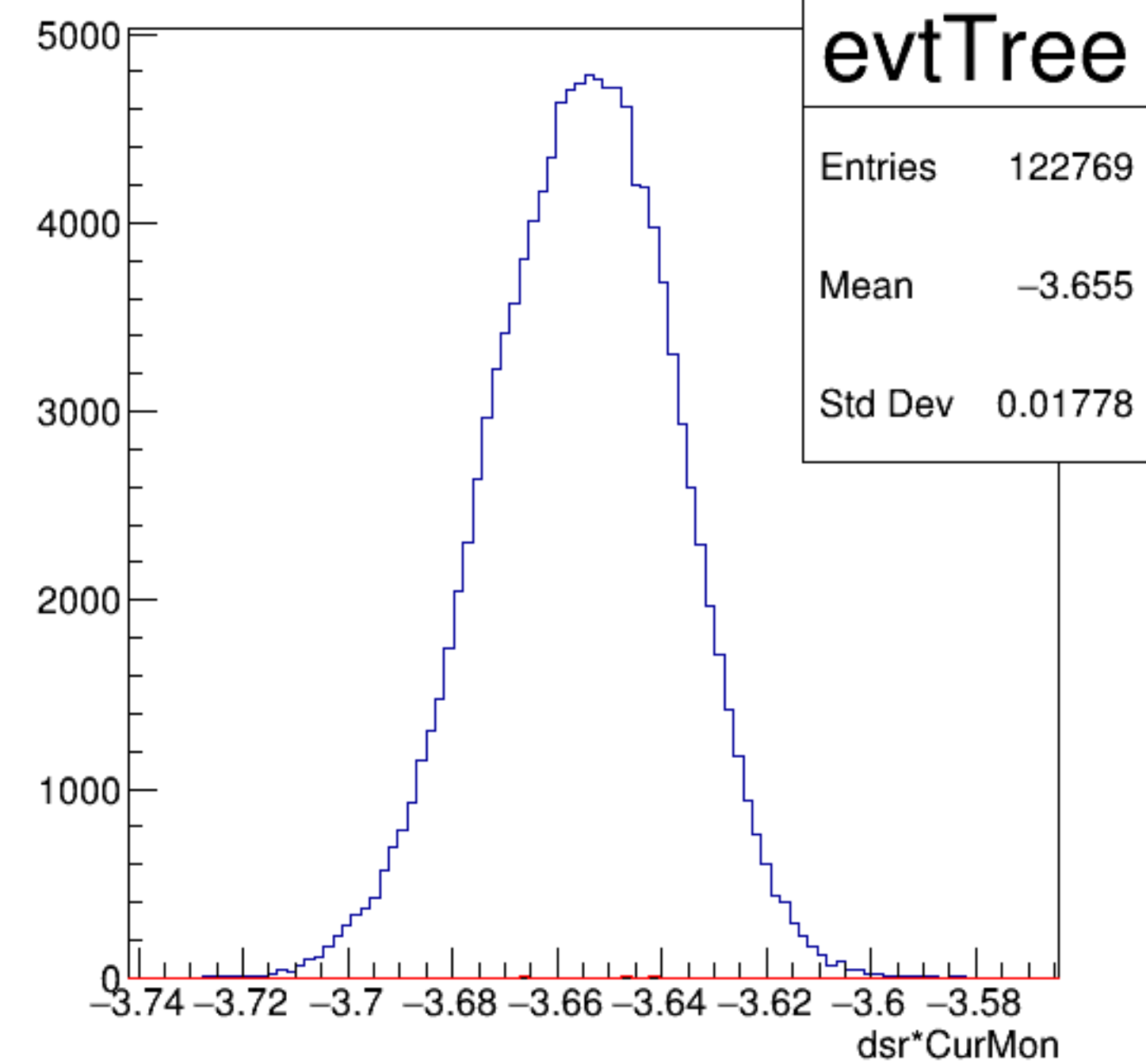
Entries	30617
Mean	17.29
Std Dev	1659
Underflow	5769
Overflow	5991

dsr*CurMon:Entry\$



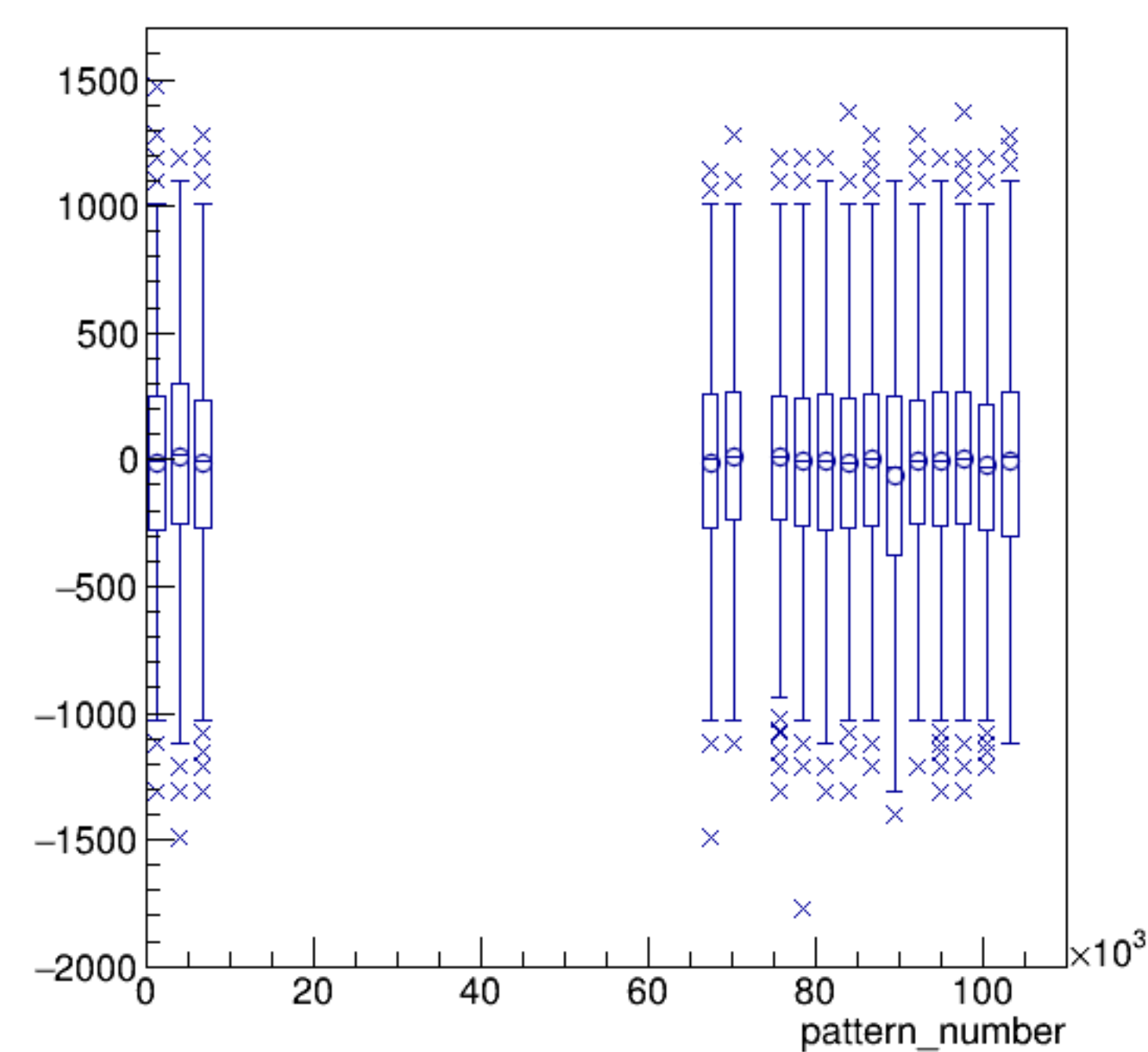
run7838_000_maindet_dsr.png

dsr*CurMon {ErrorFlag==0}

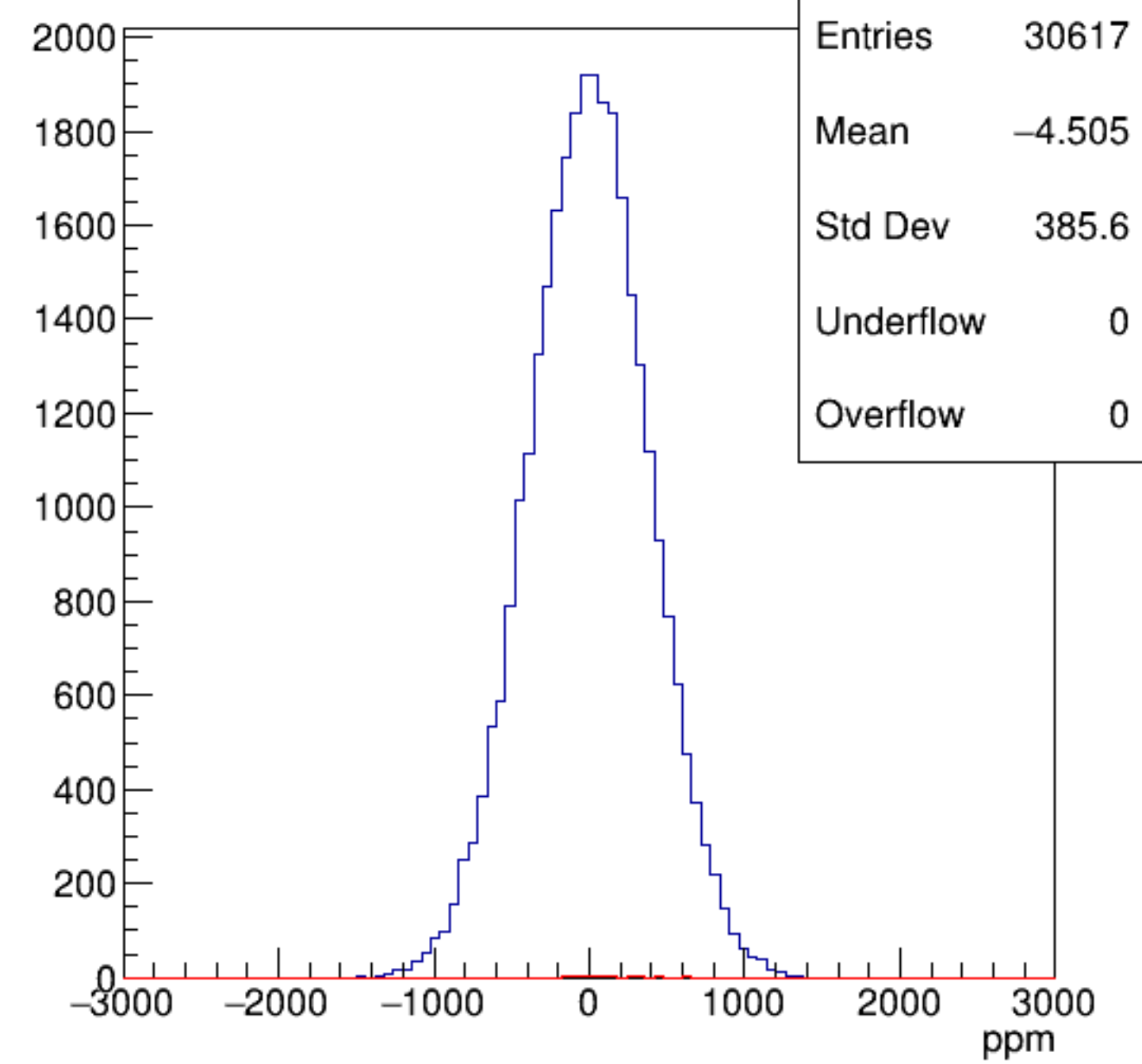


evtTree	
Entries	122769
Mean	-3.655
Std Dev	0.01778

asym_dsr/ppm:pattern_number {ErrorFlag==0}

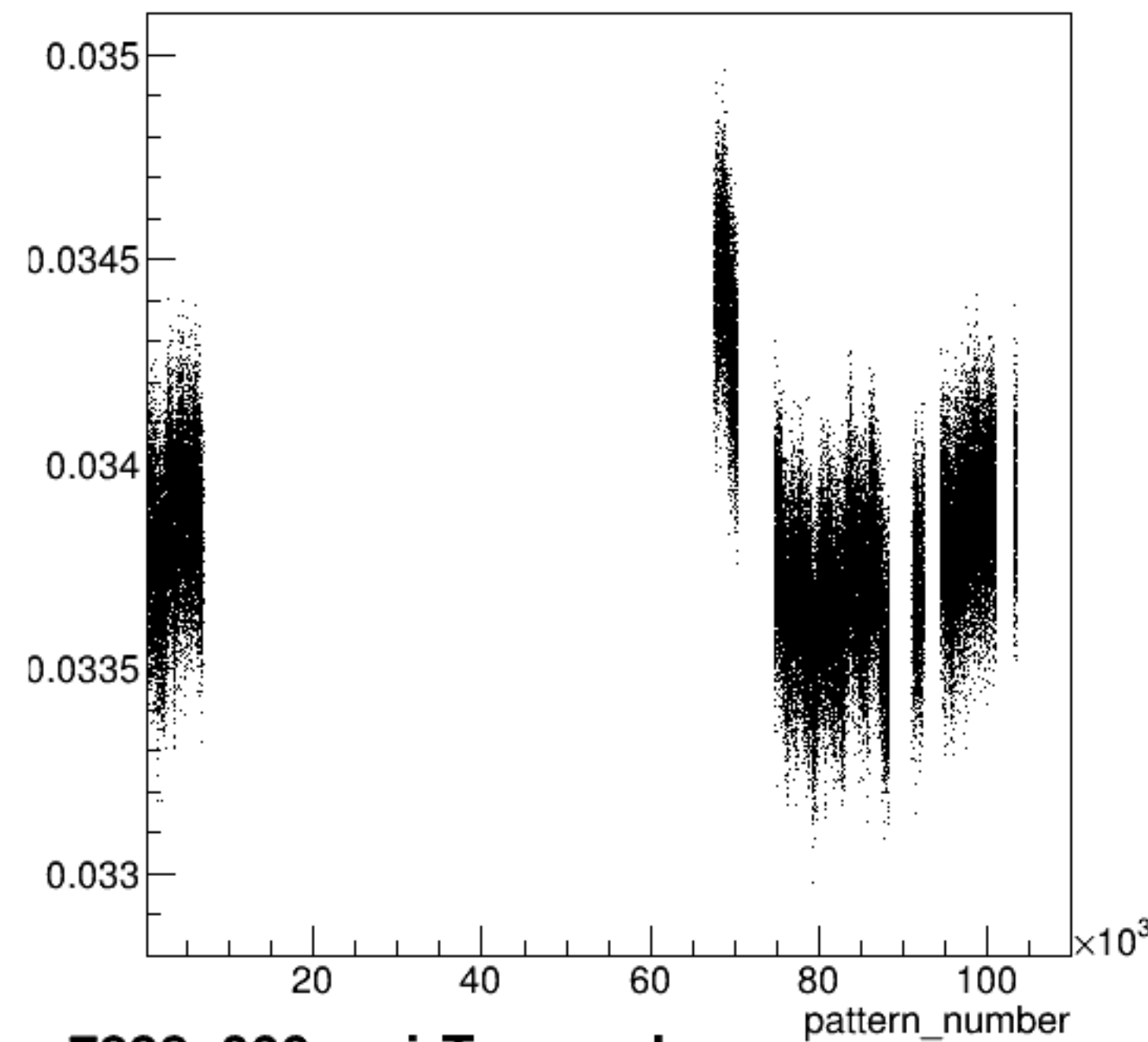


asym_dsr



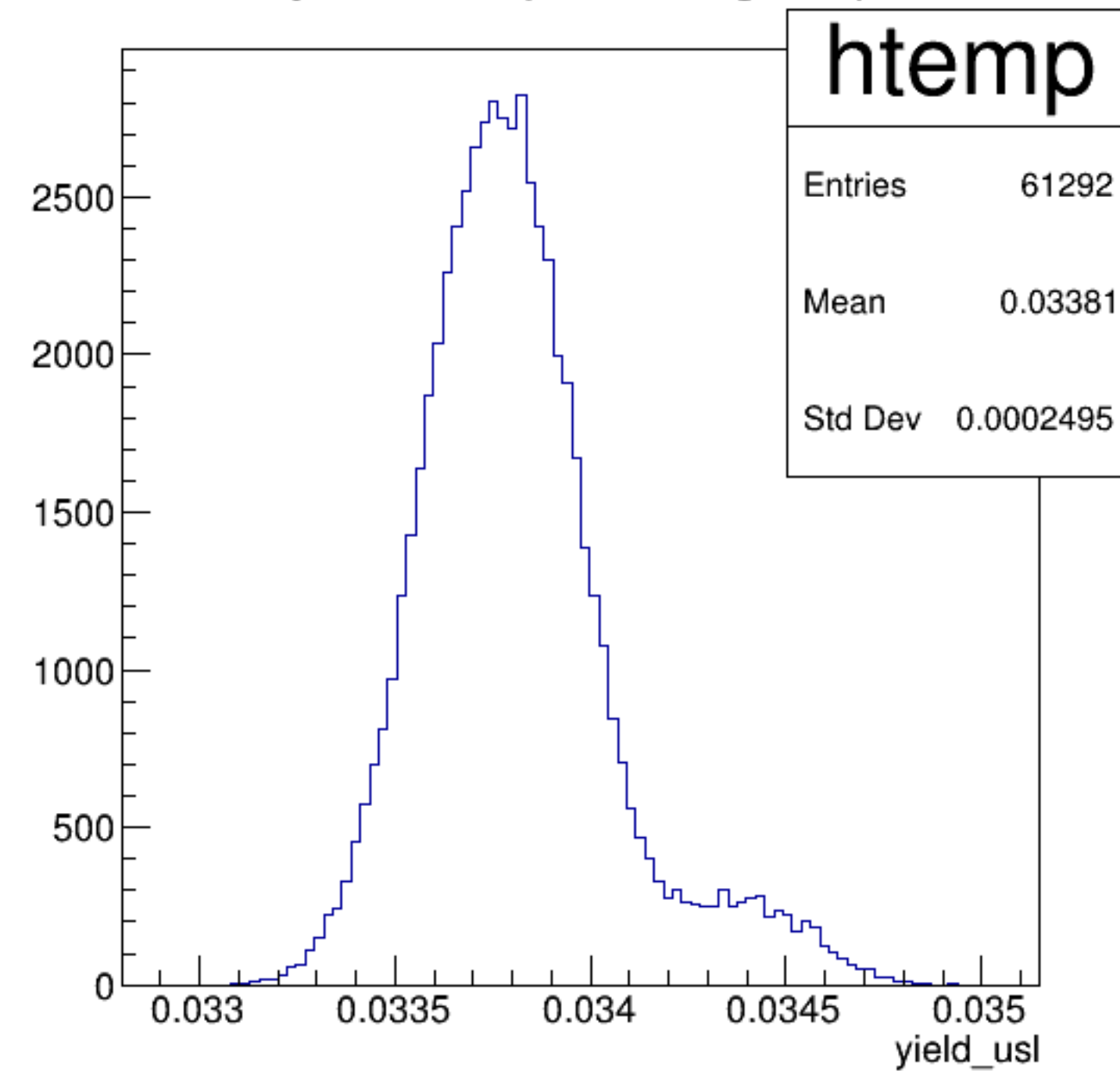
Entries	30617
Mean	-4.505
Std Dev	385.6
Underflow	0
Overflow	0

yield_usl:pattern_number {ErrorFlag==0}

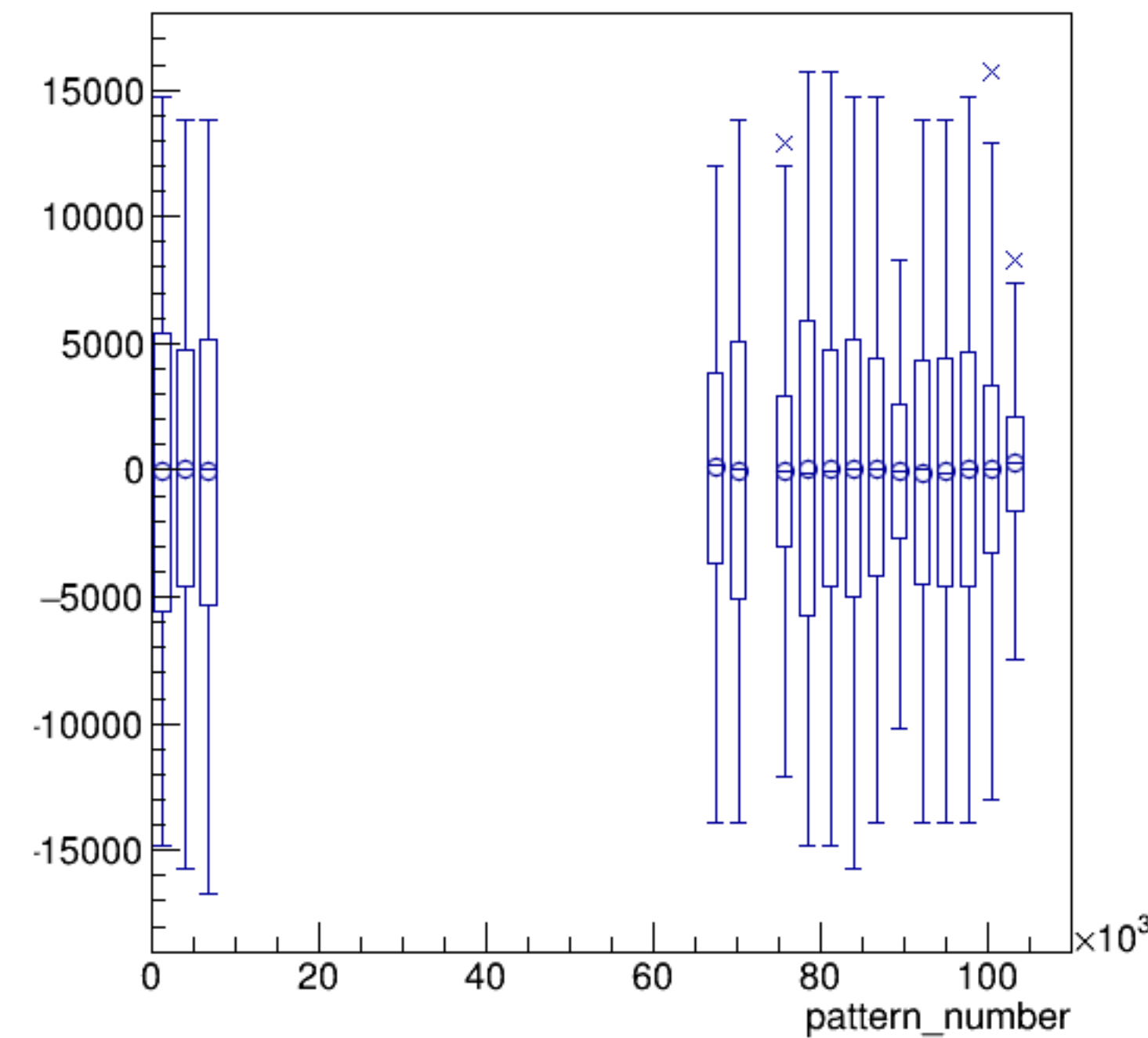


run7838_000_pairTree_usl.png

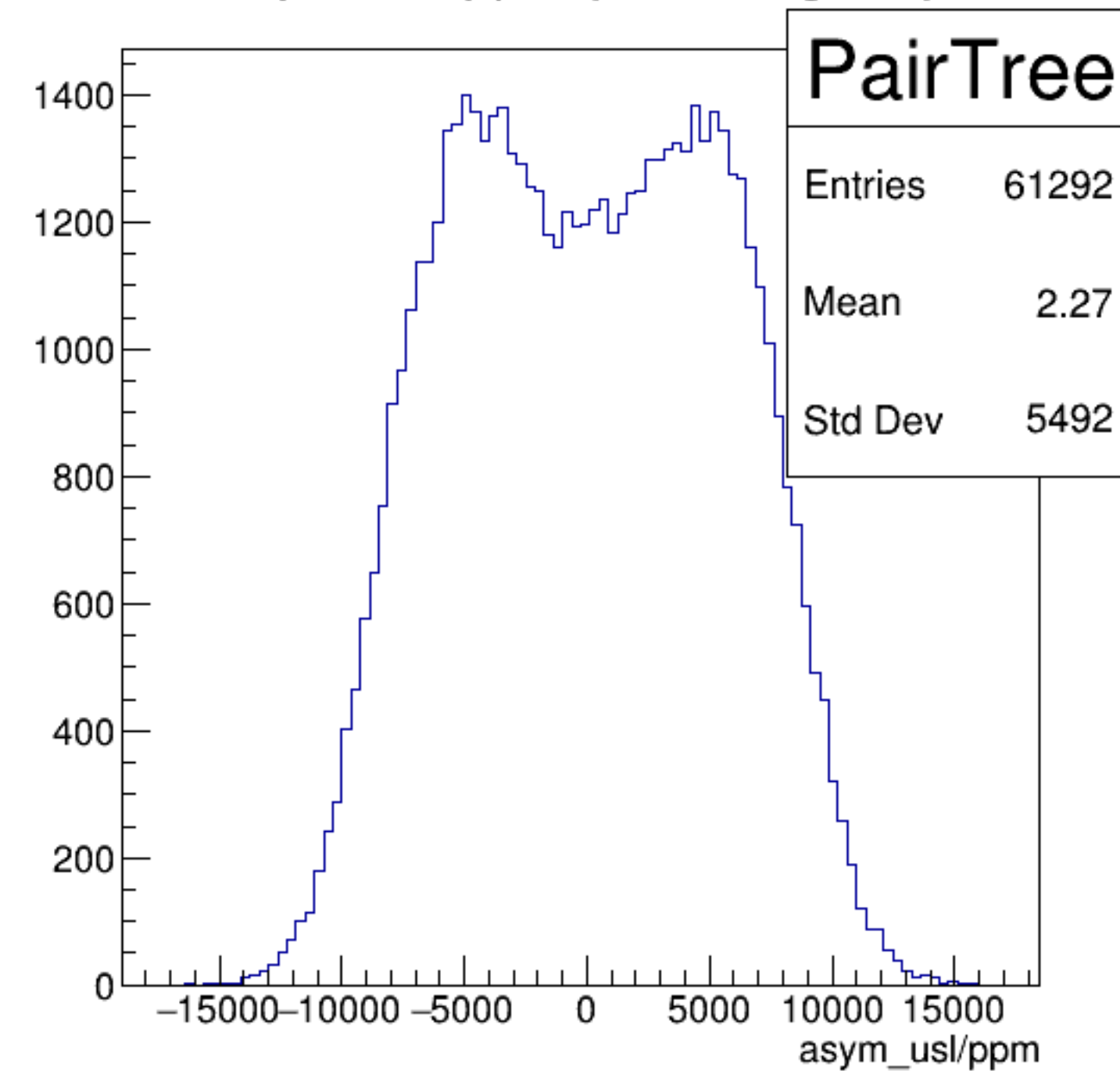
yield_usl {ErrorFlag==0}



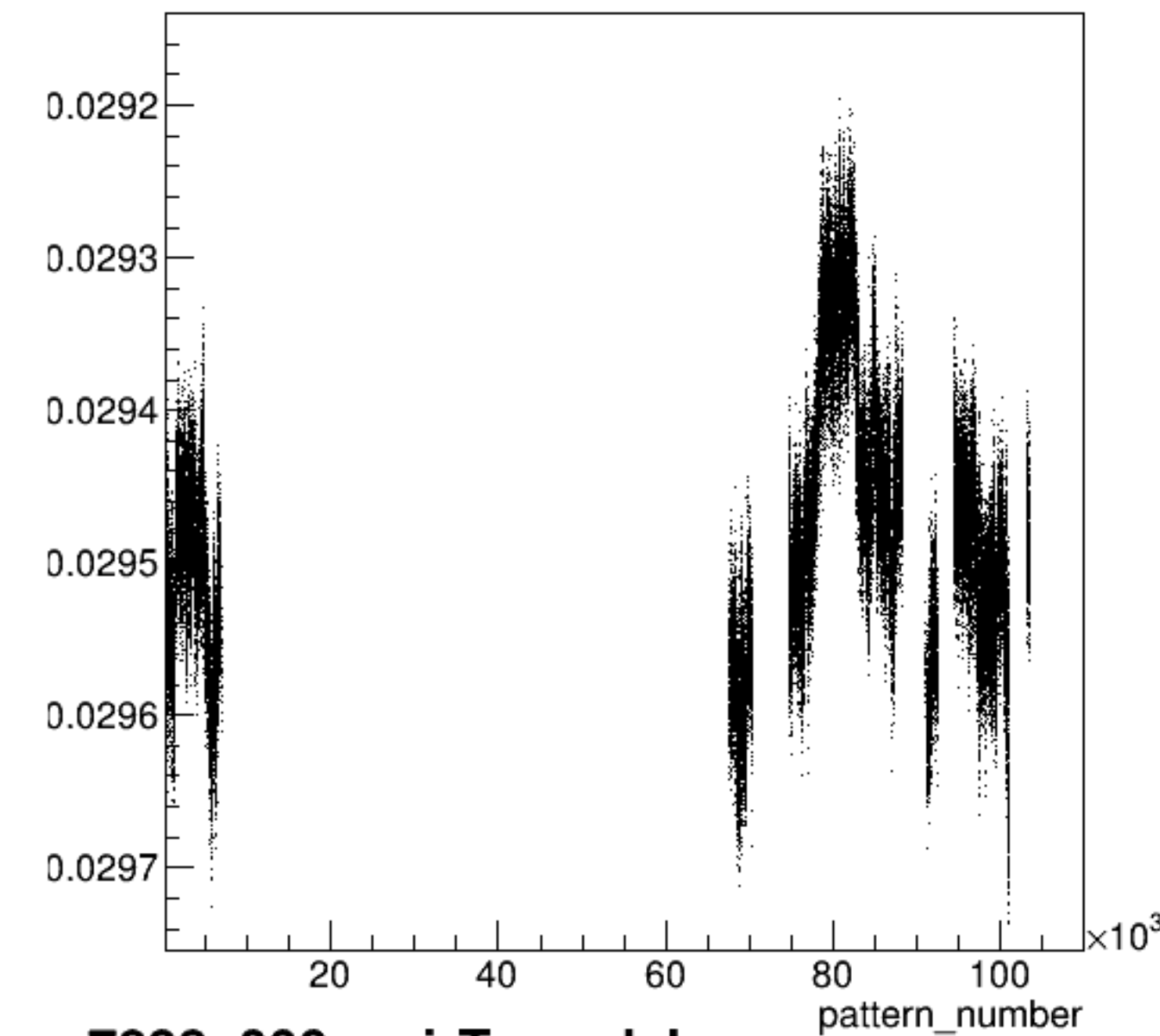
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl/ppm {ErrorFlag==0}

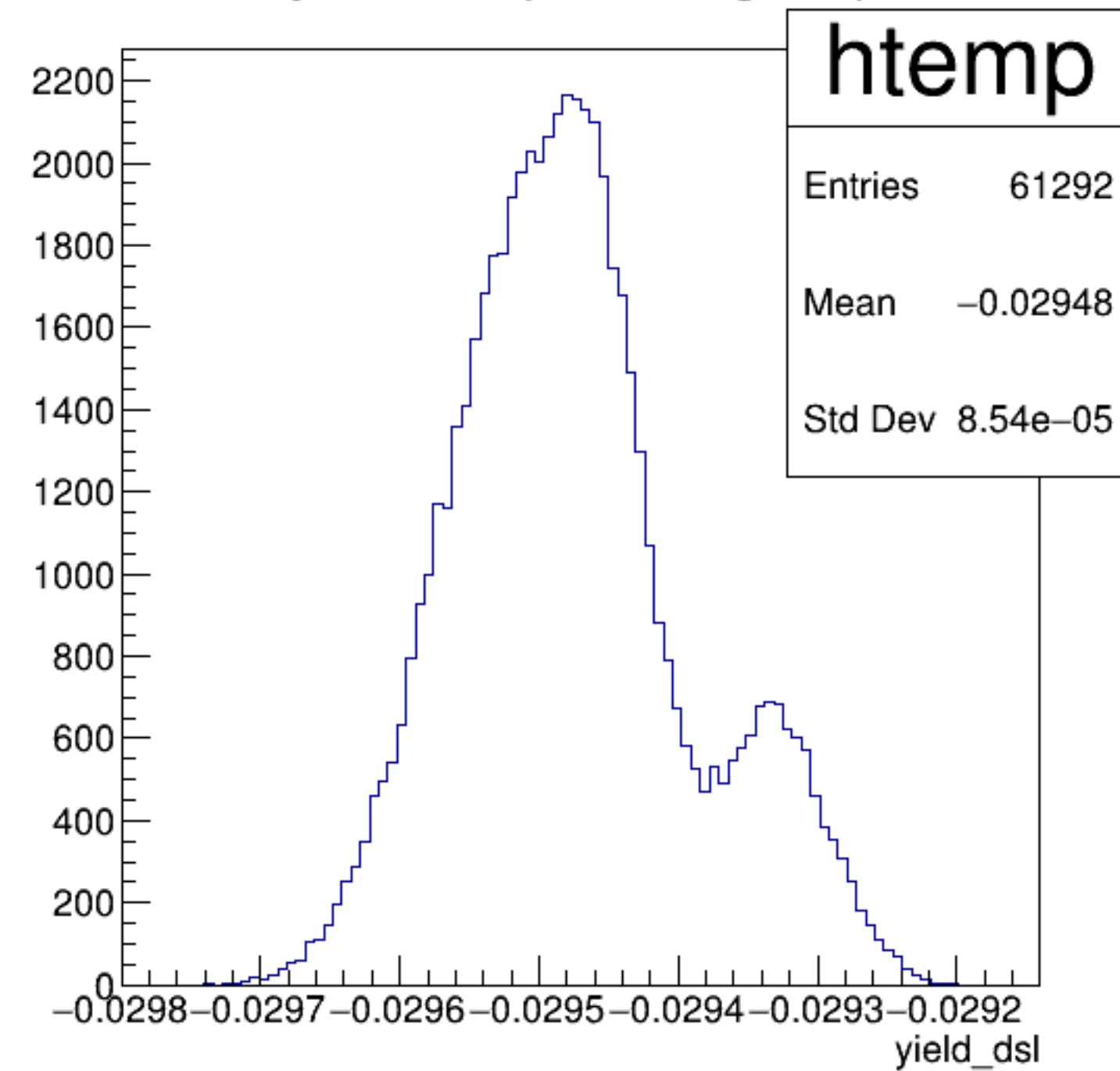


yield_dsl:pattern_number {ErrorFlag==0}

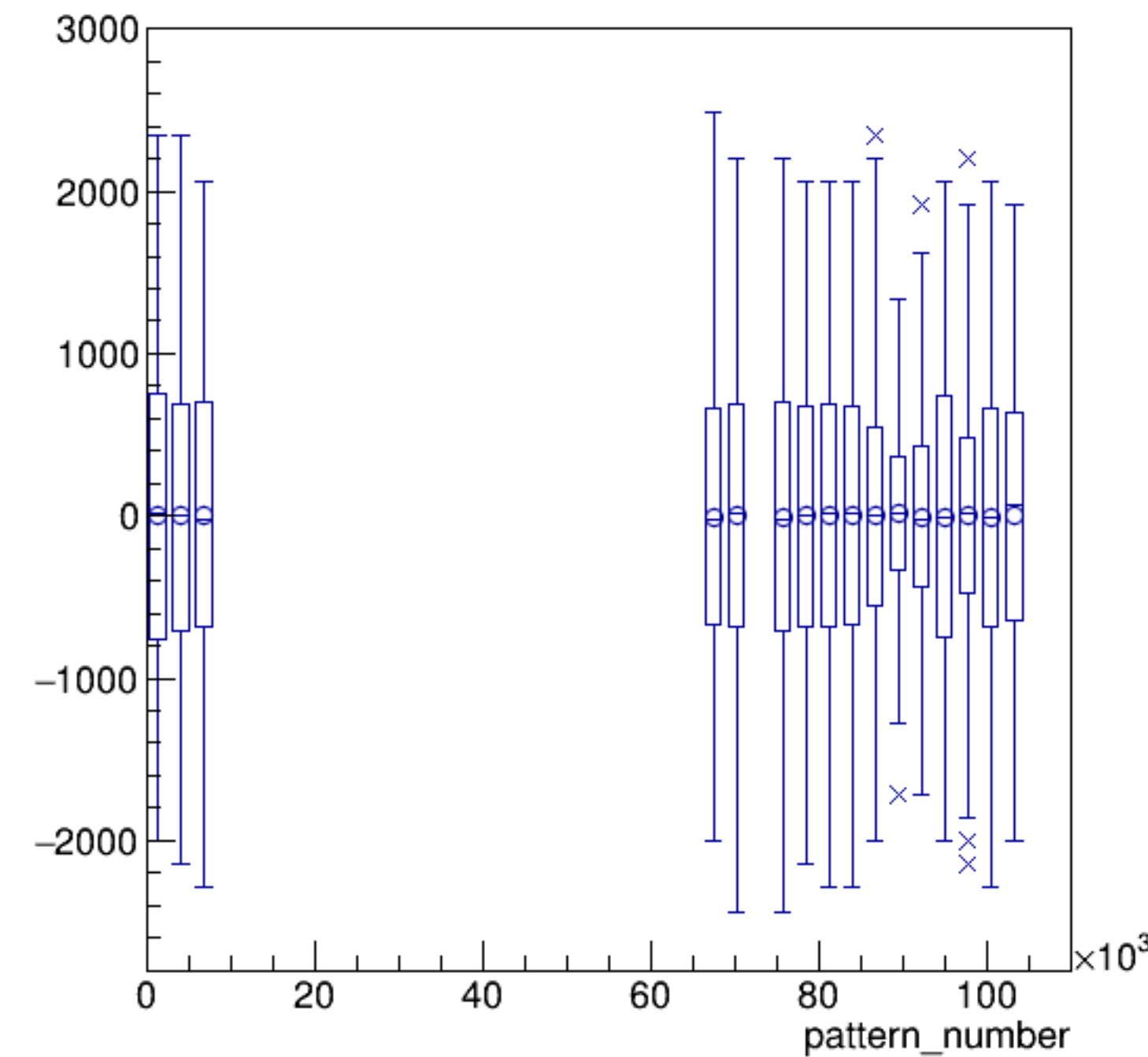


run7838_000_pairTree_dsl.png

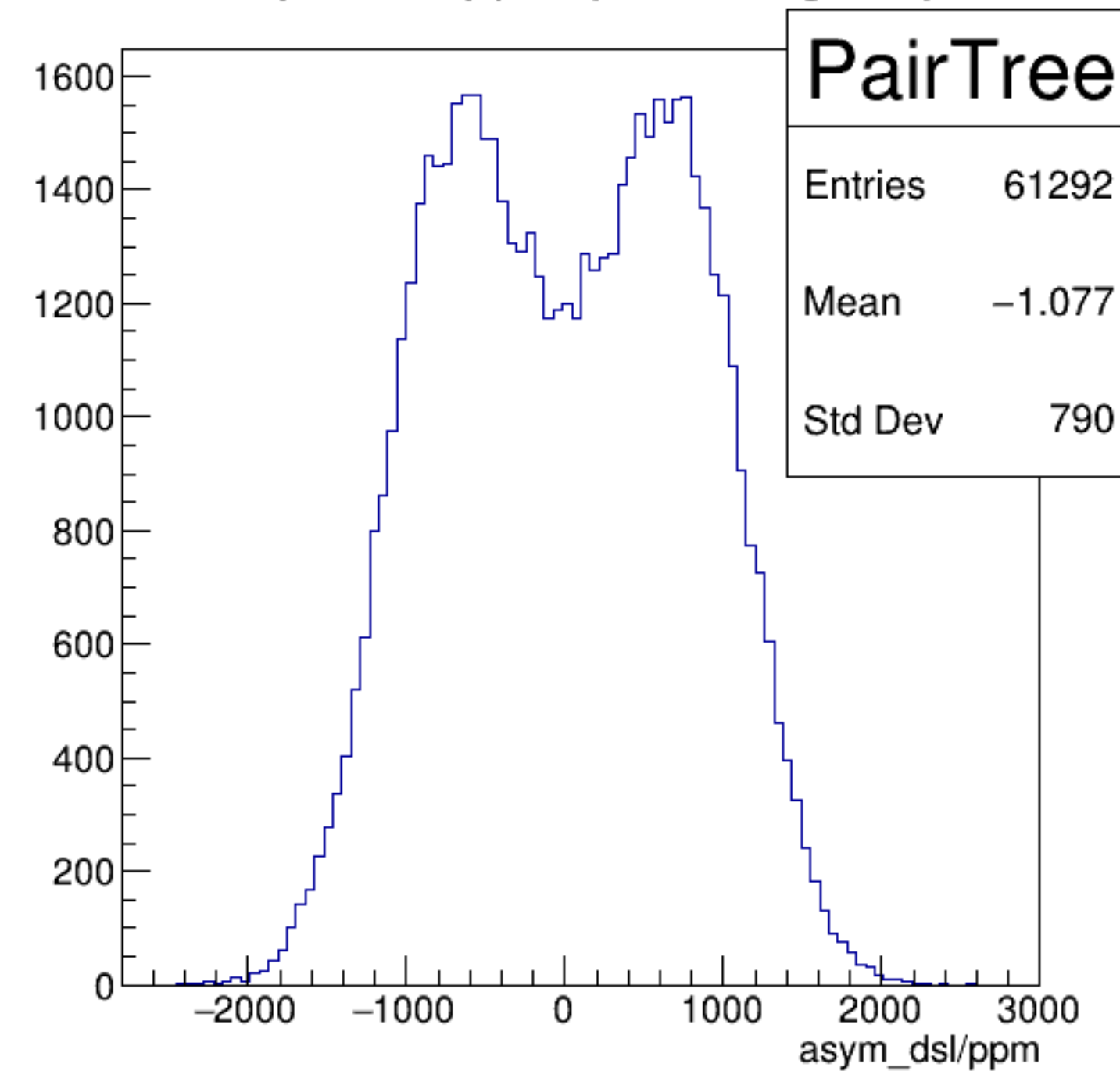
yield_dsl {ErrorFlag==0}



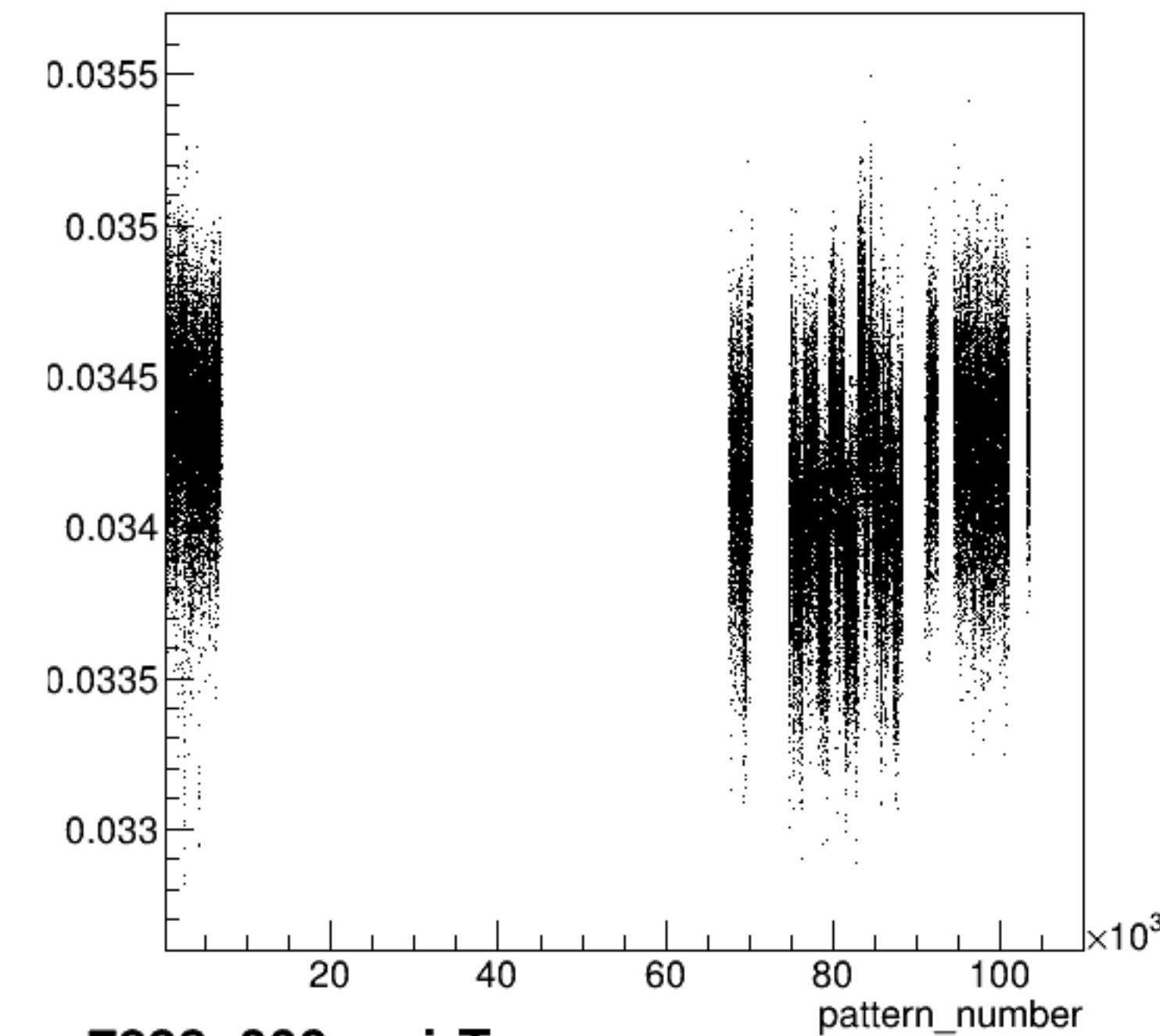
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl/ppm {ErrorFlag==0}

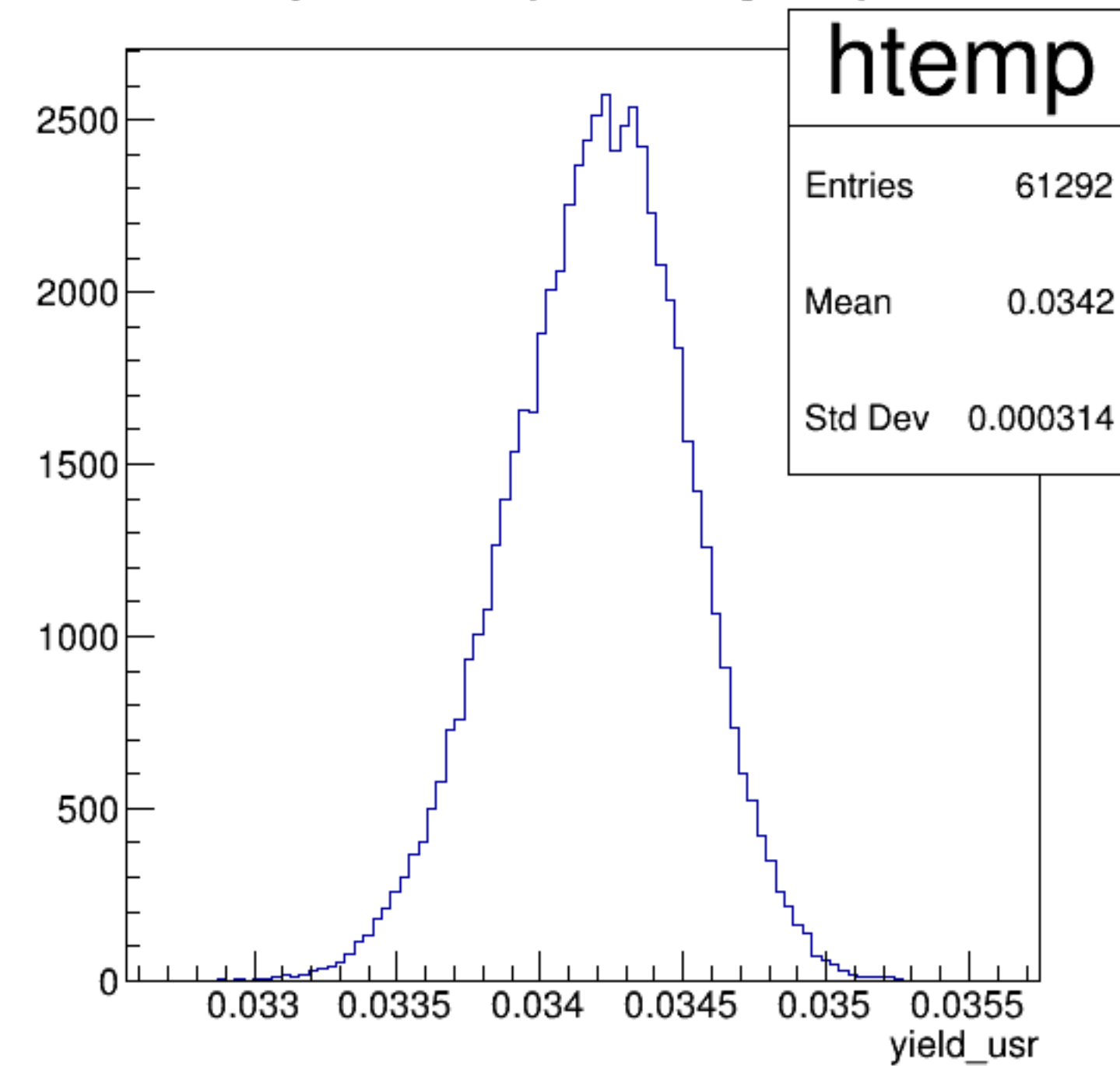


yield_usr:pattern_number {ErrorFlag==0}

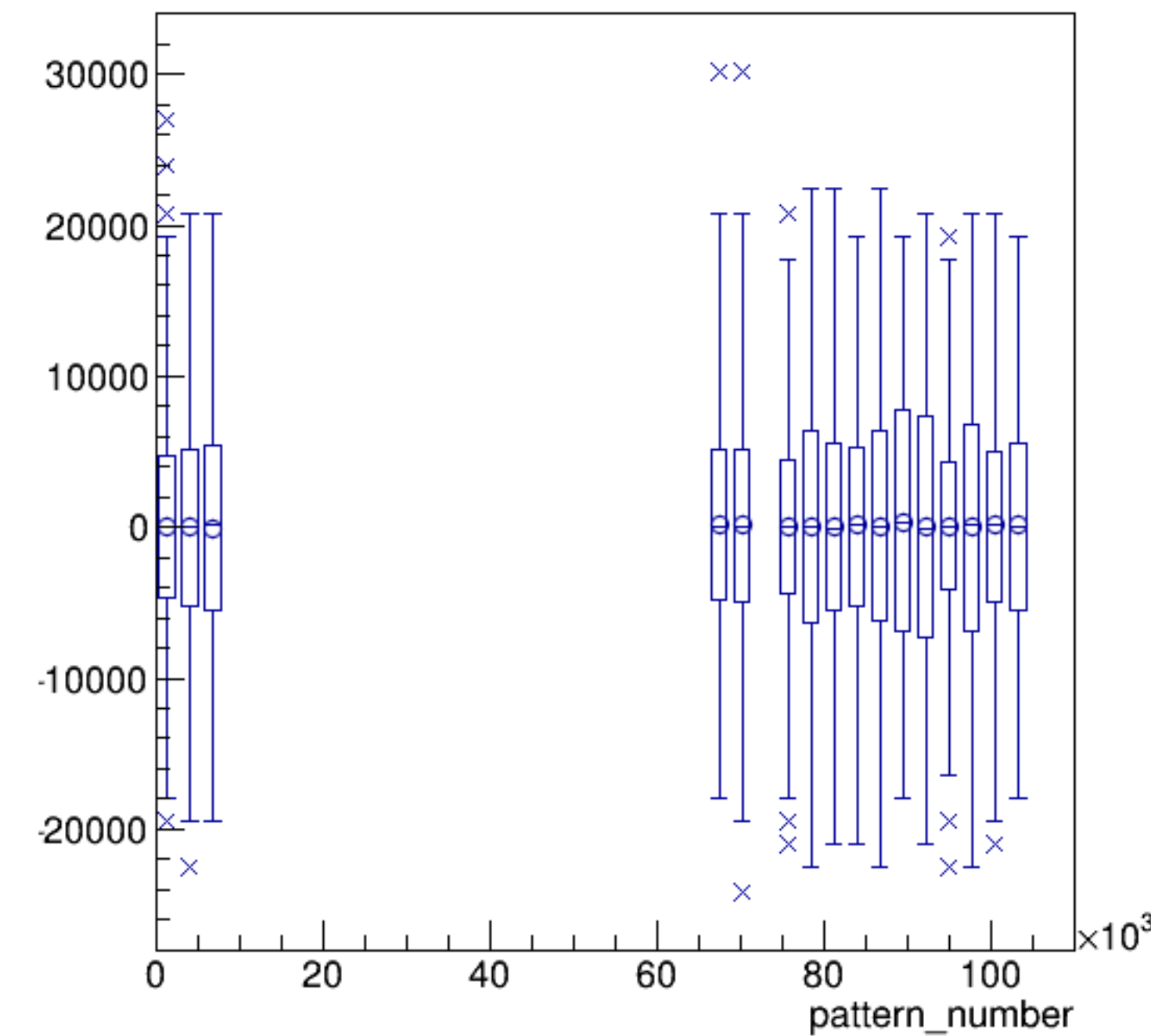


run7838_000_pairTree_usr.png

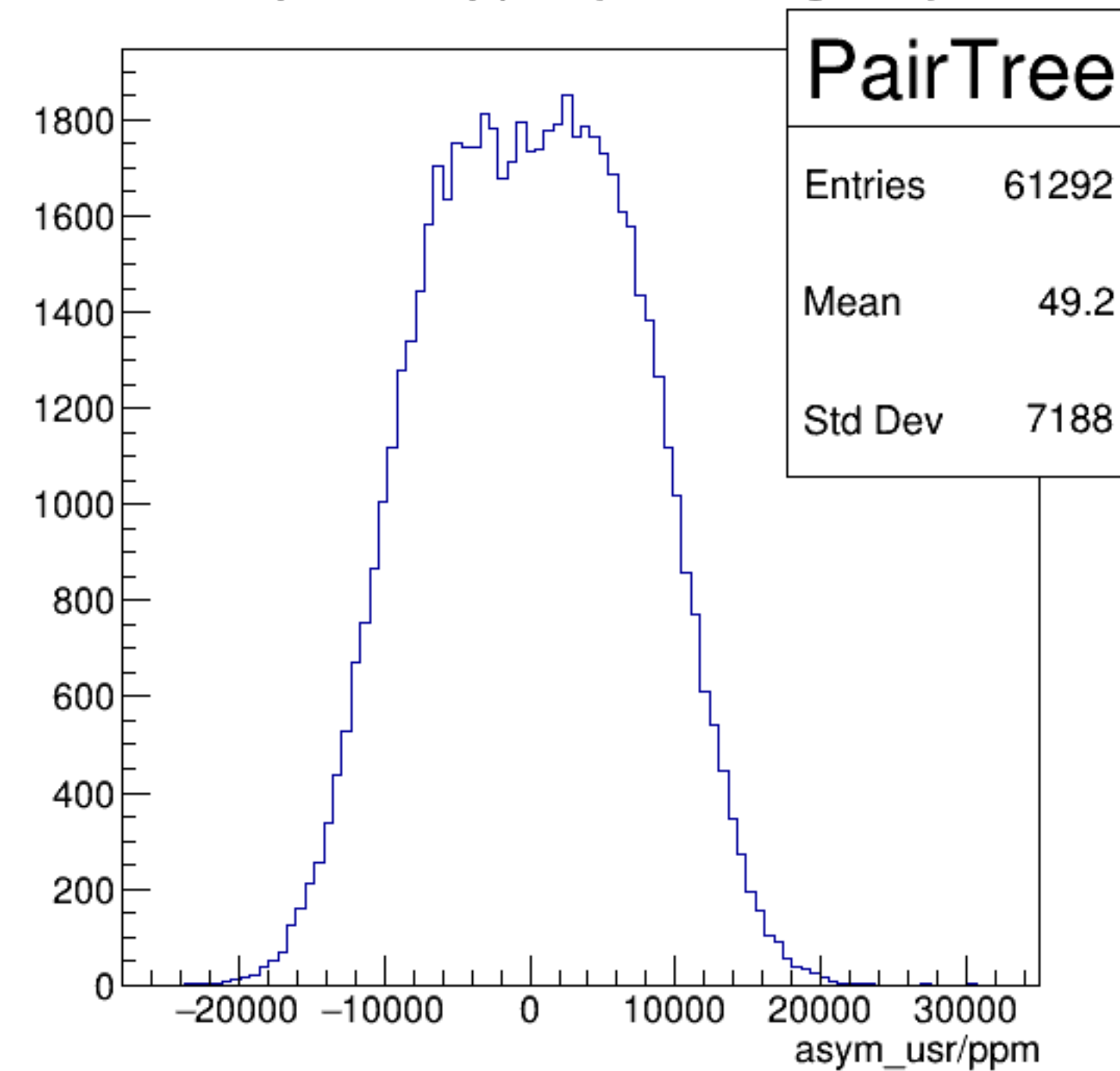
yield_usr {ErrorFlag==0}



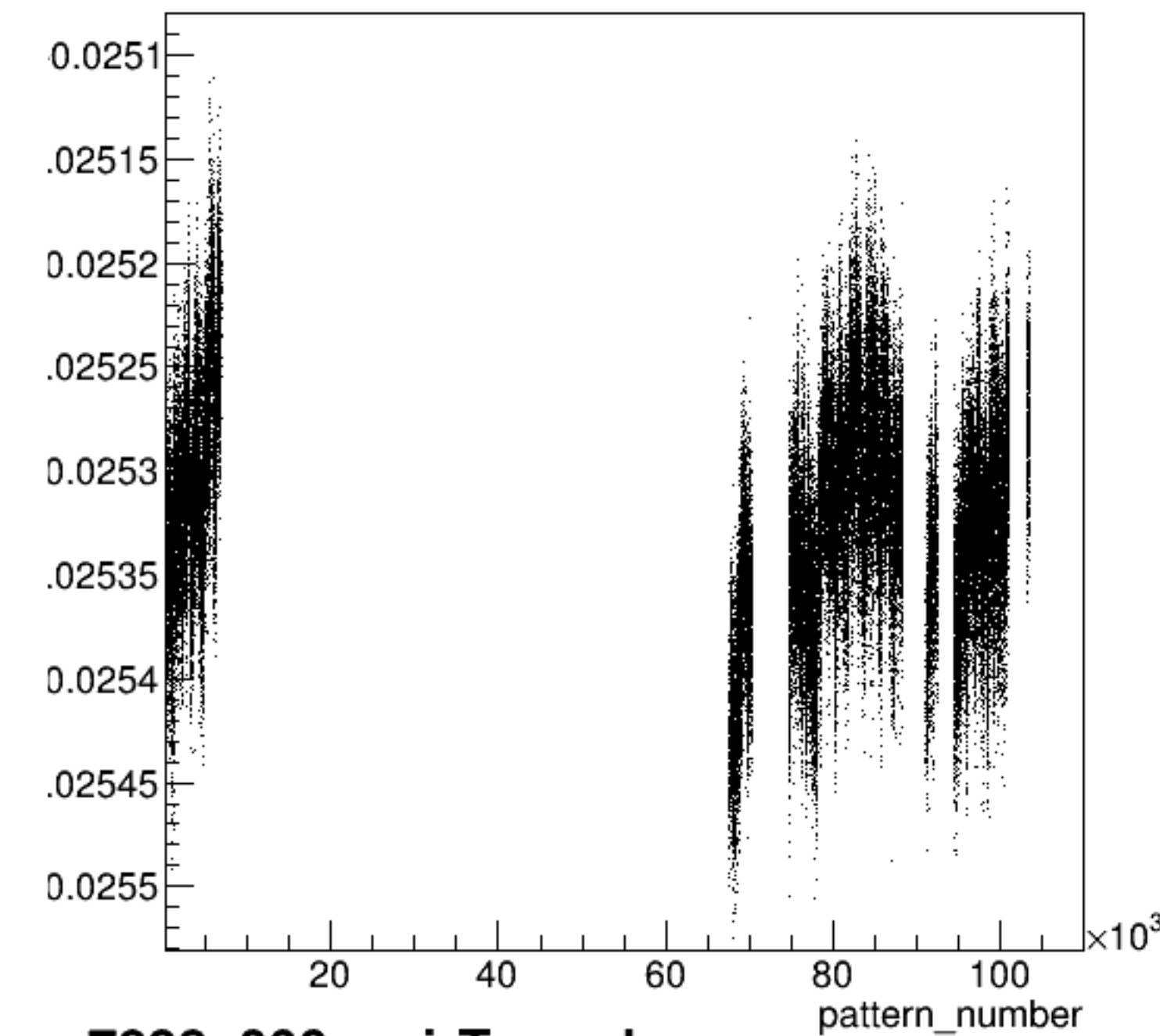
asym_usr/ppm:pattern_number {ErrorFlag==0}



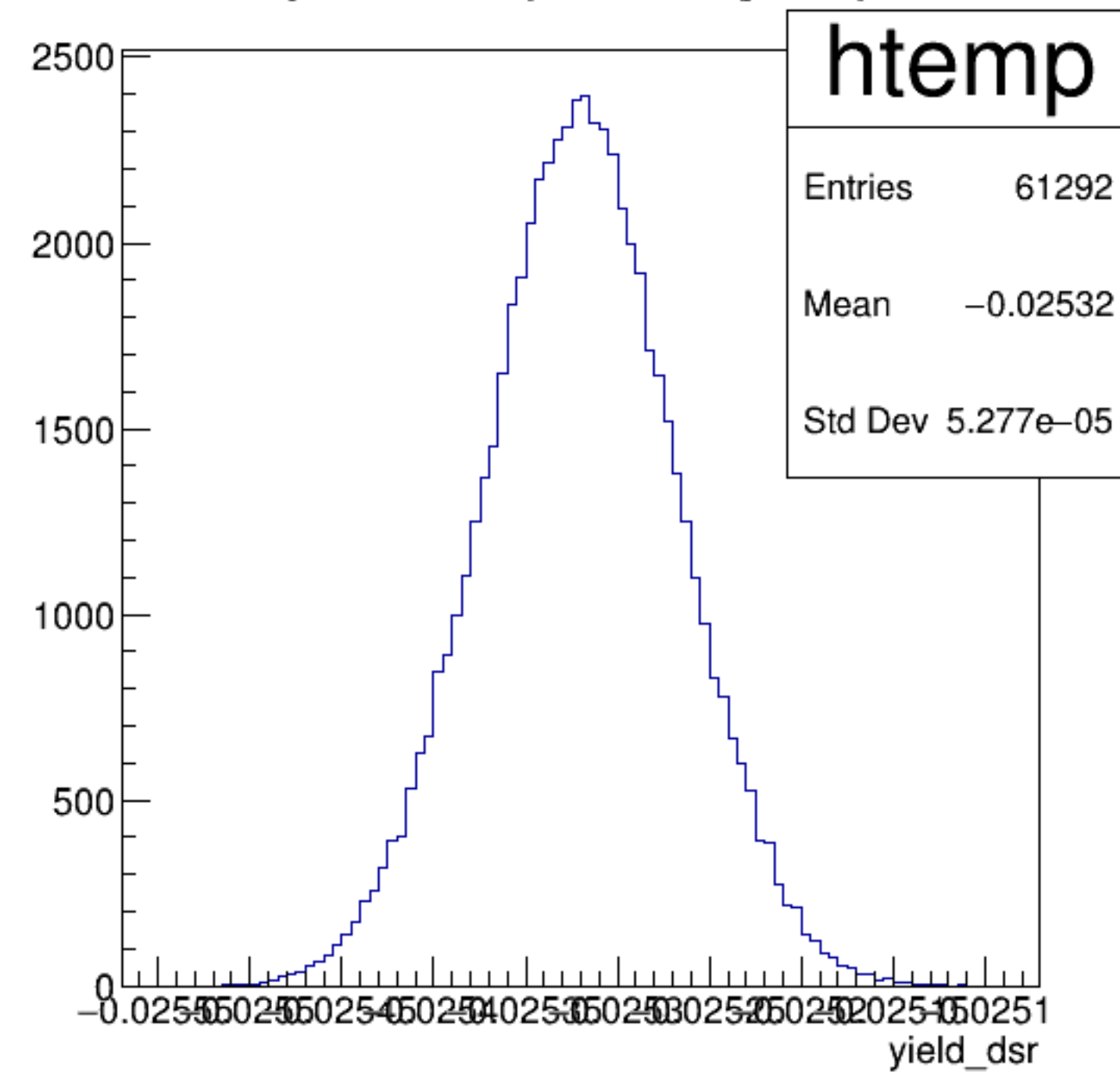
asym_usr/ppm {ErrorFlag==0}



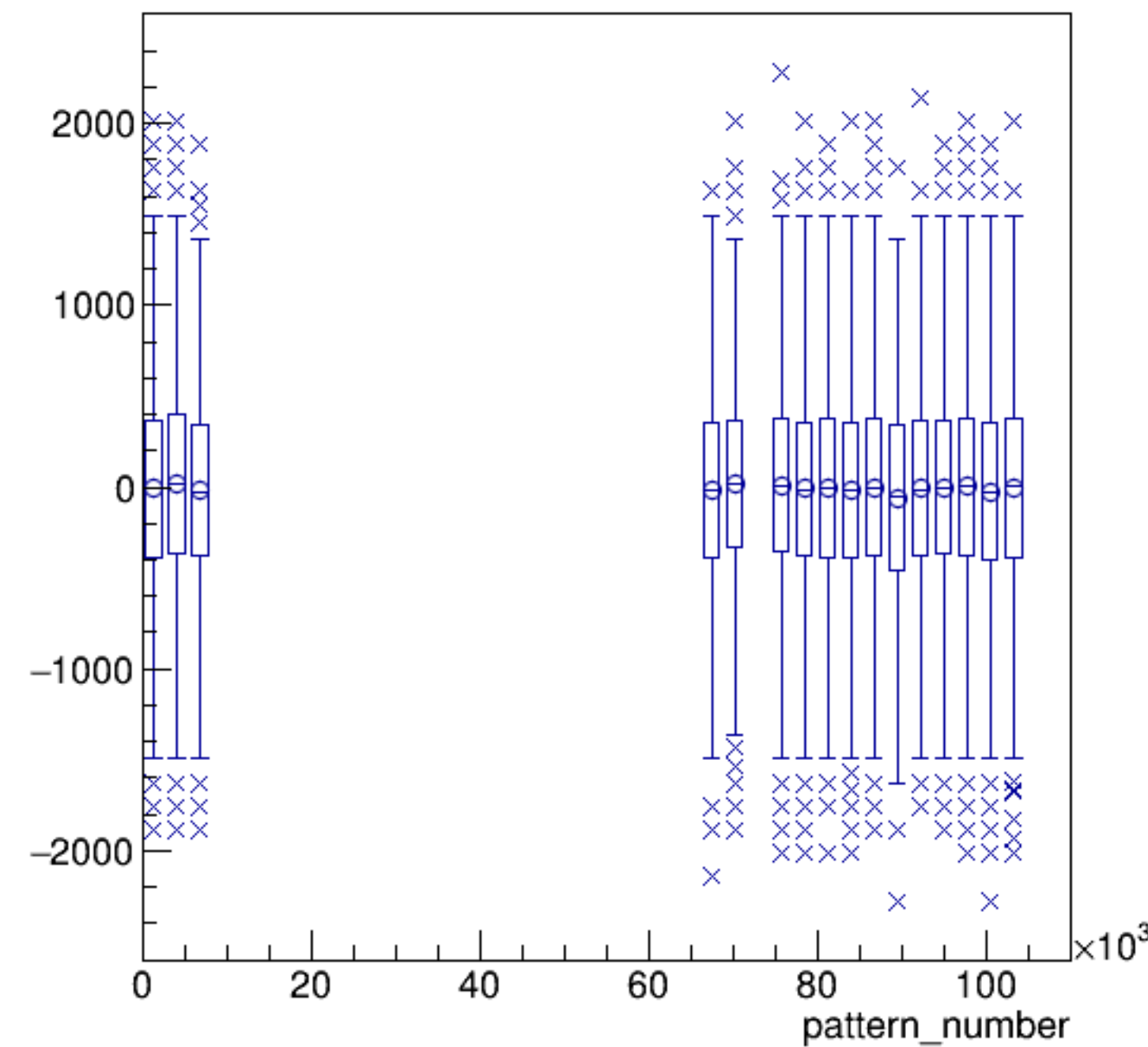
yield_dsr:pattern_number {ErrorFlag==0}



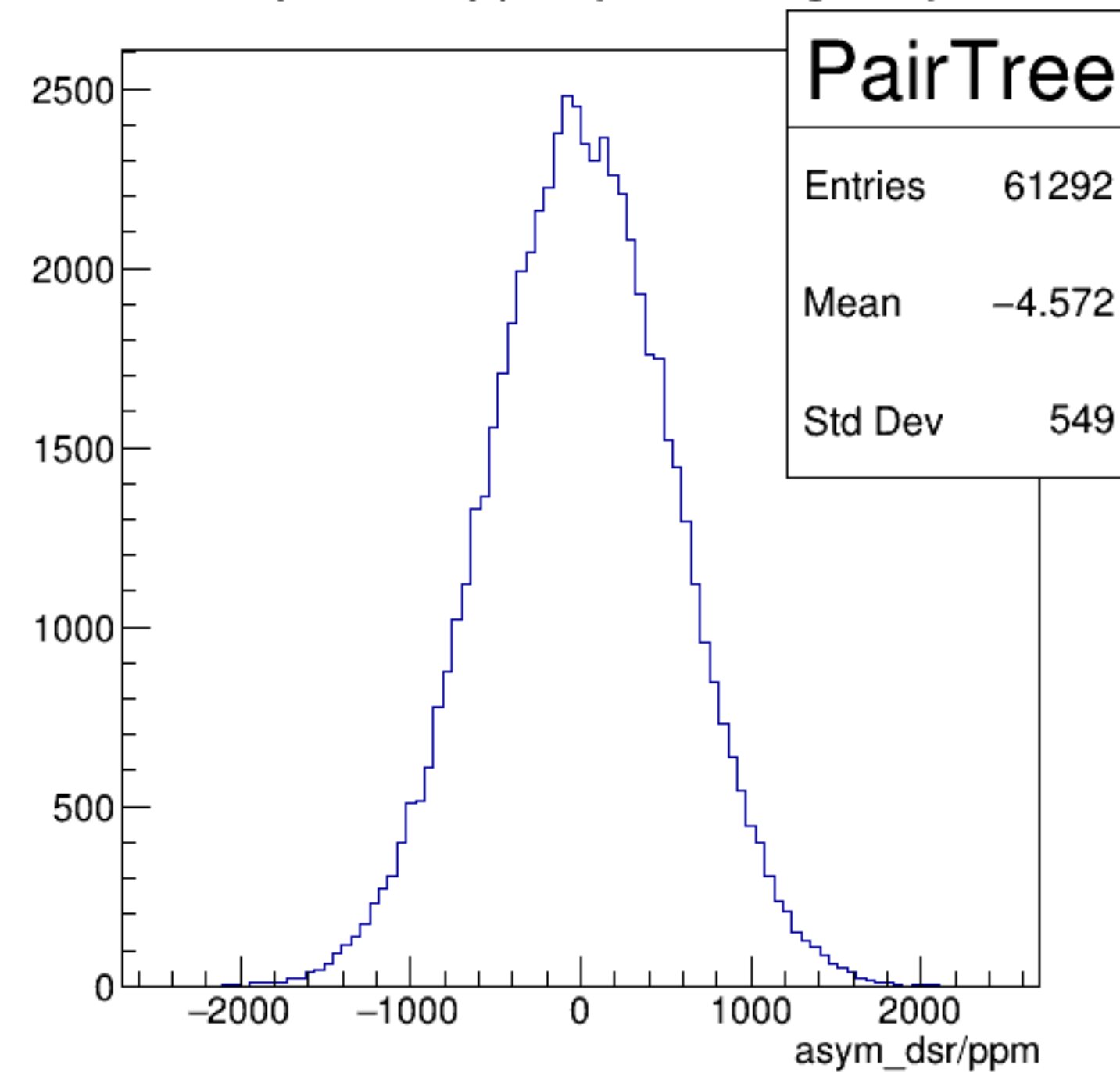
yield_dsr {ErrorFlag==0}



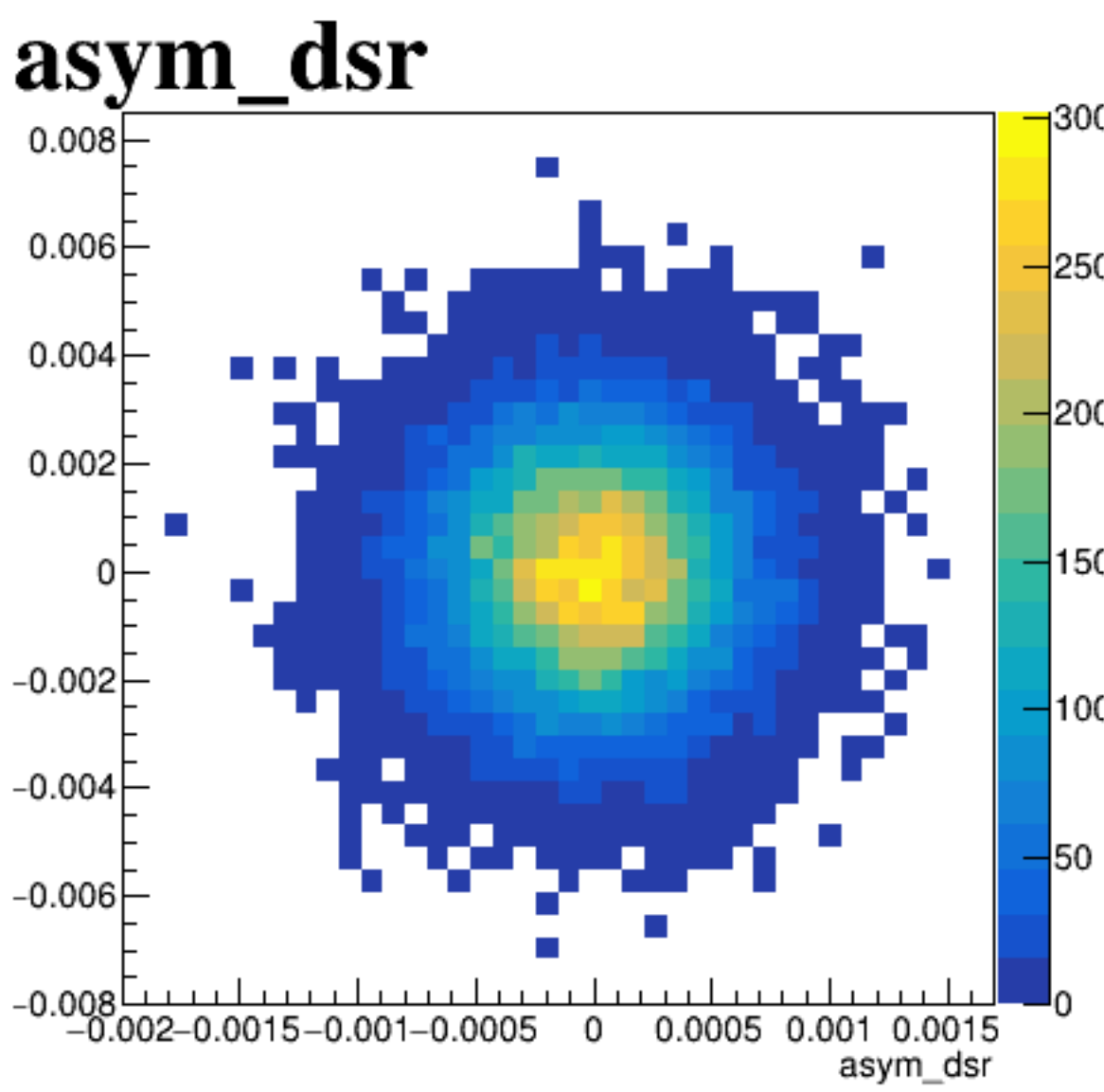
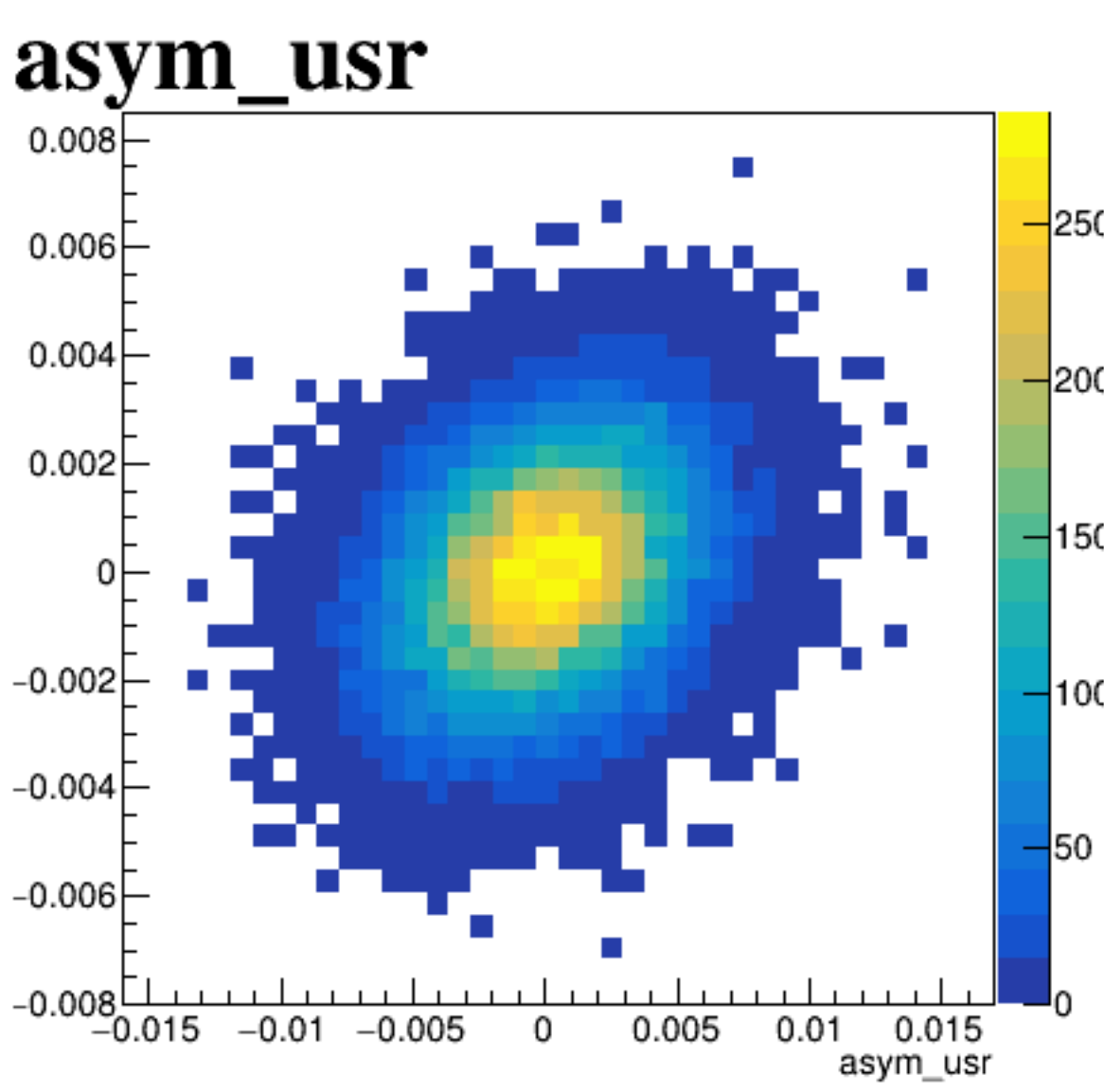
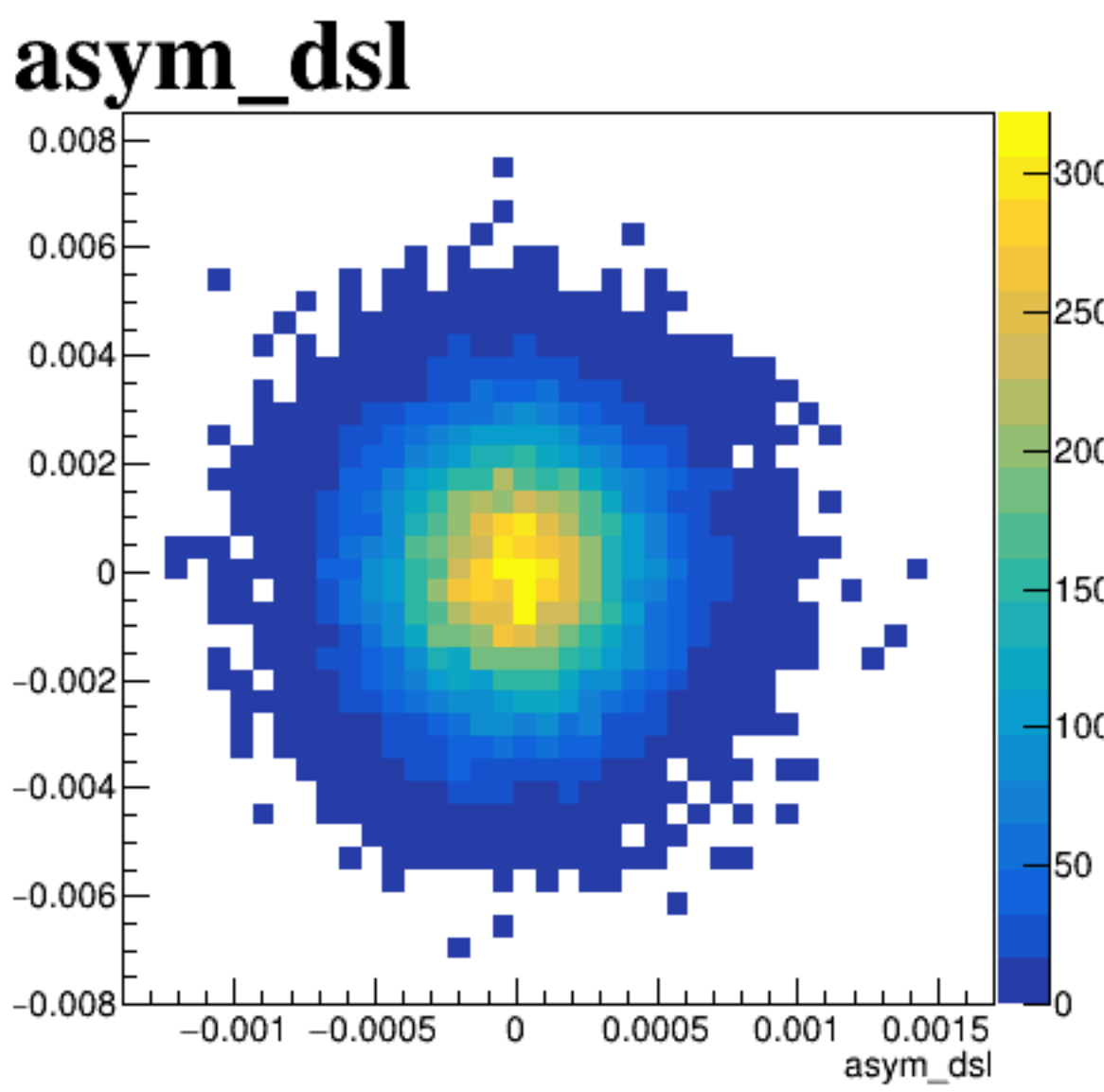
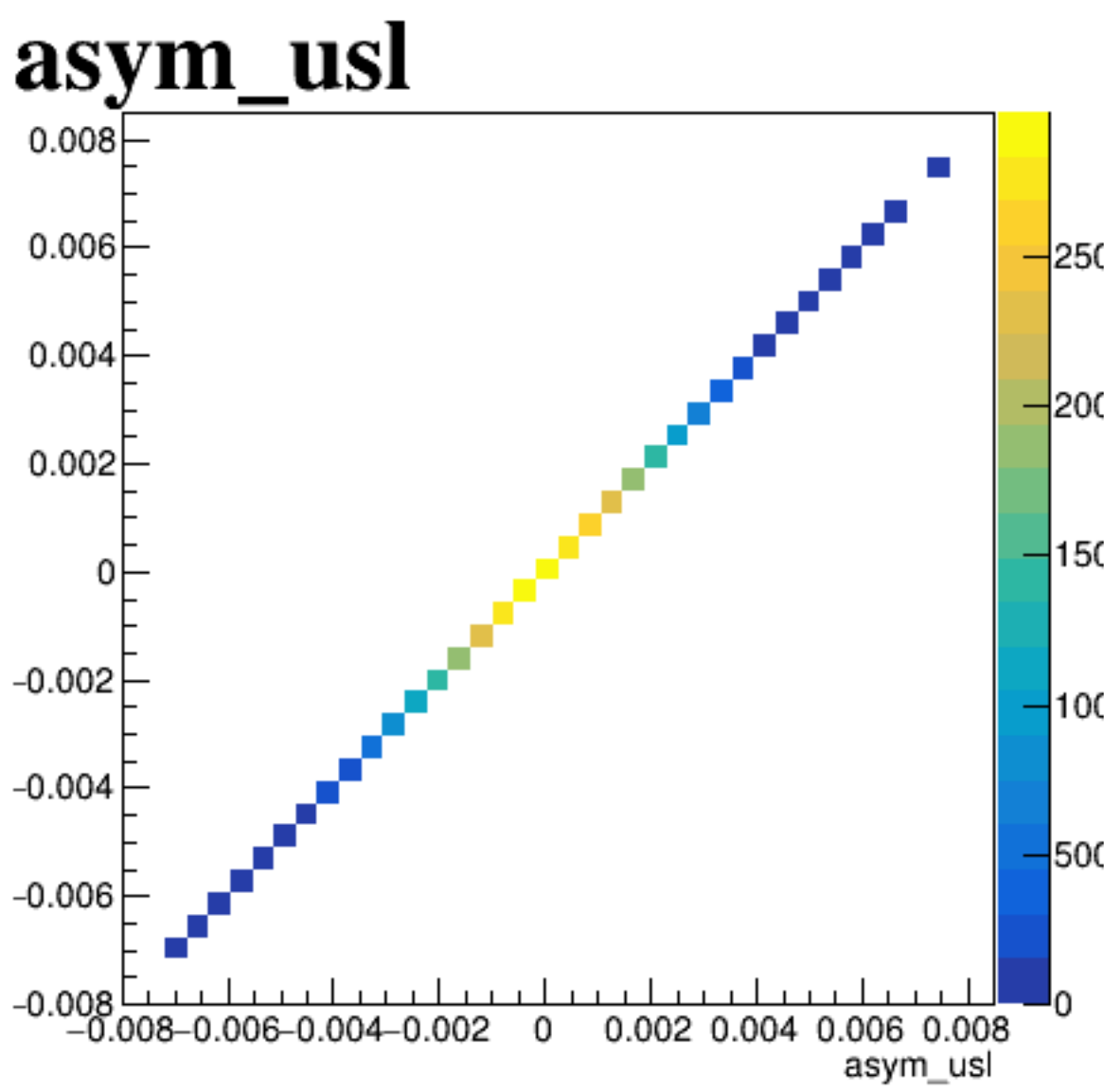
asym_dsr/ppm:pattern_number {ErrorFlag==0}



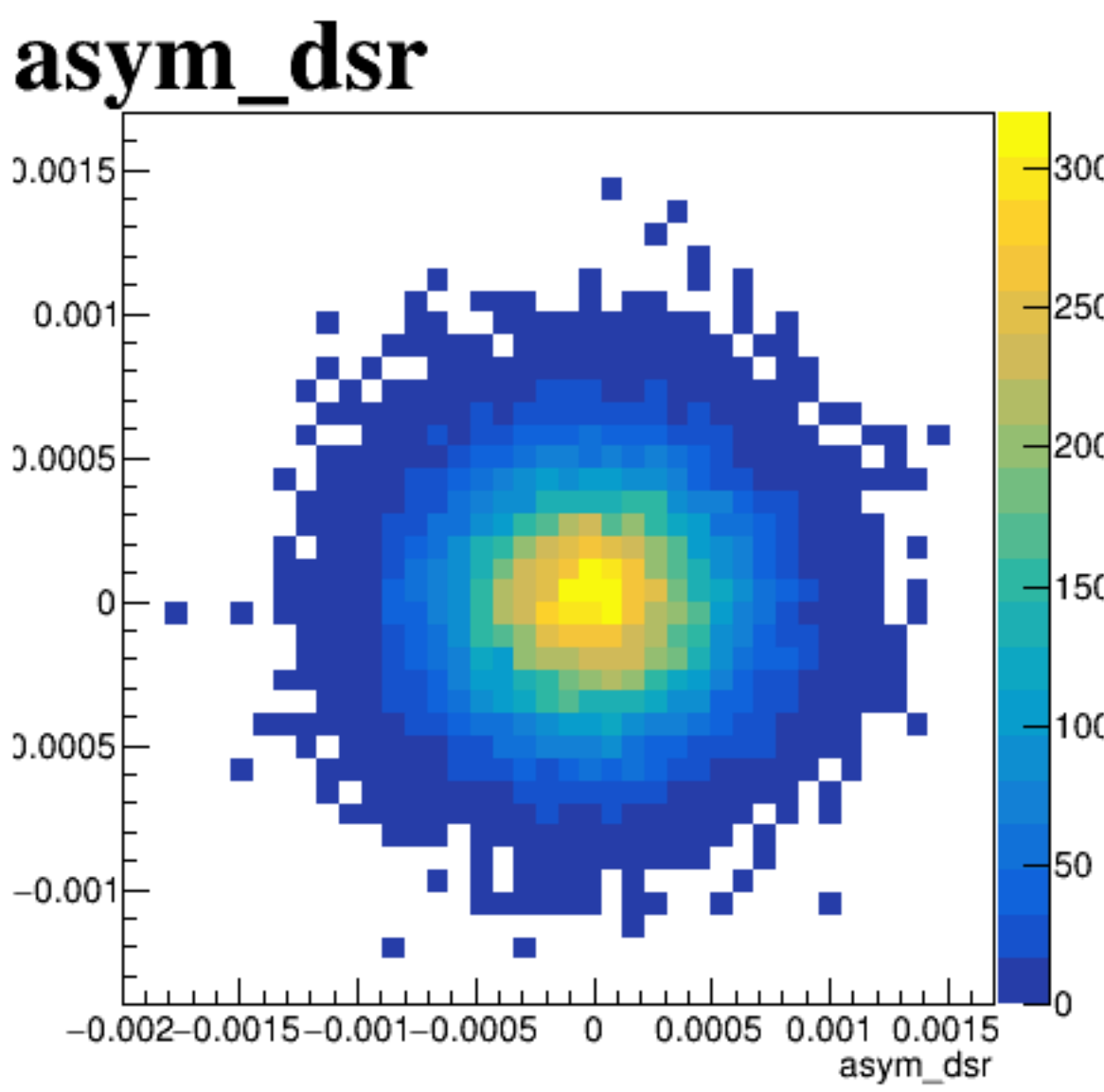
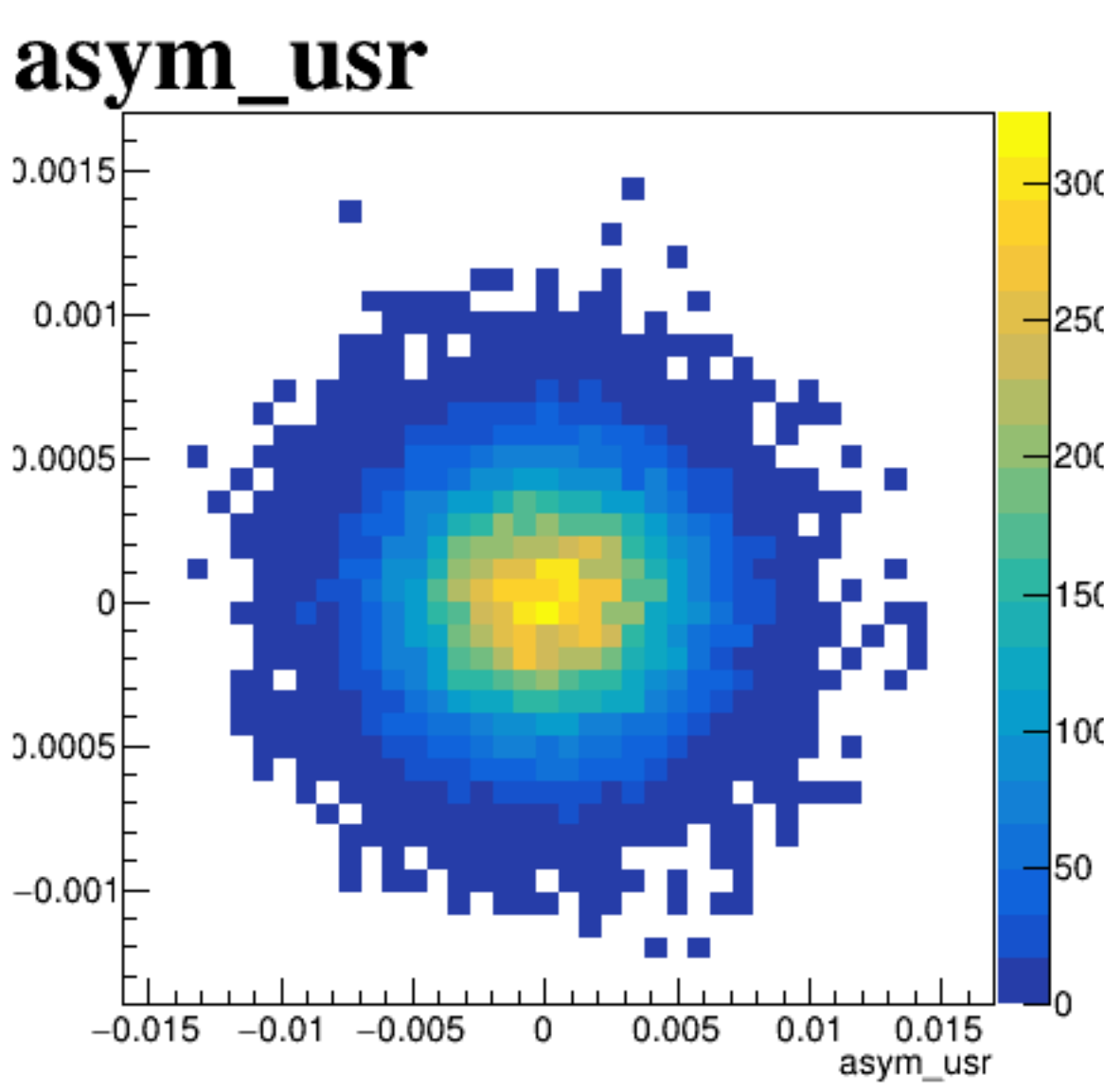
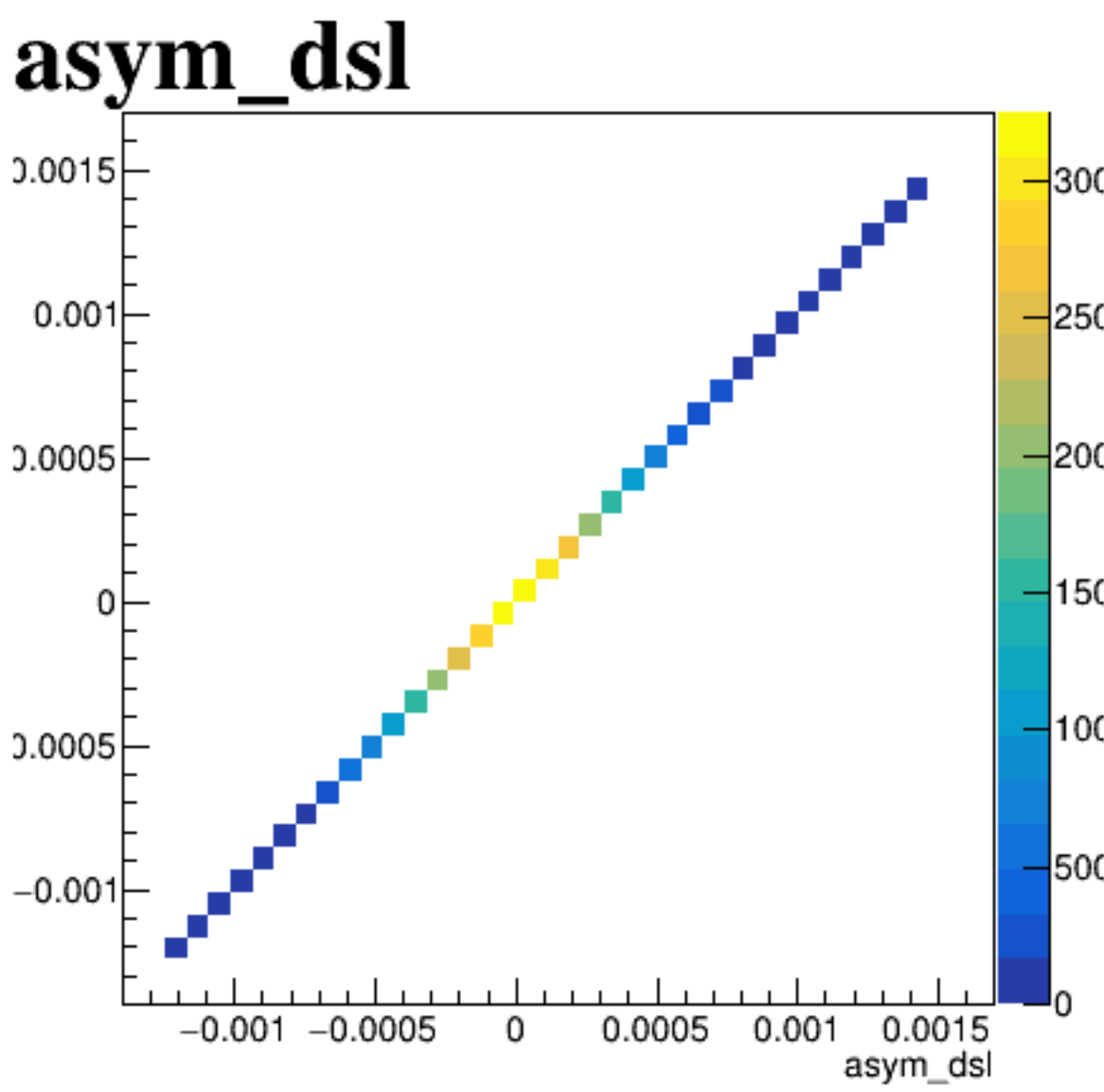
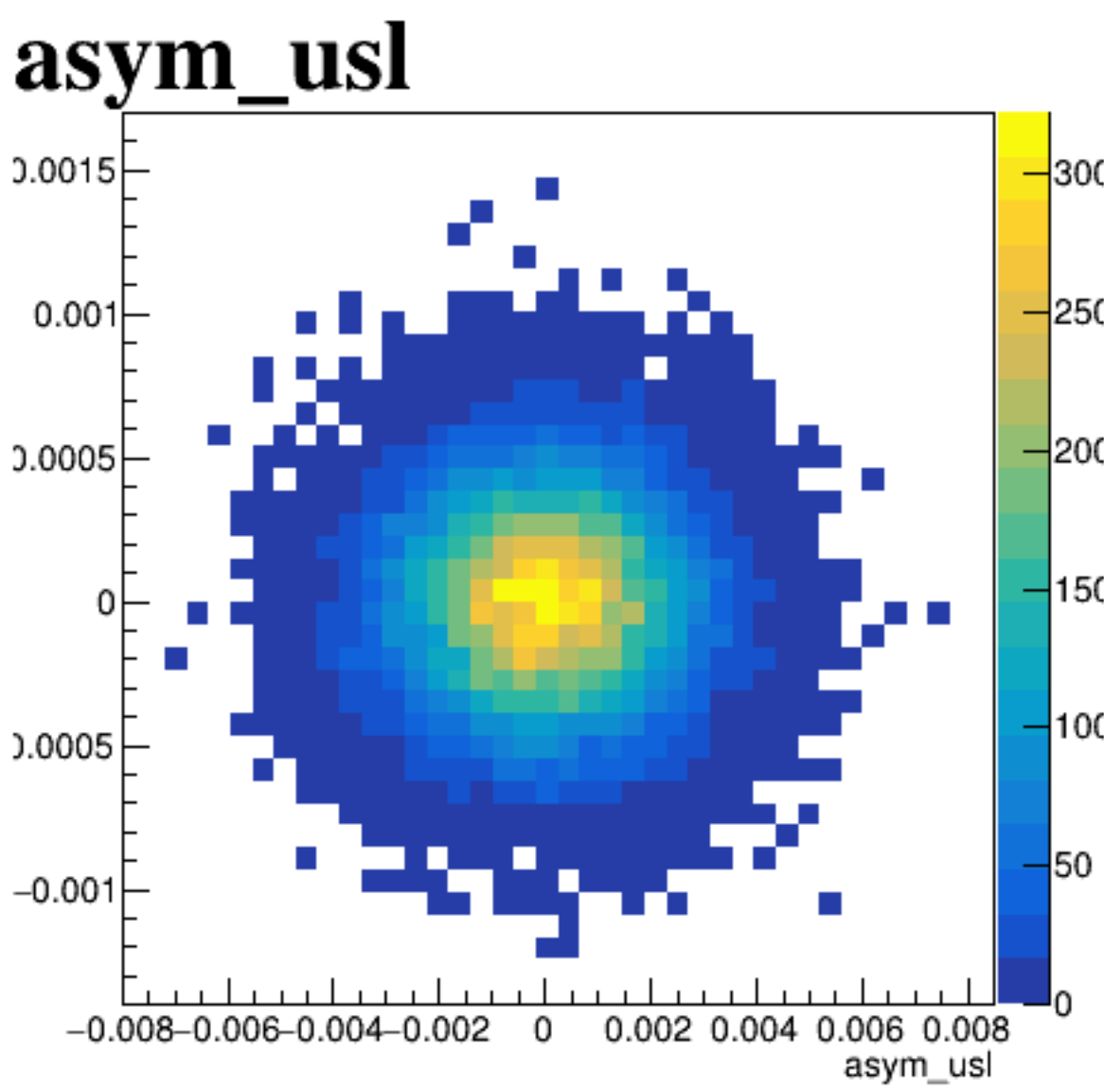
asym_dsr/ppm {ErrorFlag==0}



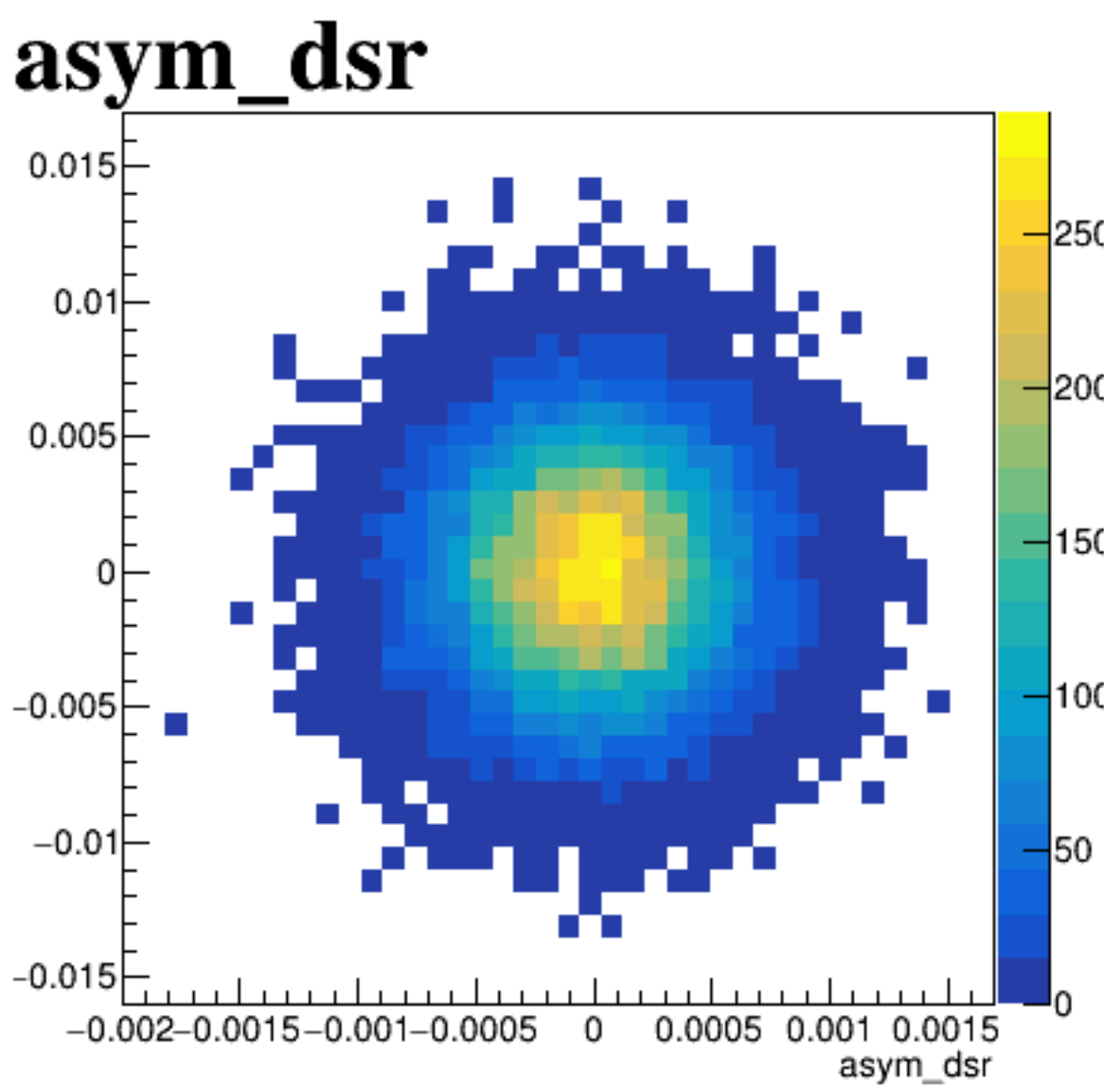
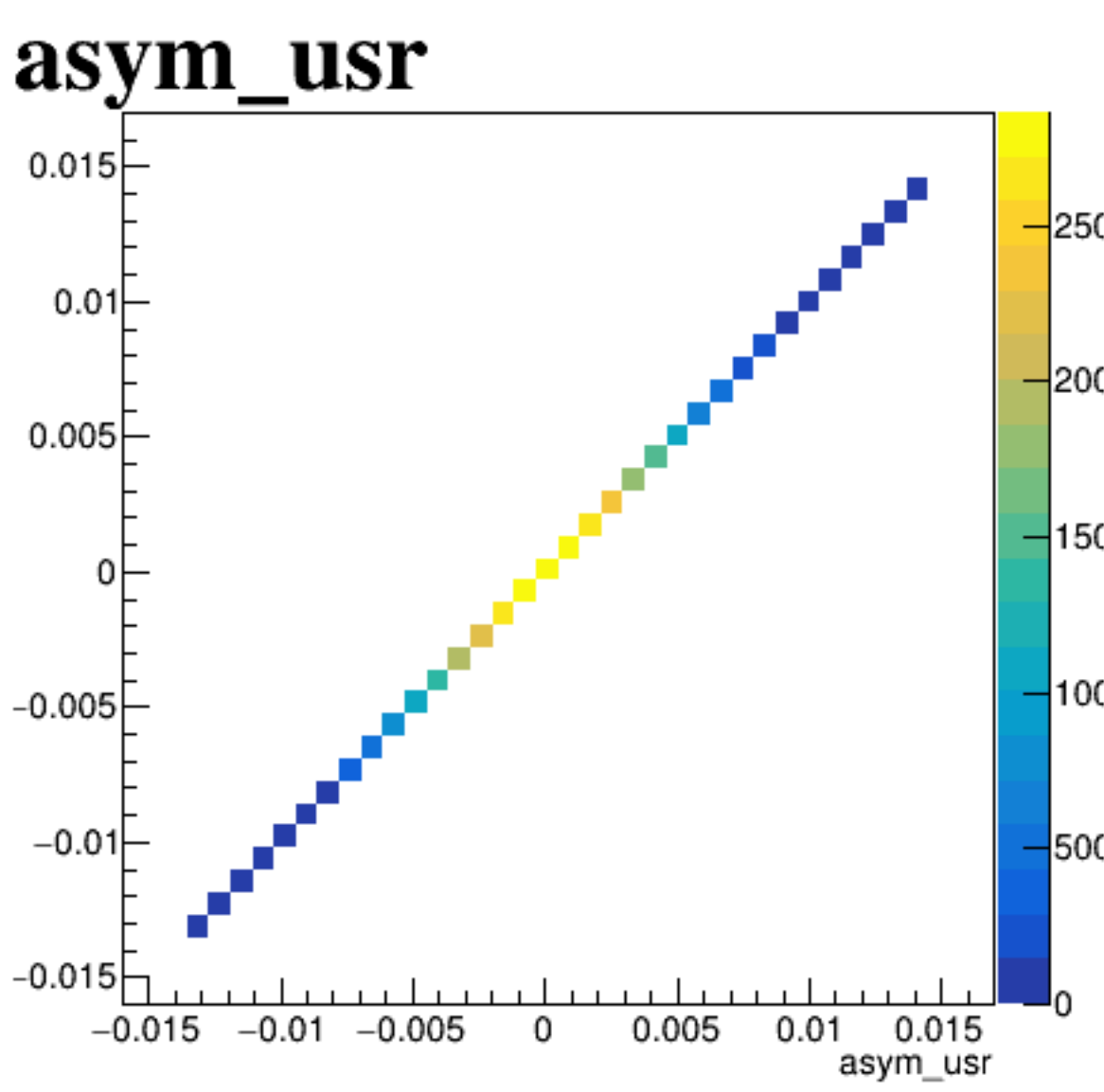
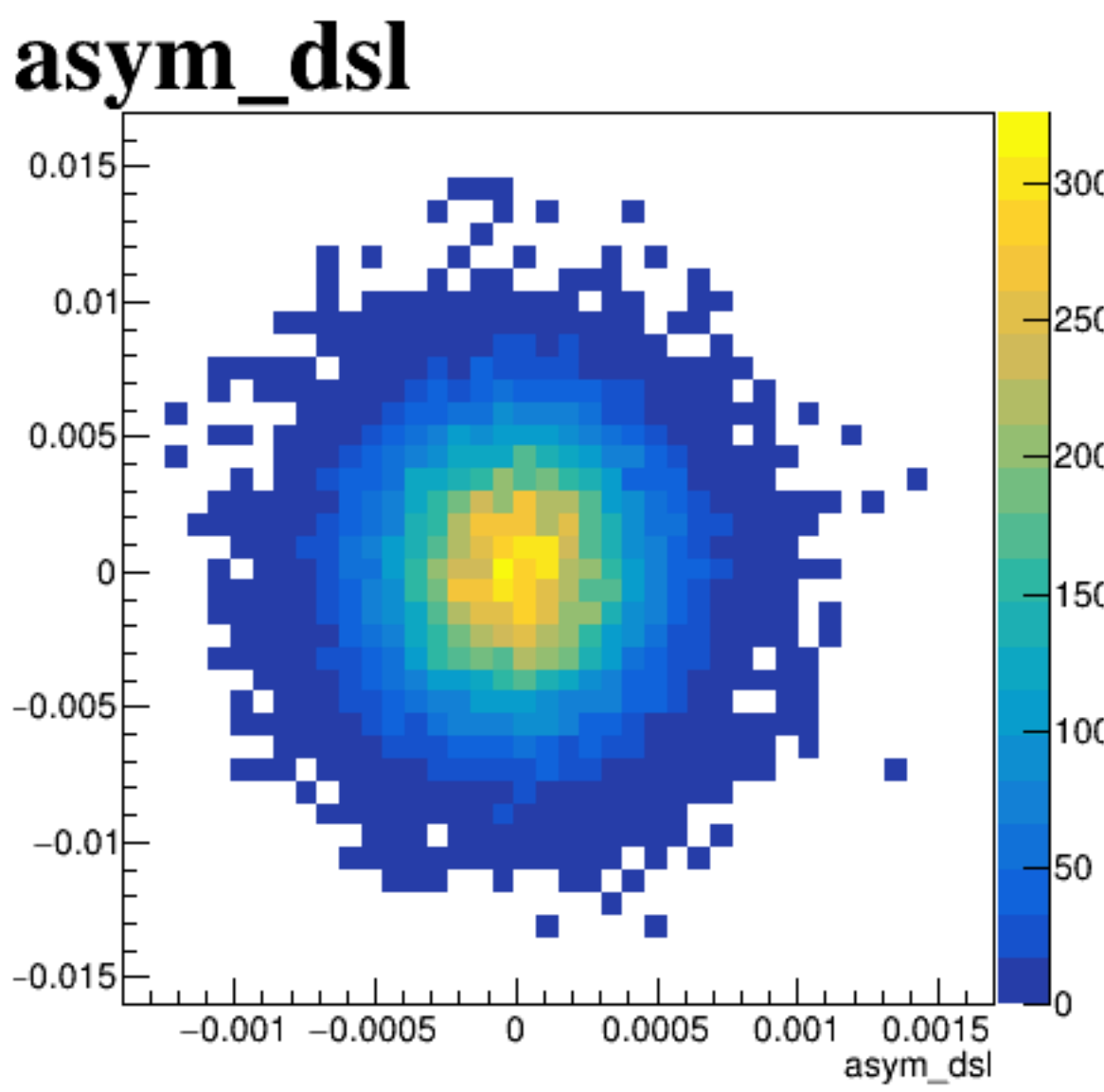
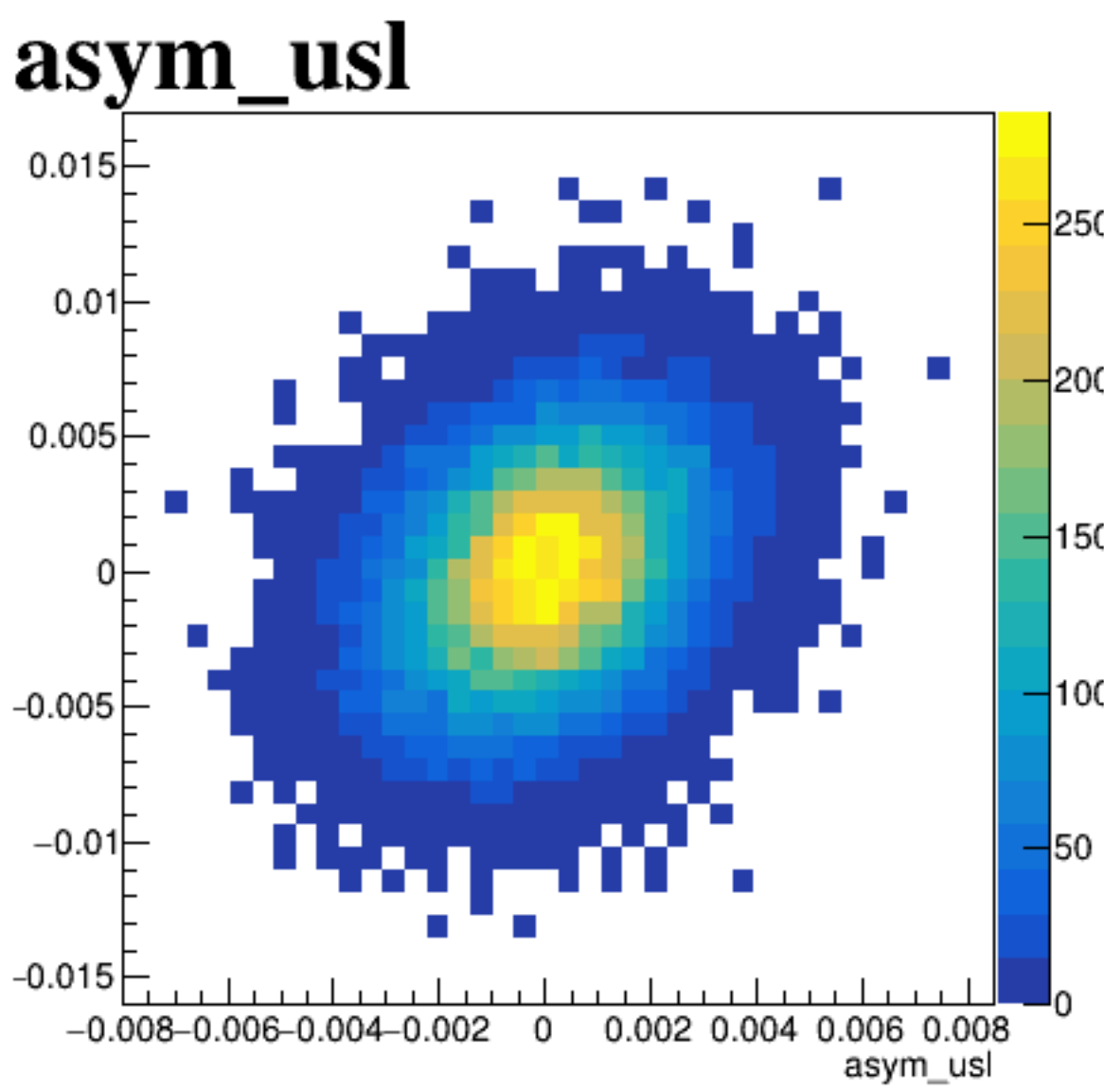
asym_usl



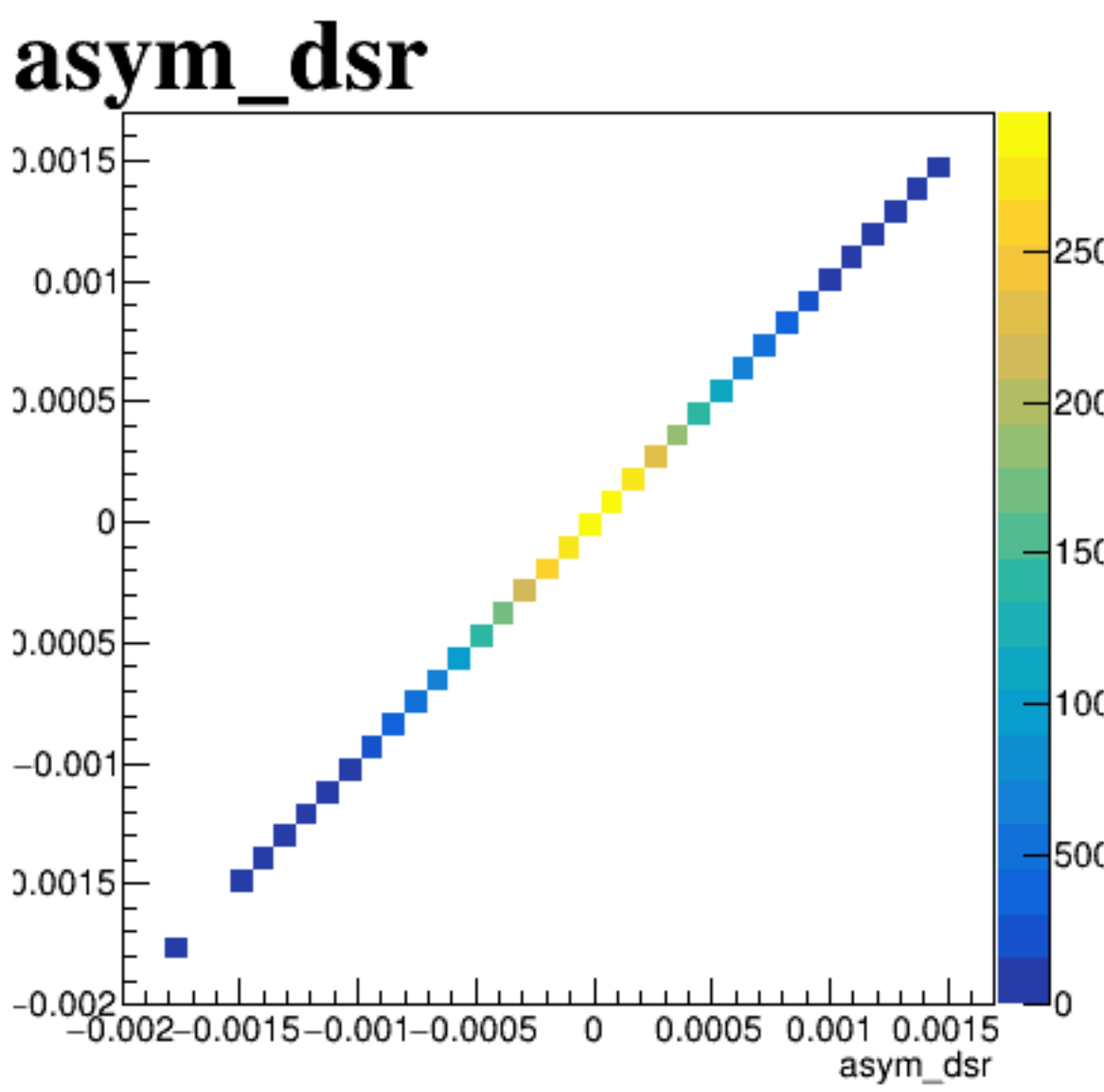
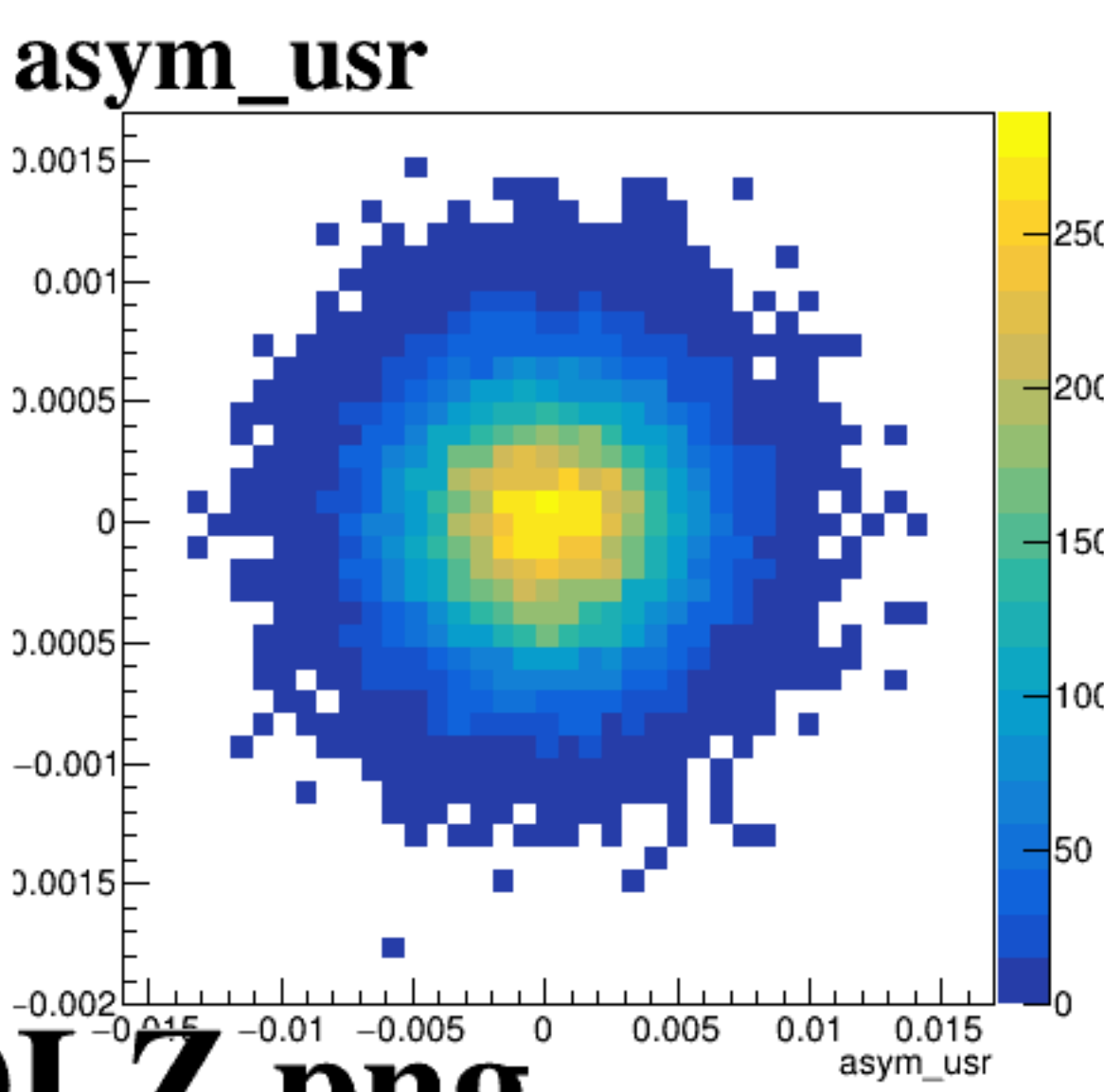
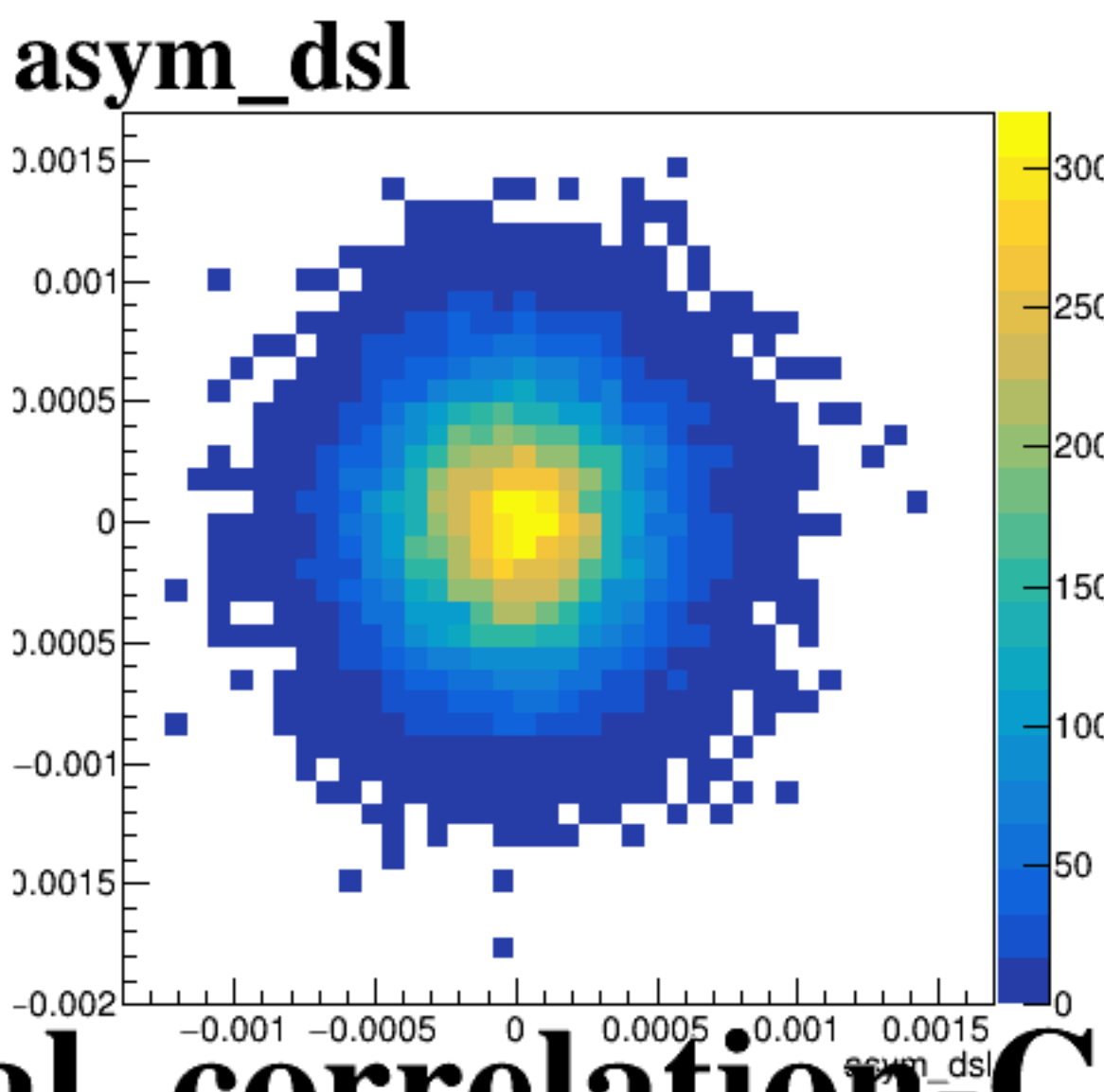
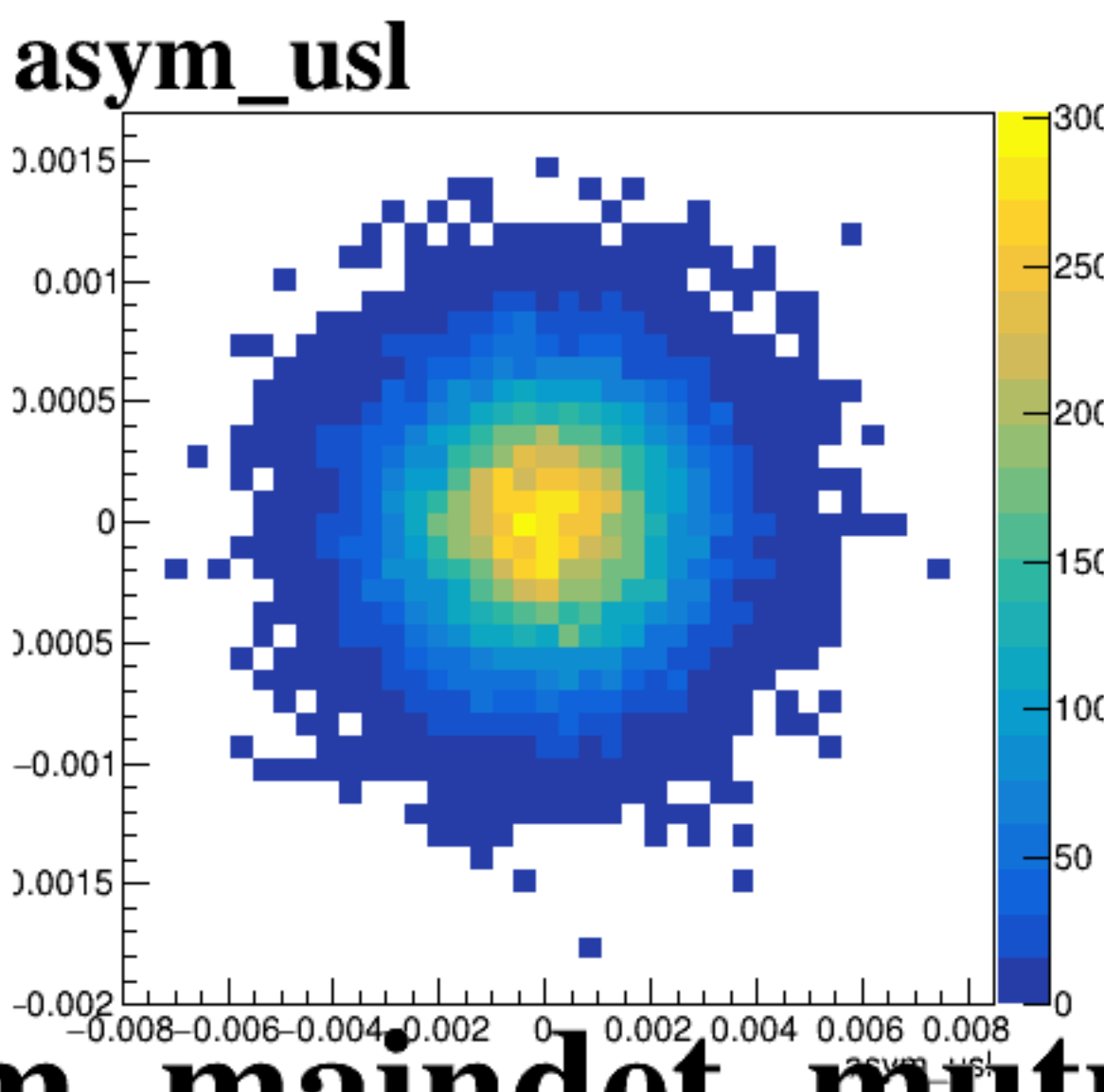
asym_dsl



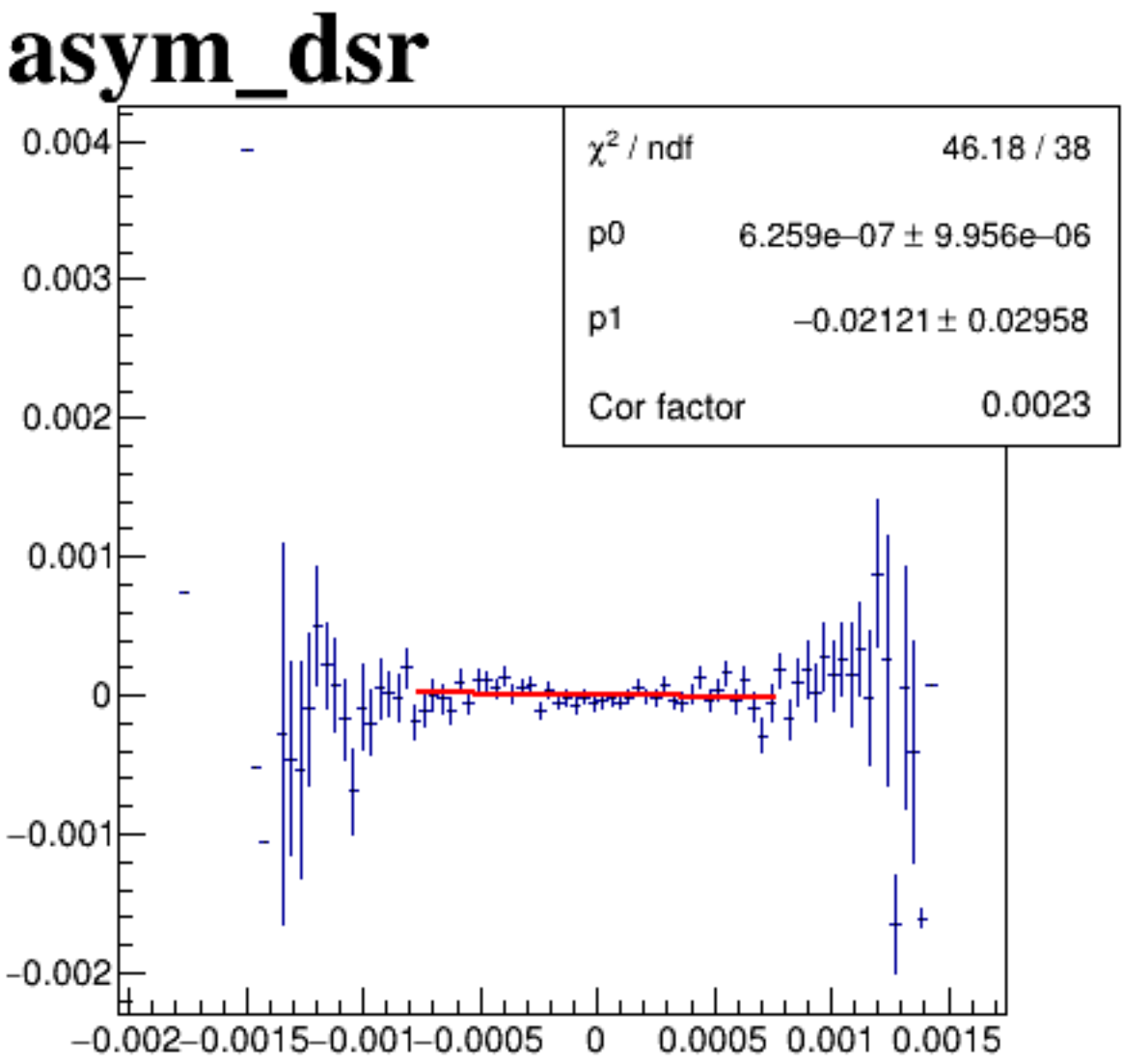
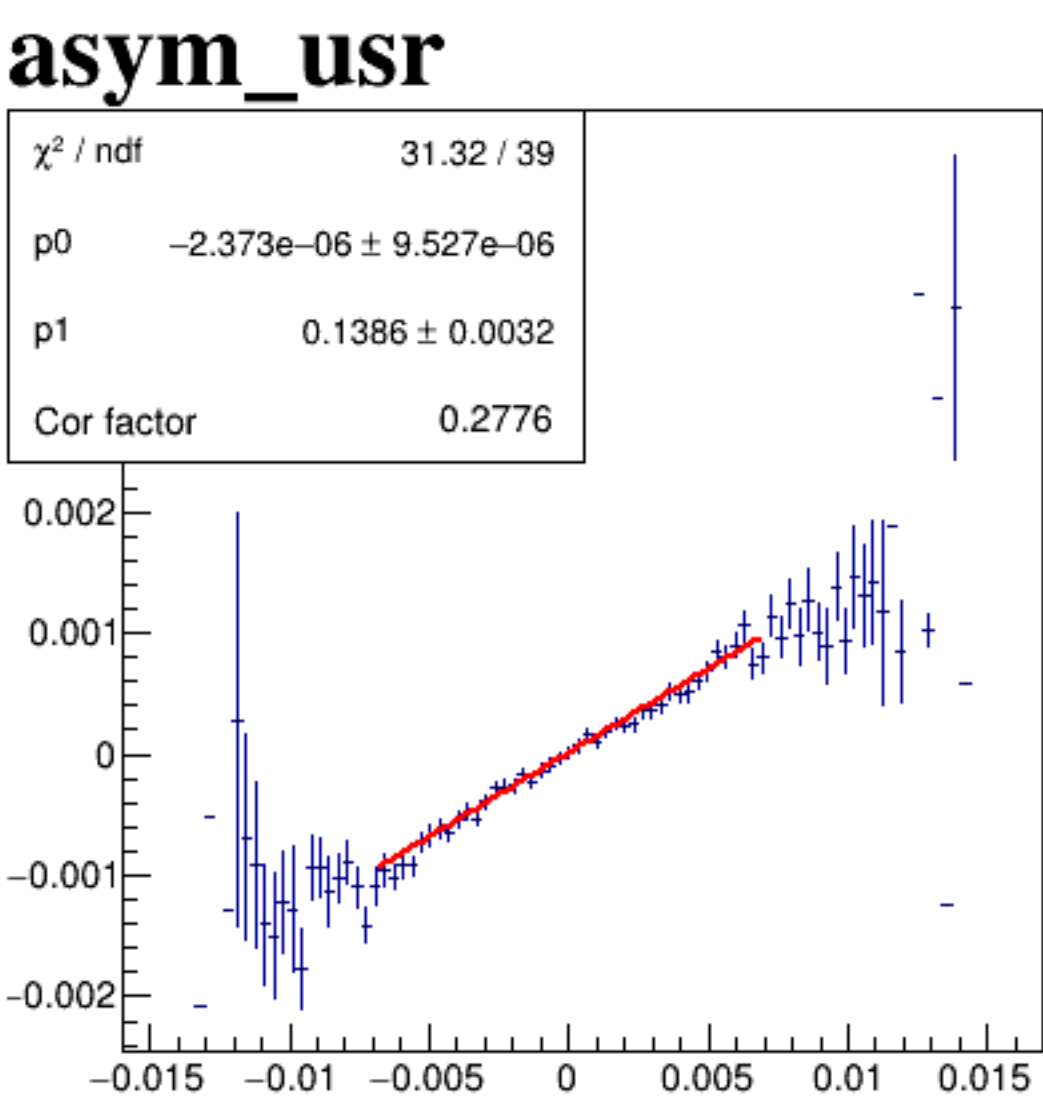
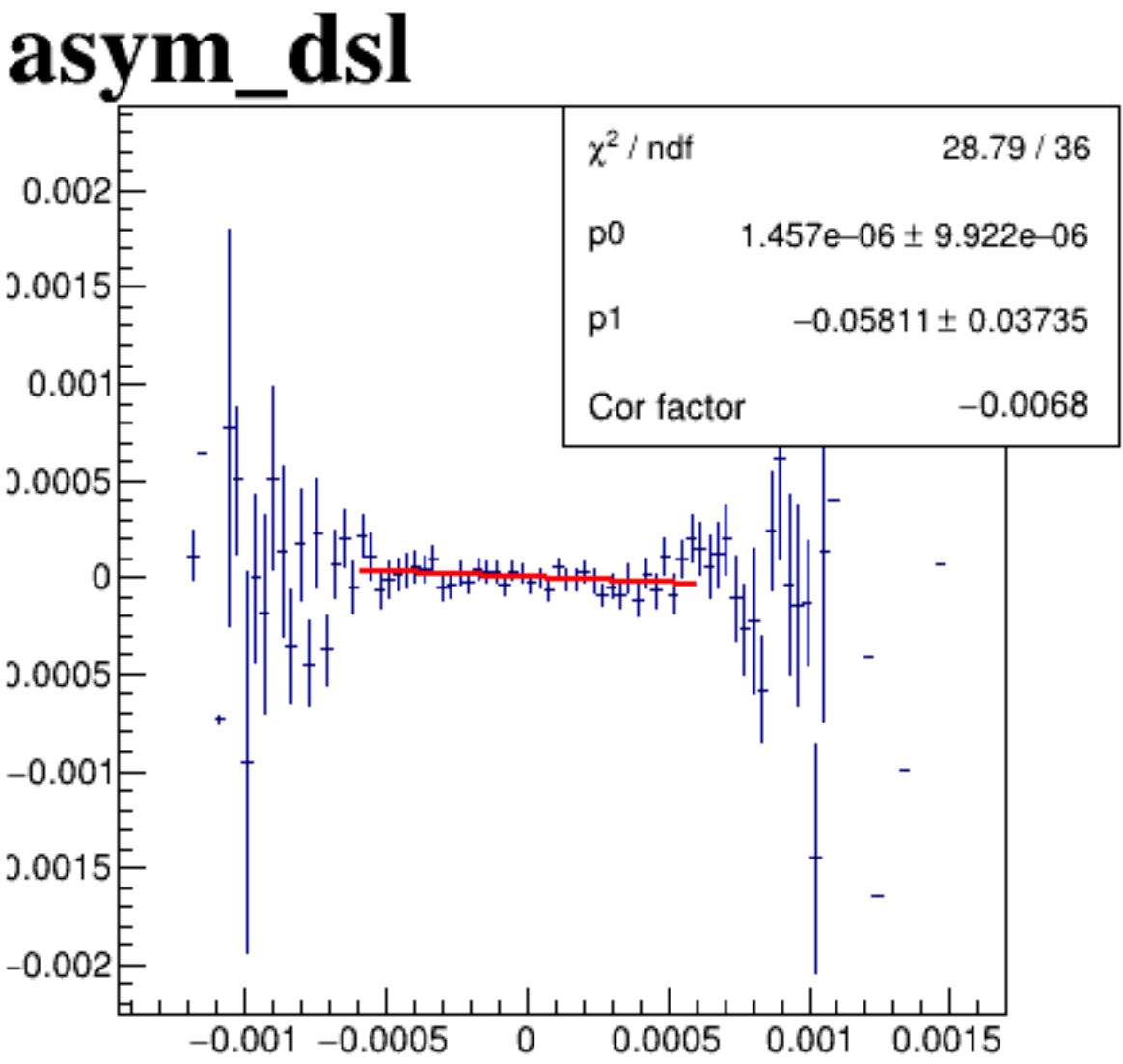
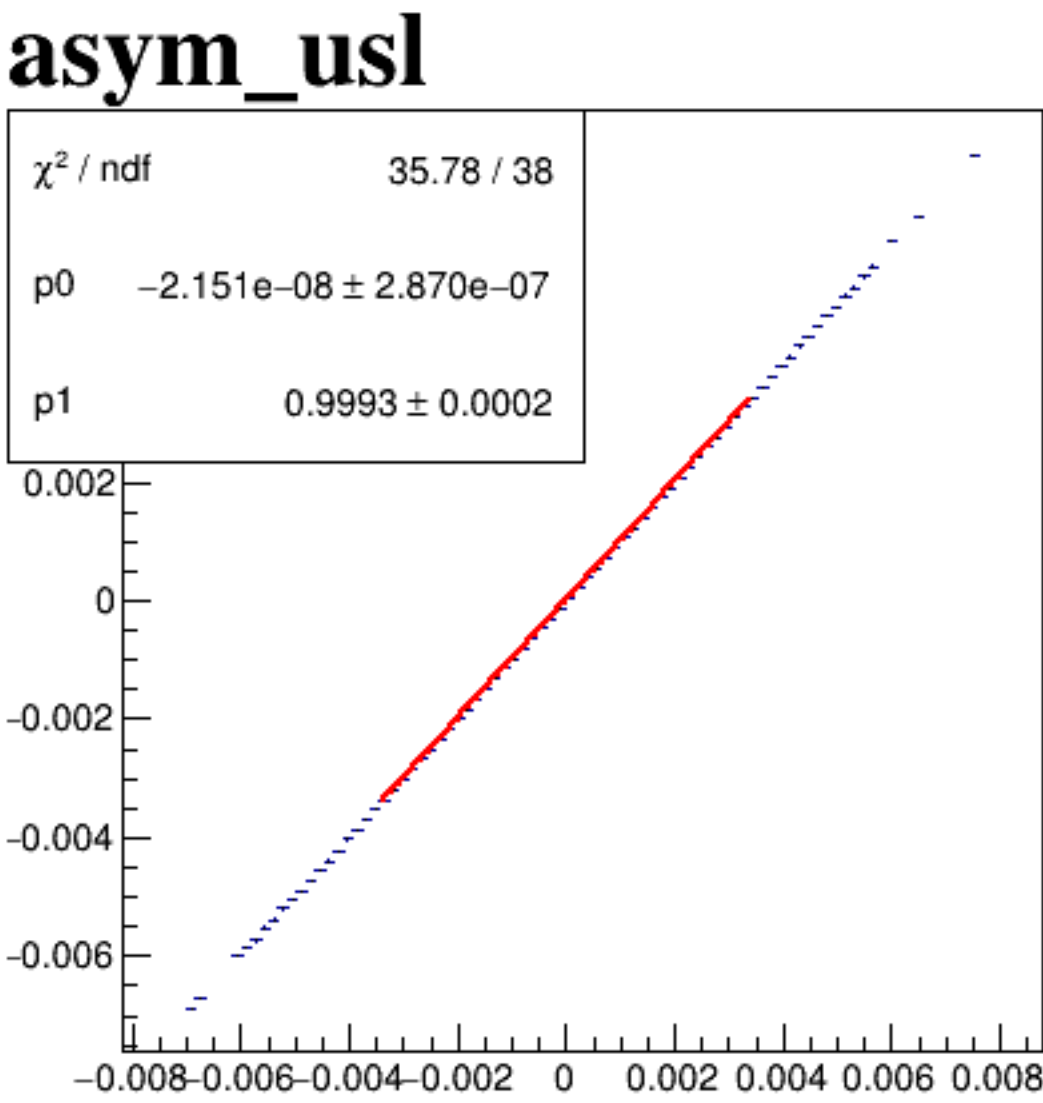
asym_usr



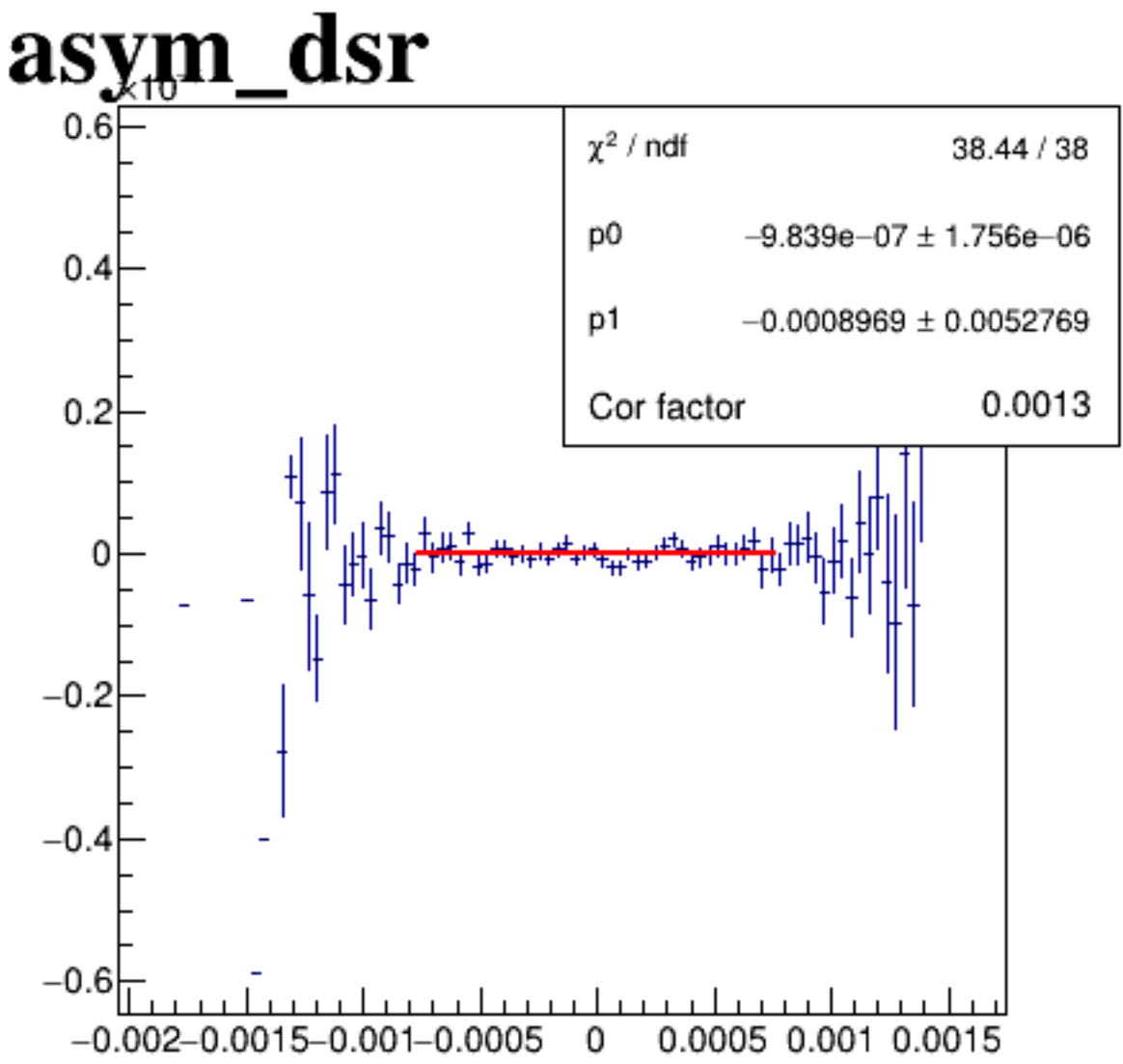
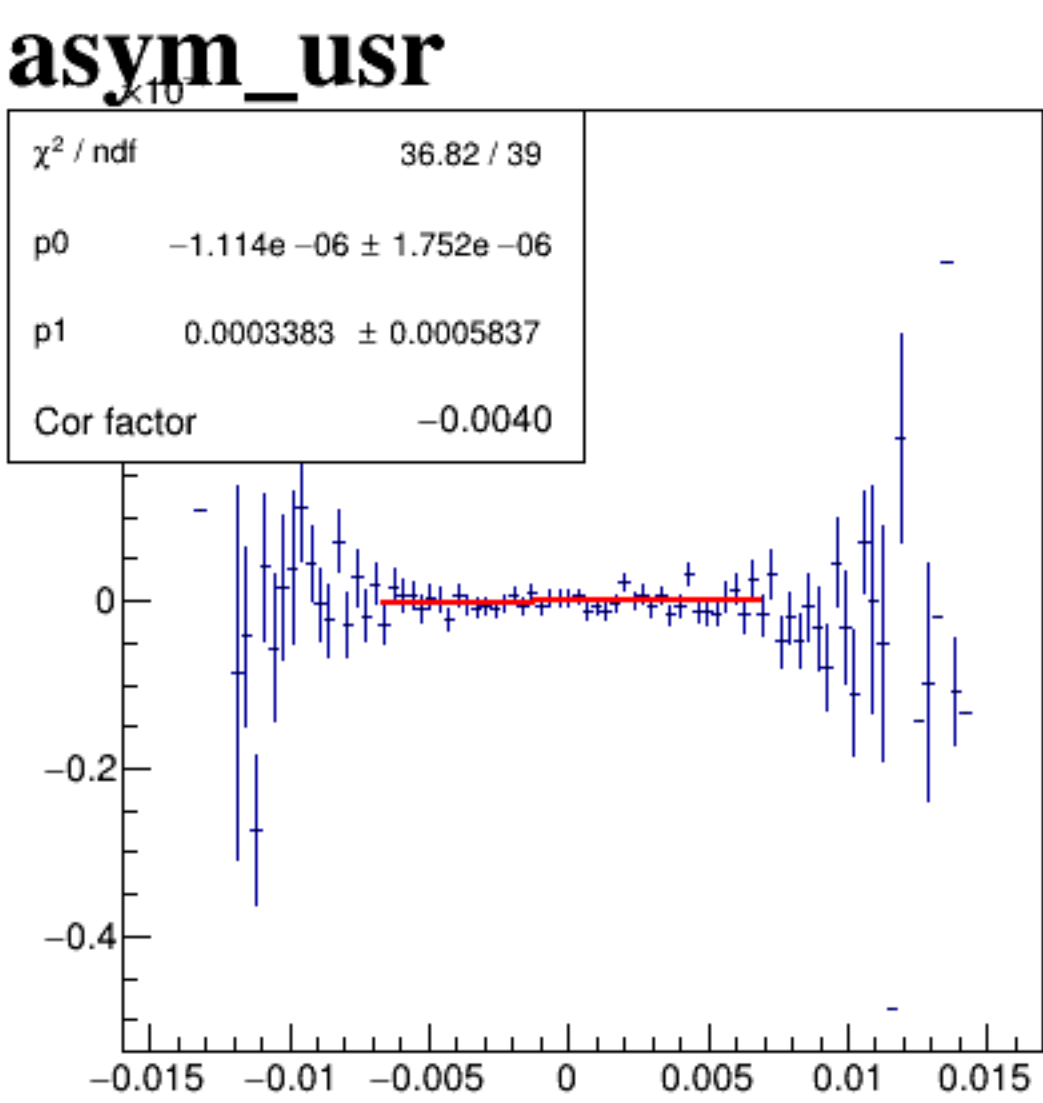
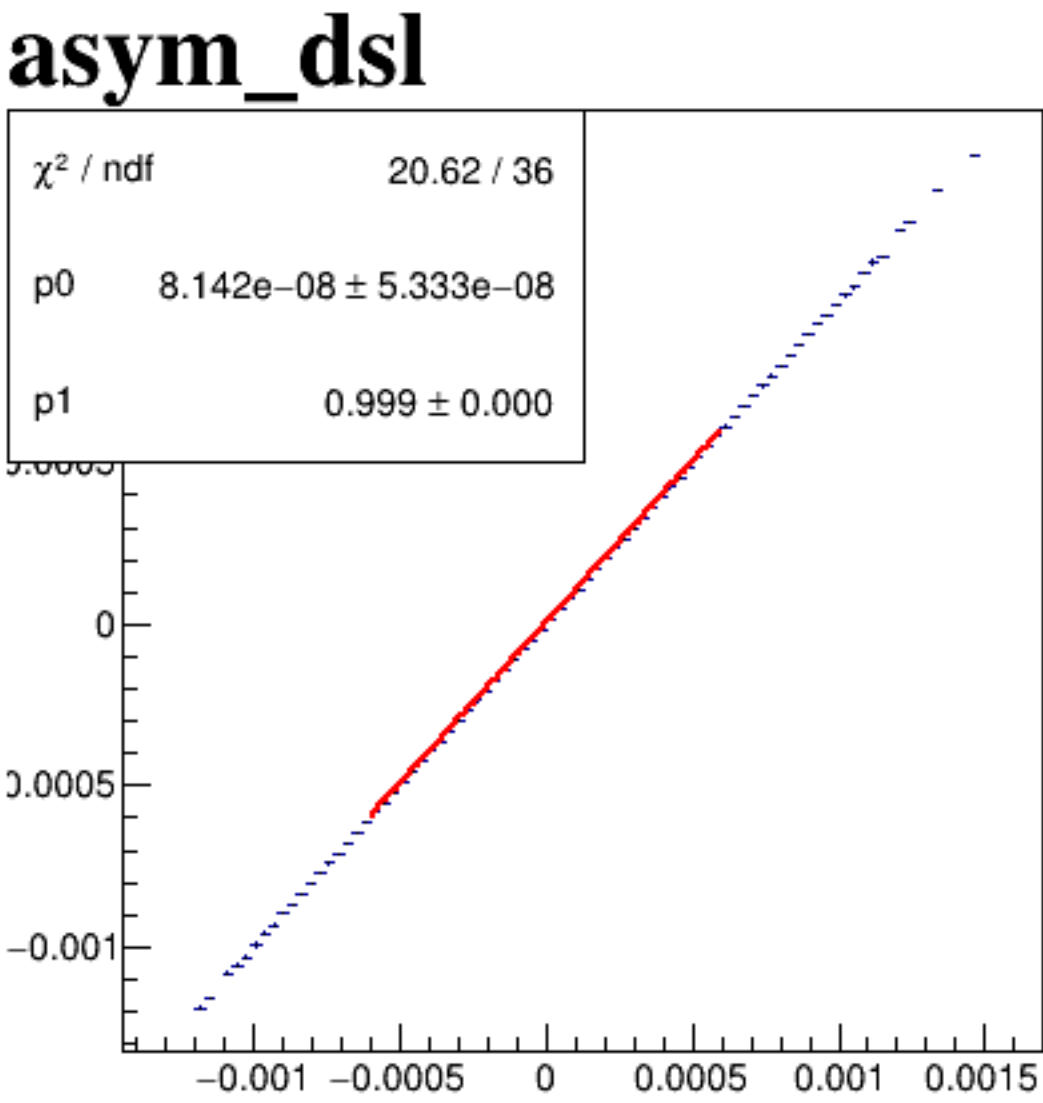
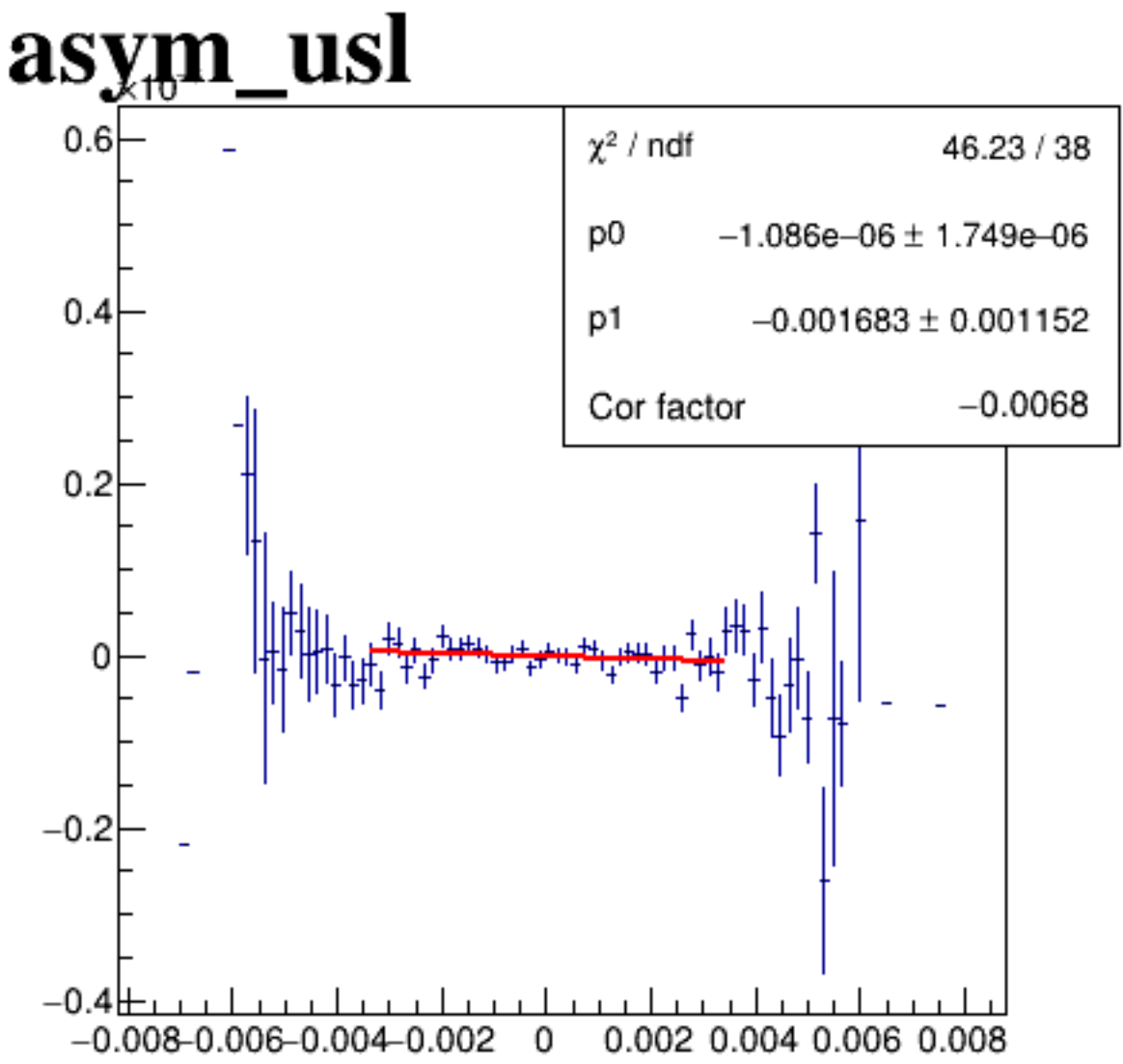
asym_dsr



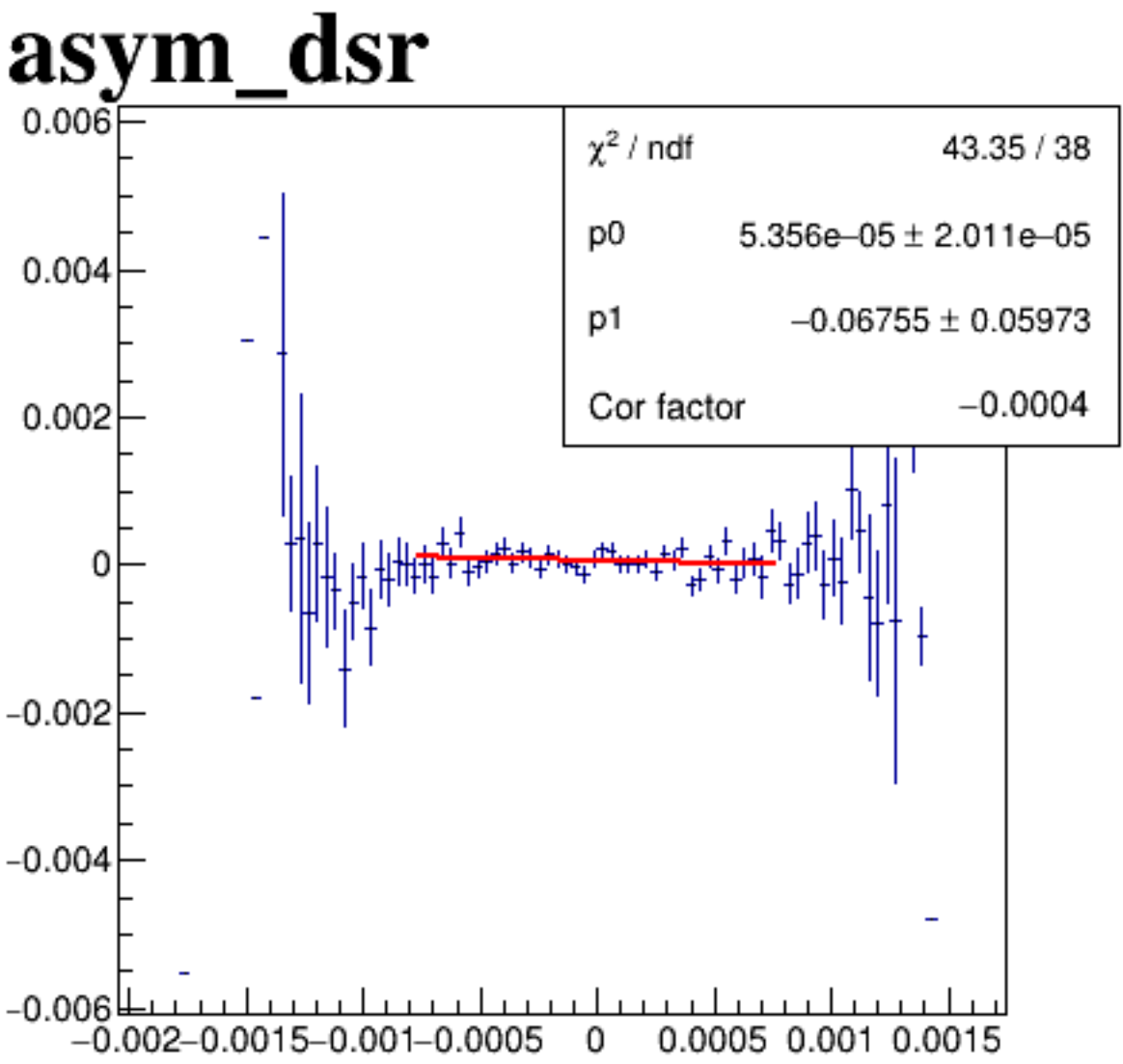
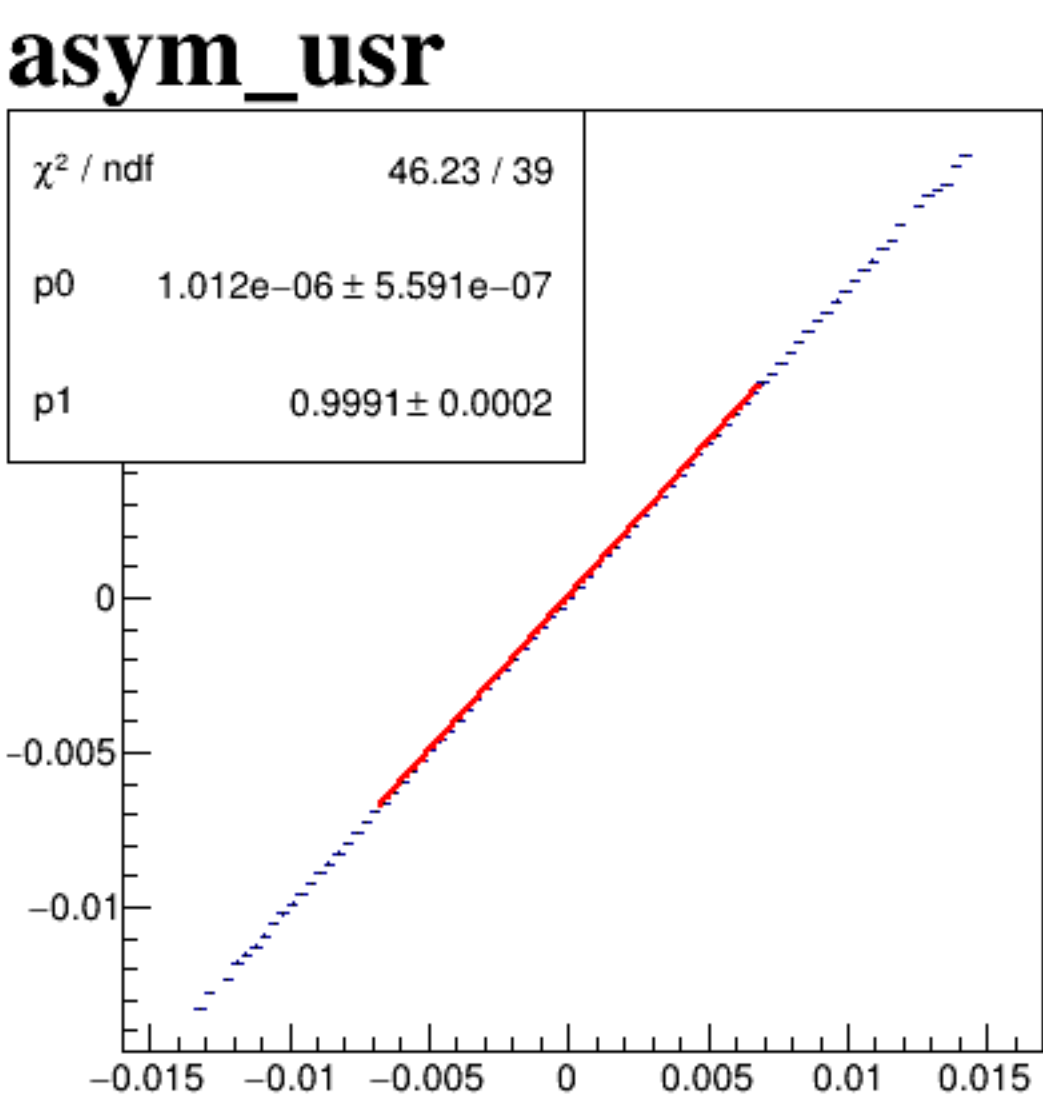
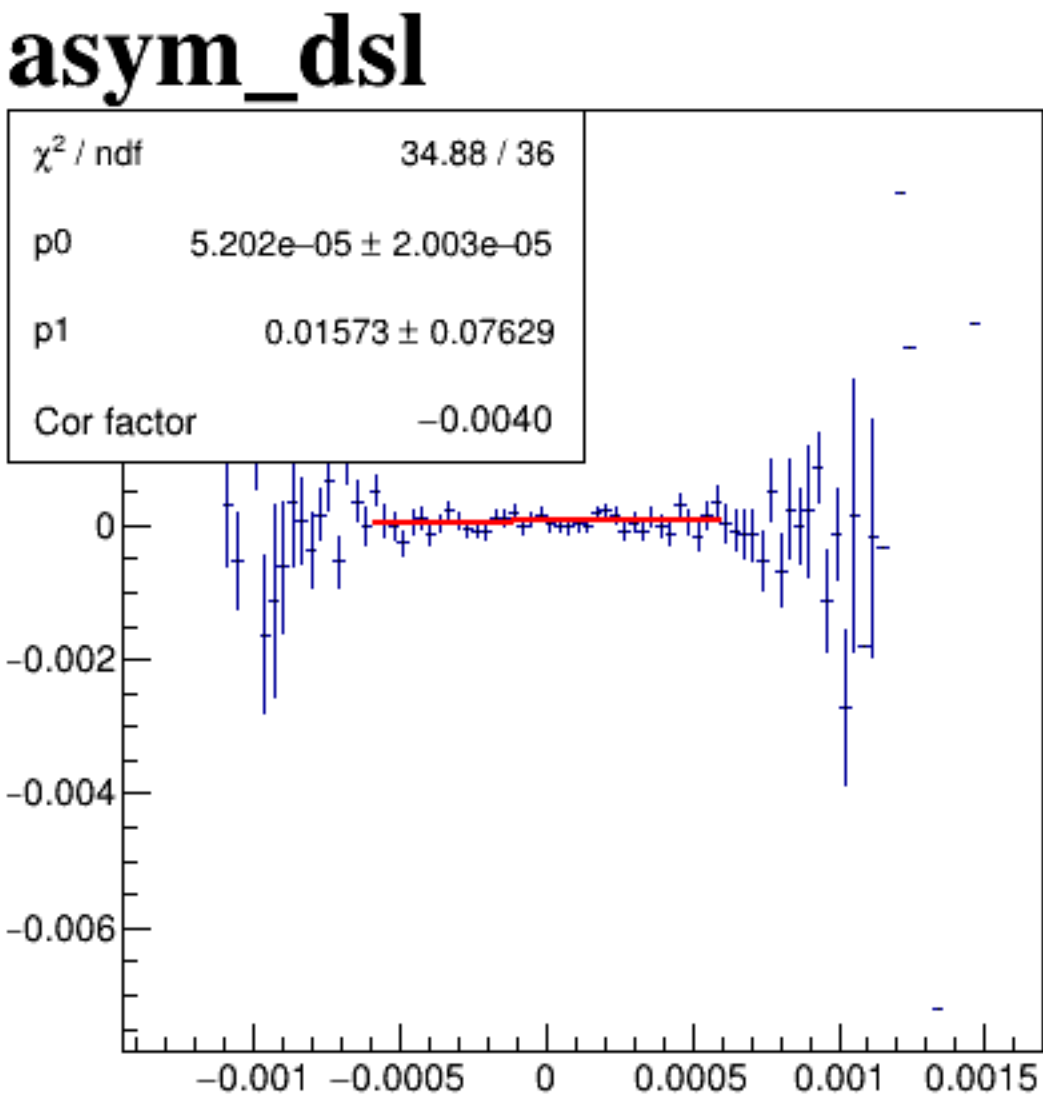
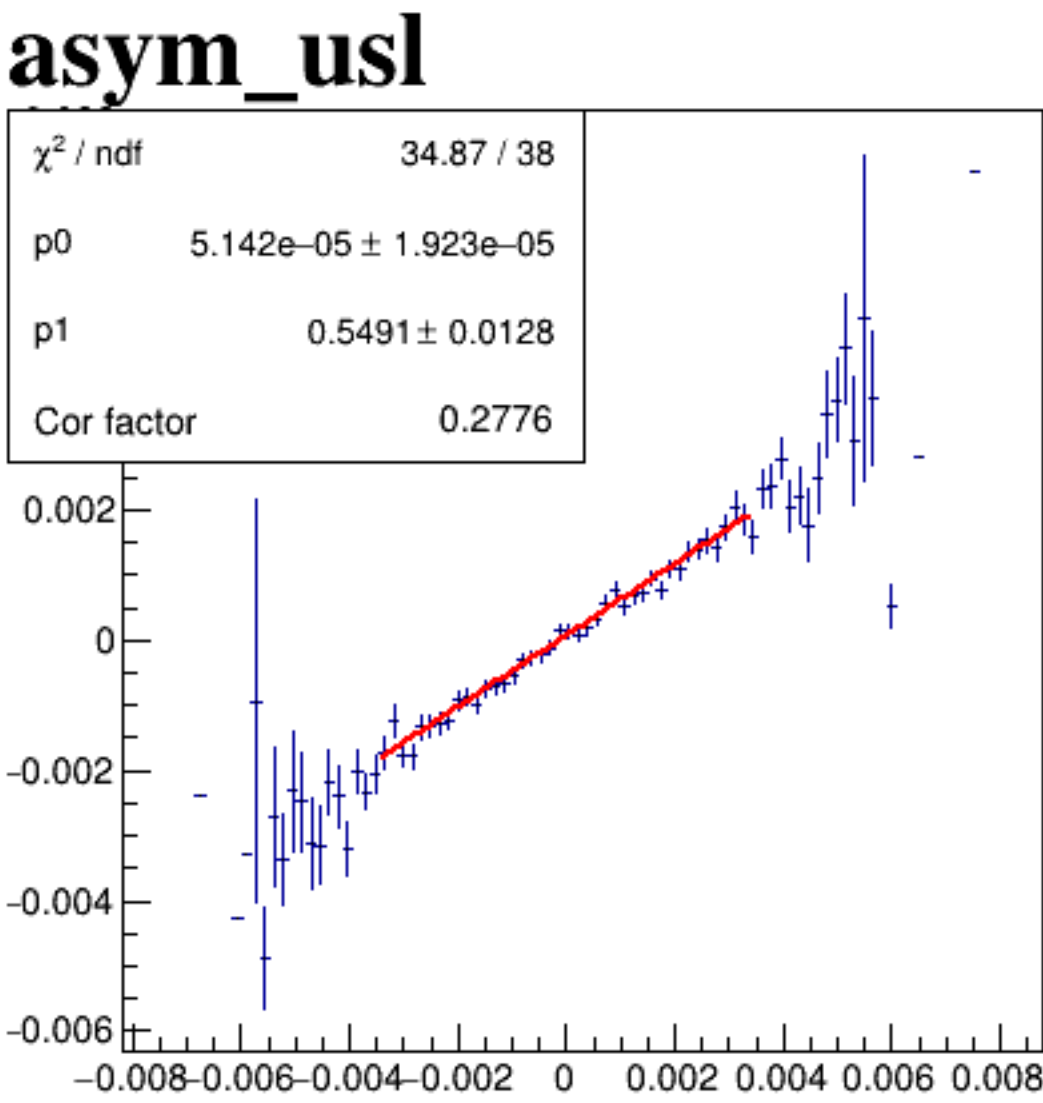
asym_usl



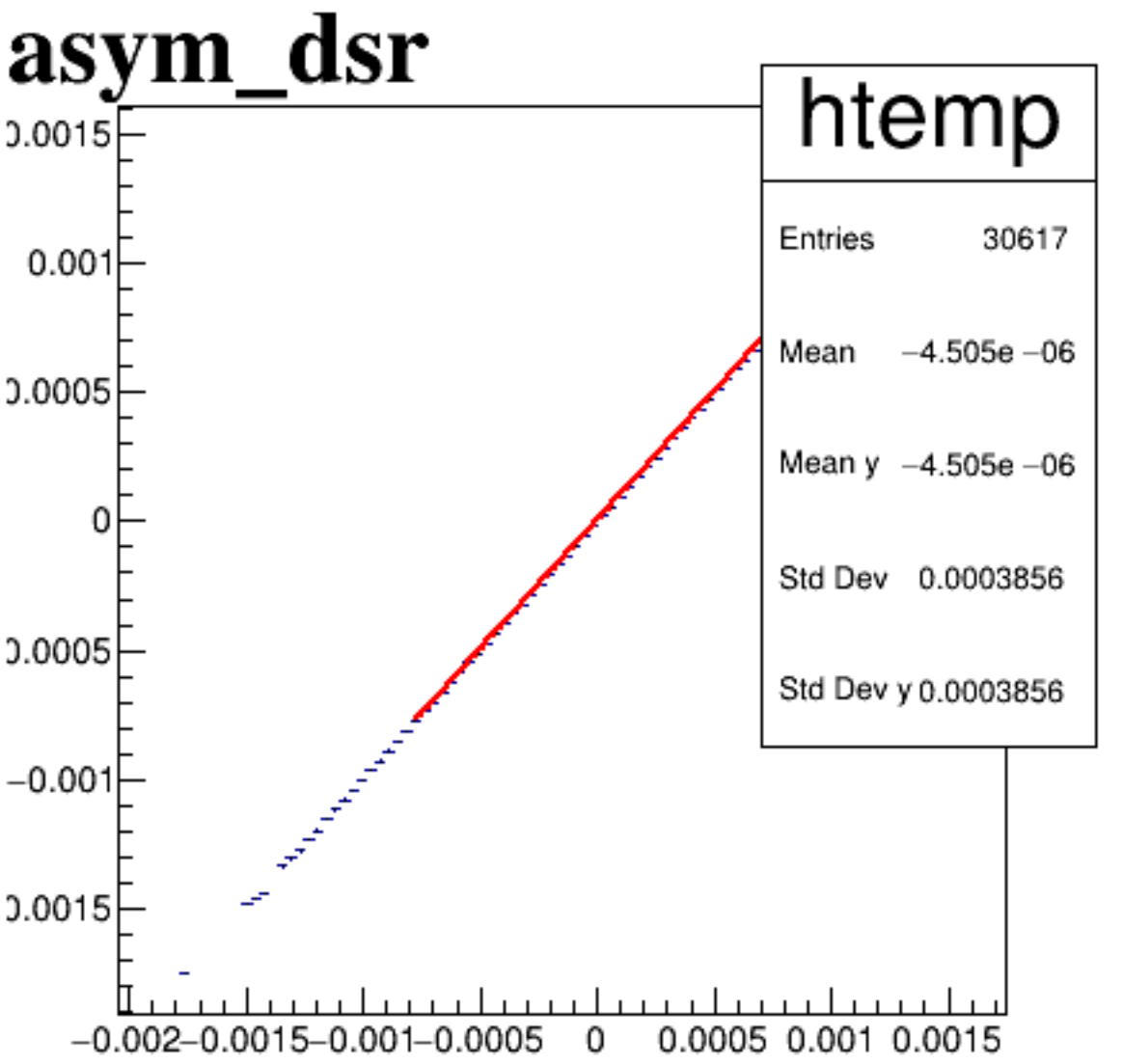
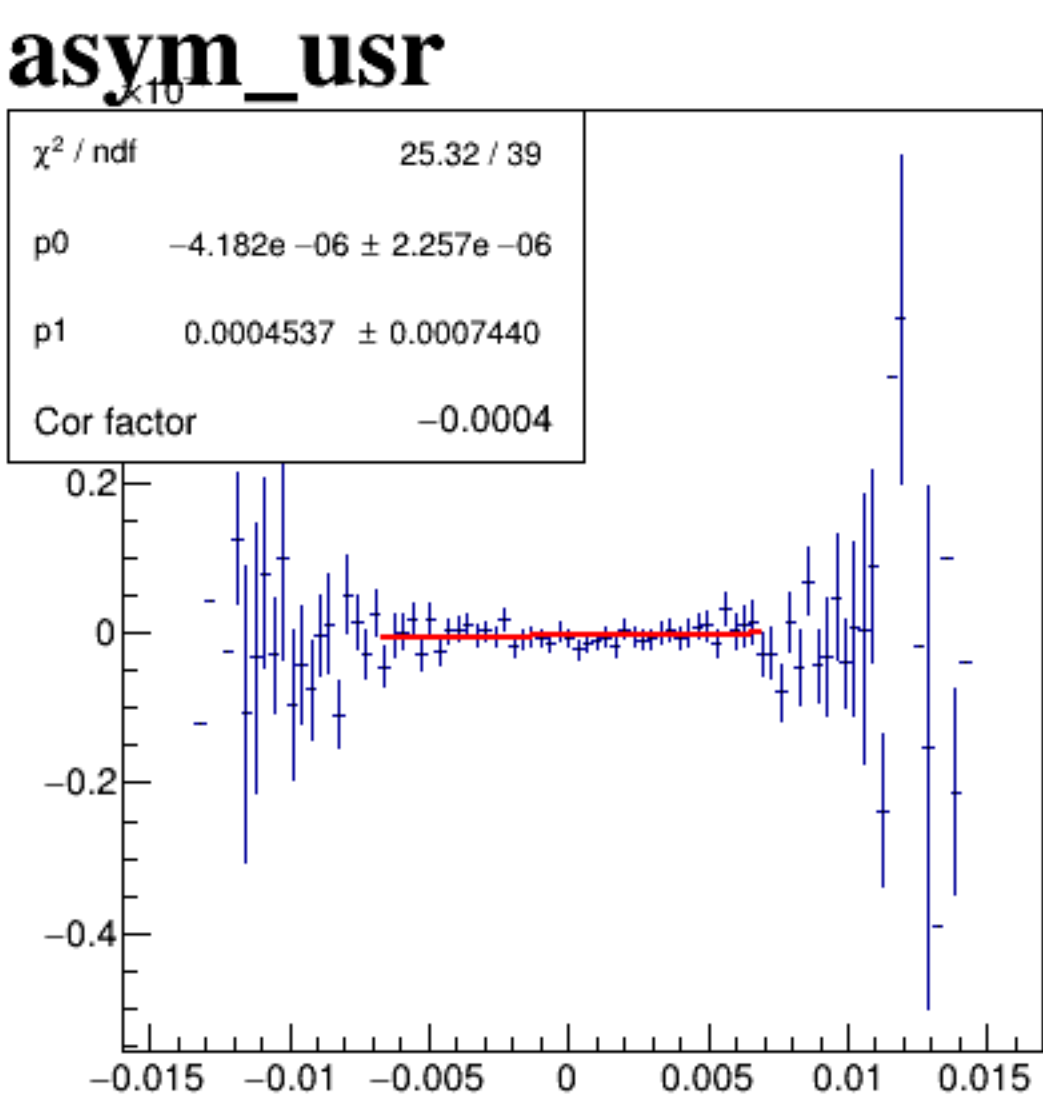
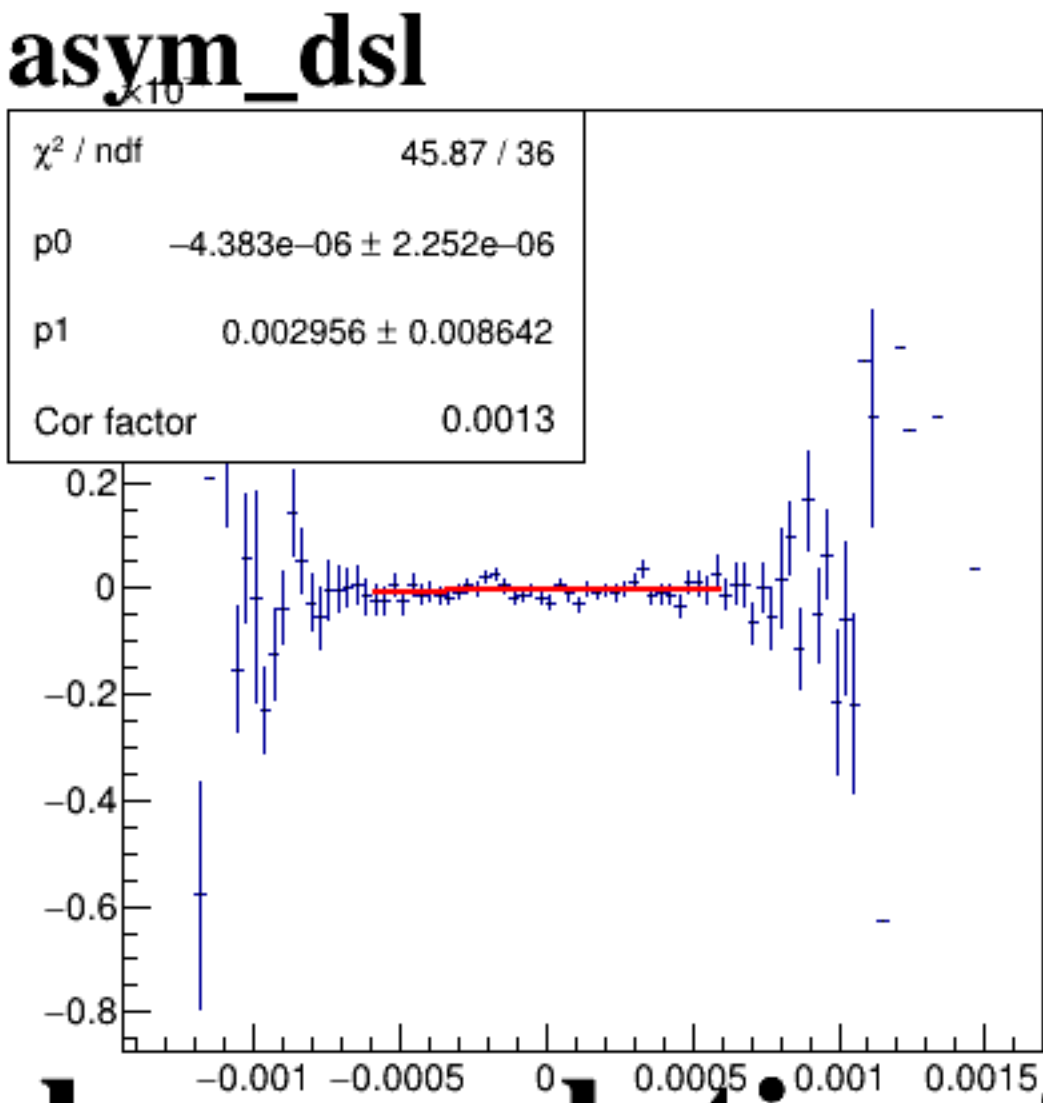
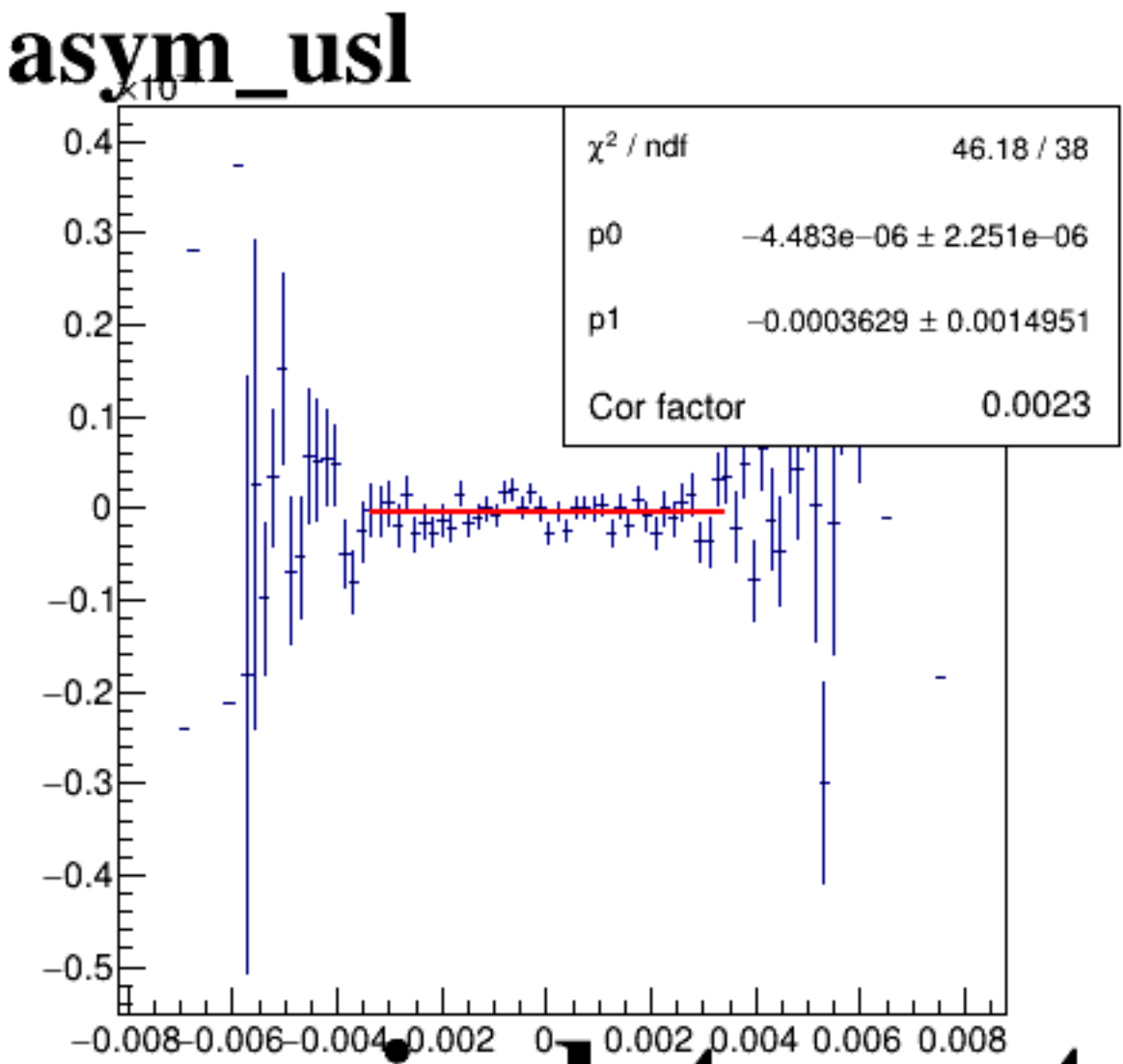
asym_dsl



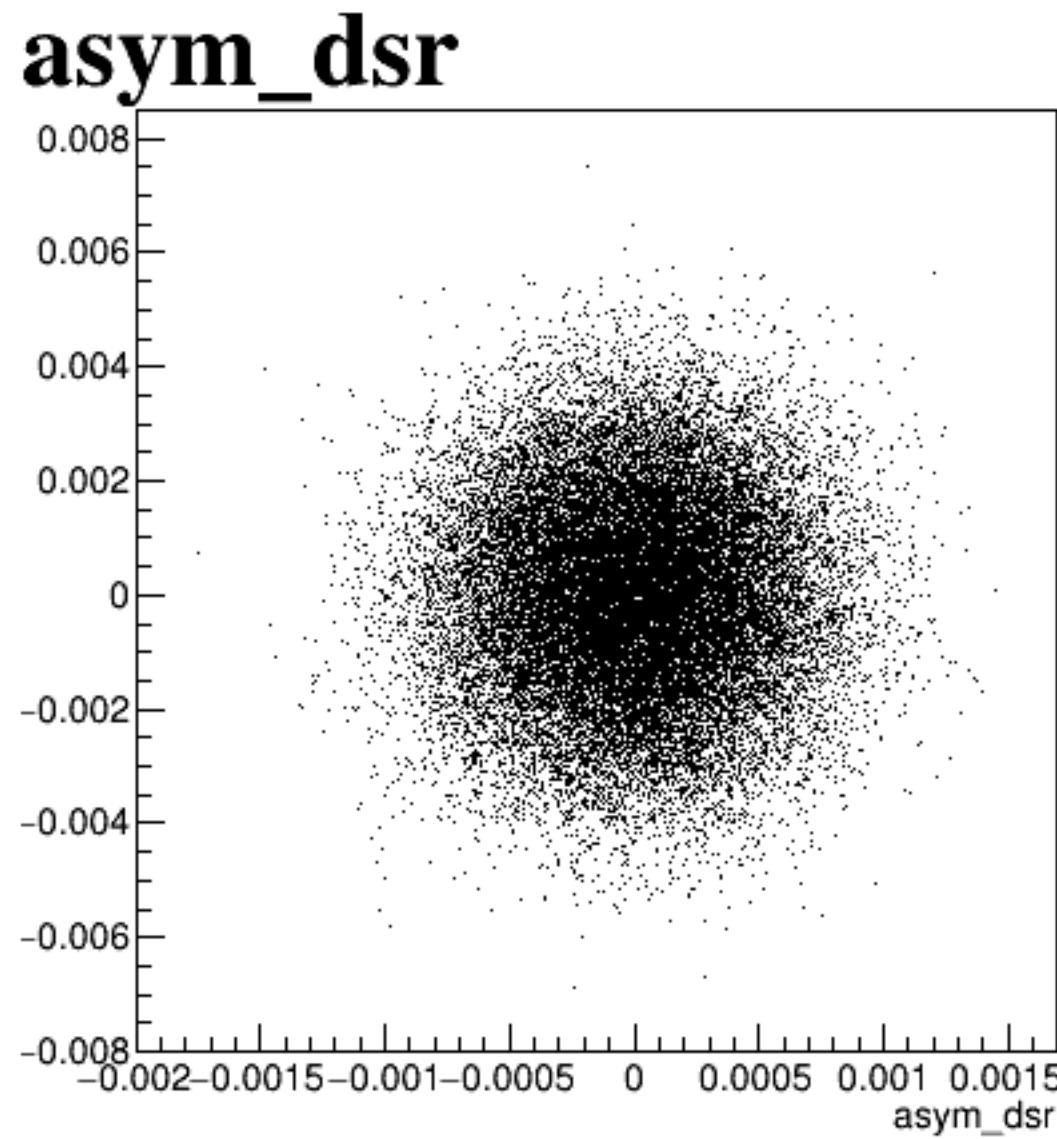
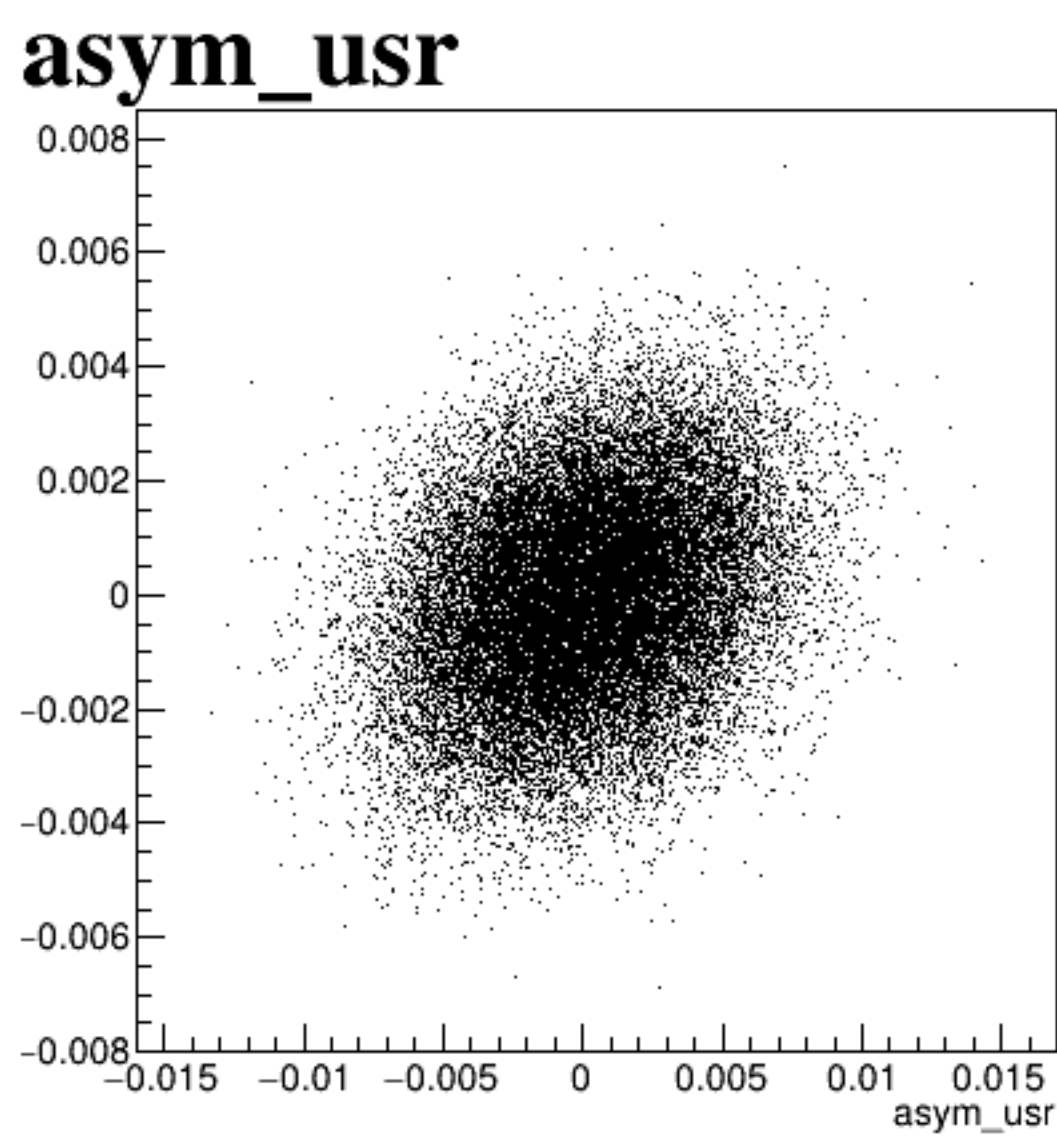
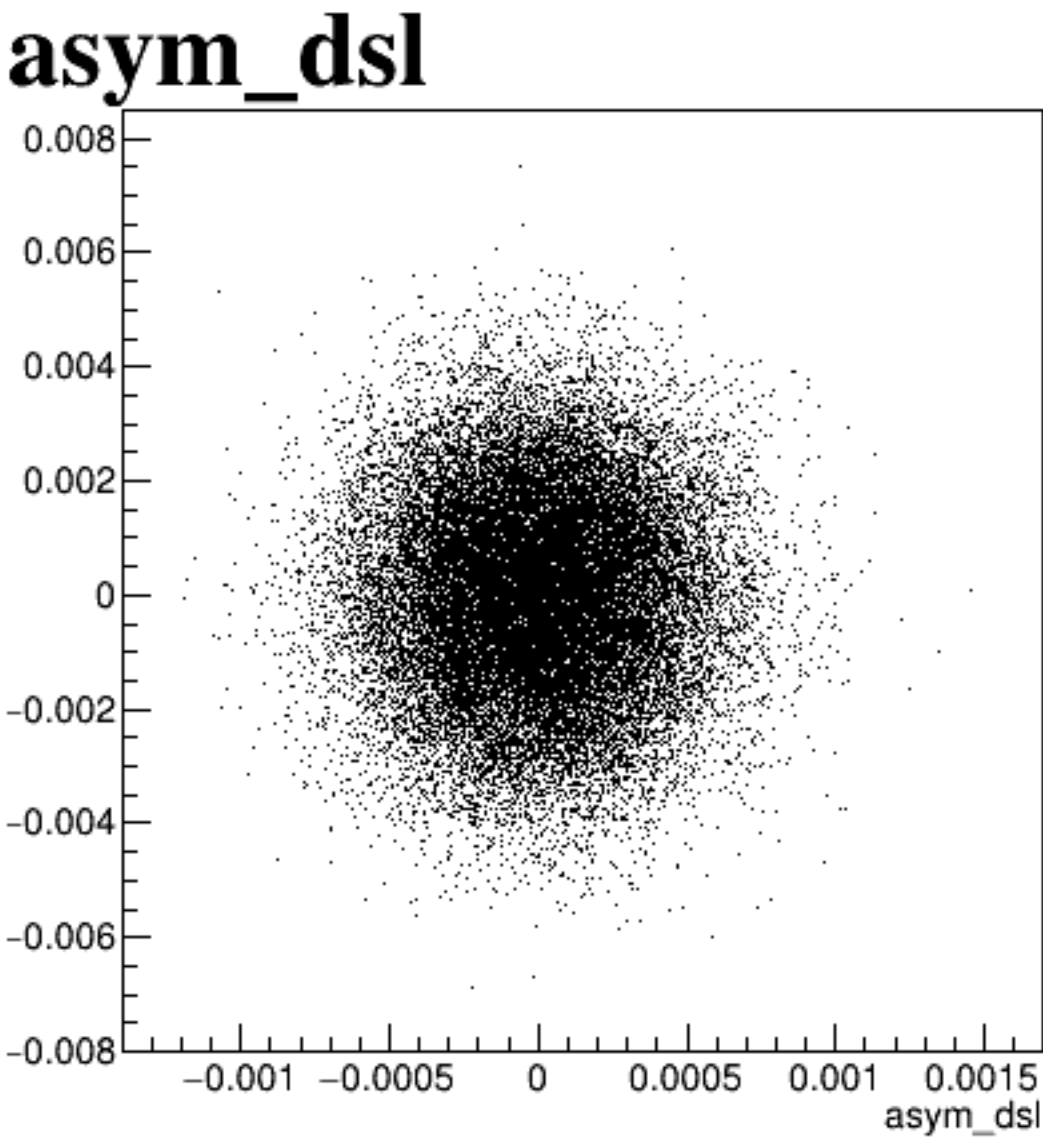
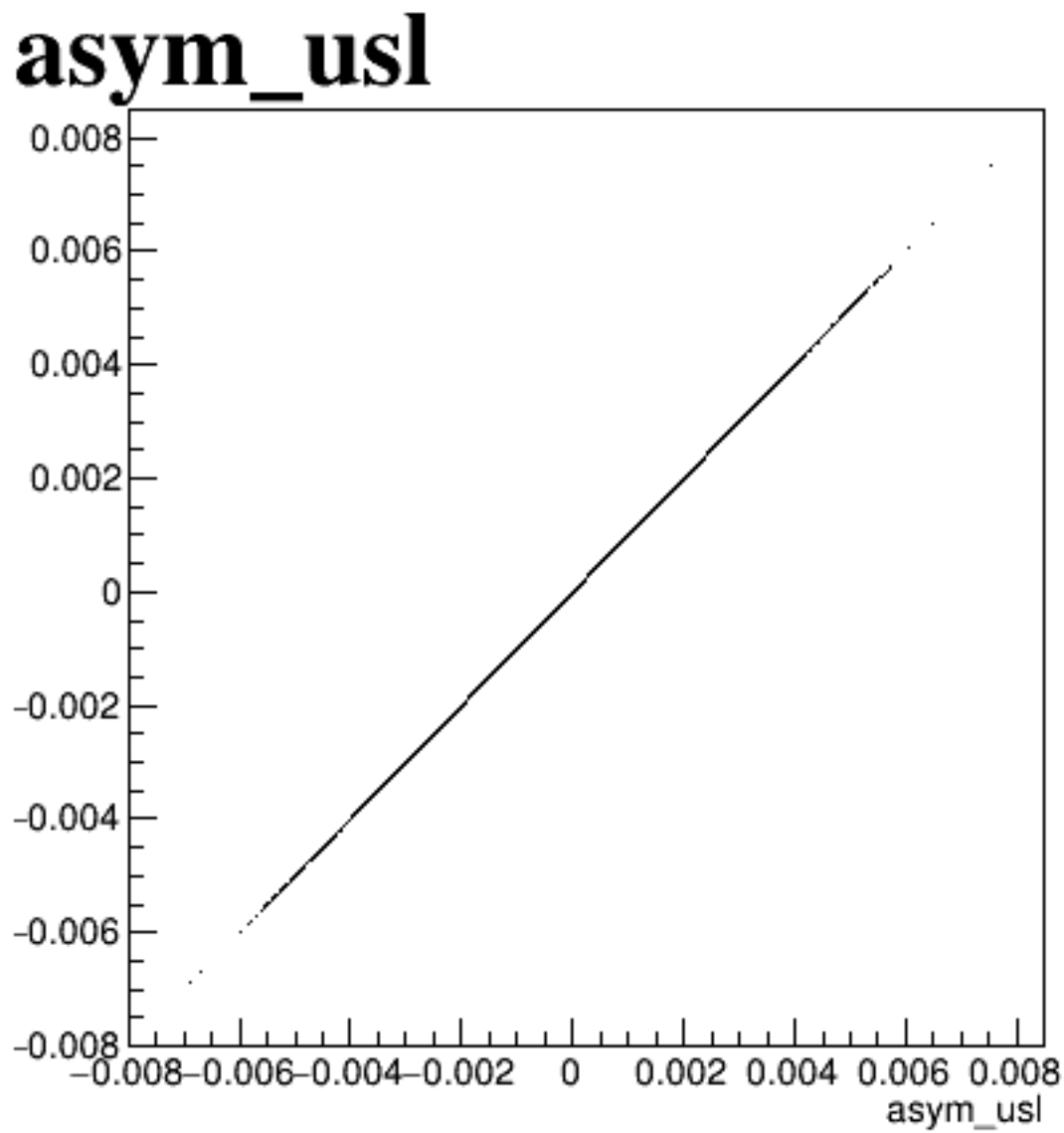
asym_usr



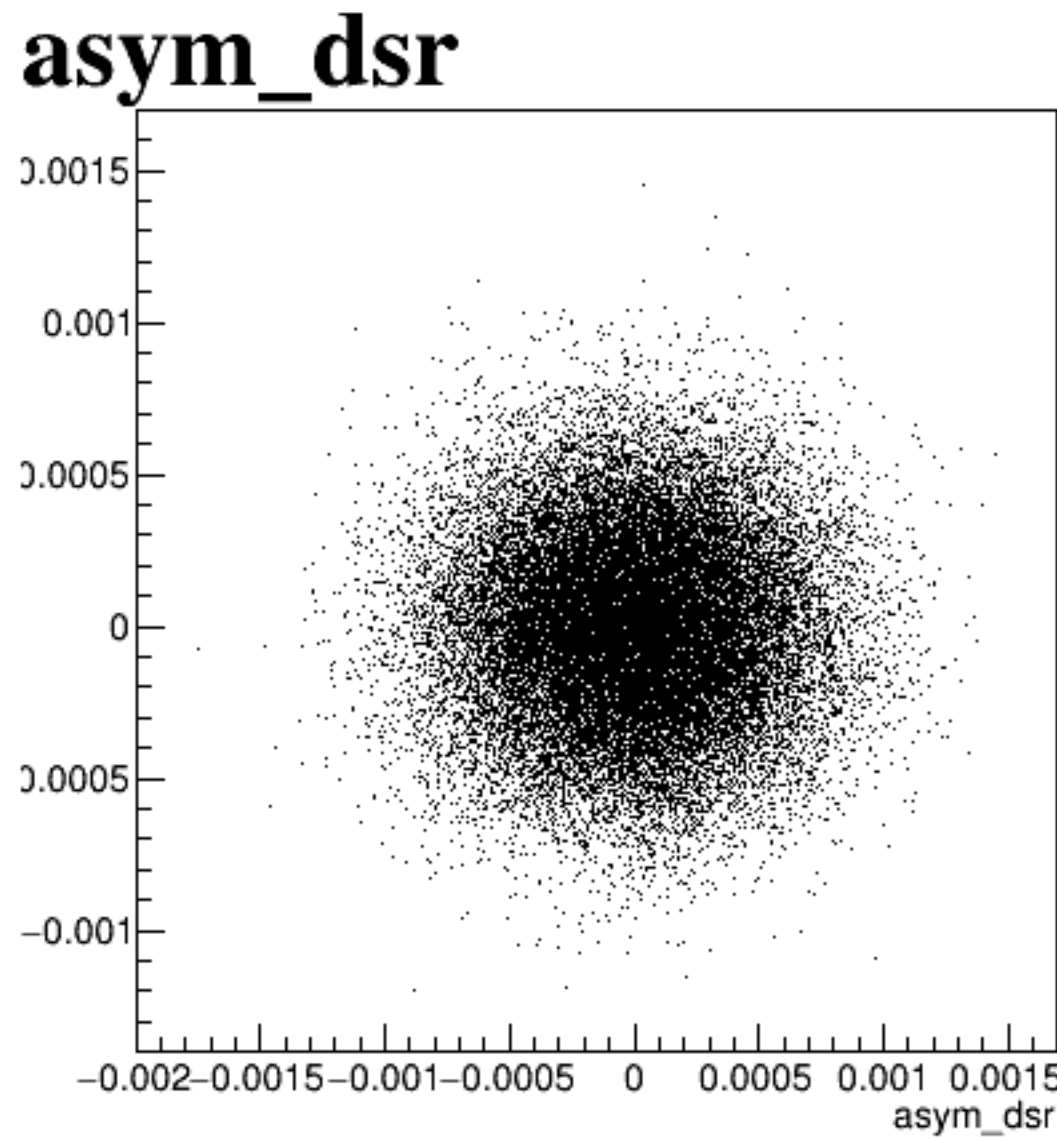
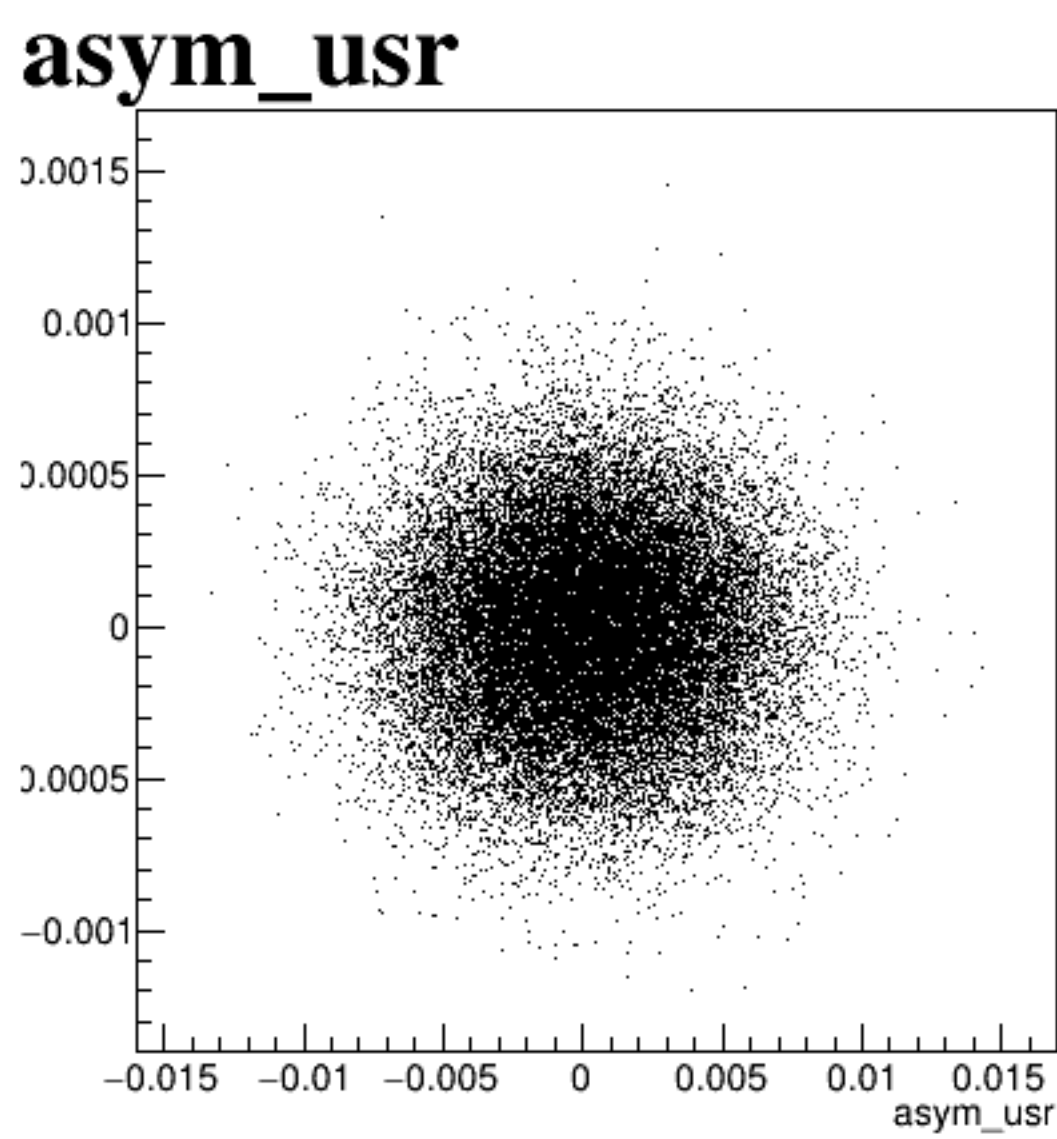
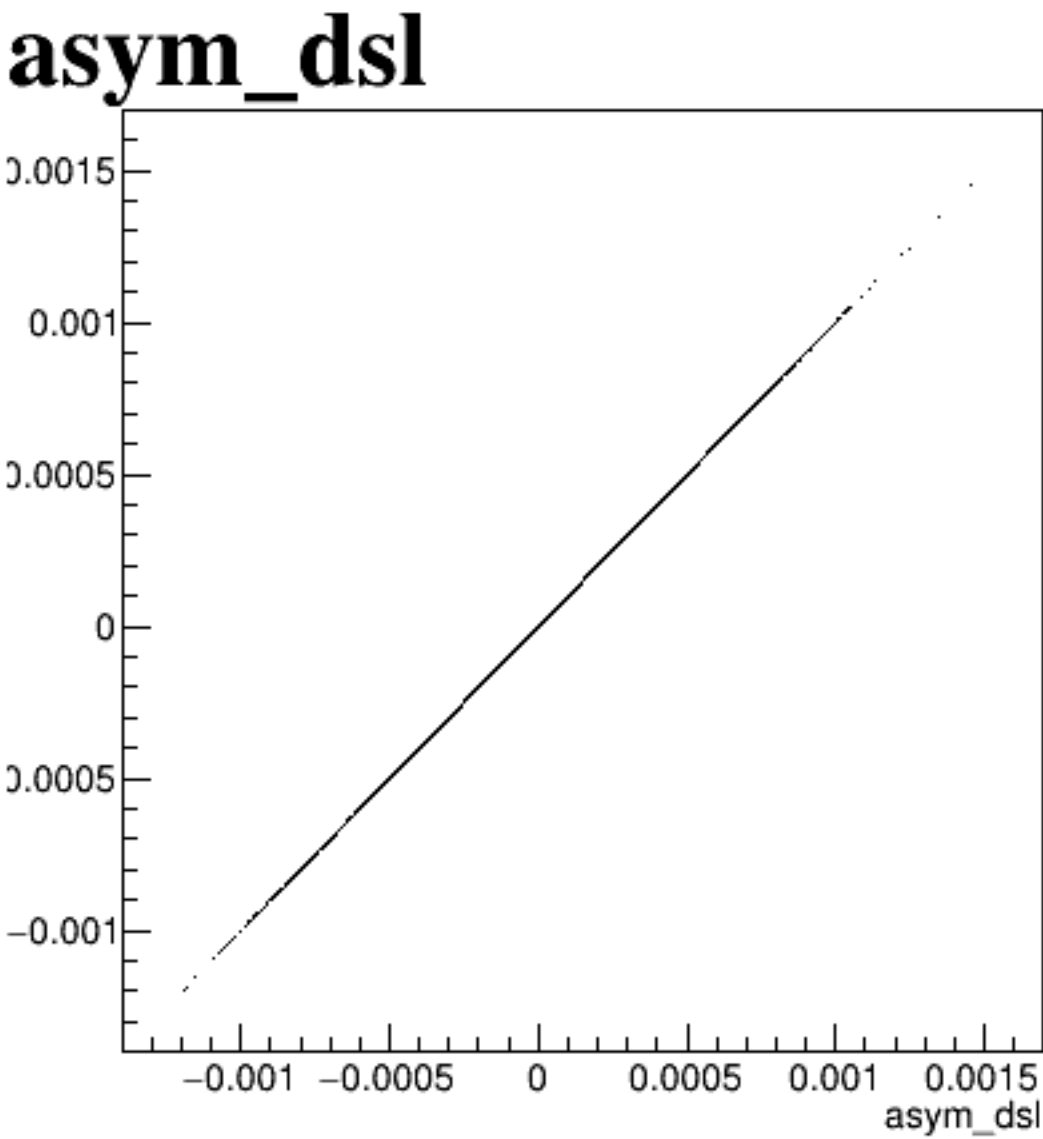
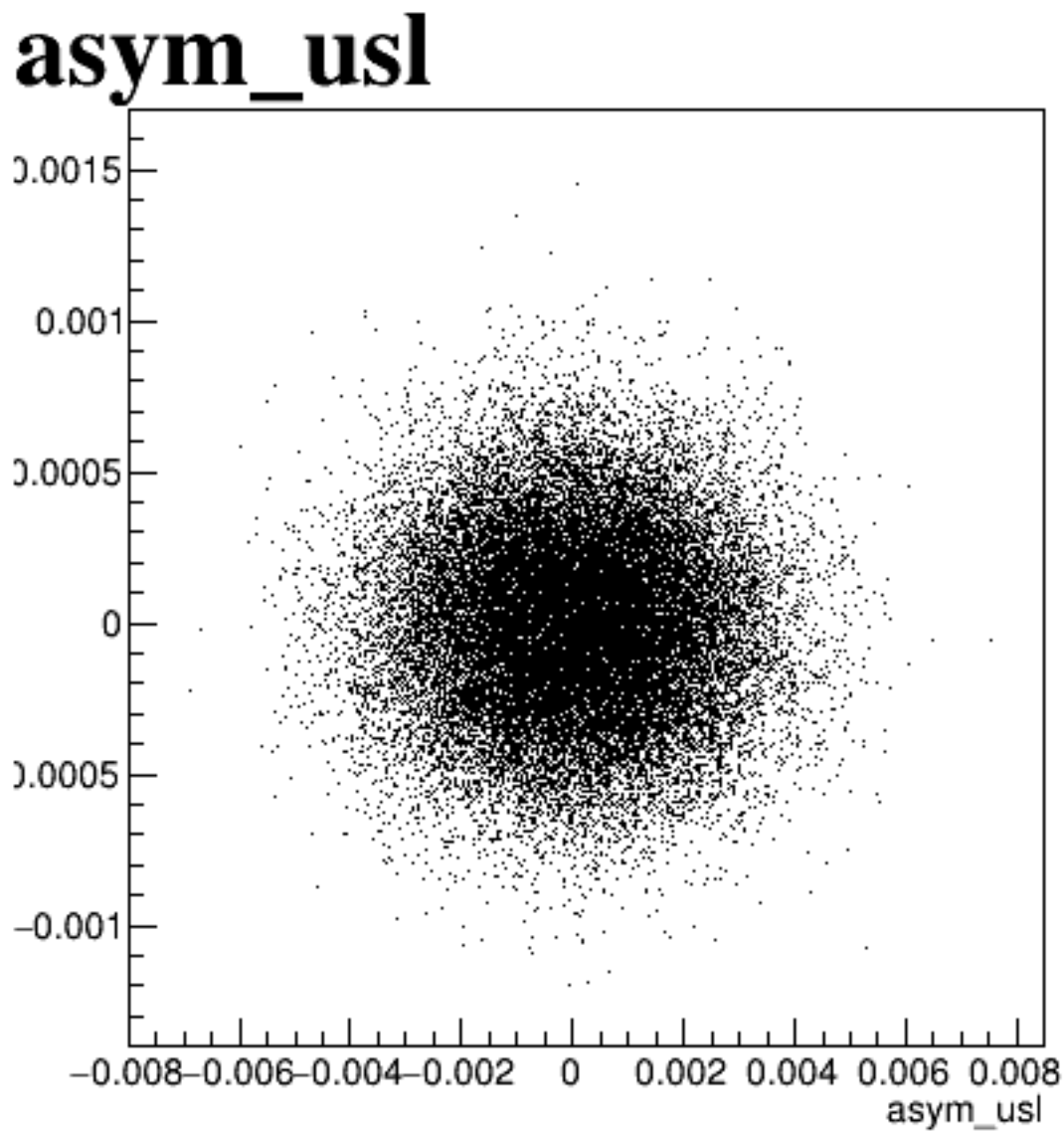
asym_dsr



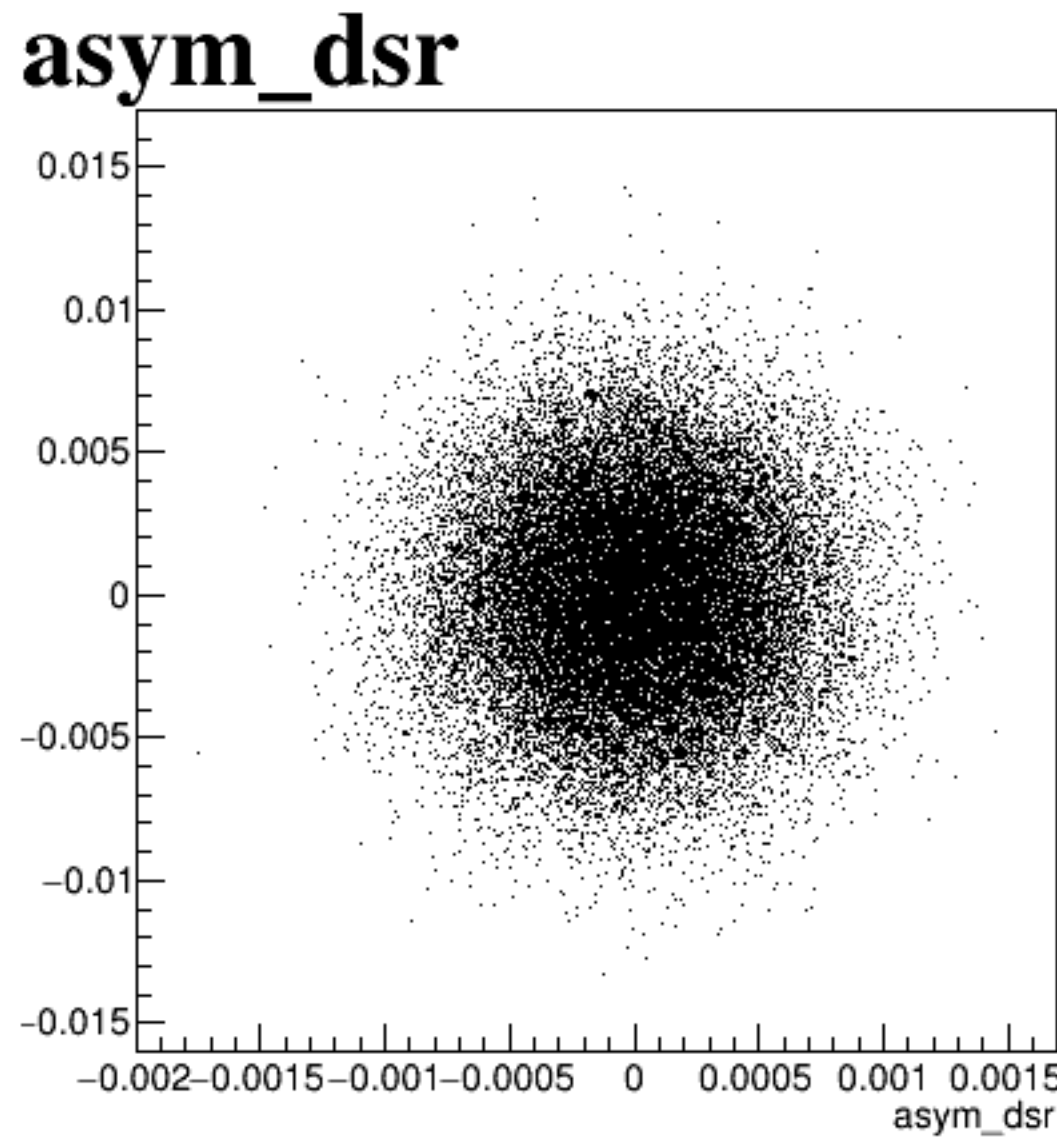
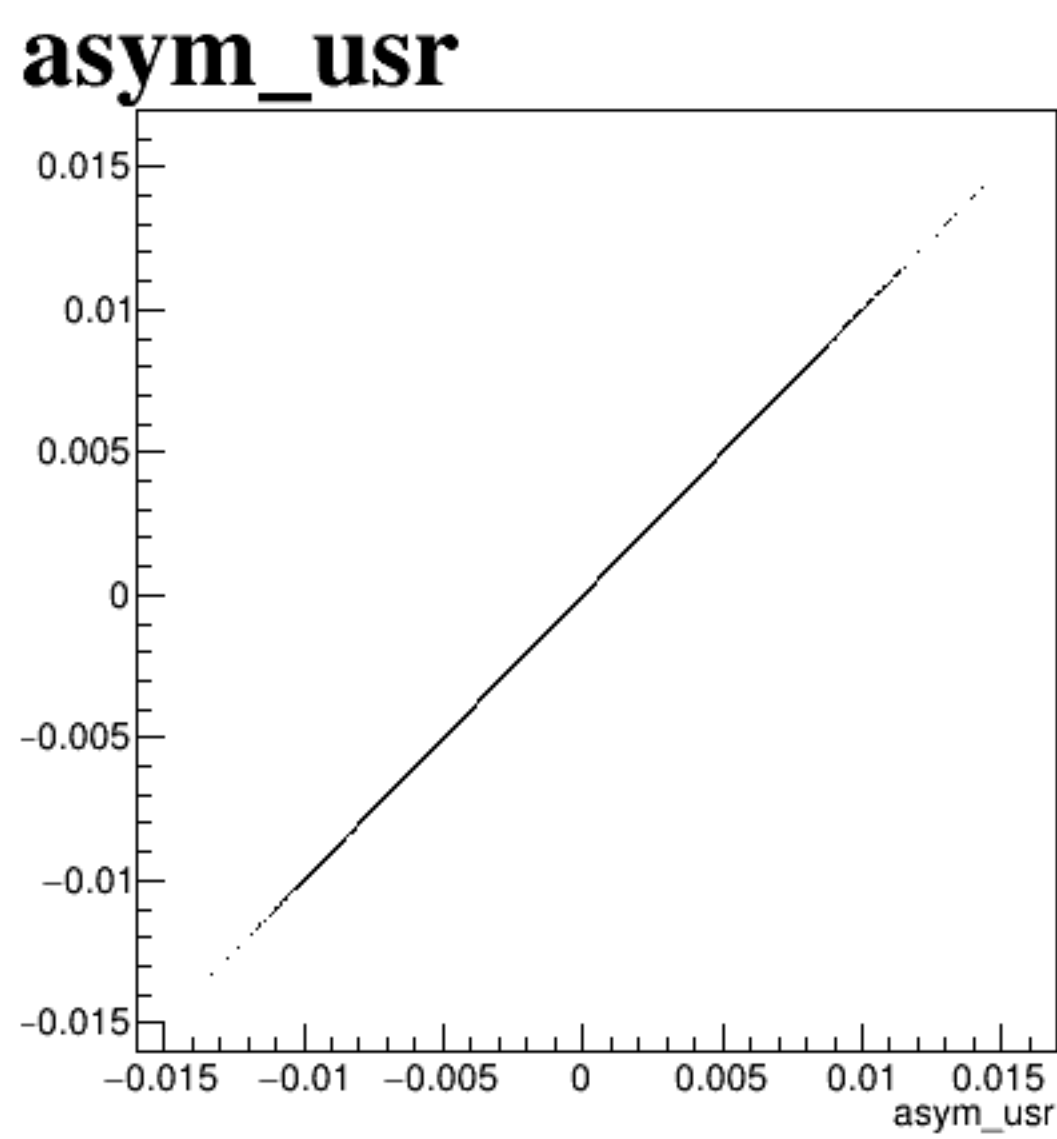
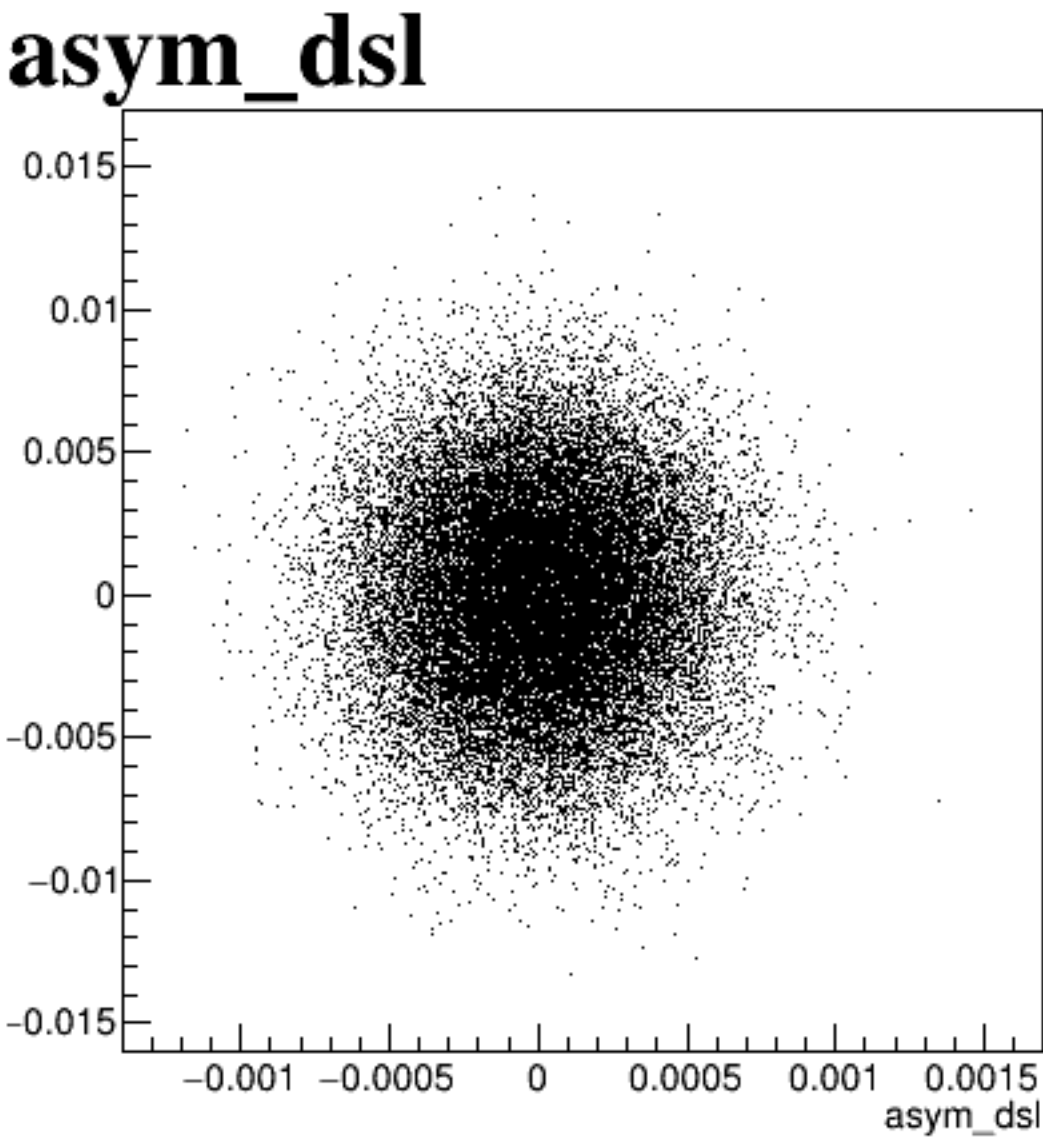
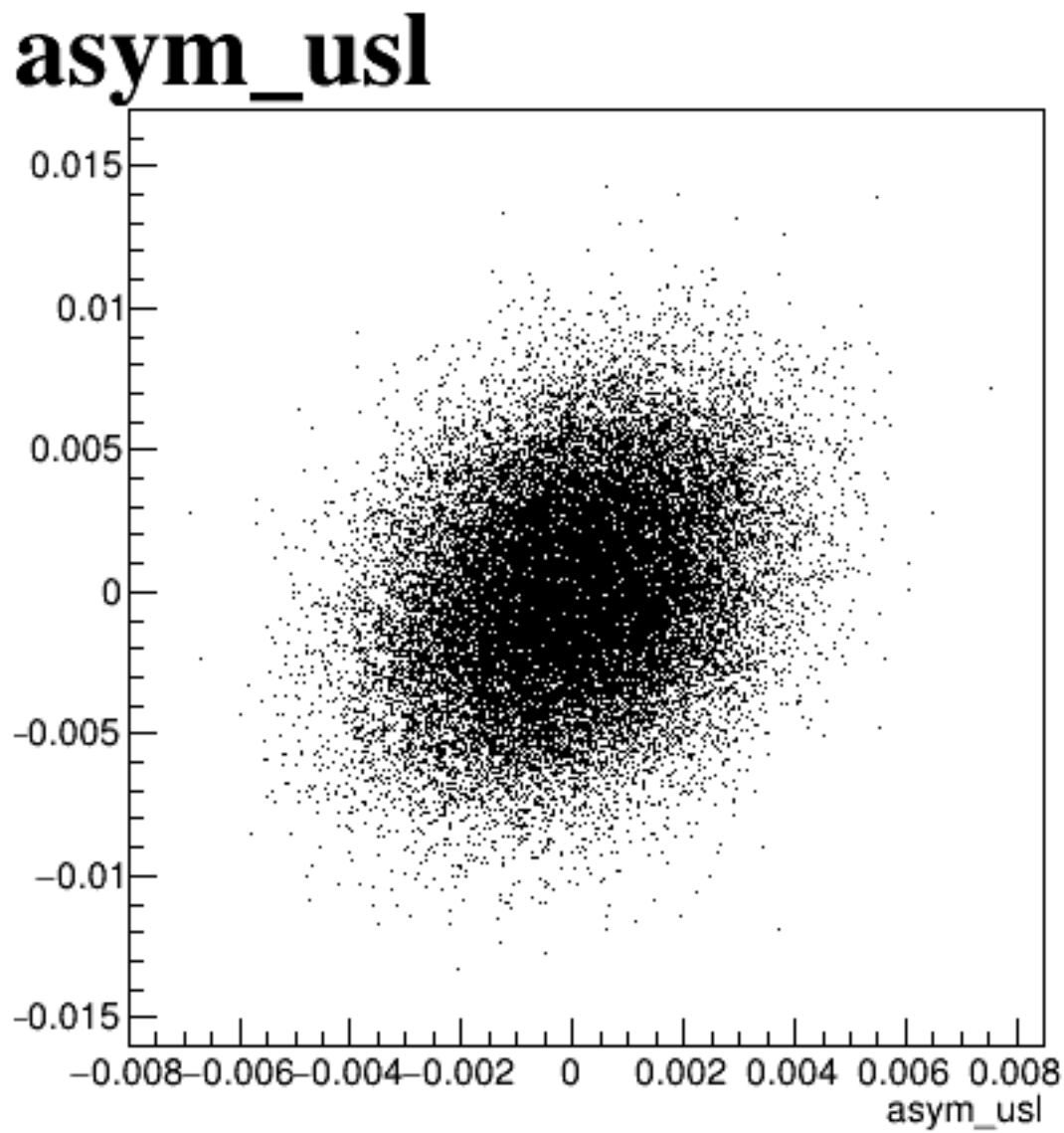
asym_usl



asym_dsl



asym_usr



asym_dsr

