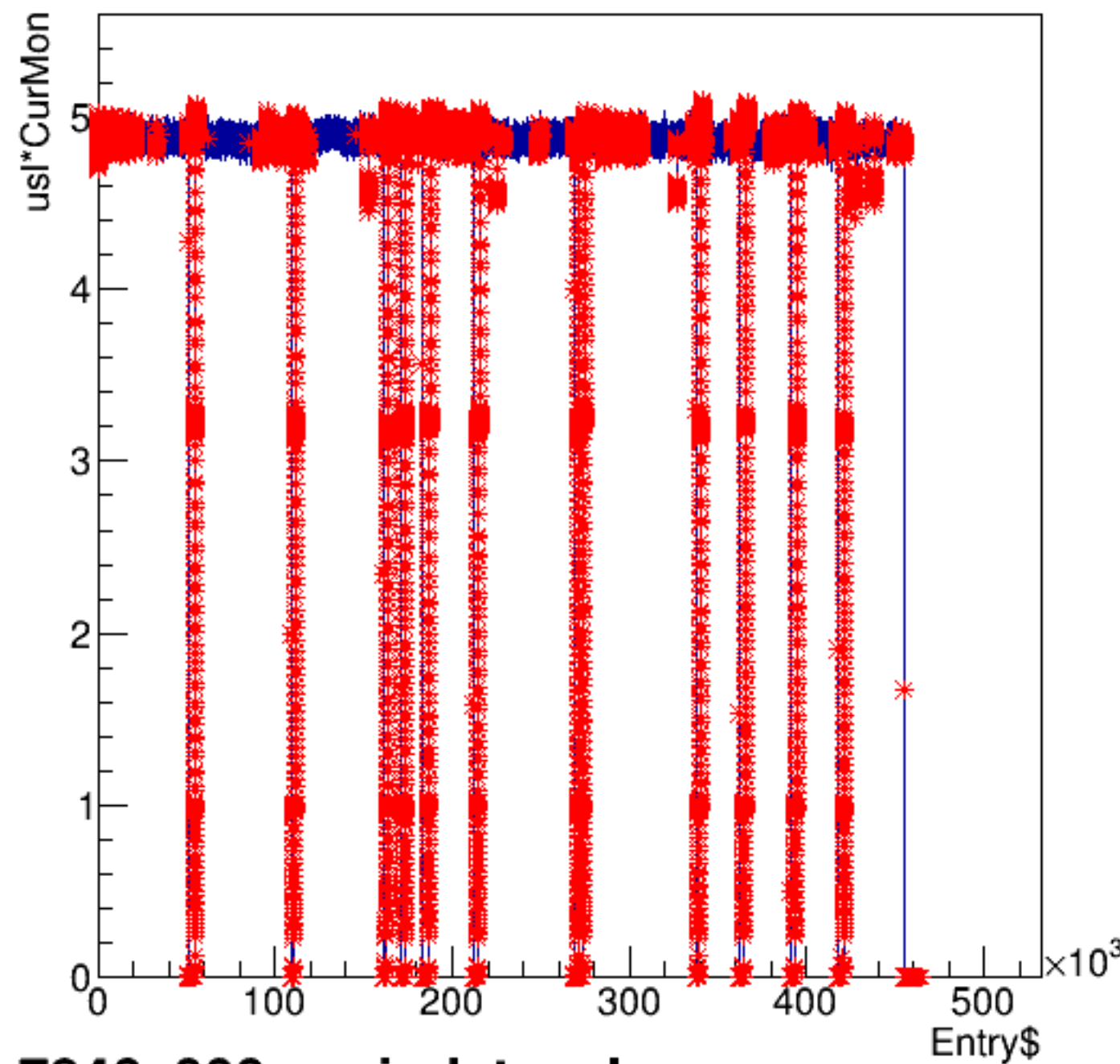
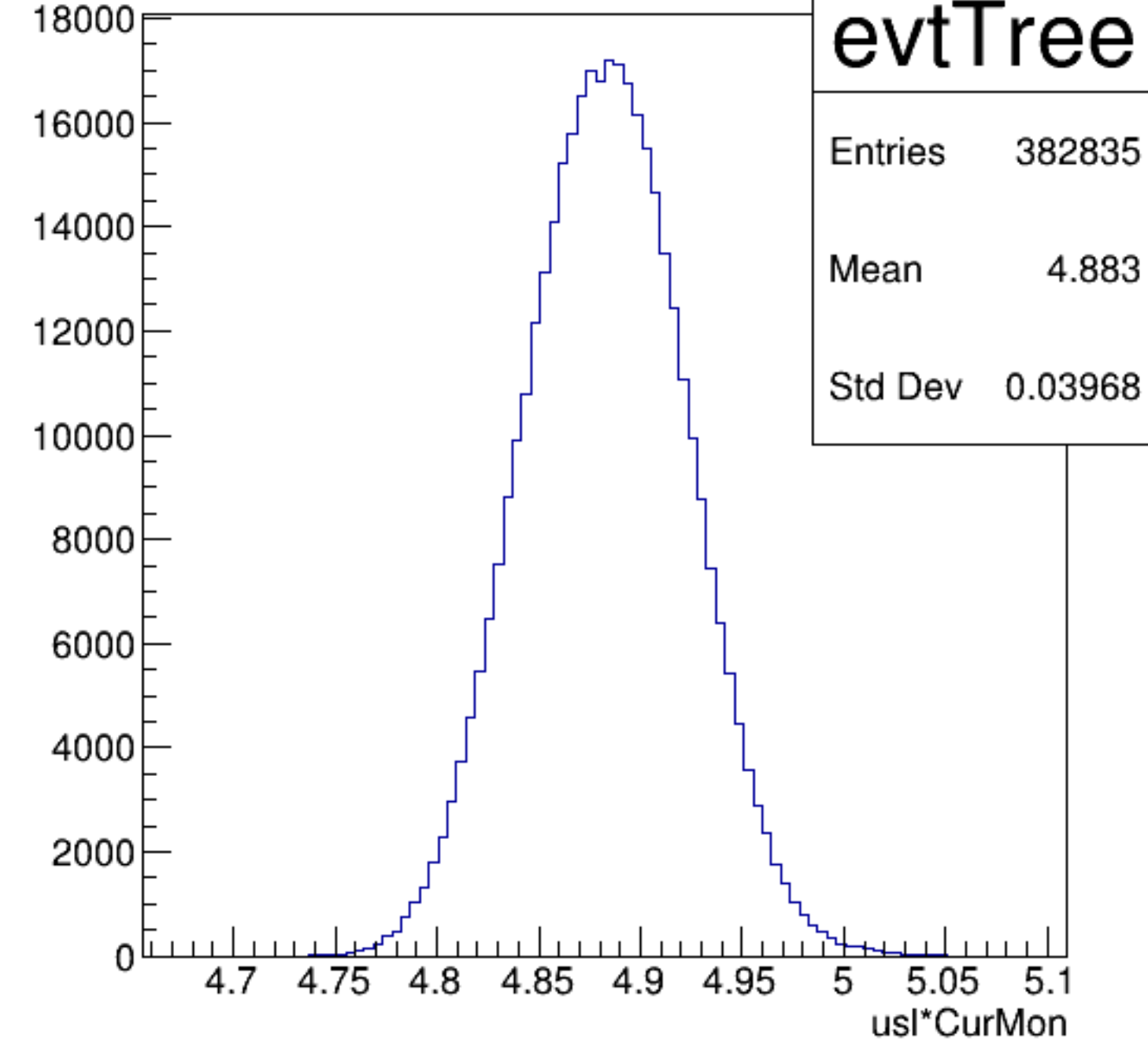


usl*CurMon:Entry\$

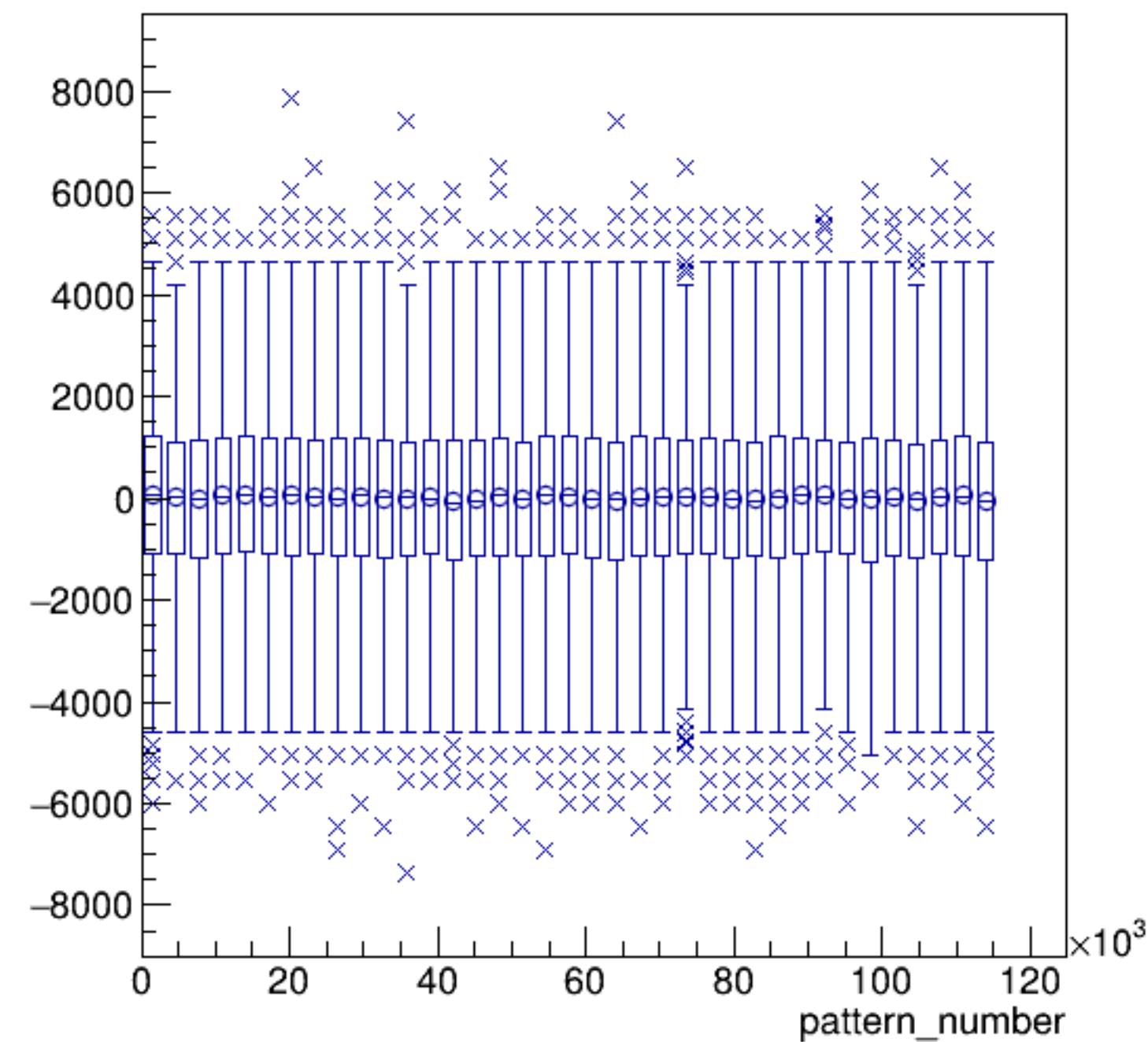


run7846_000_maindet_usl.png

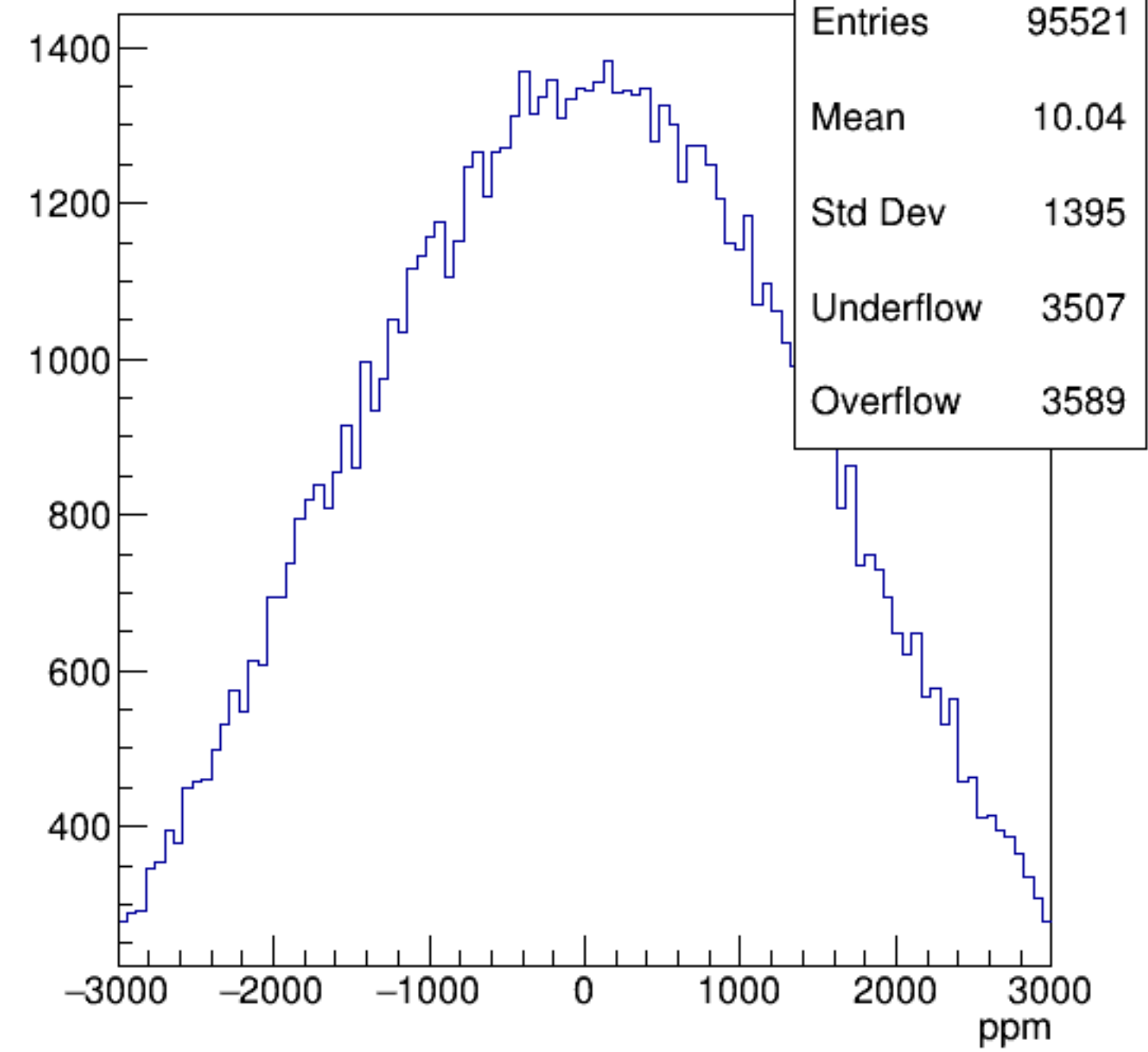
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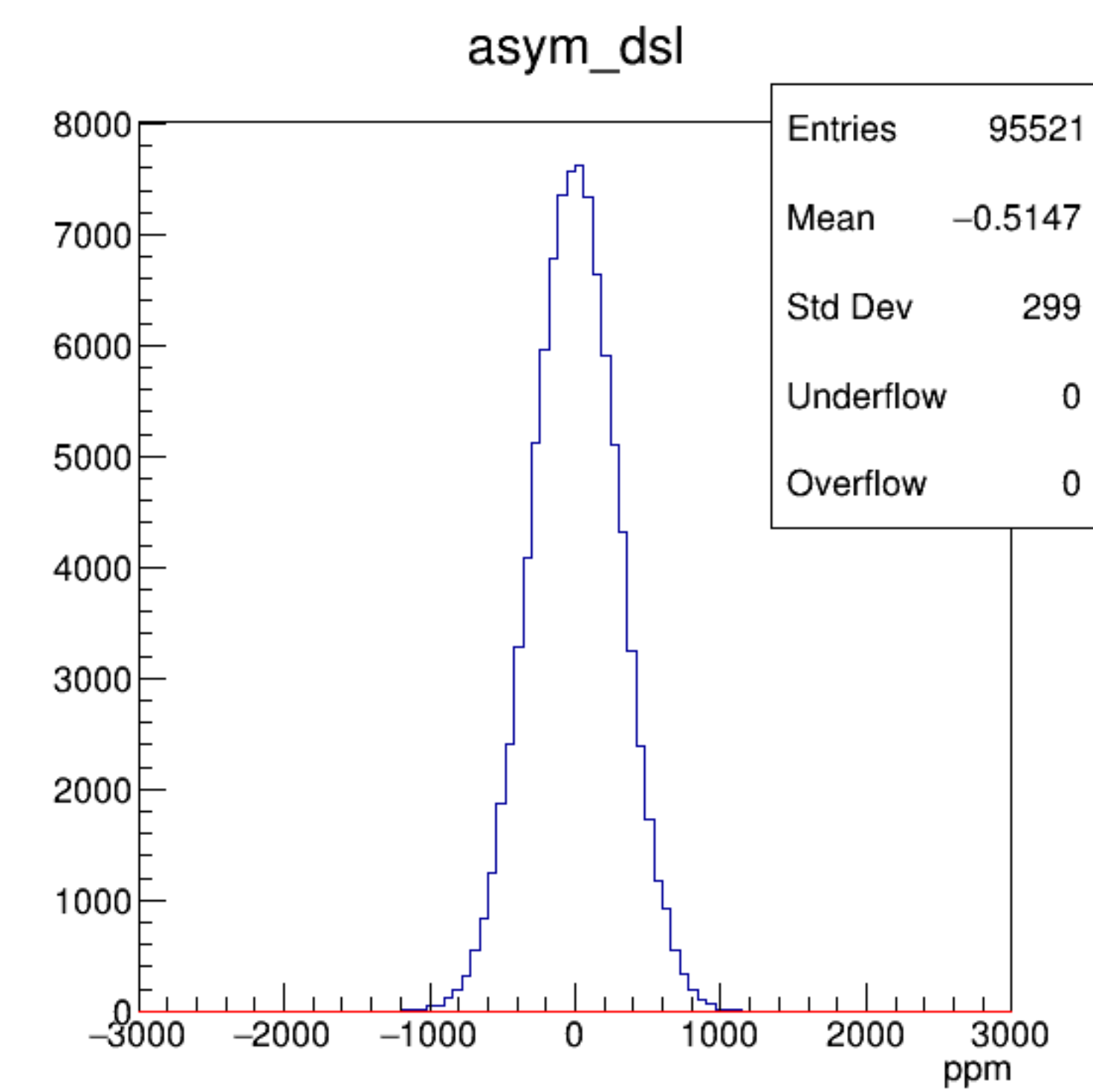
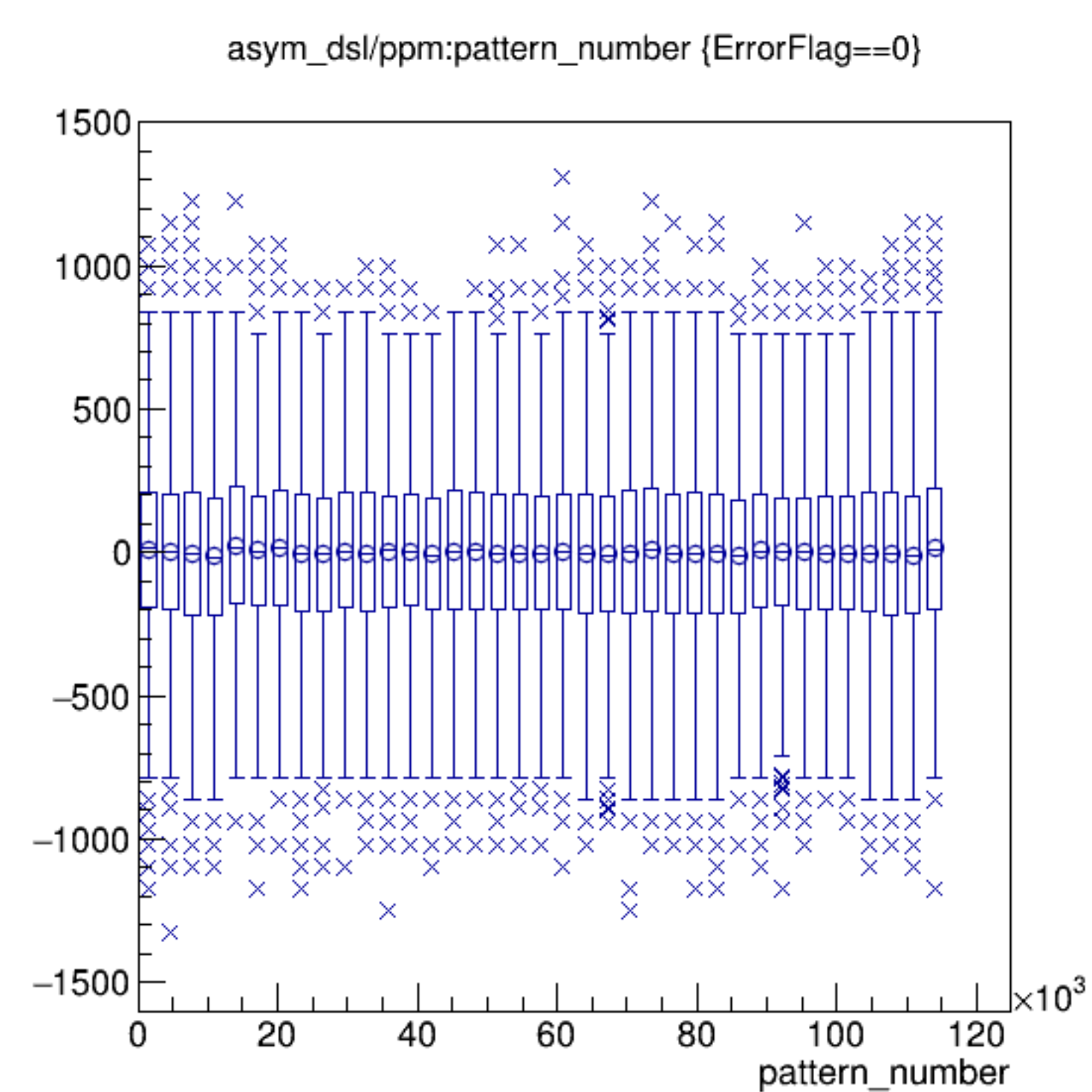
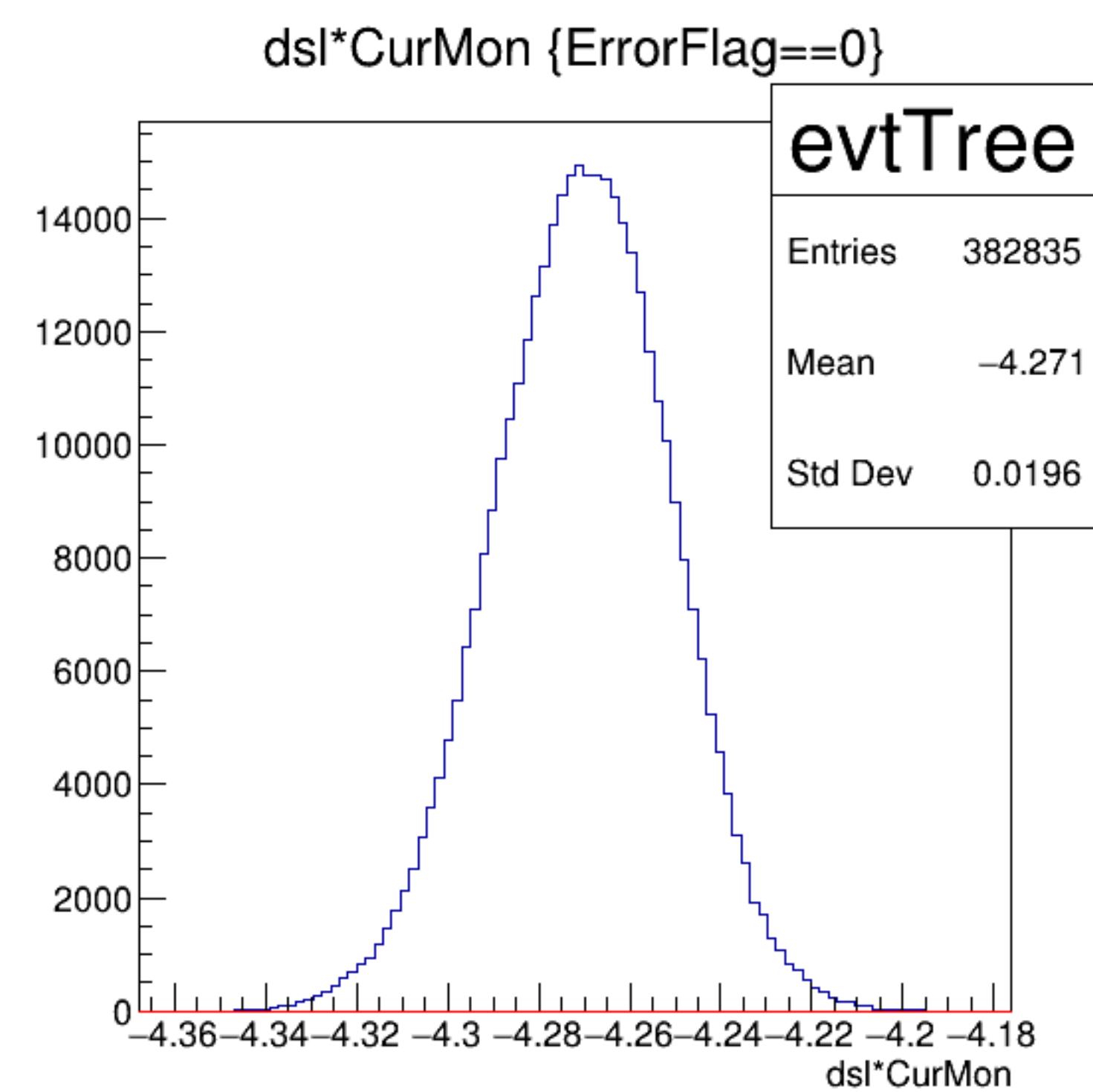
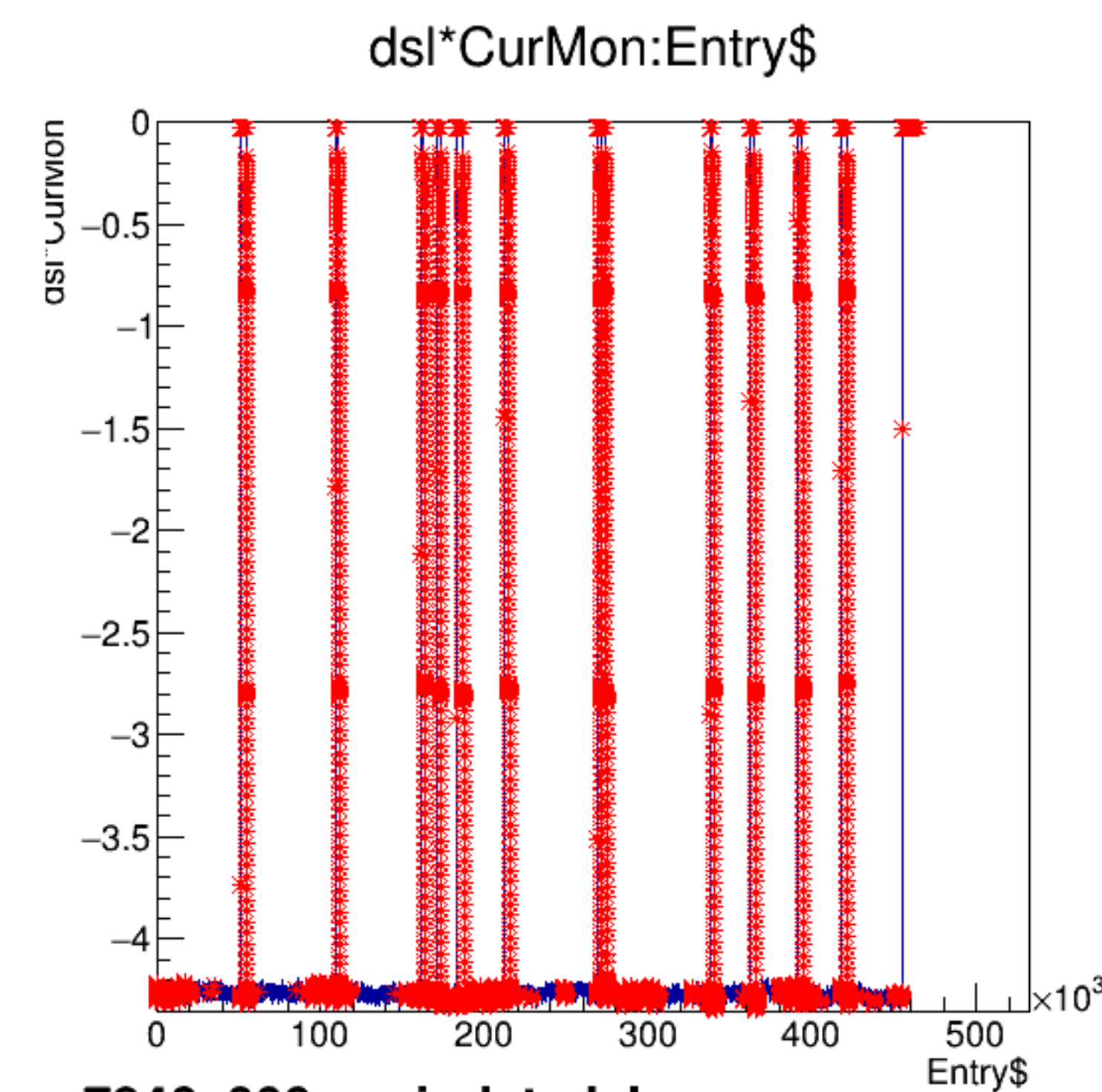


asym_usl/ppm:pattern_number {ErrorFlag==0}

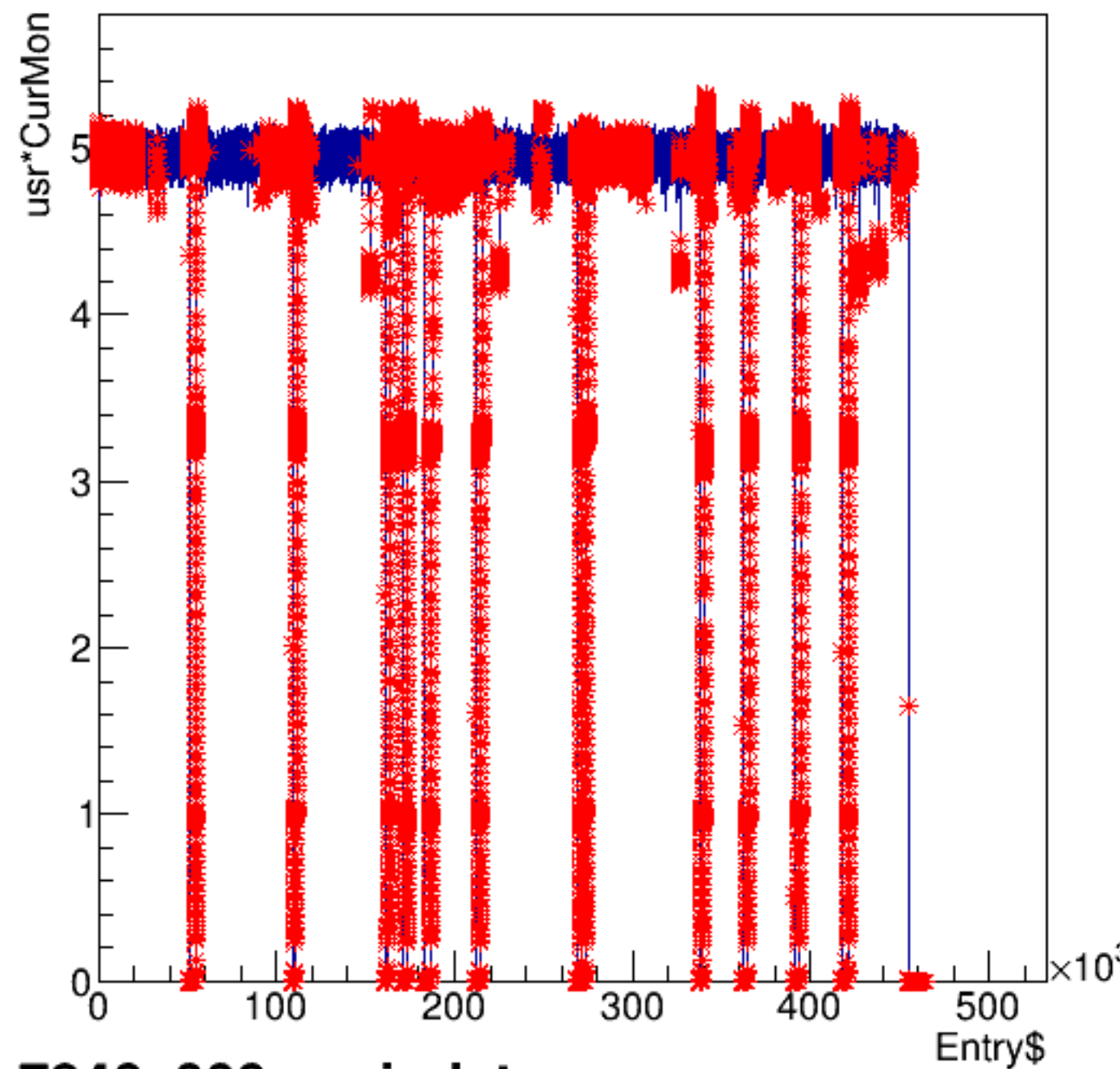


asym_usl



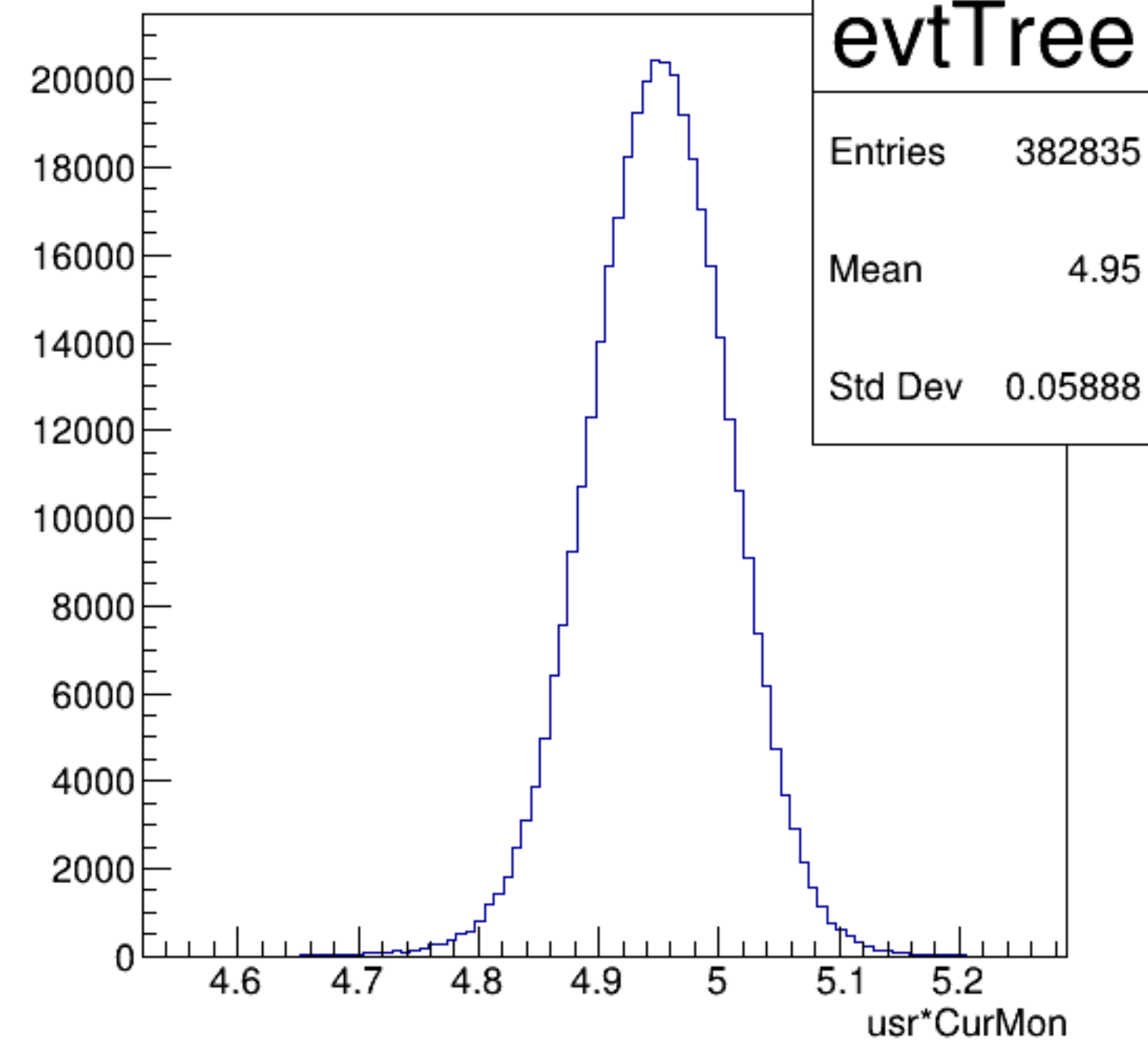


usr*CurMon:Entry\$

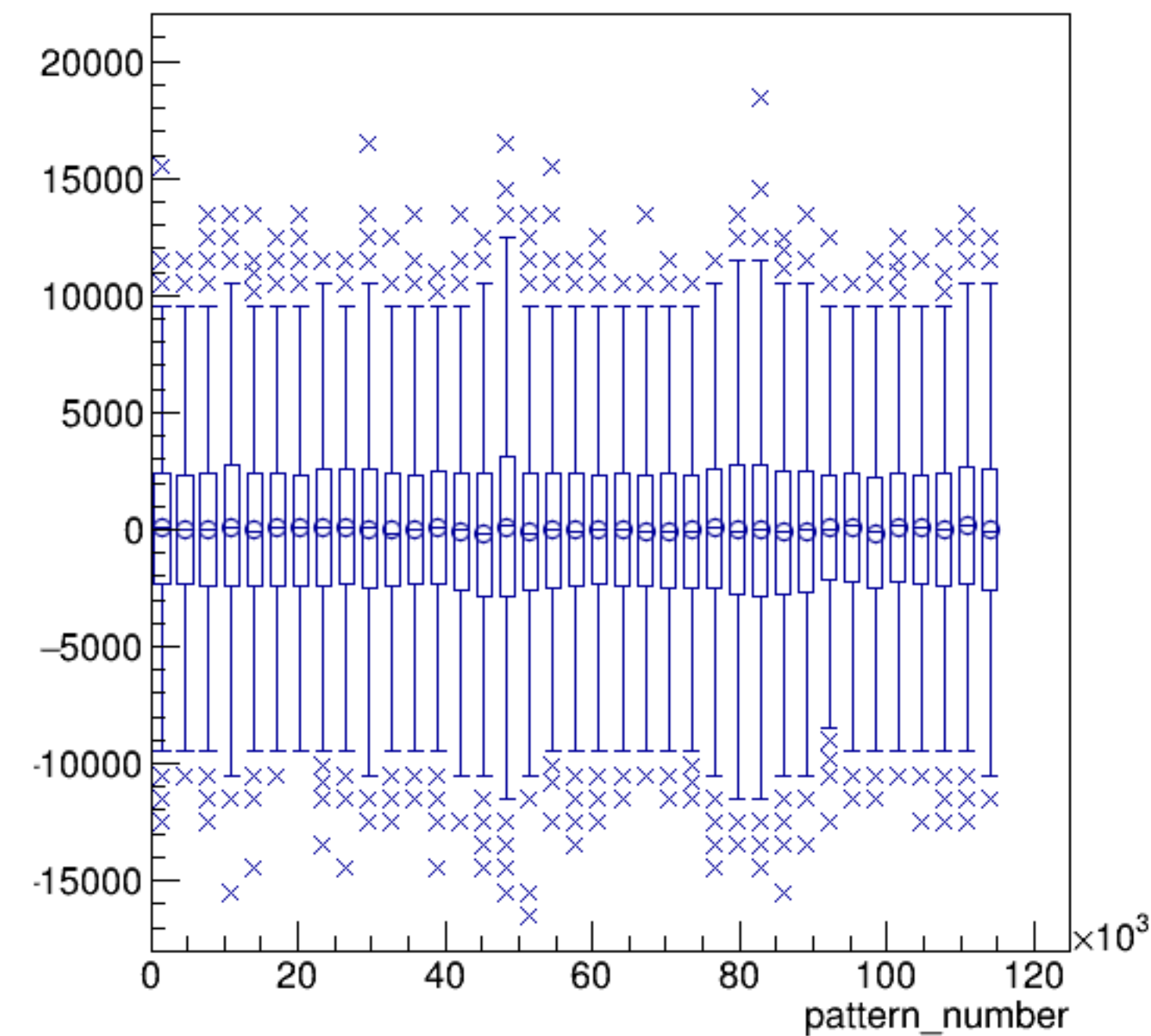


run7846_000_maindet_usr.png

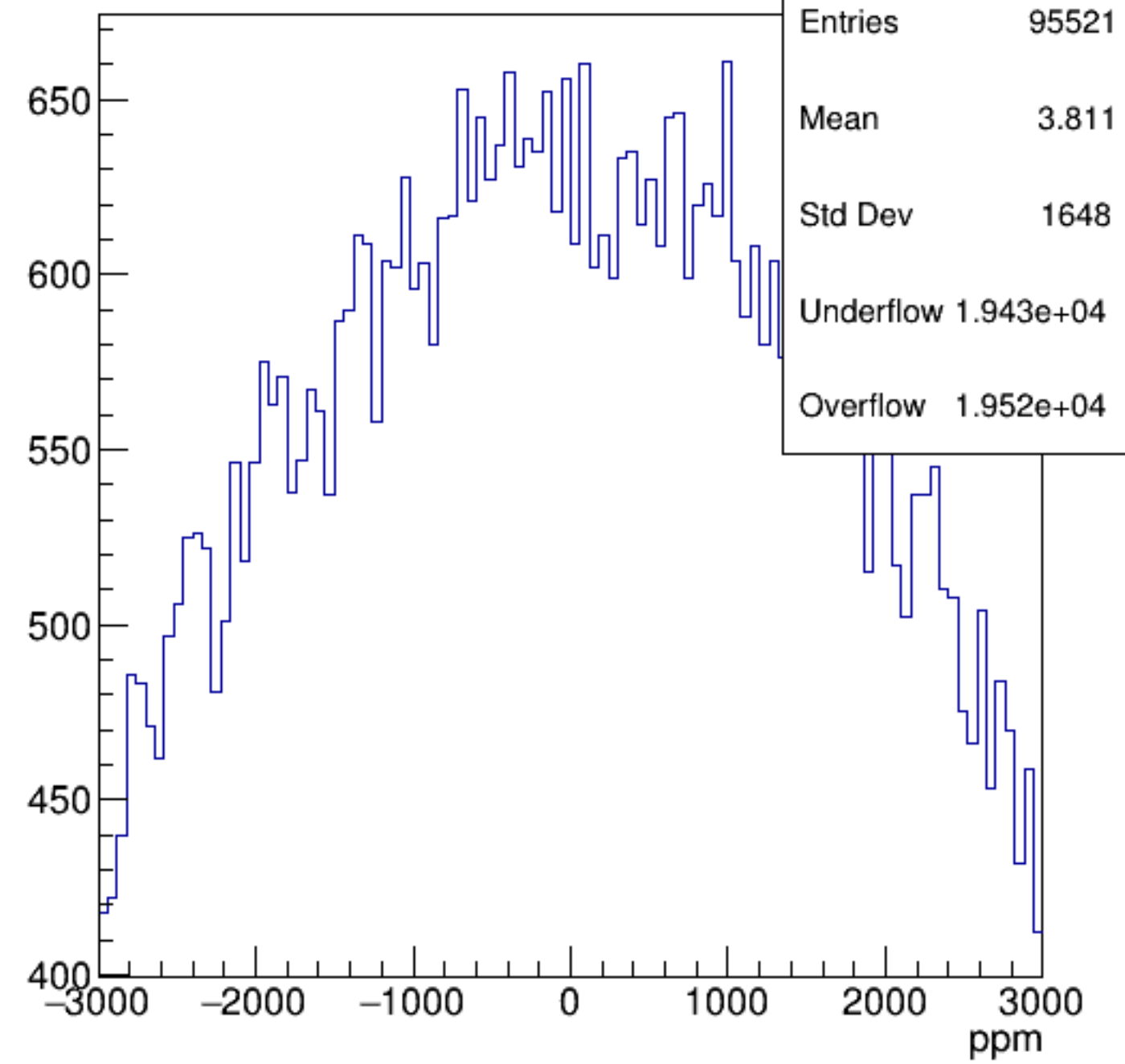
usr*CurMon {ErrorFlag==0}

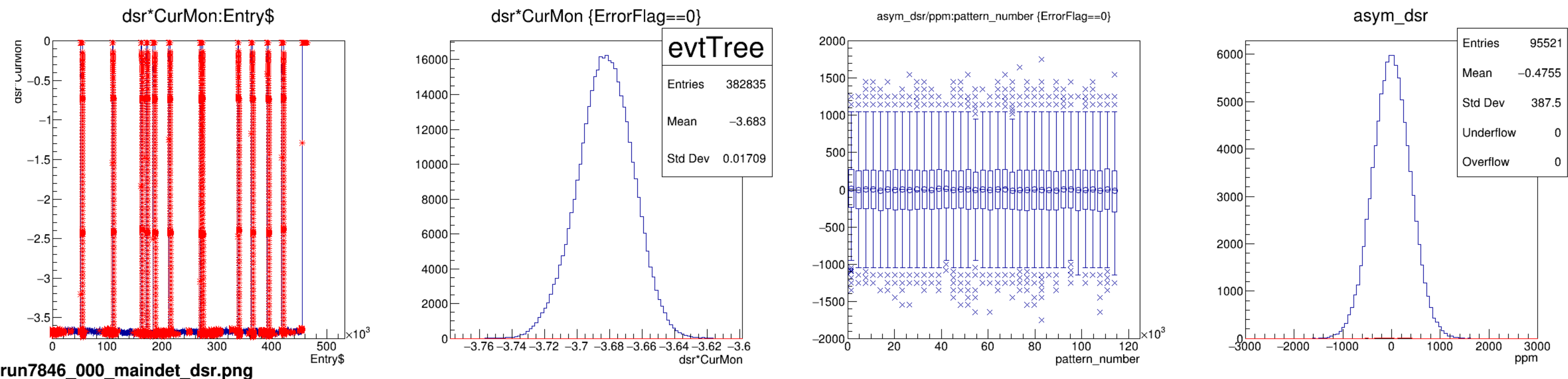


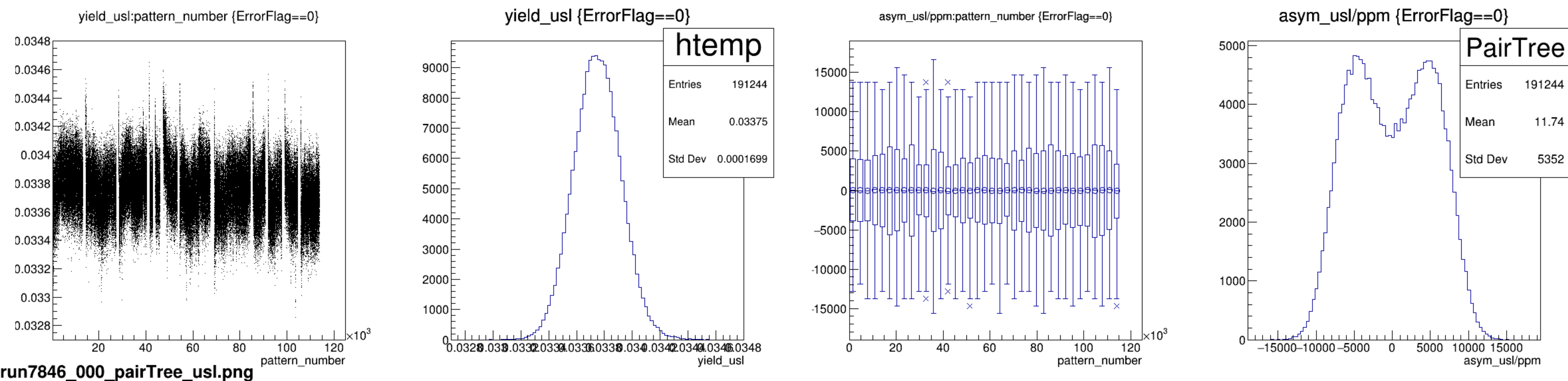
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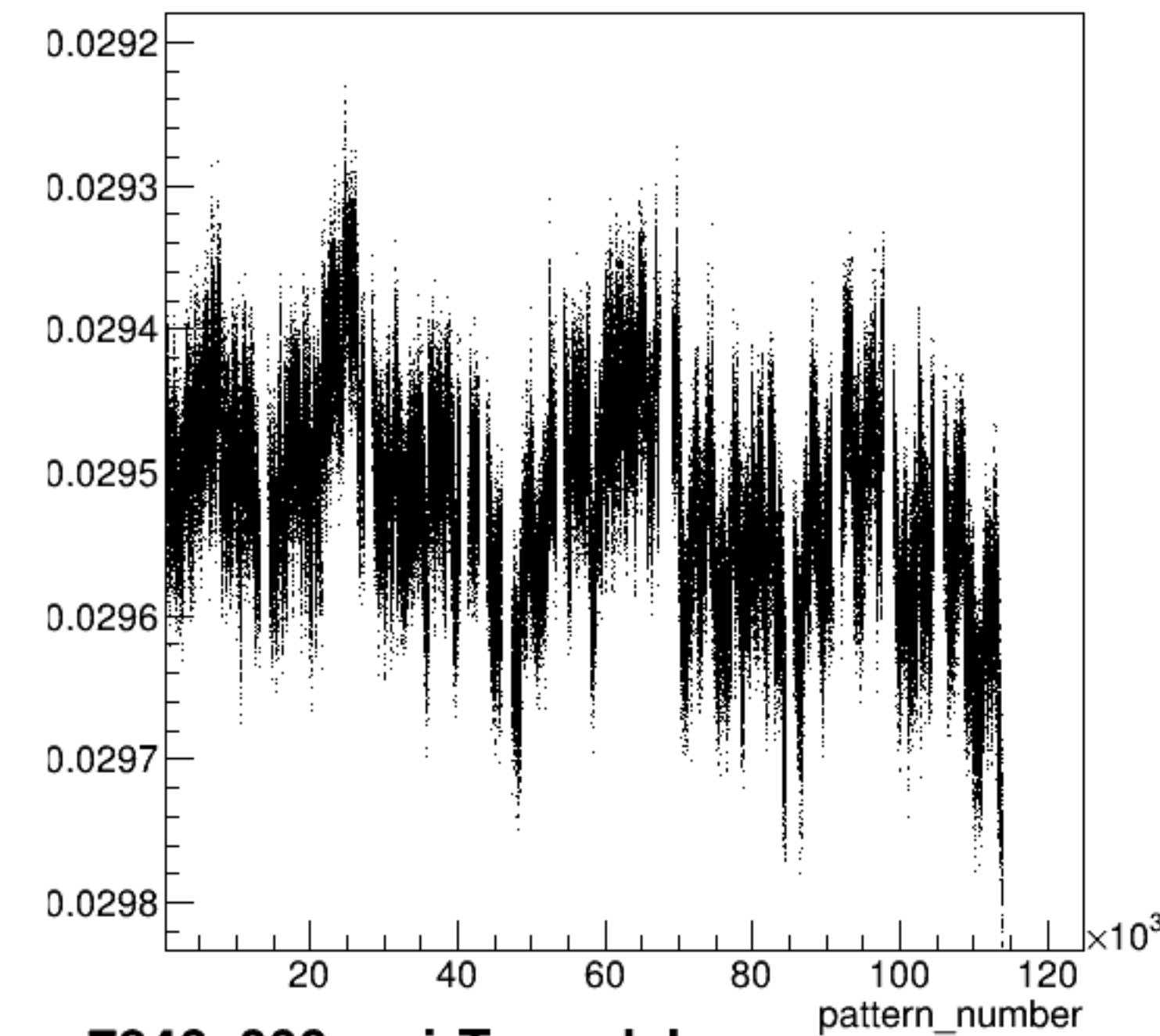
asym_usr





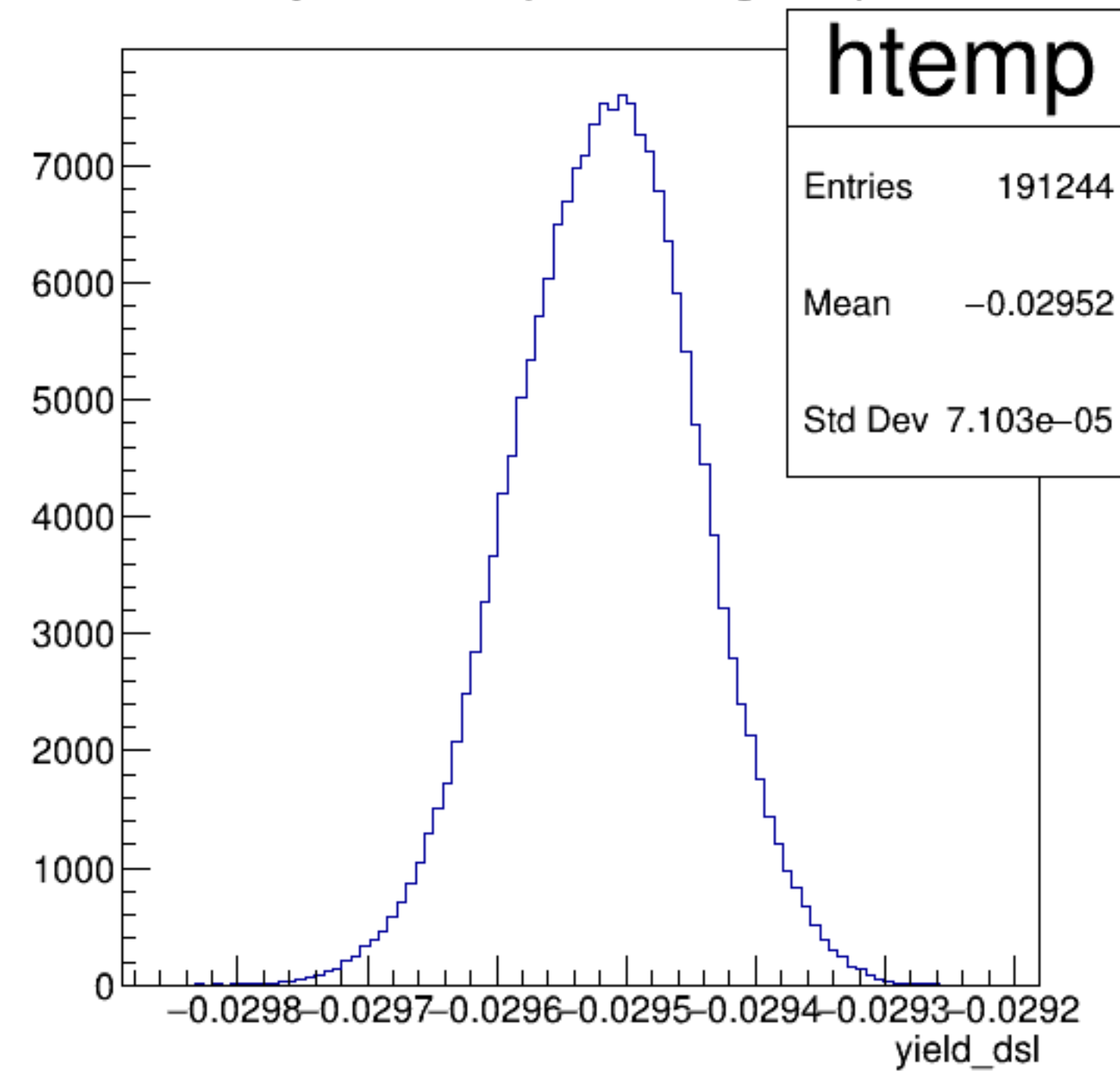


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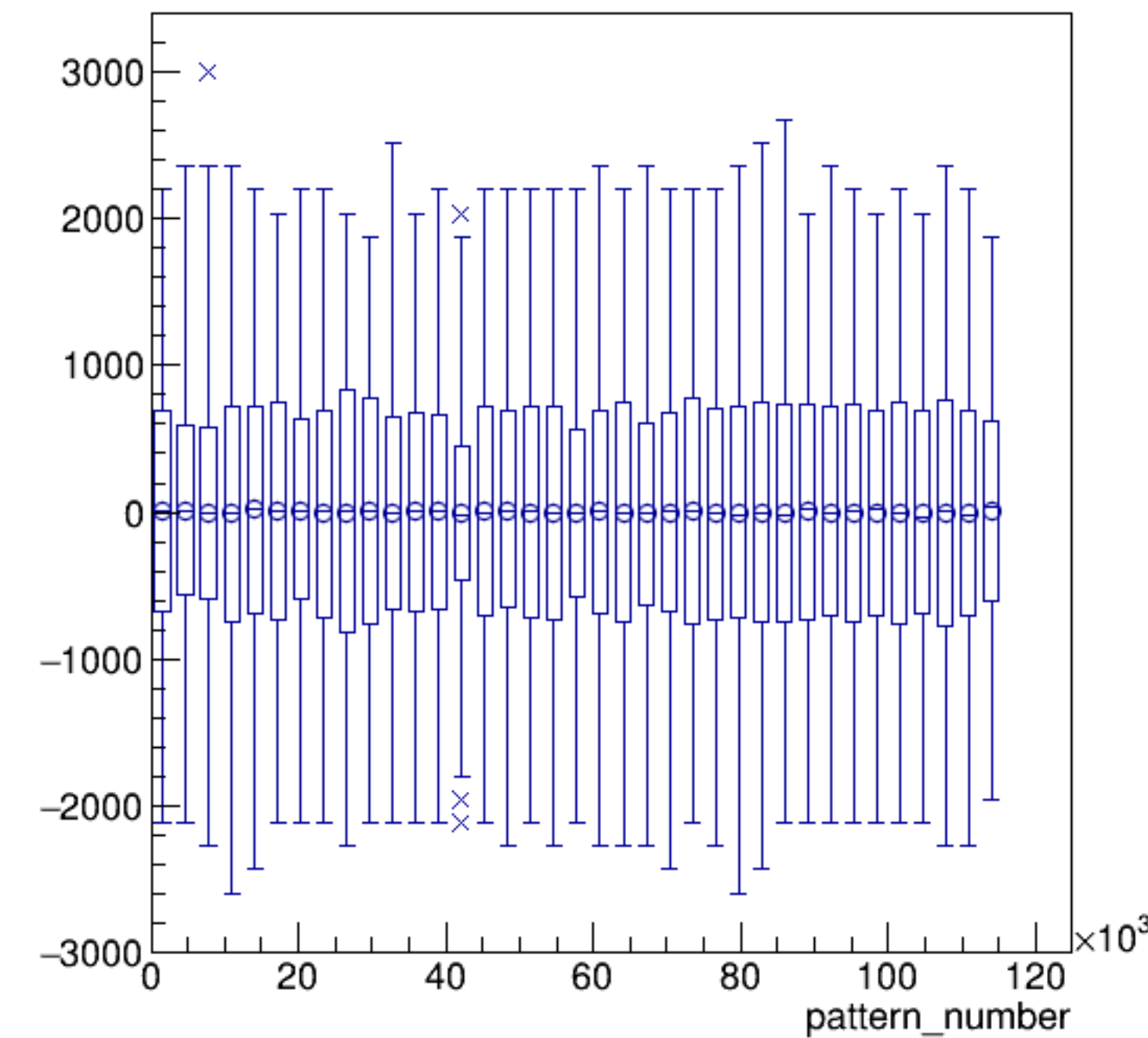


run7846_000_pairTree_dsl.png

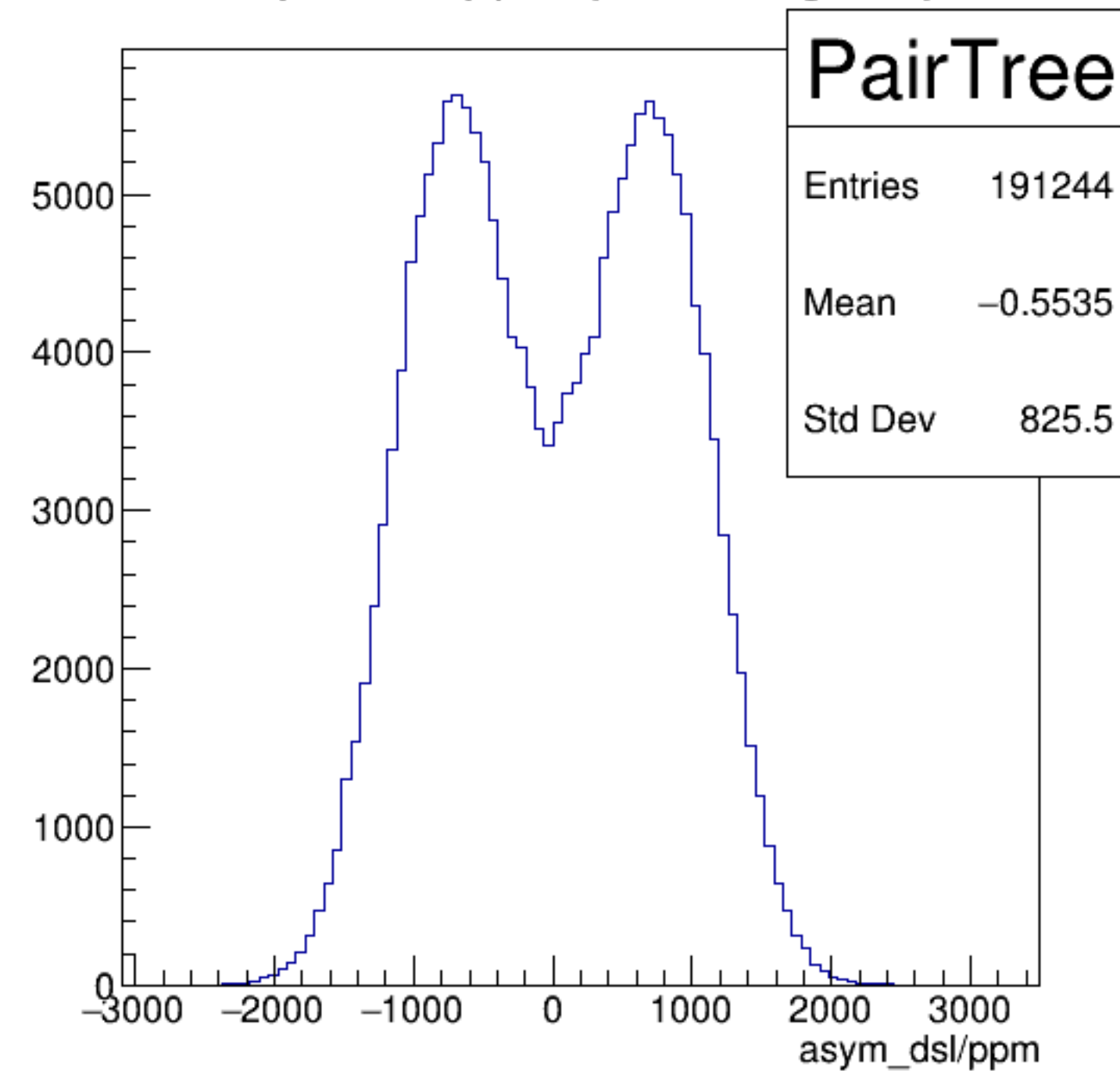
yield_dsl {ErrorFlag==0}

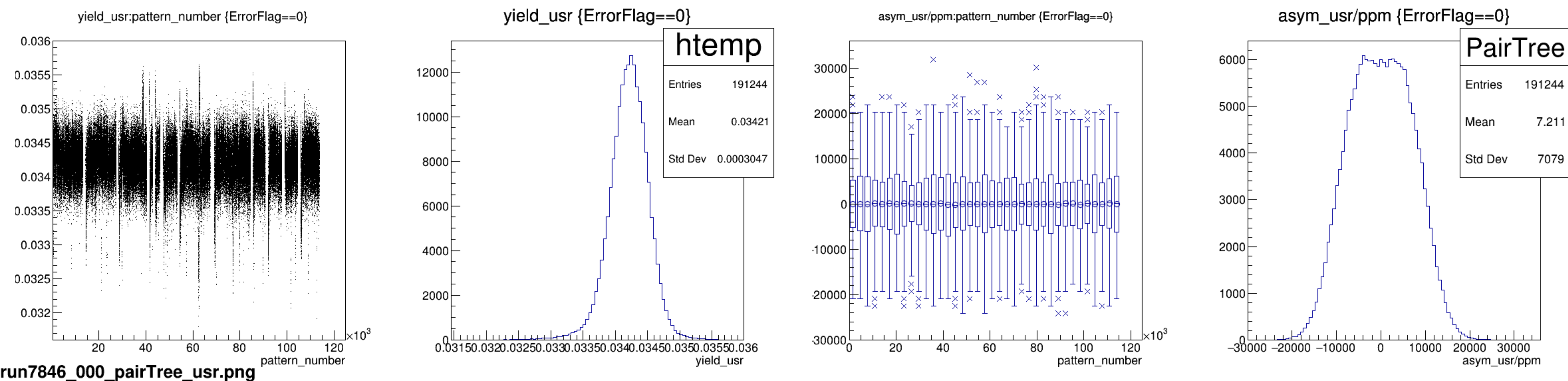


asym_dsl/ppm:pattern_number {ErrorFlag==0}

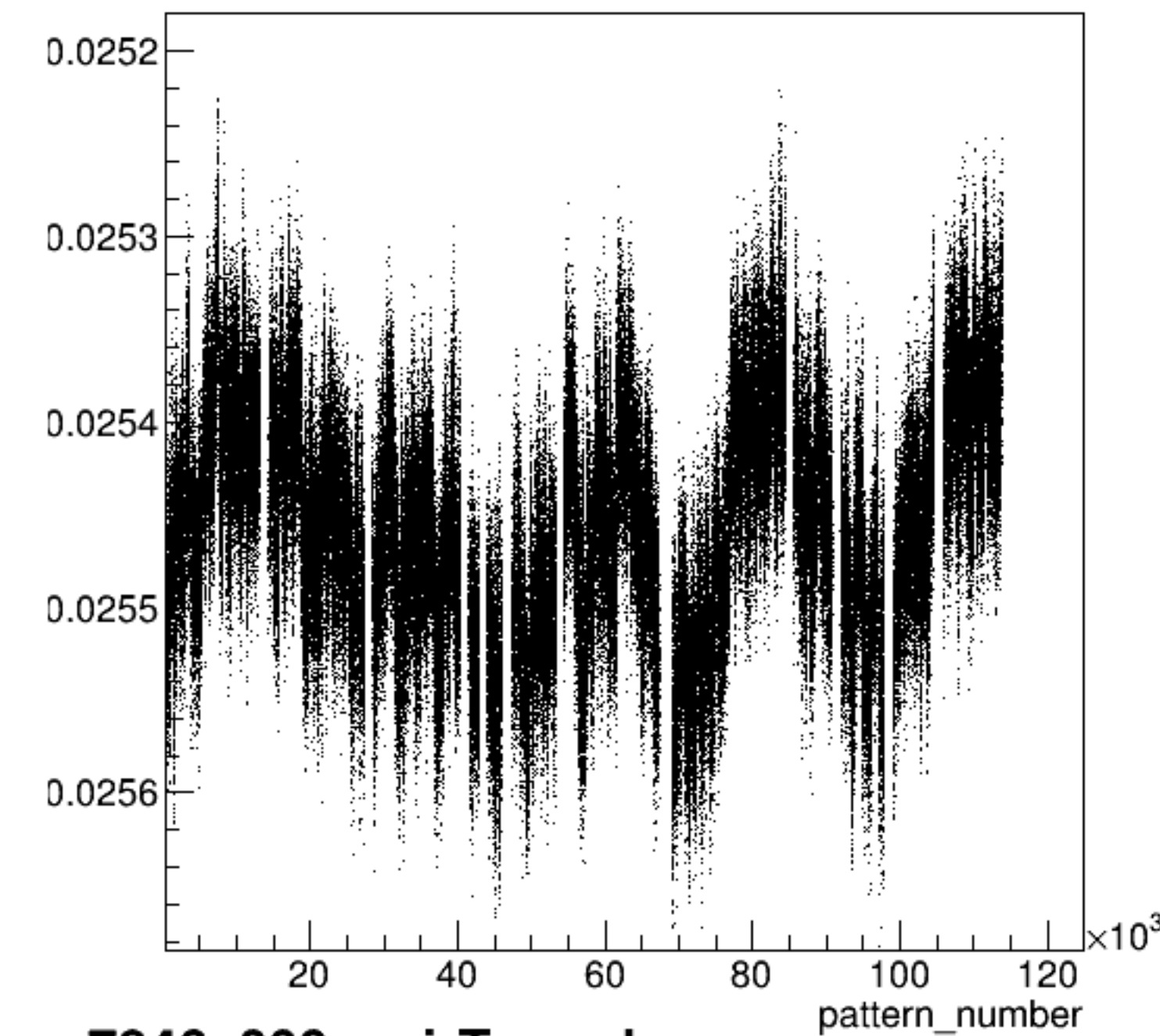


asym_dsl/ppm {ErrorFlag==0}



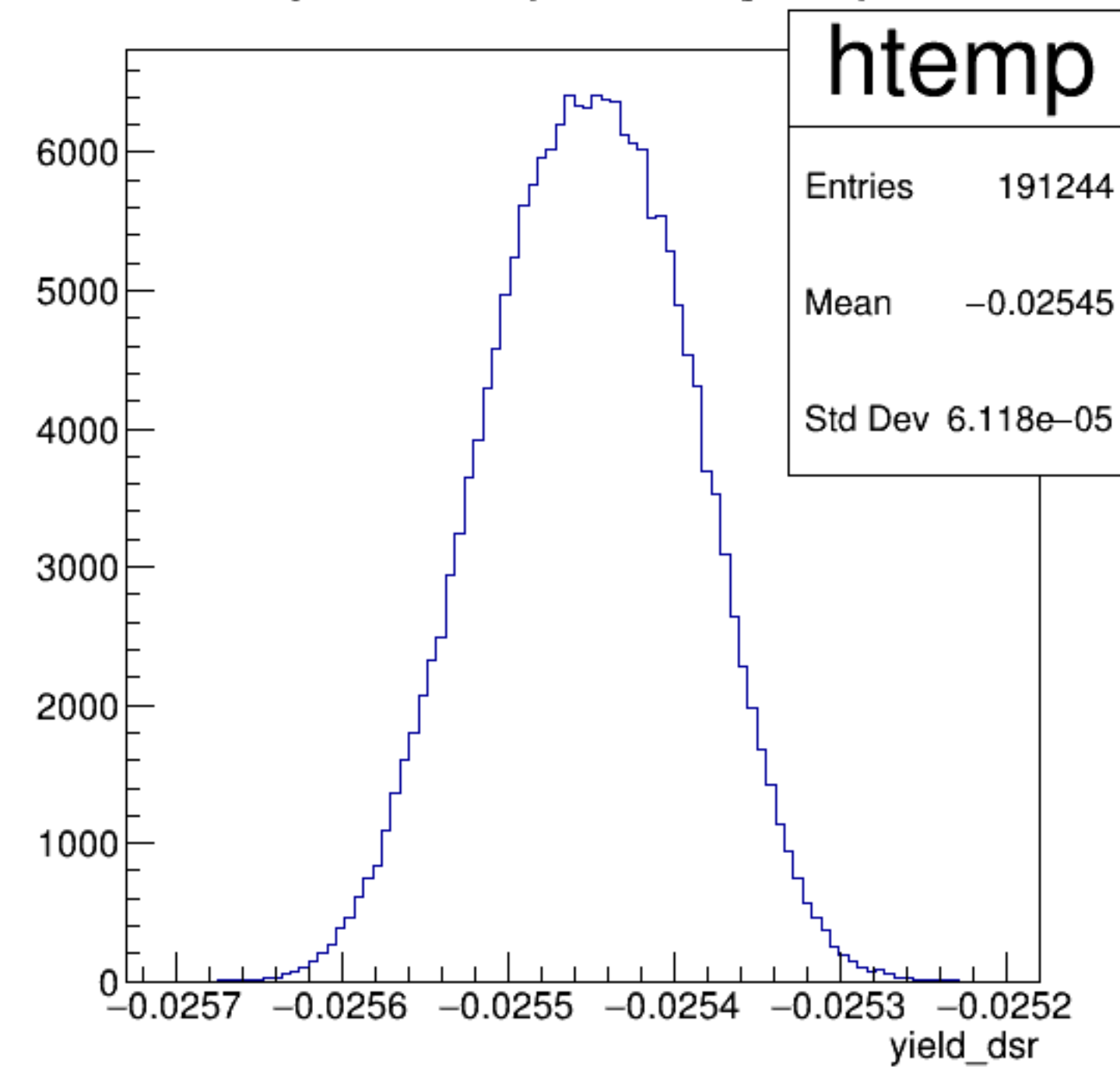


yield_dsr:pattern_number {ErrorFlag==0}

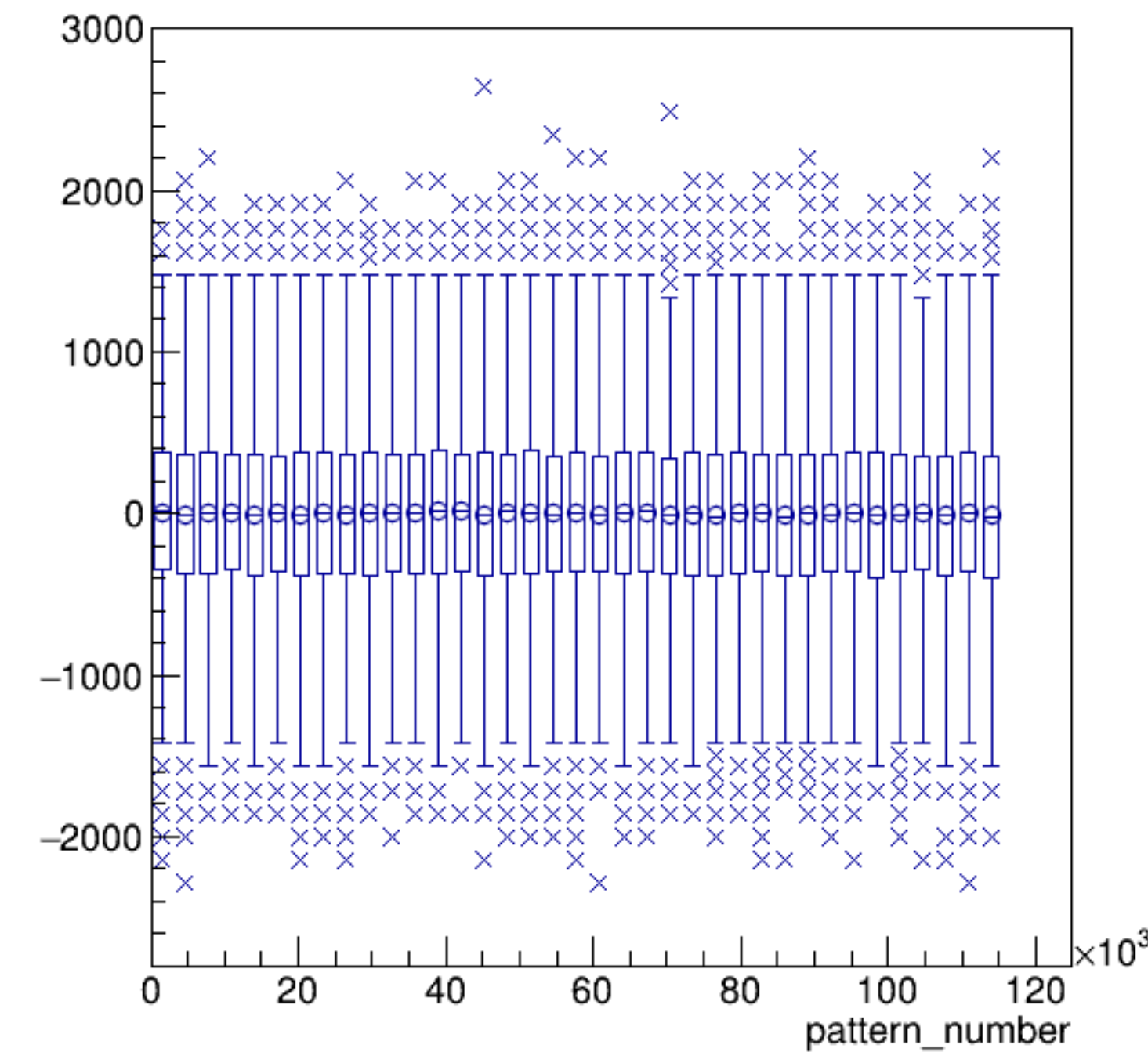


run7846_000_pairTree_dsr.png

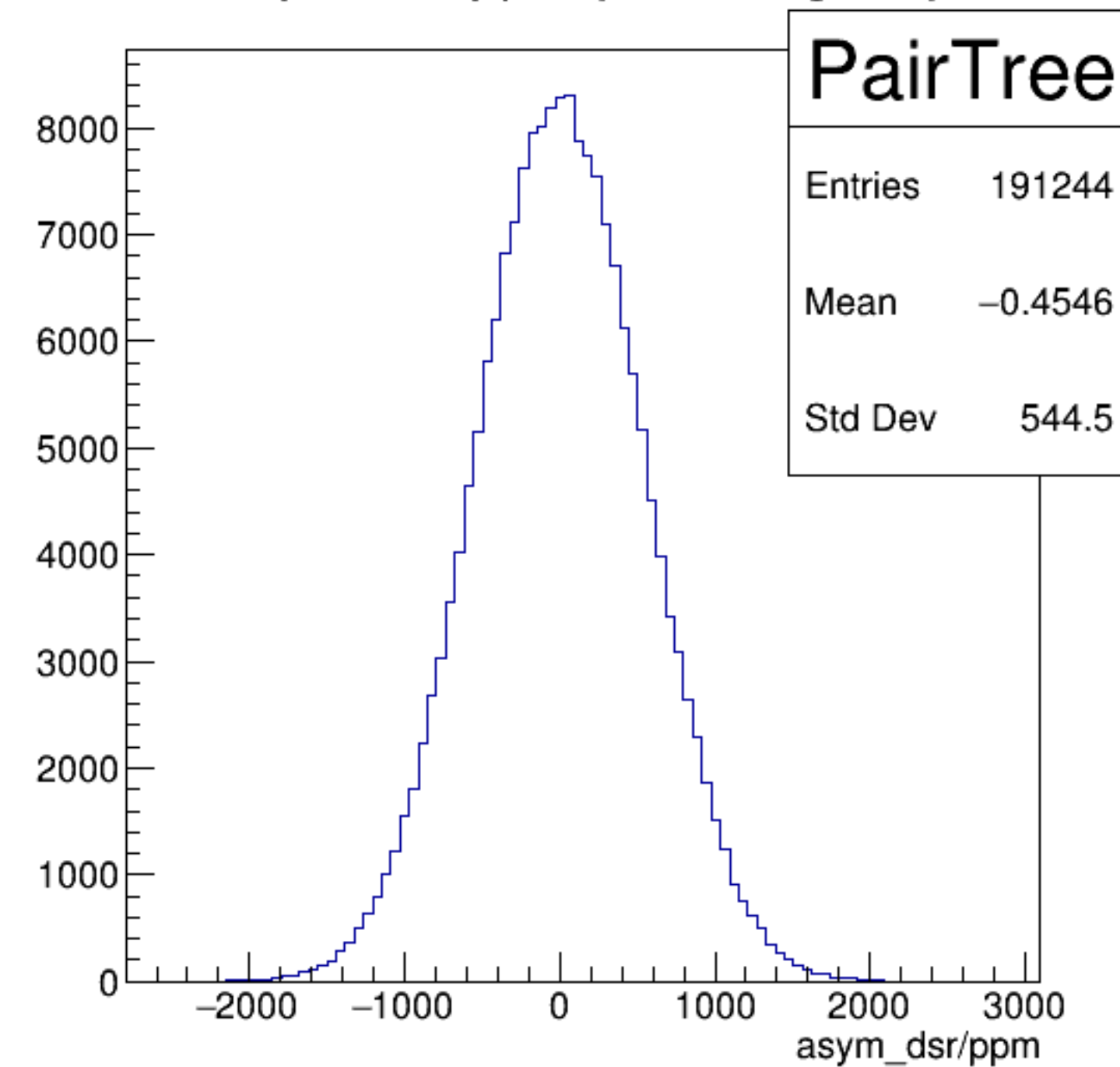
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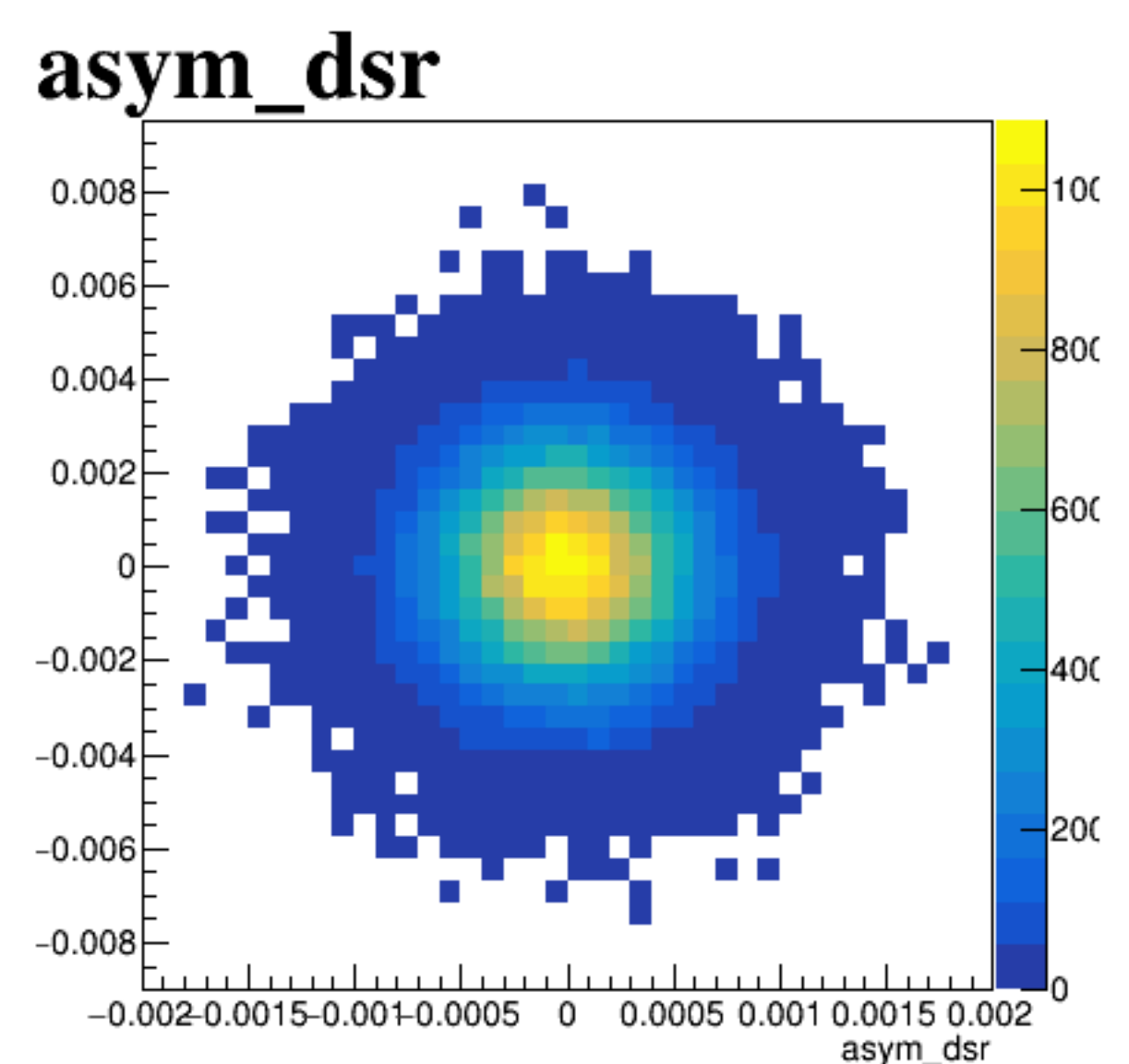
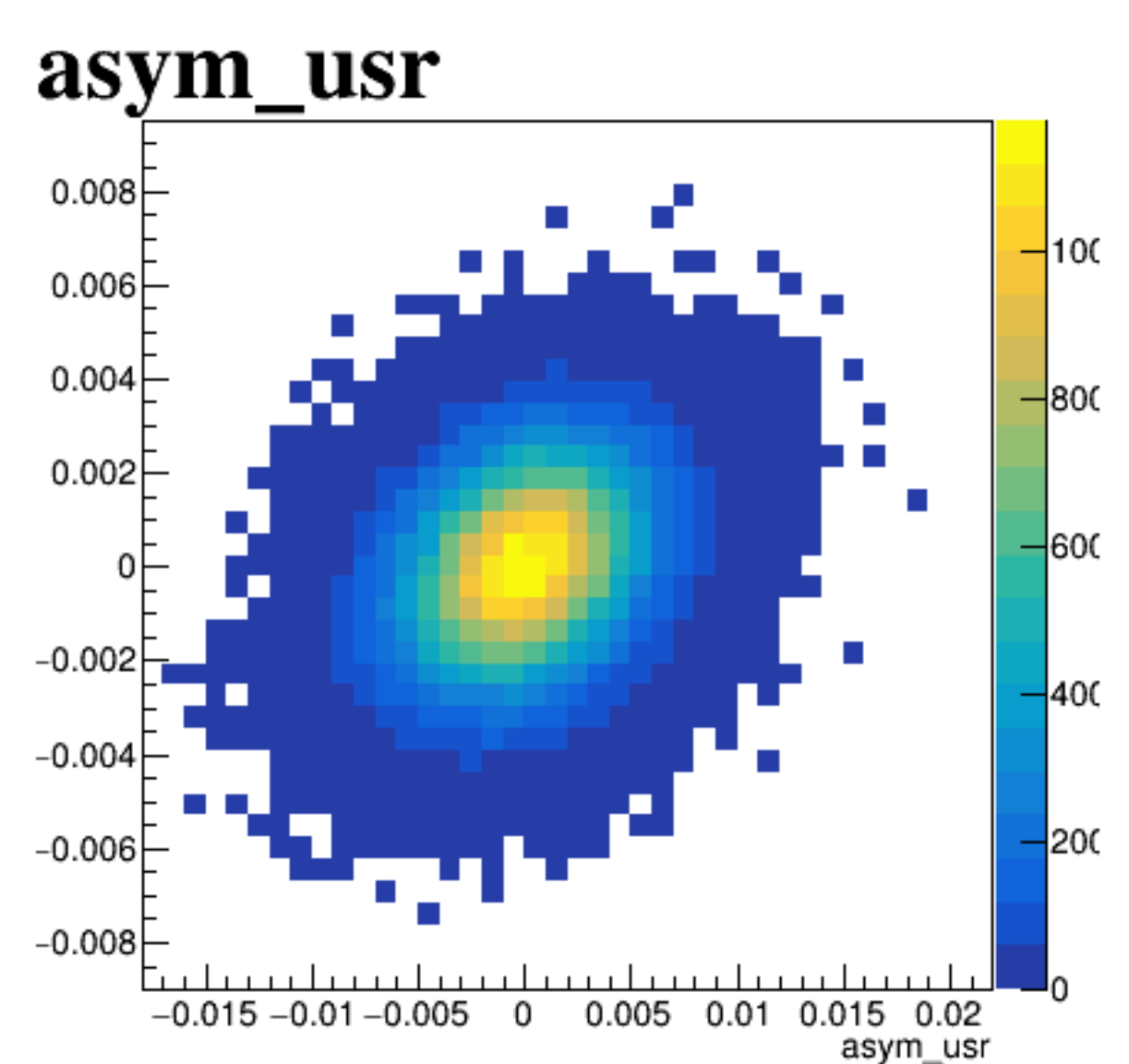
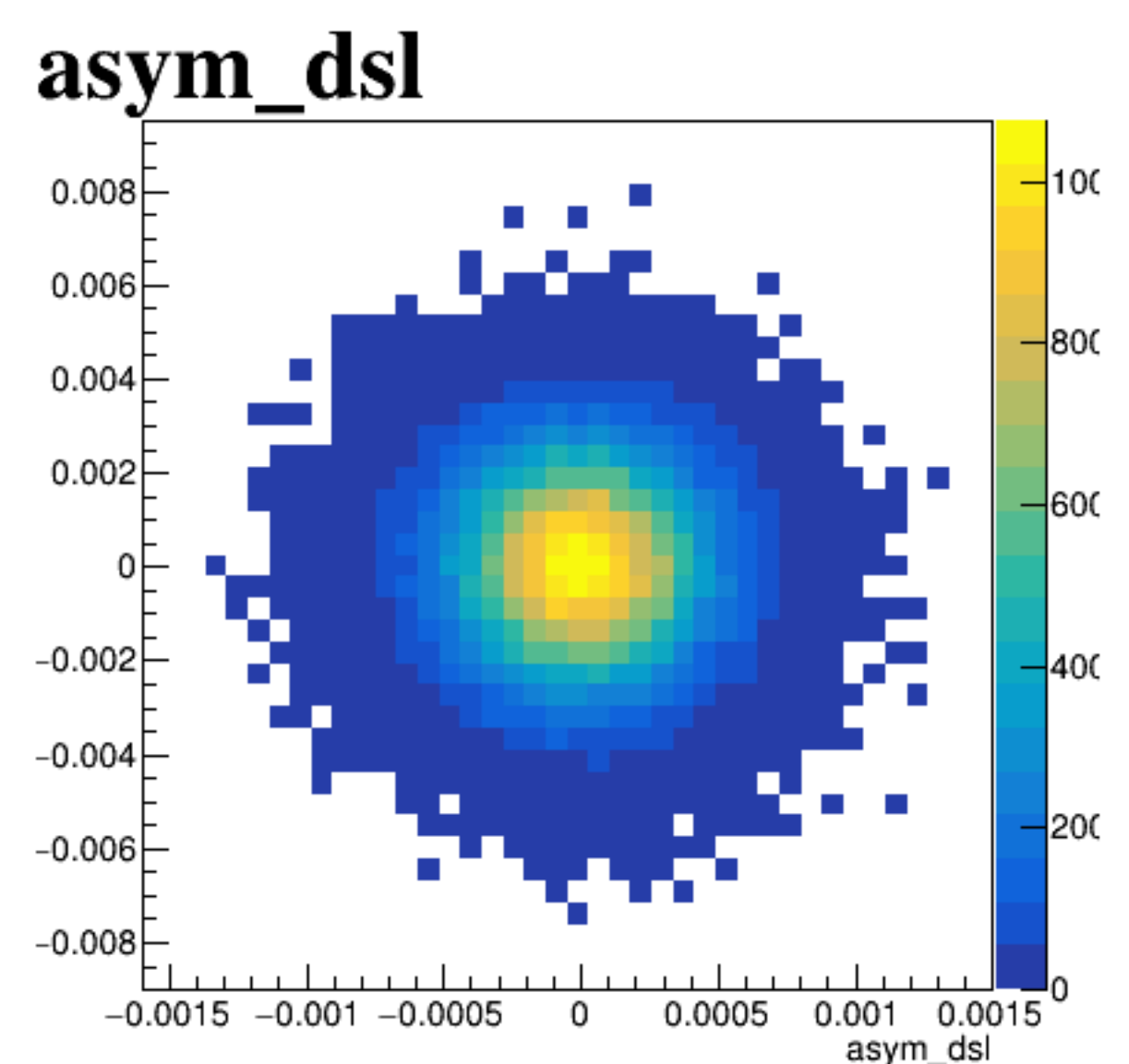
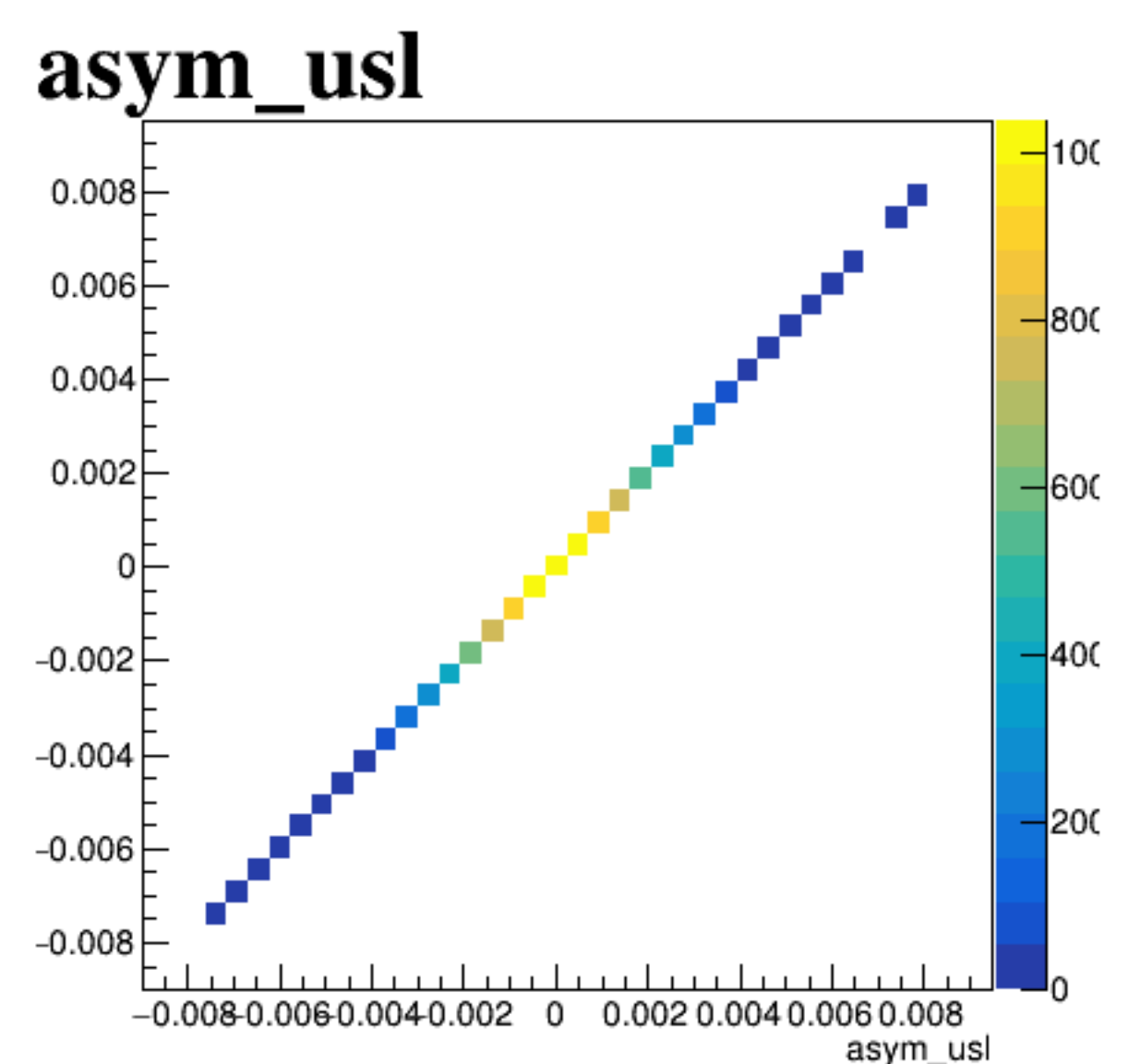
asym_dsr/ppm:pattern_number {ErrorFlag==0}



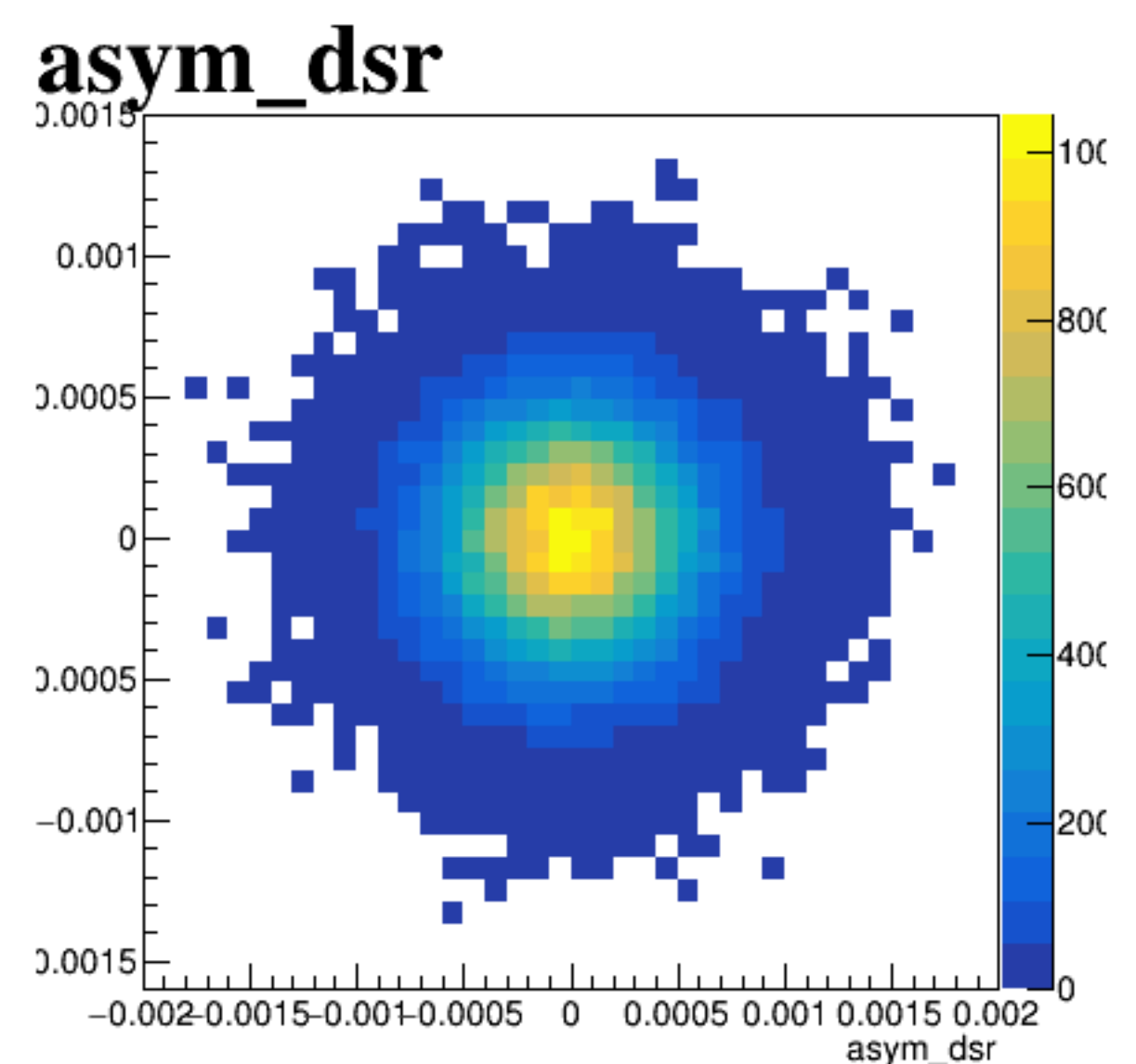
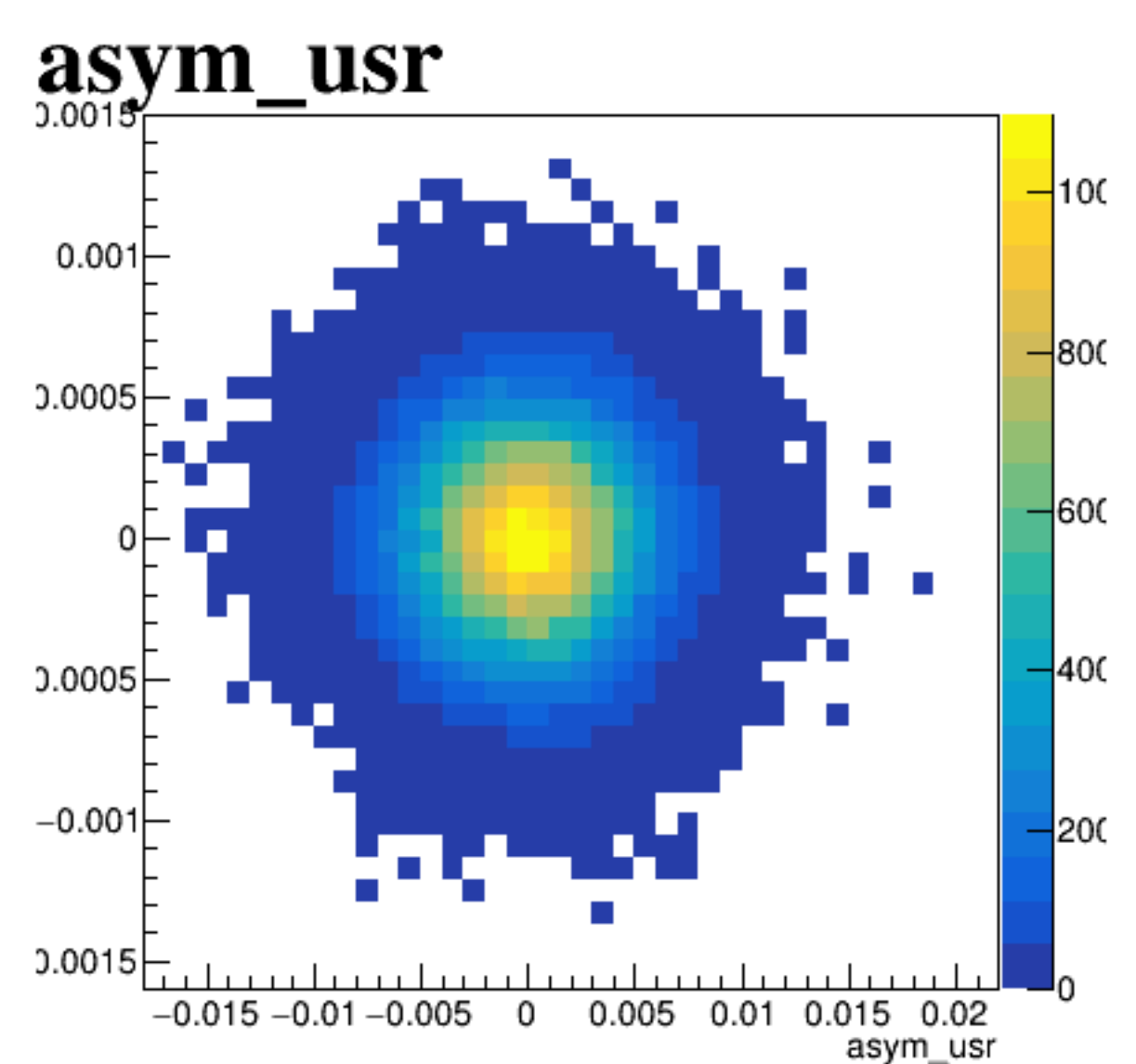
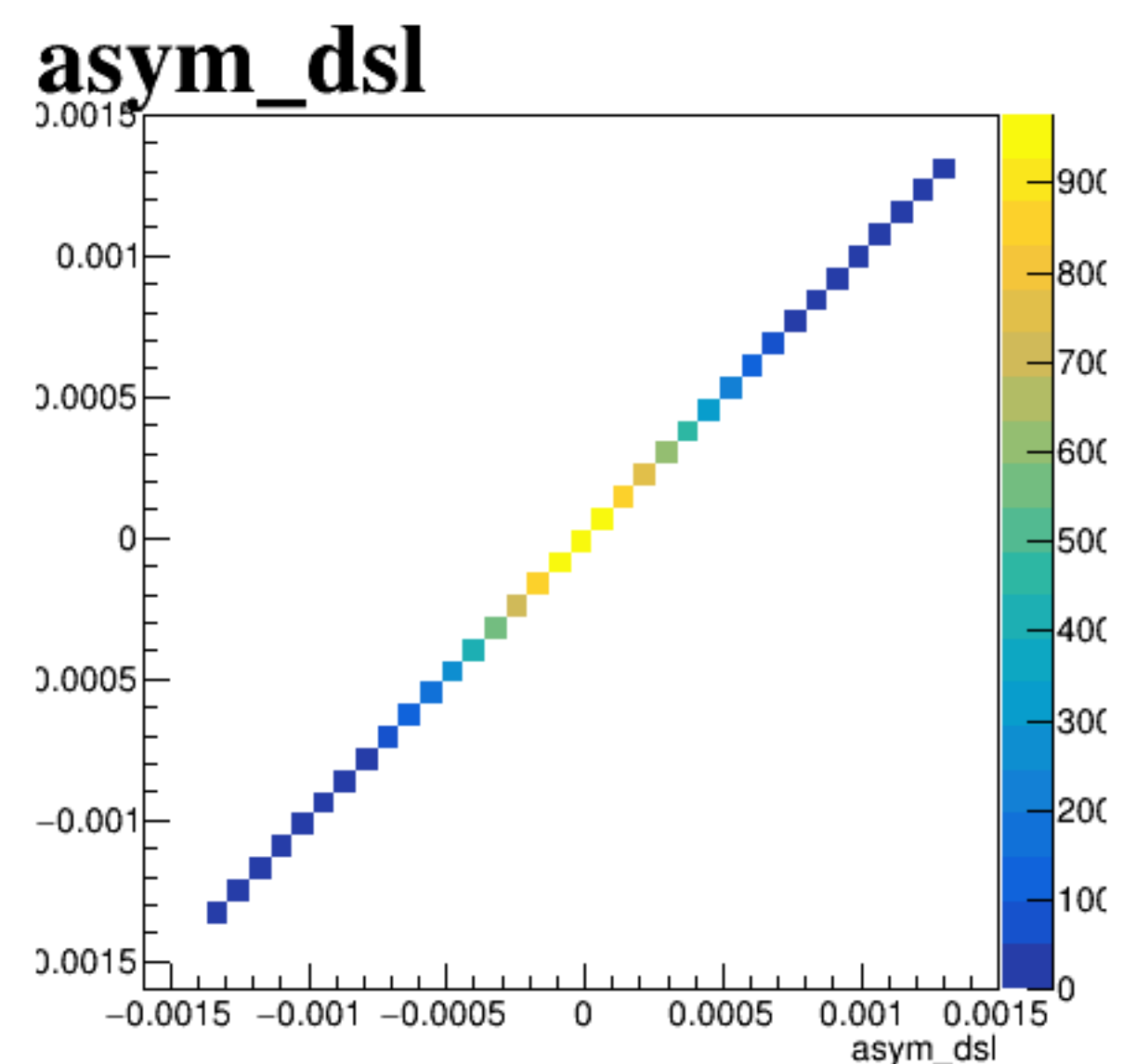
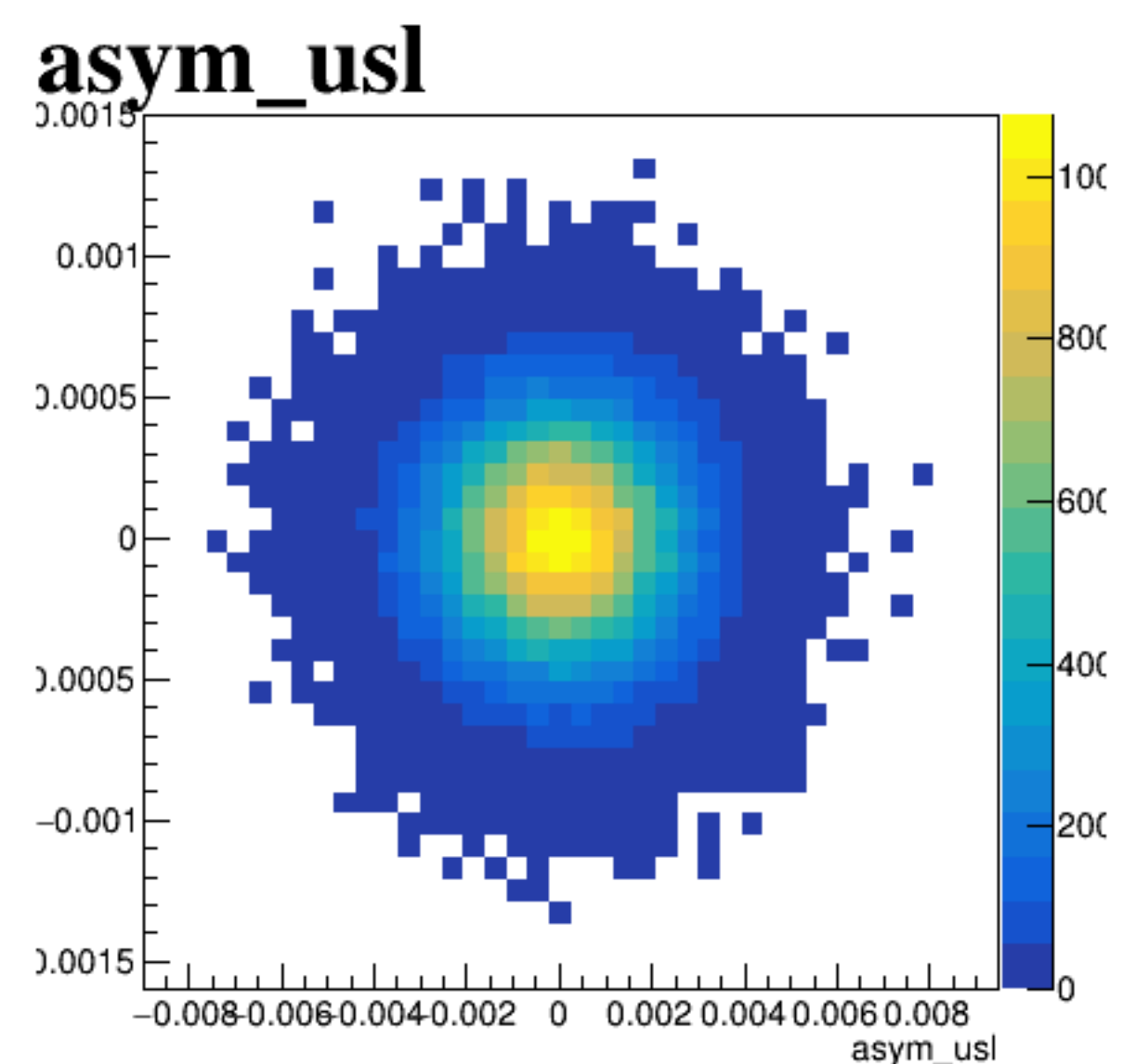
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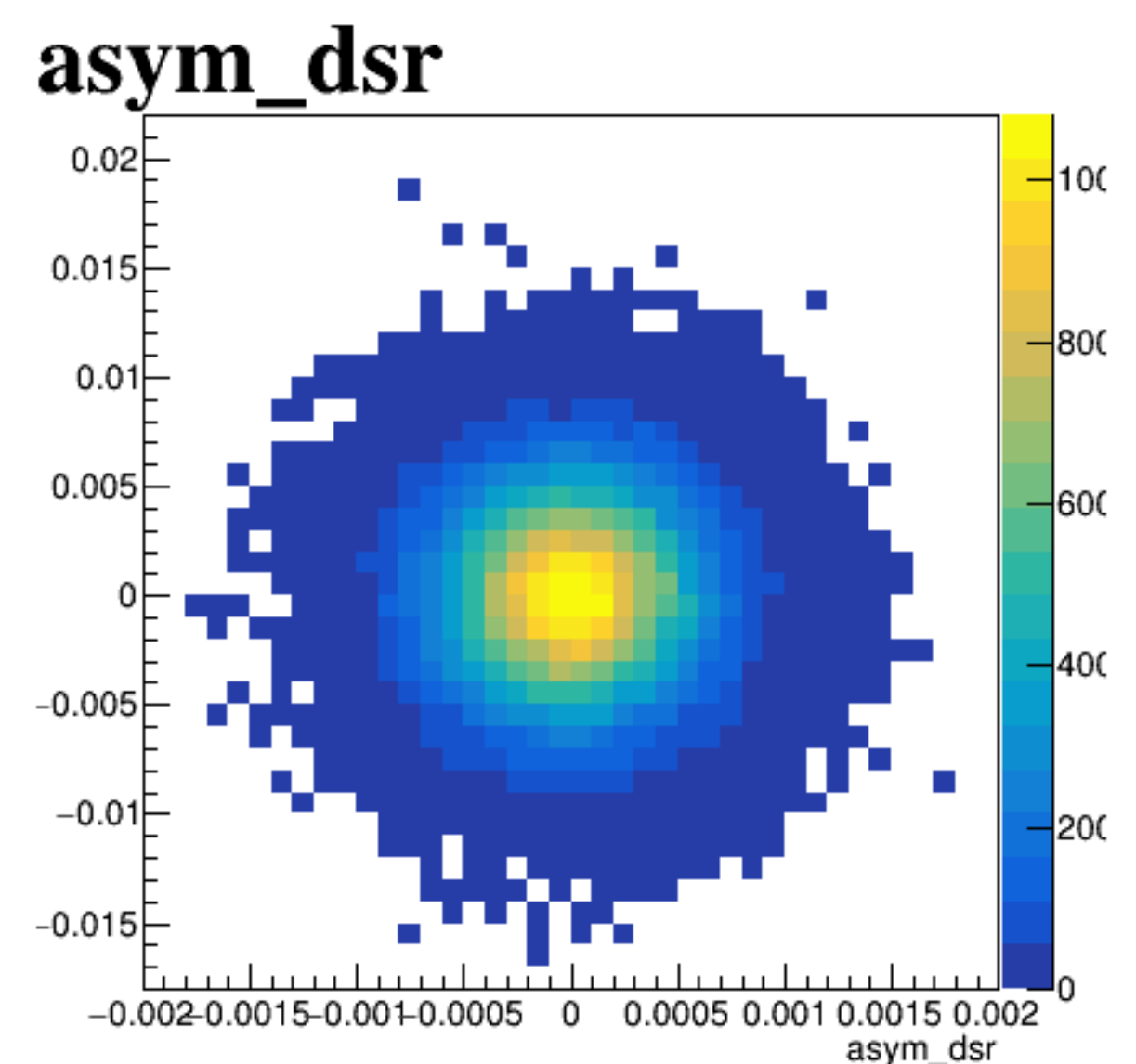
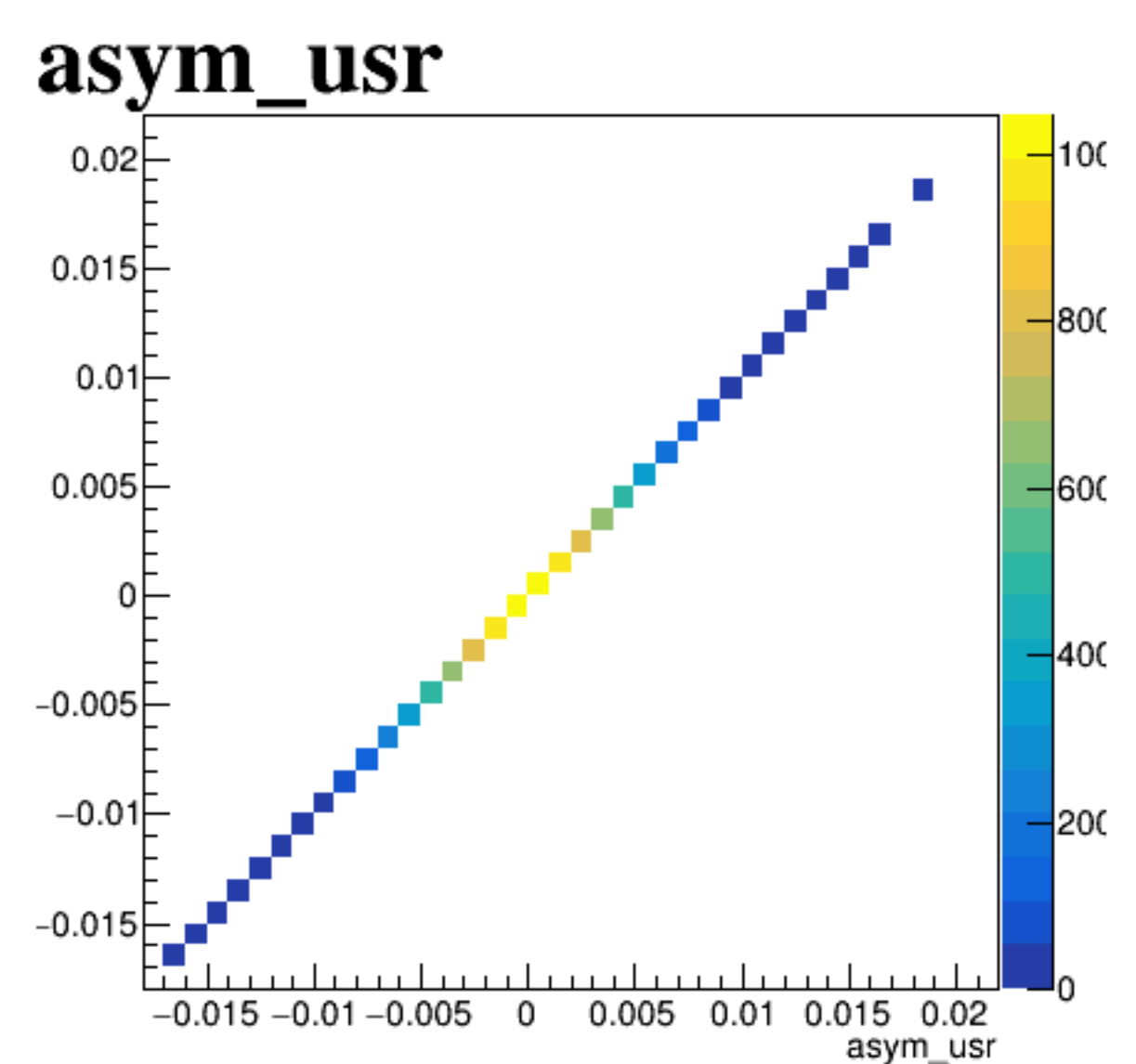
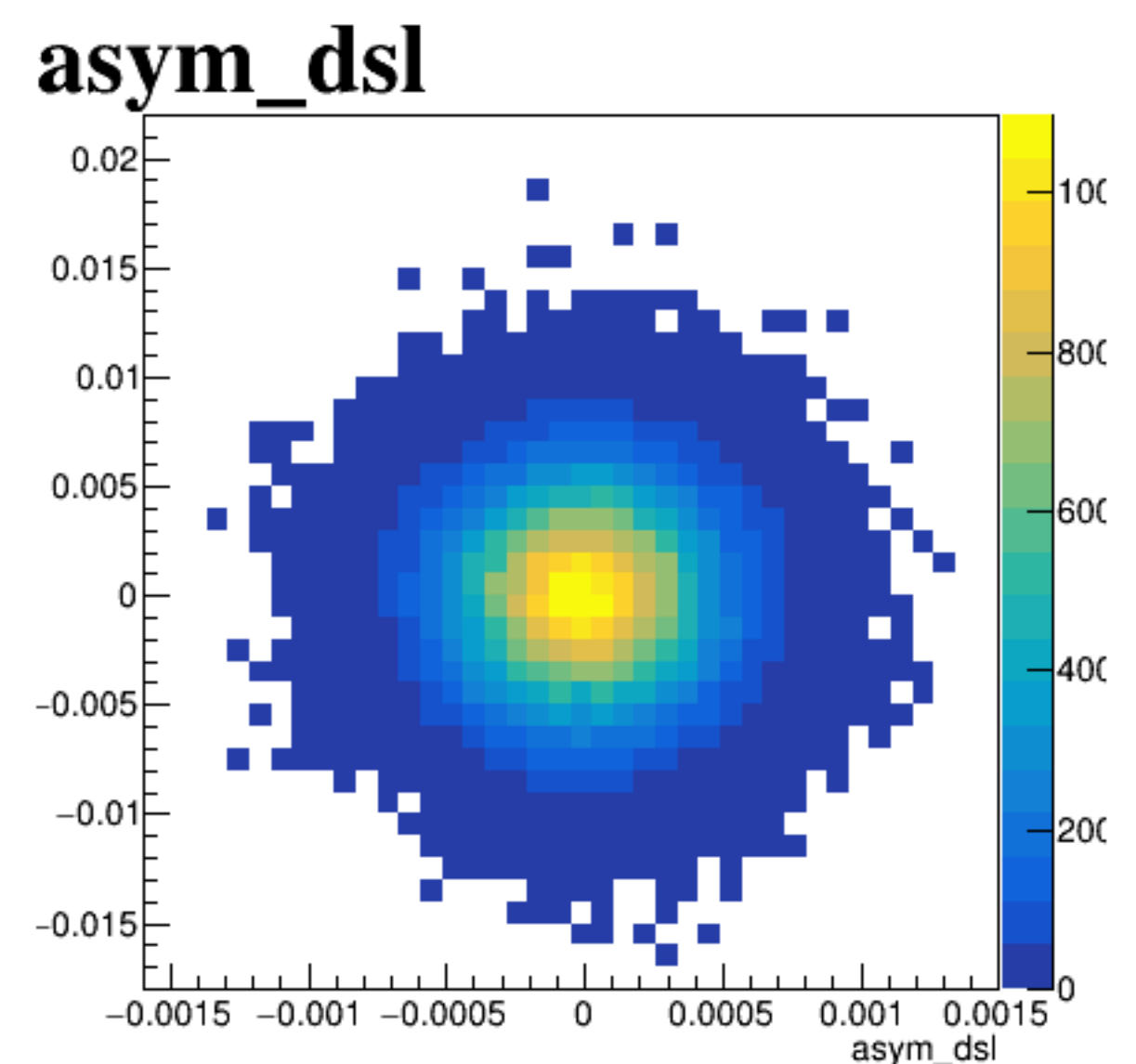
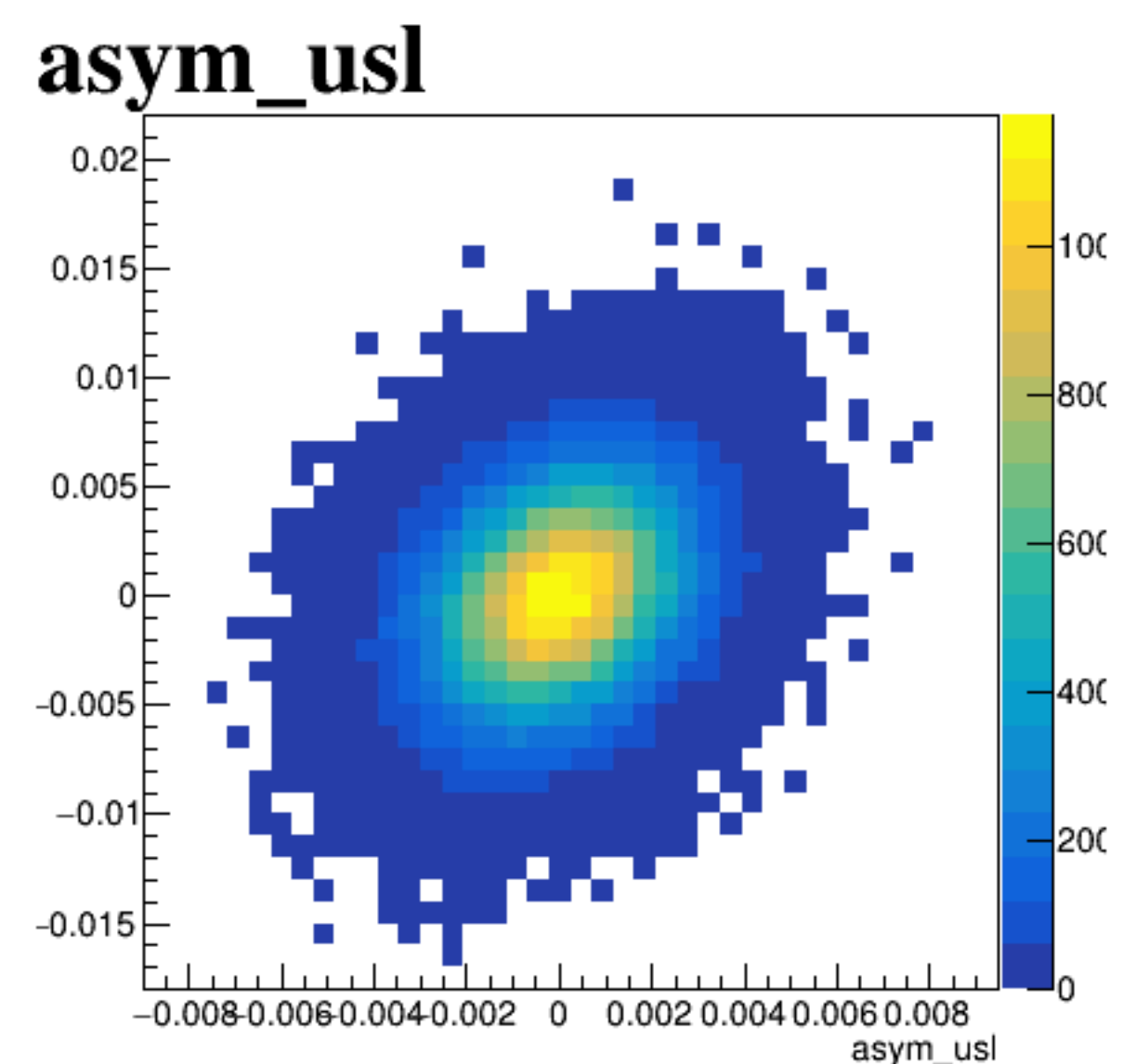
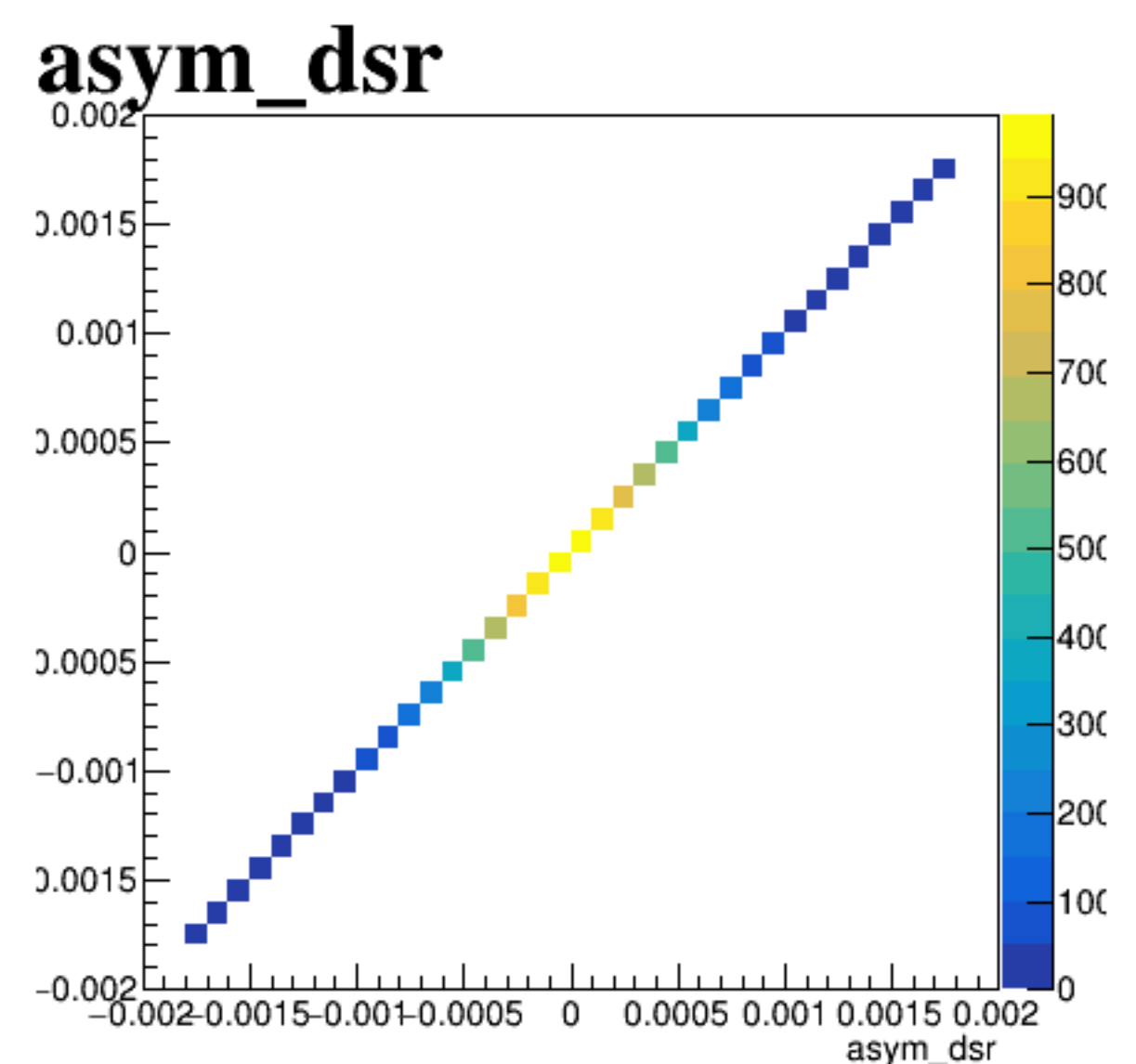
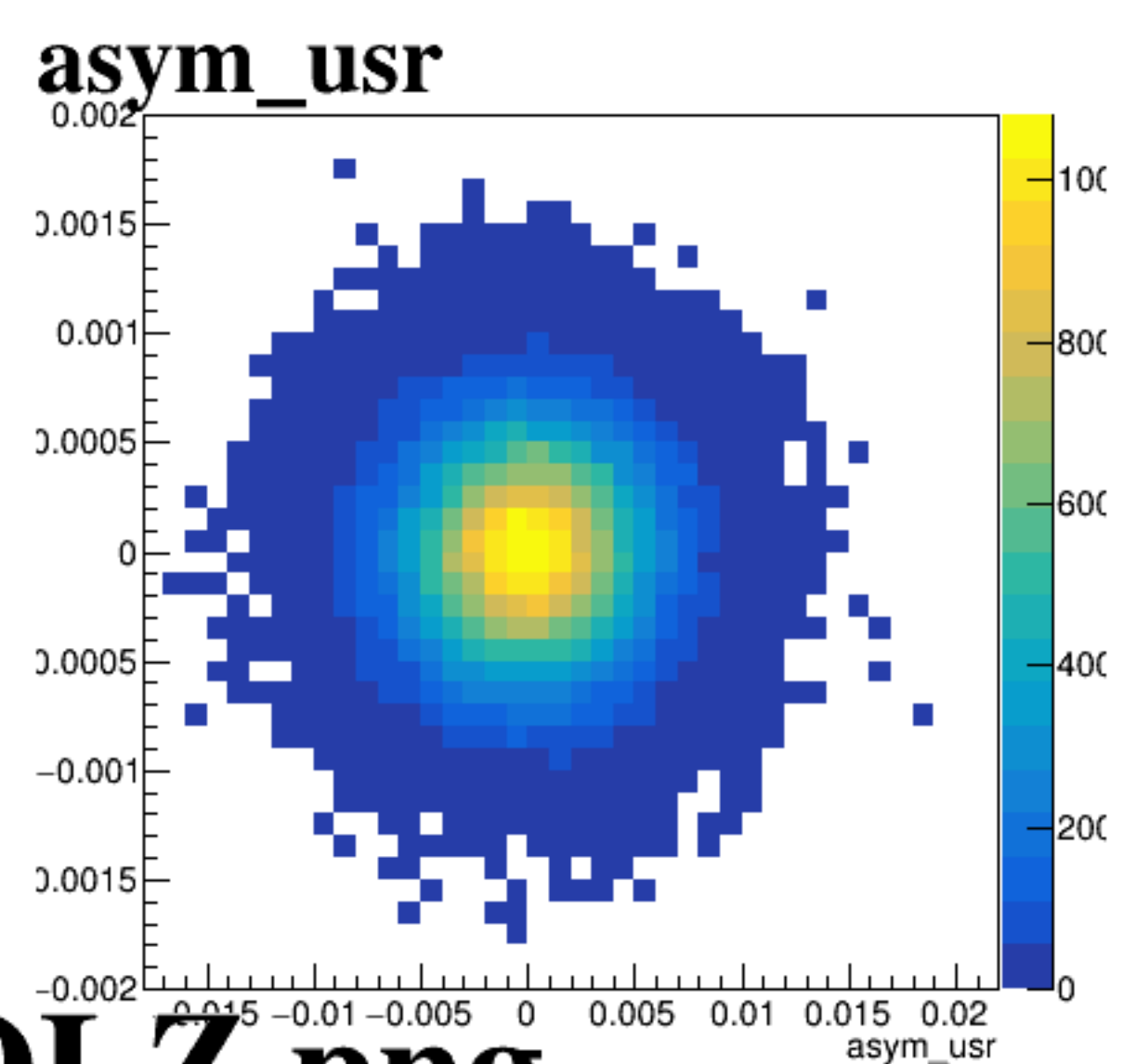
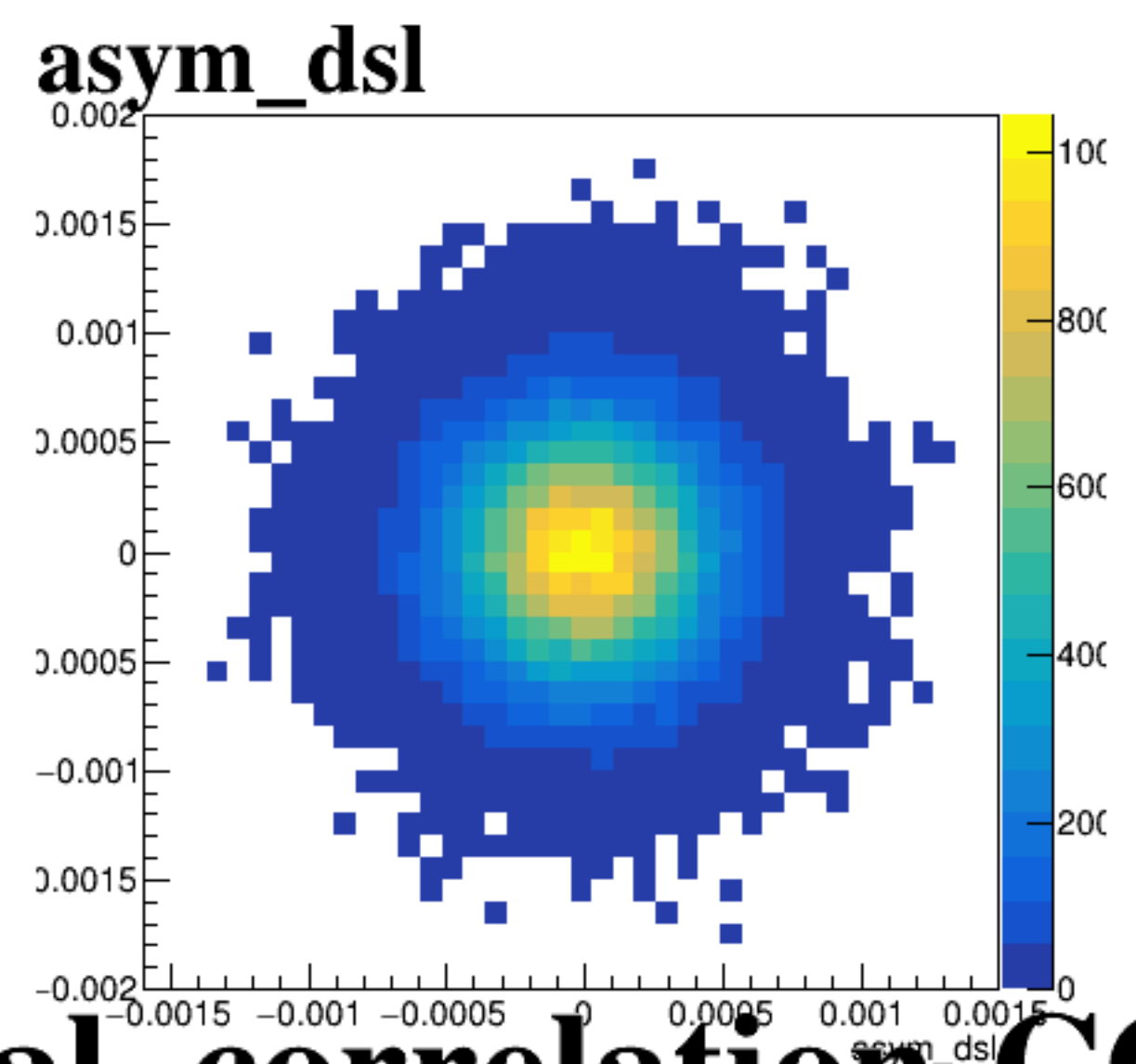
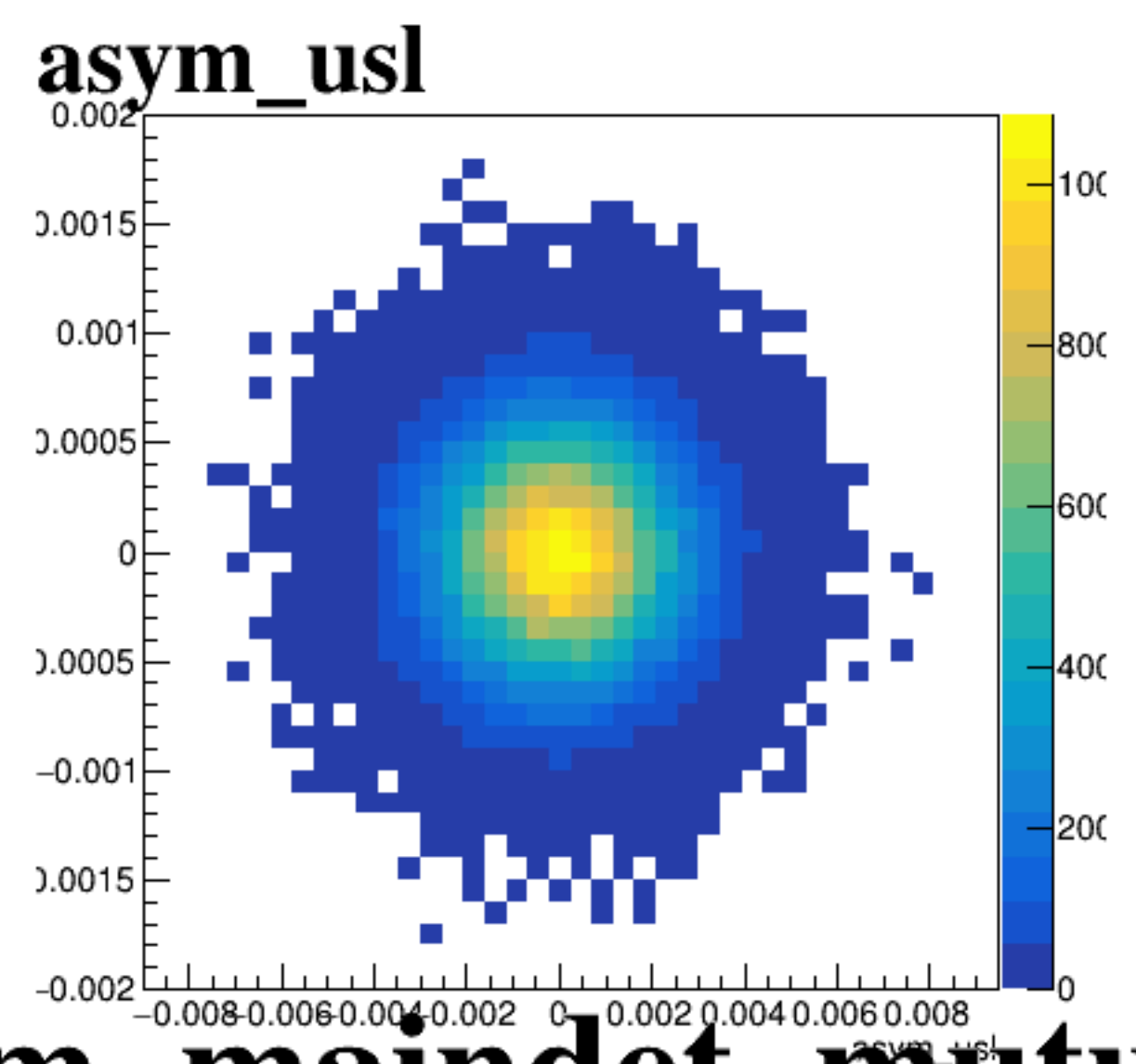
asym_usl



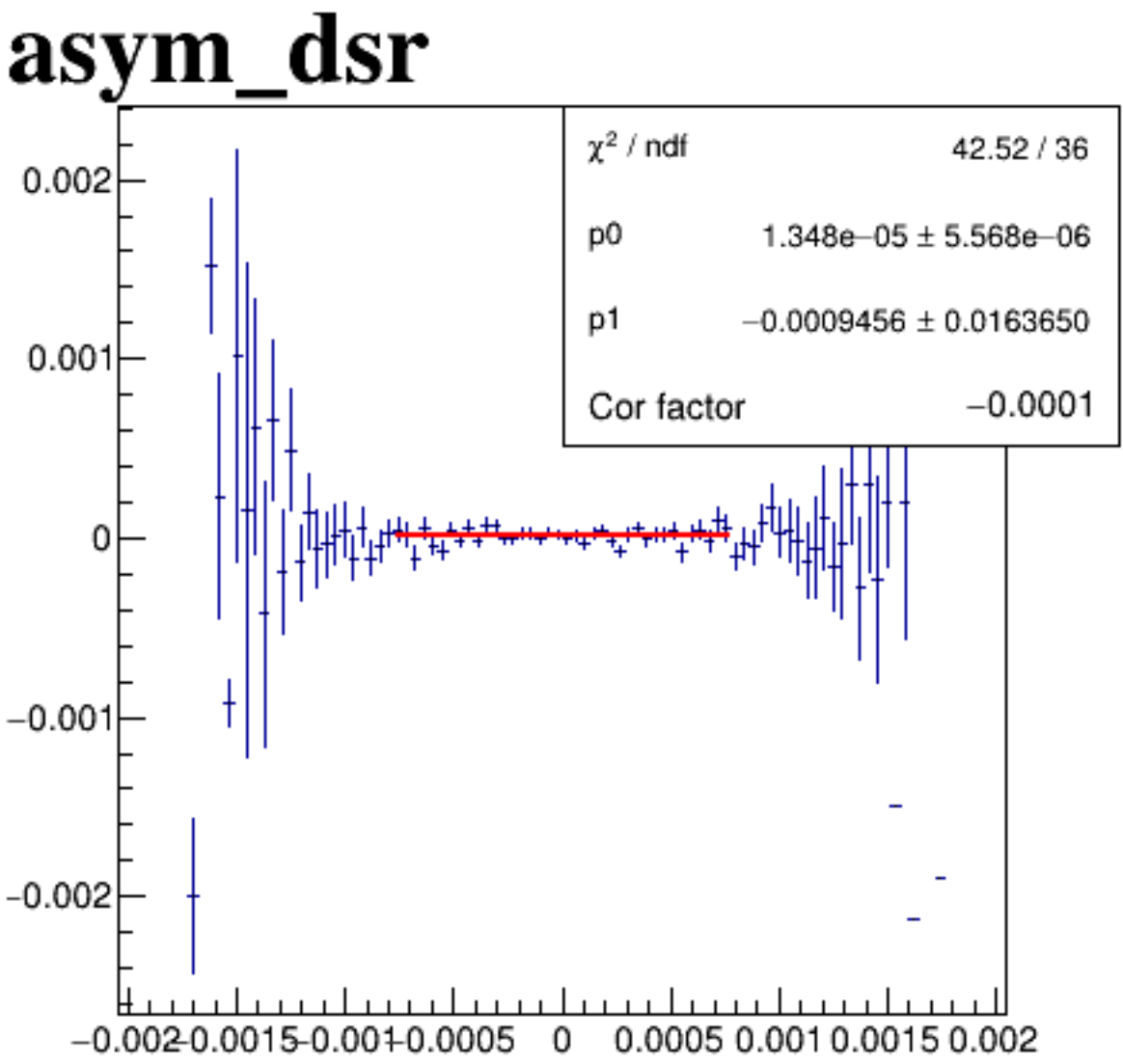
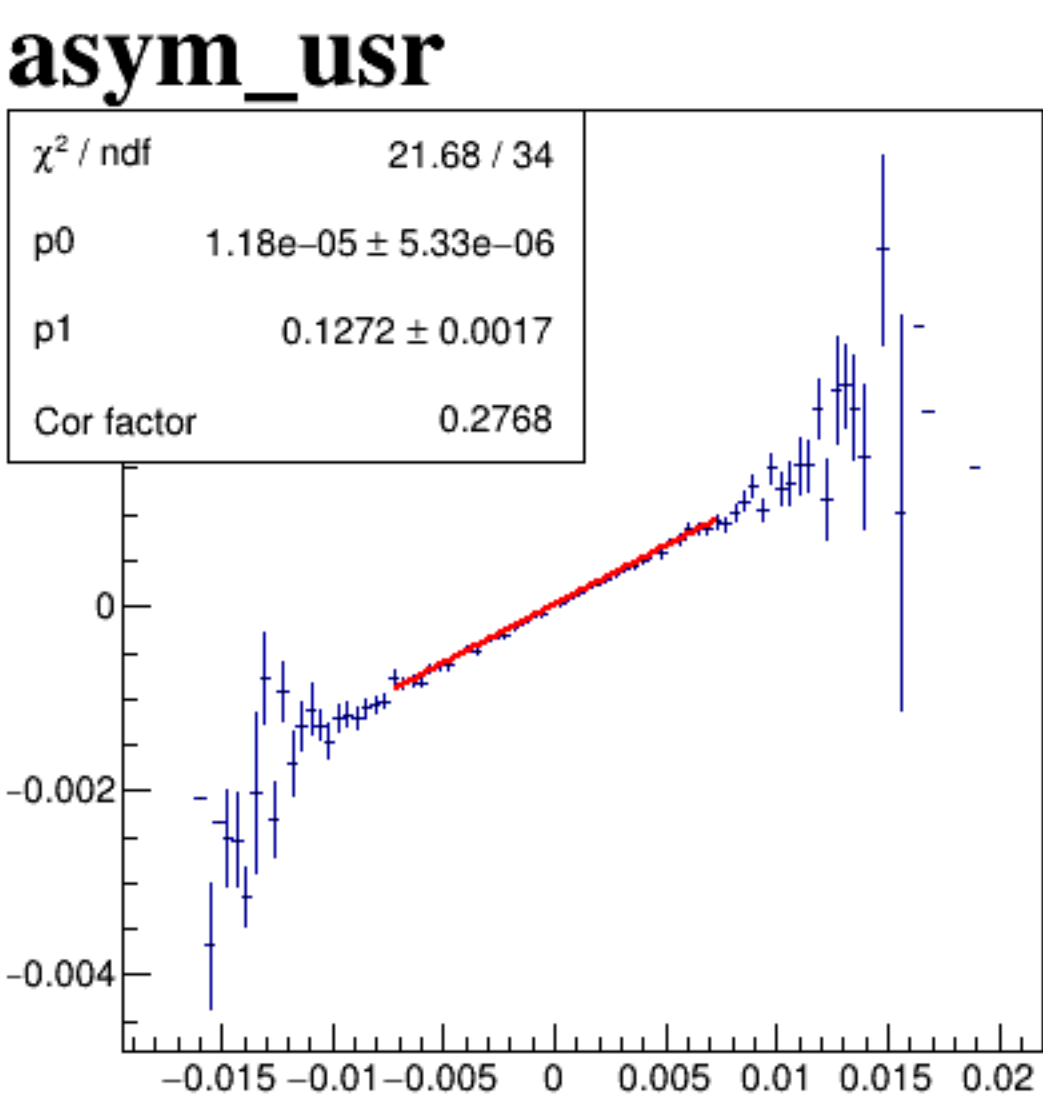
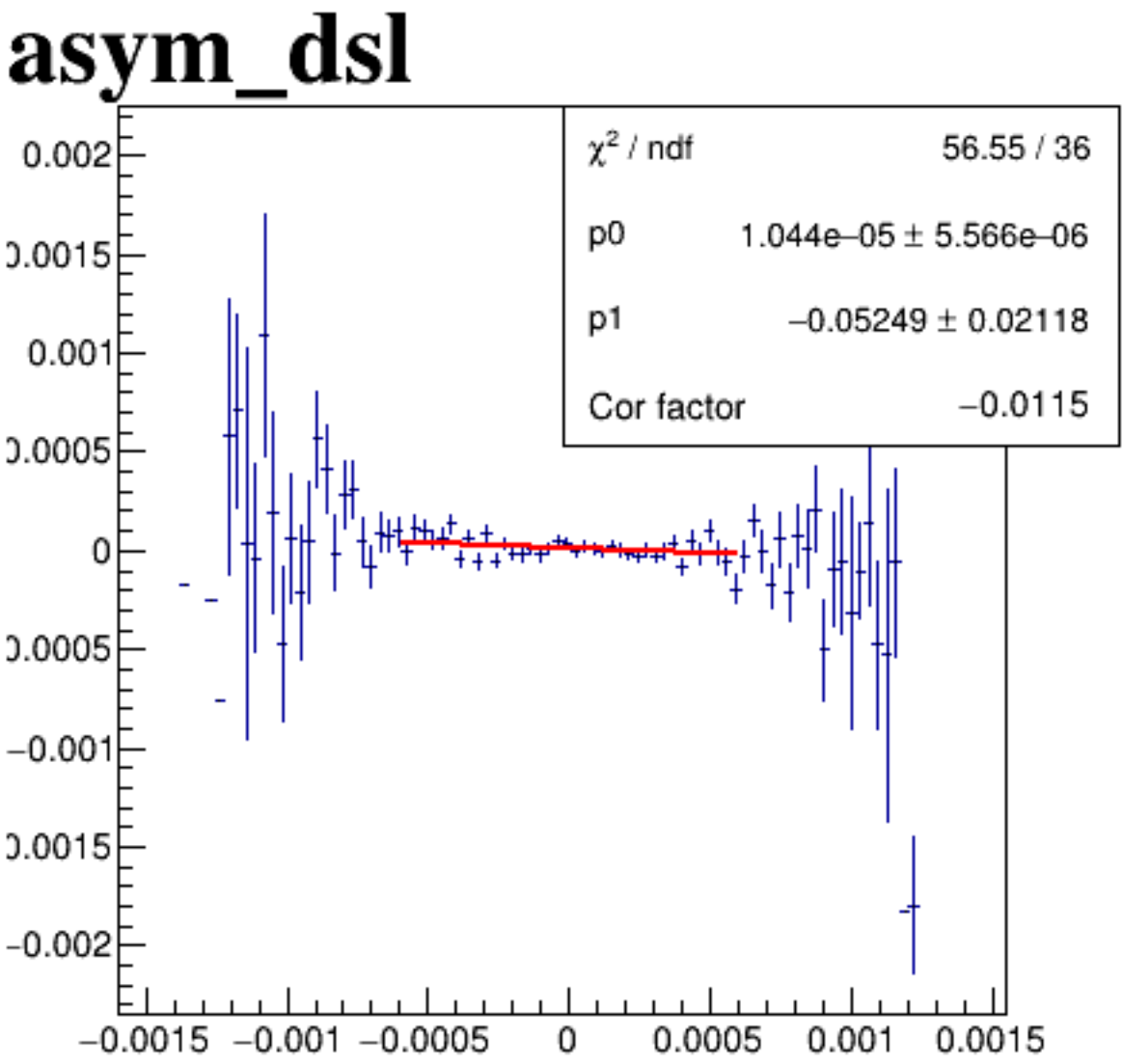
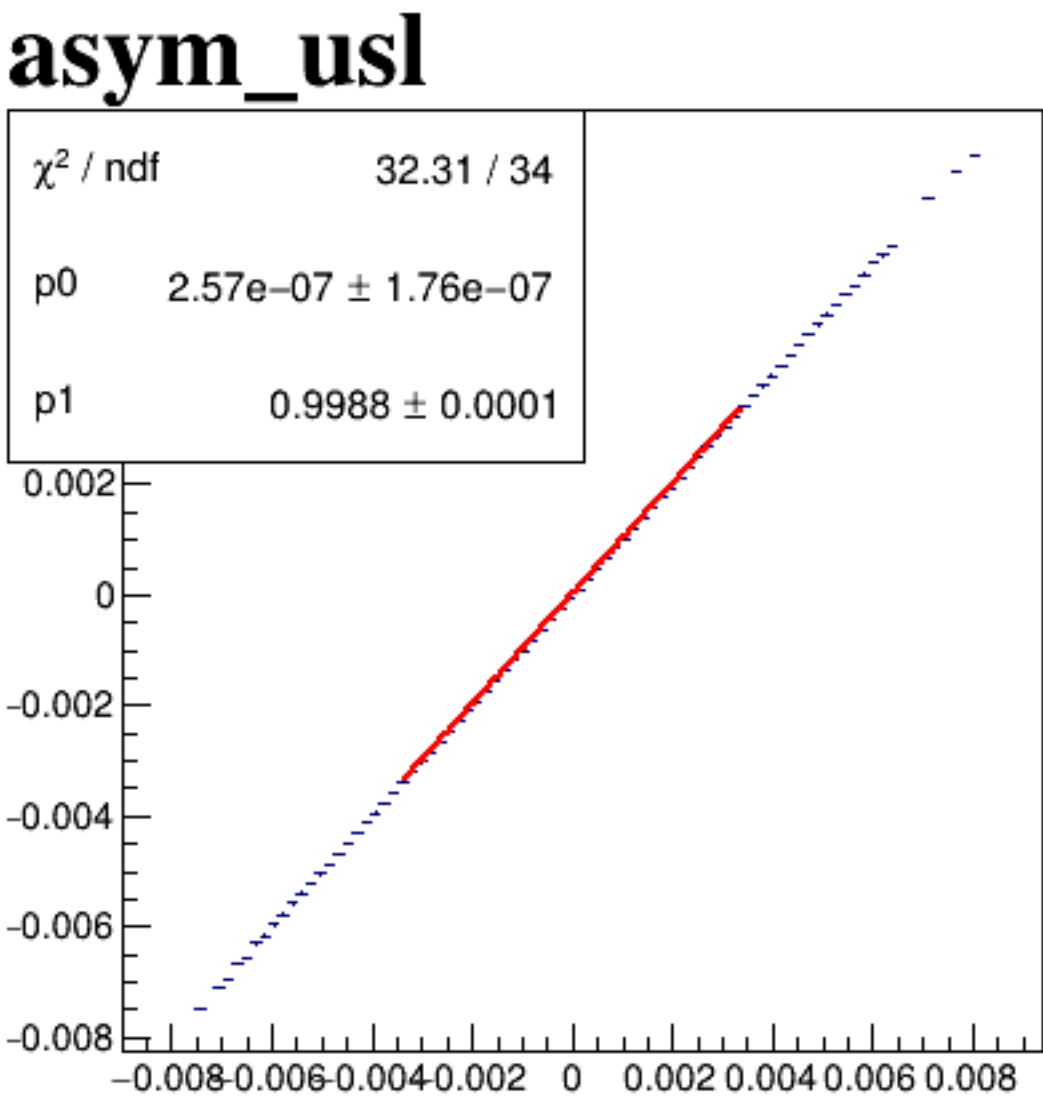
asym_dsl



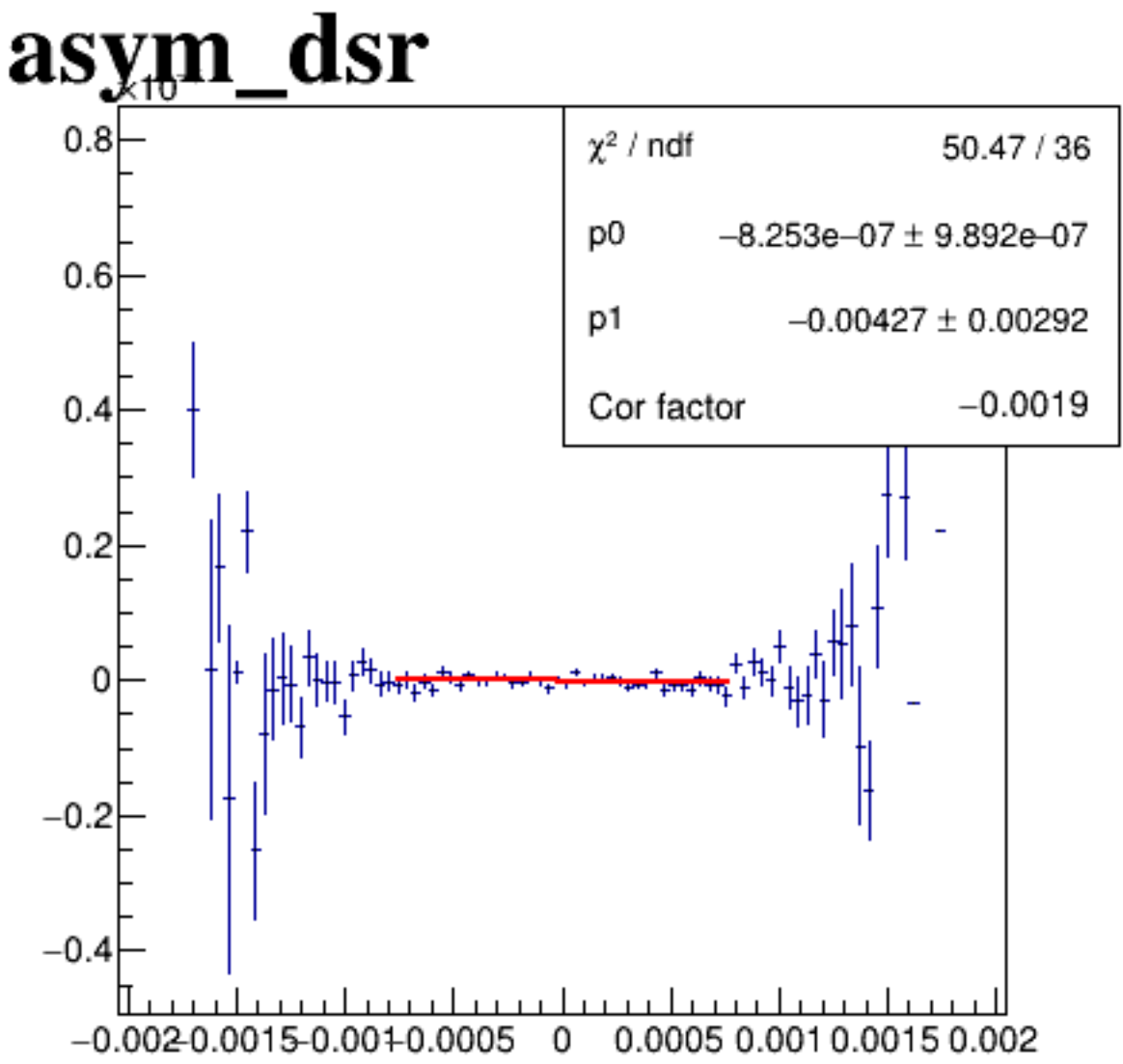
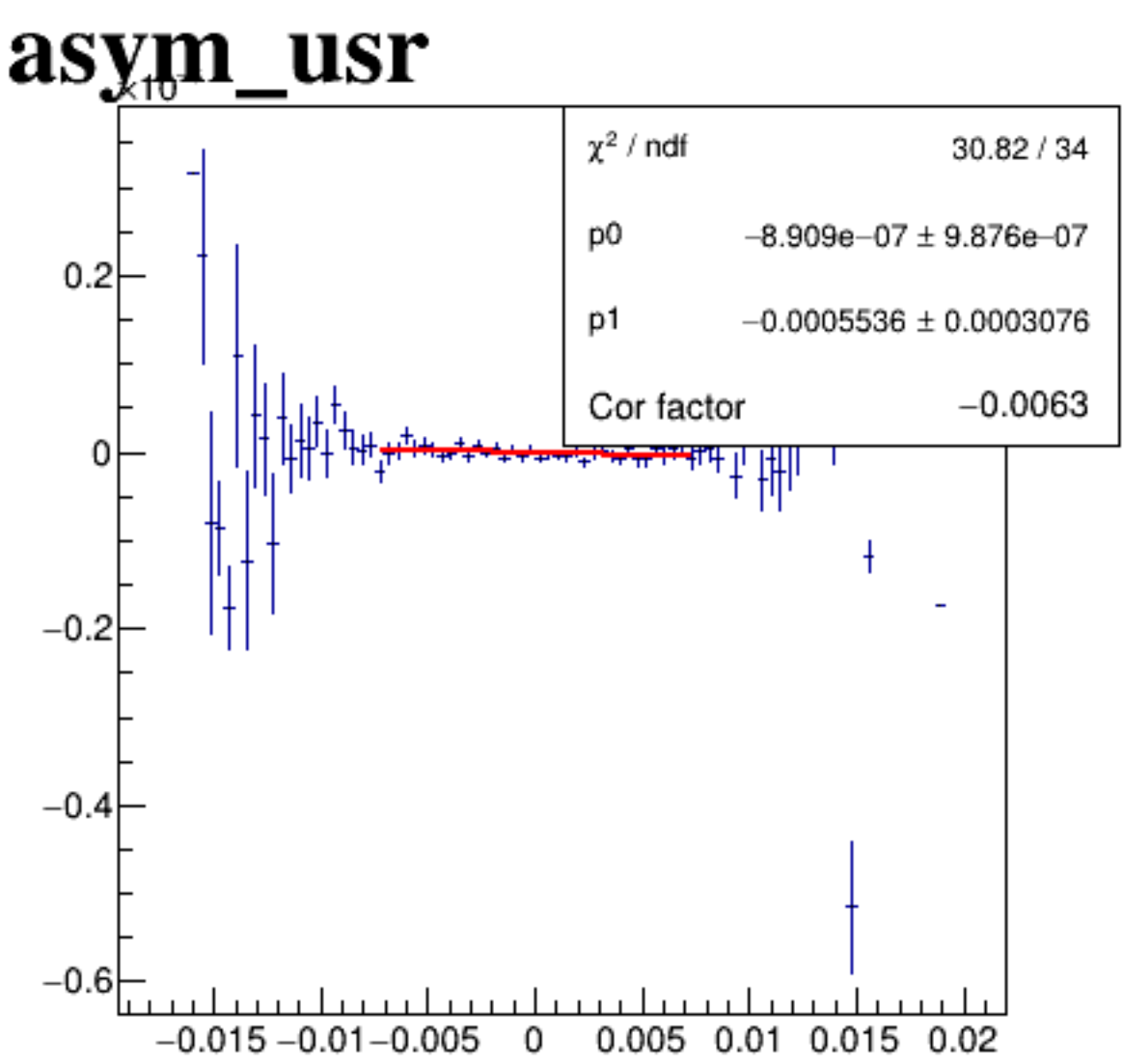
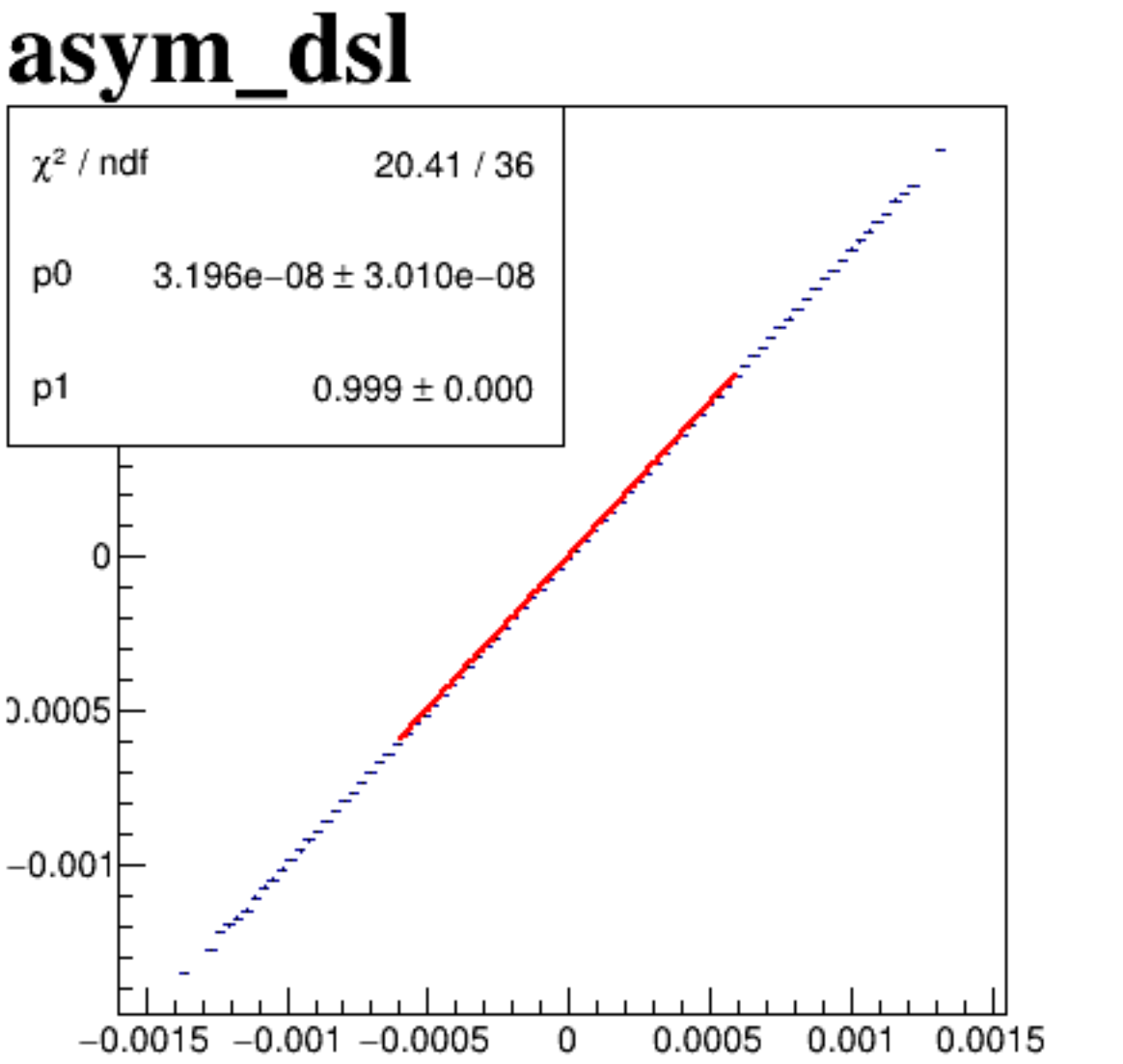
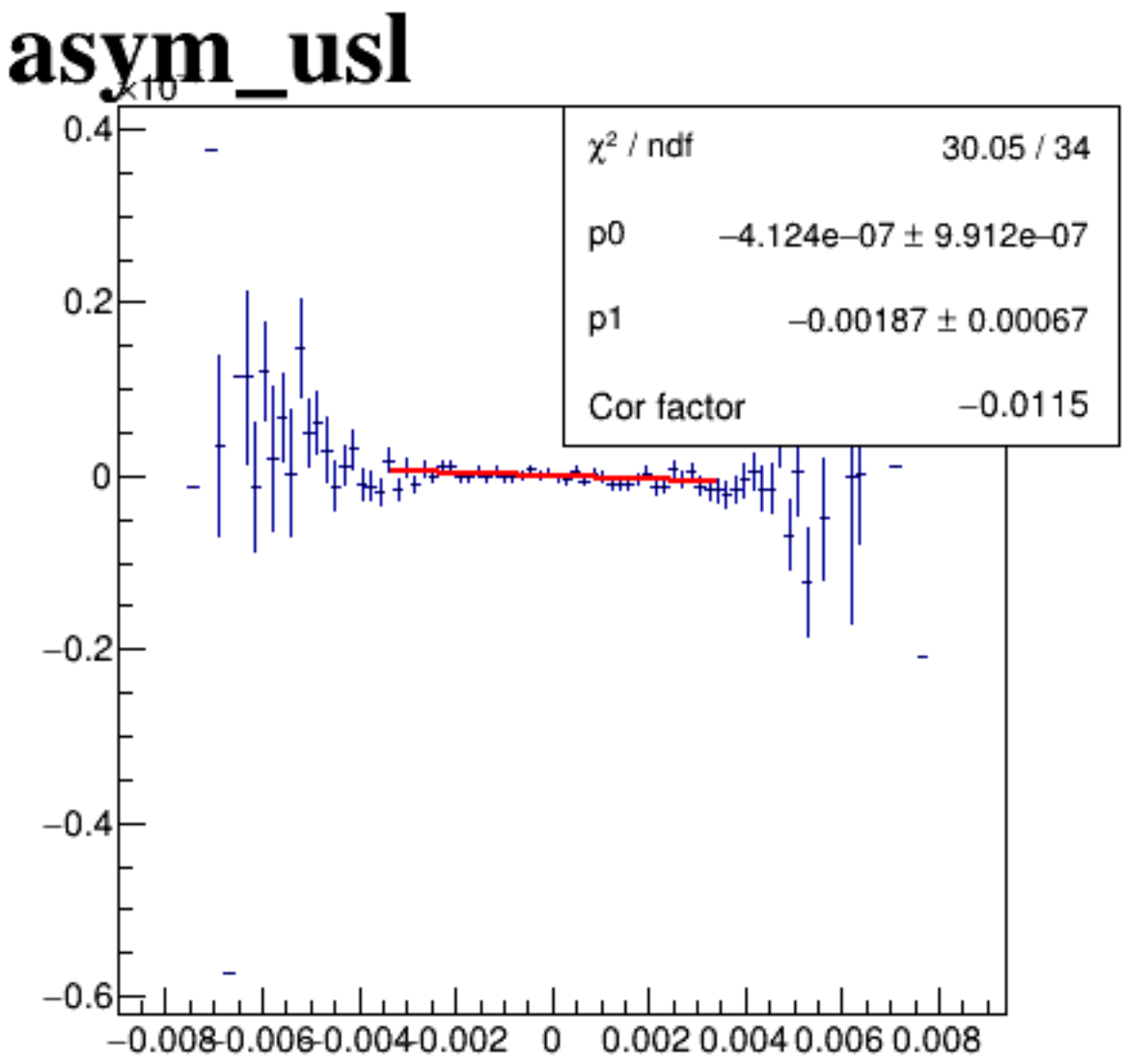
asym_usr

**asym_dsr**

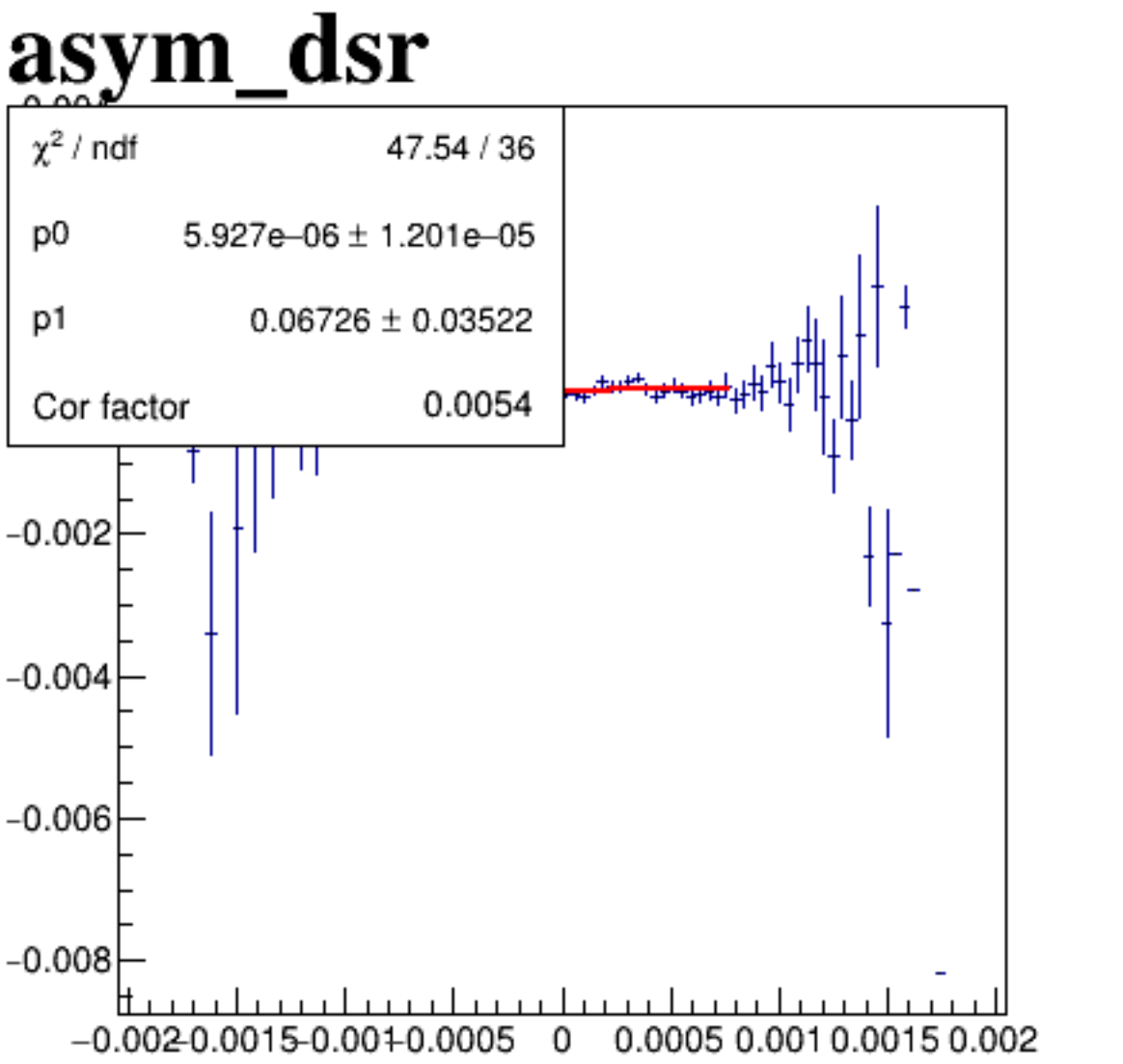
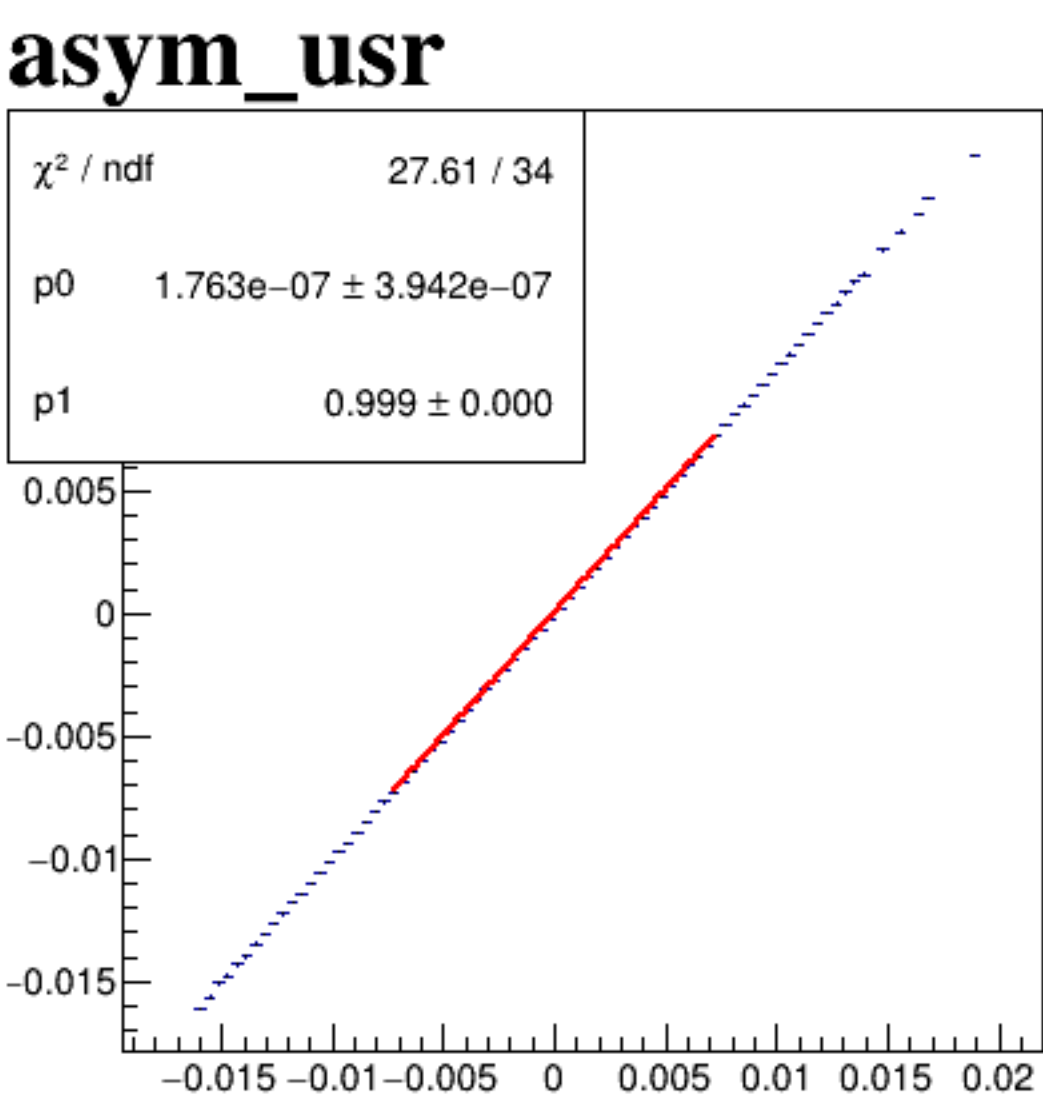
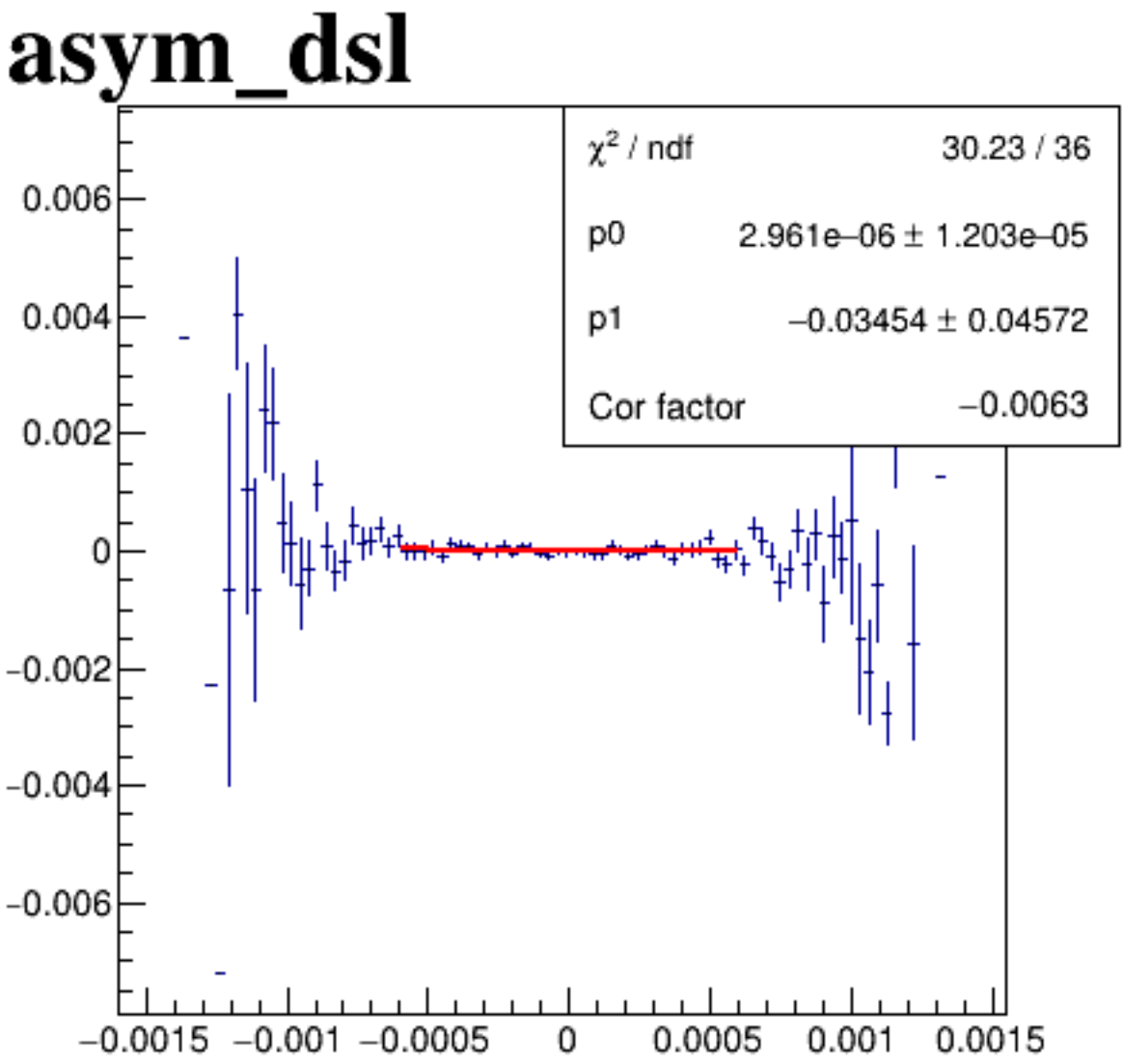
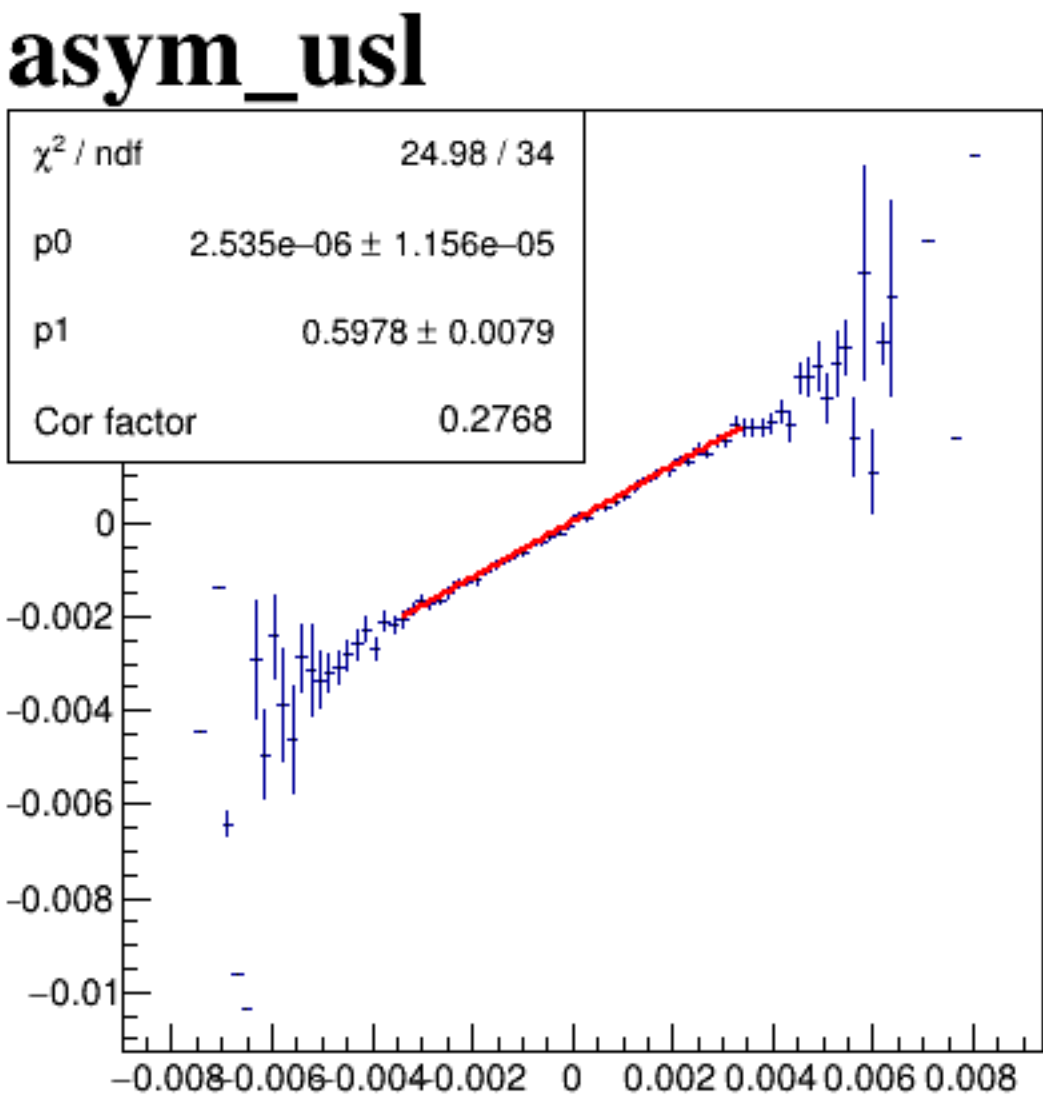
asym_usl



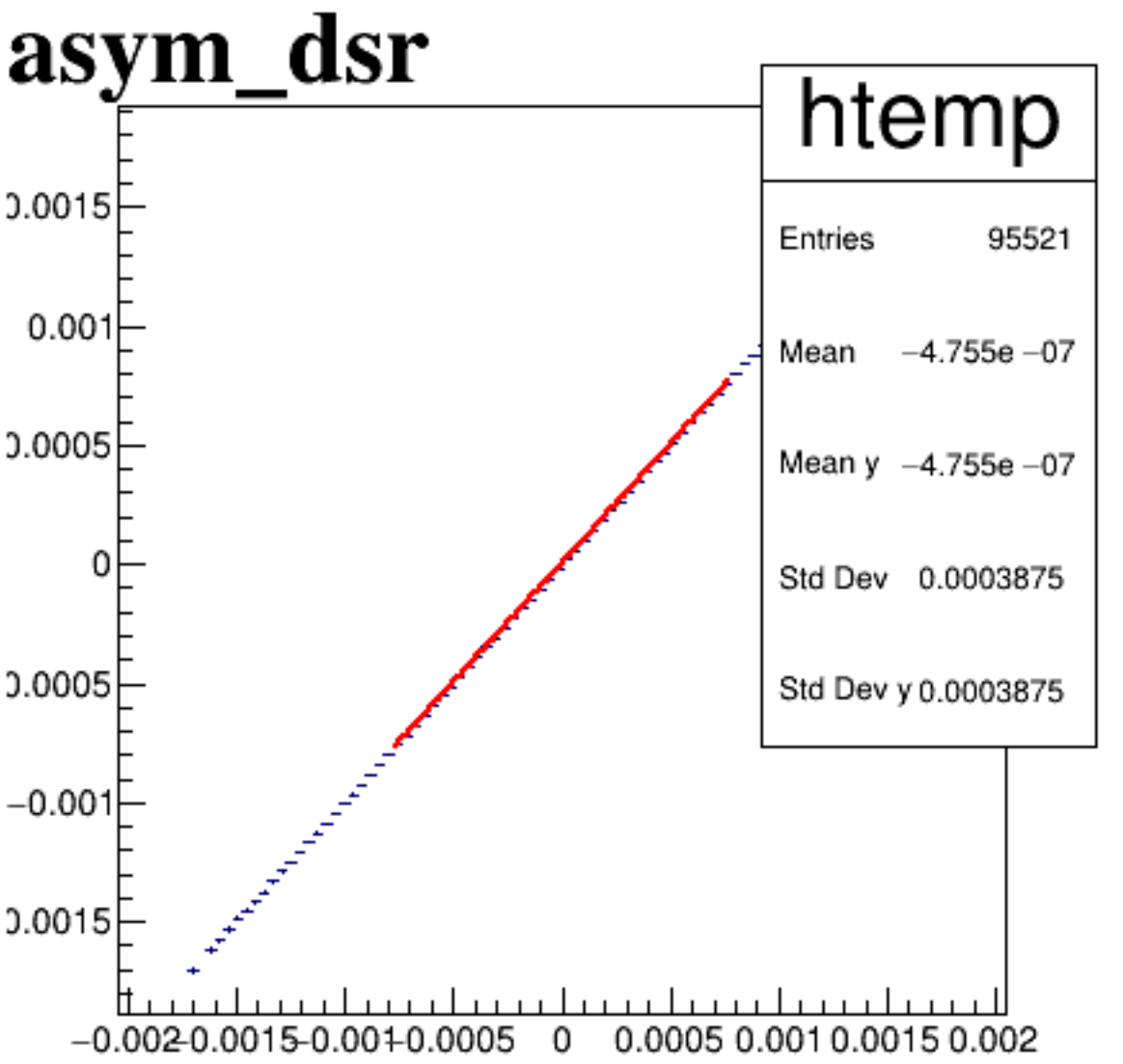
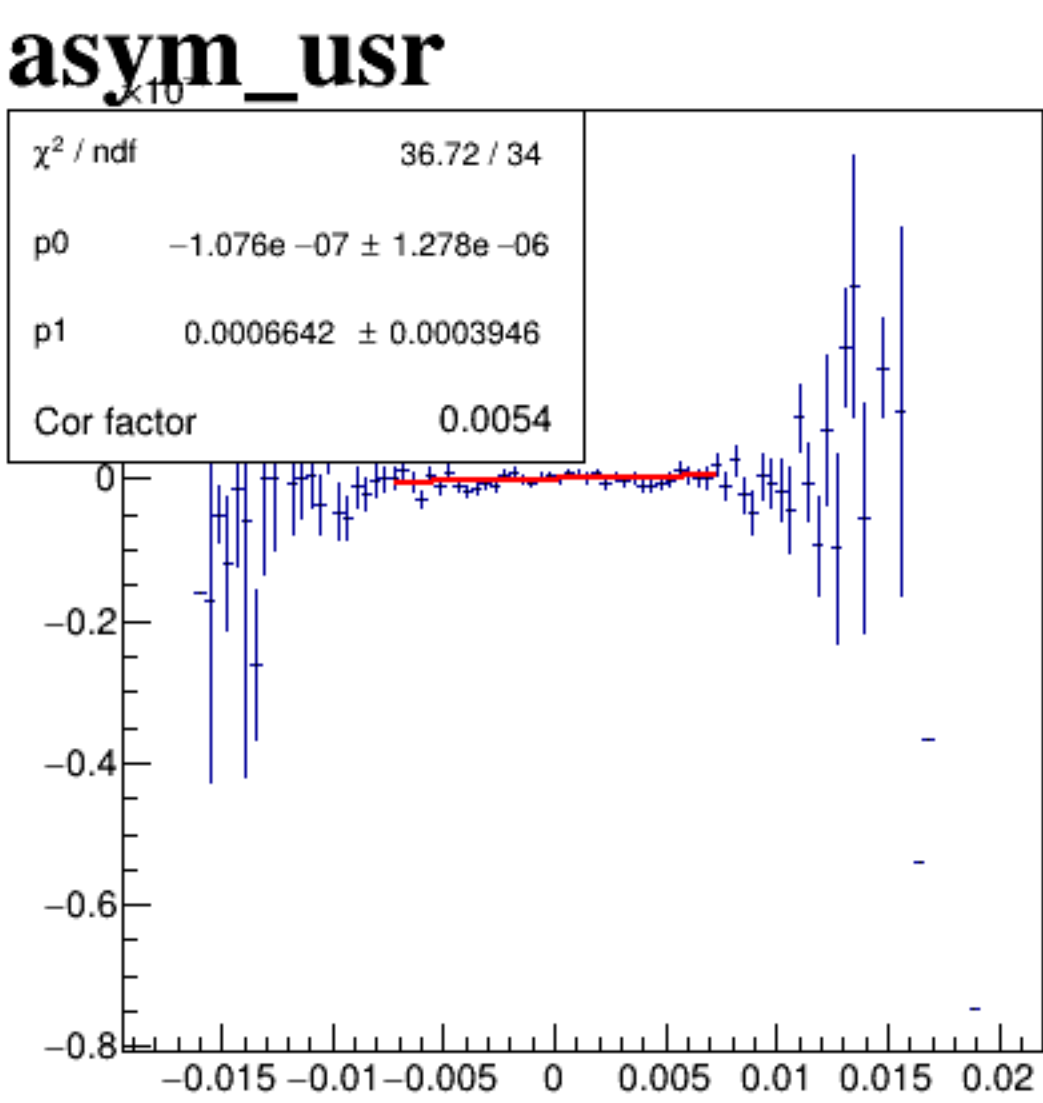
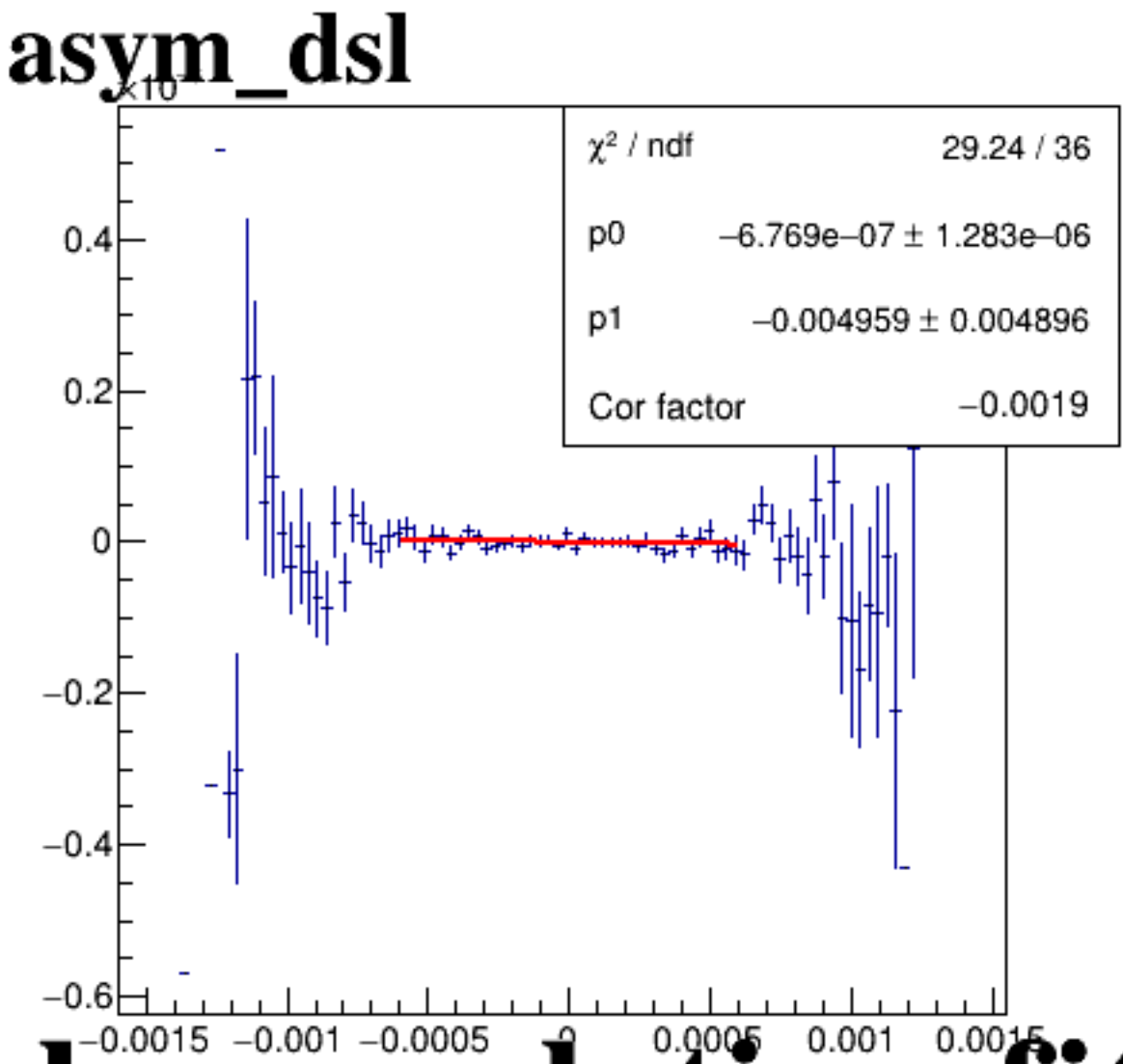
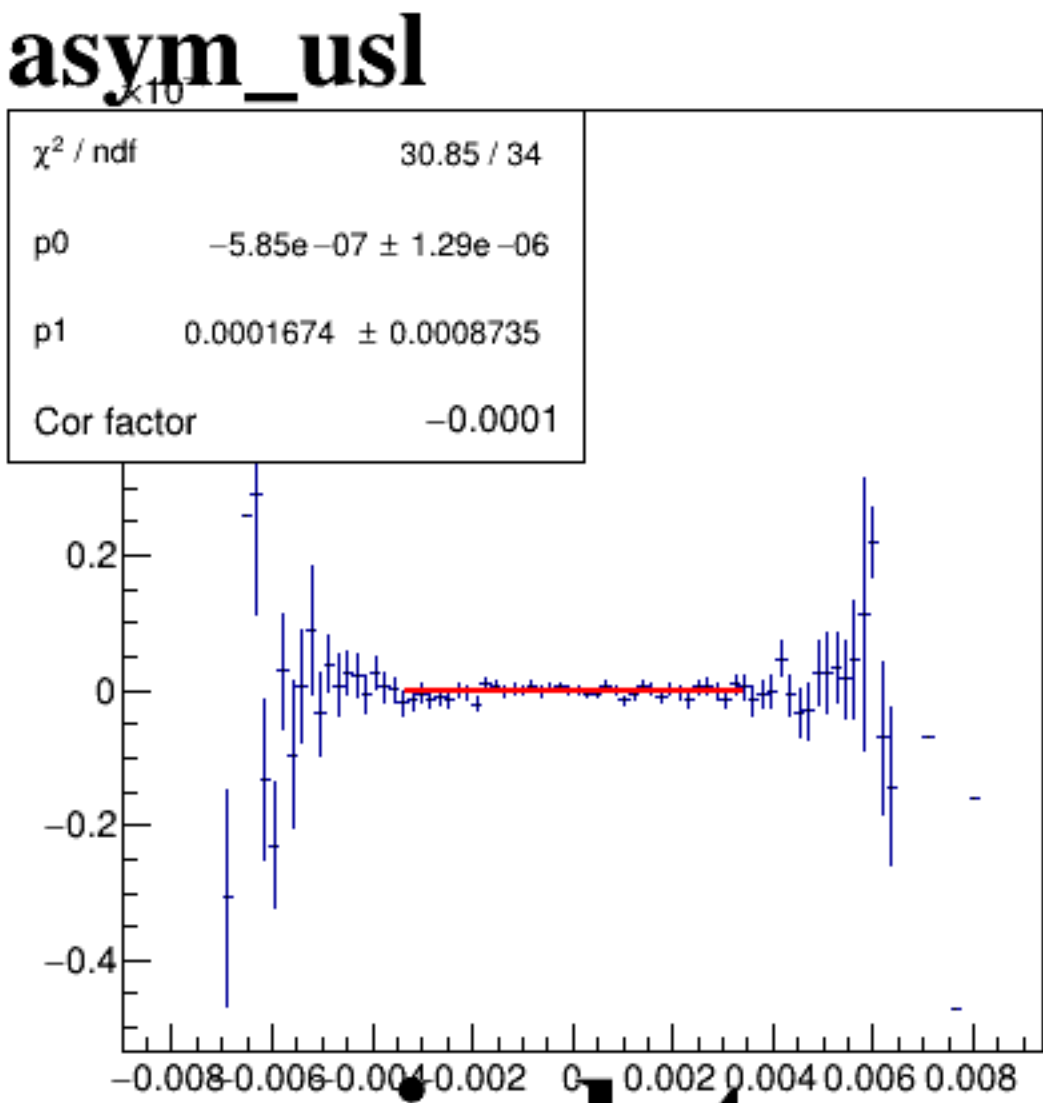
asym_dsl



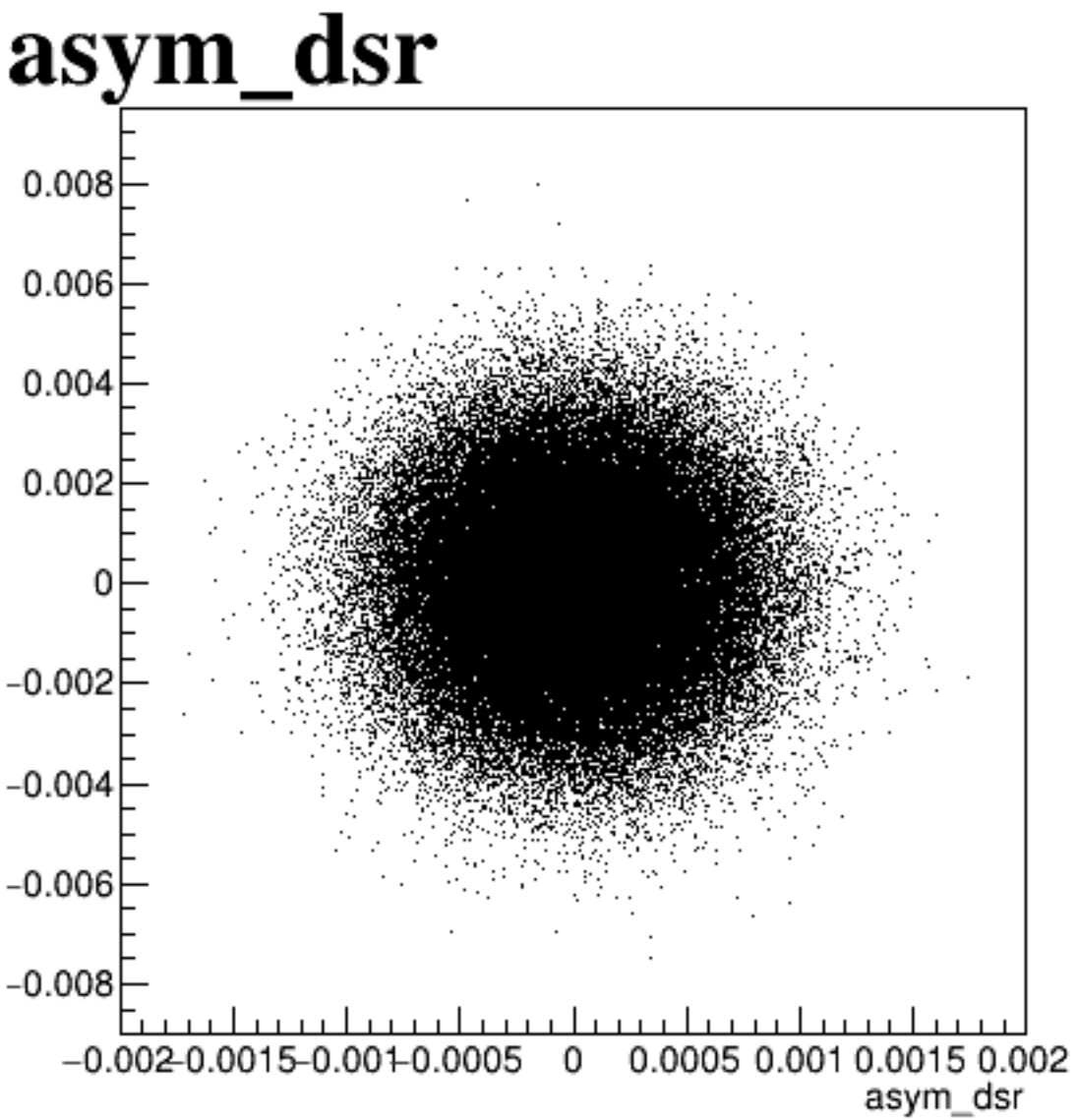
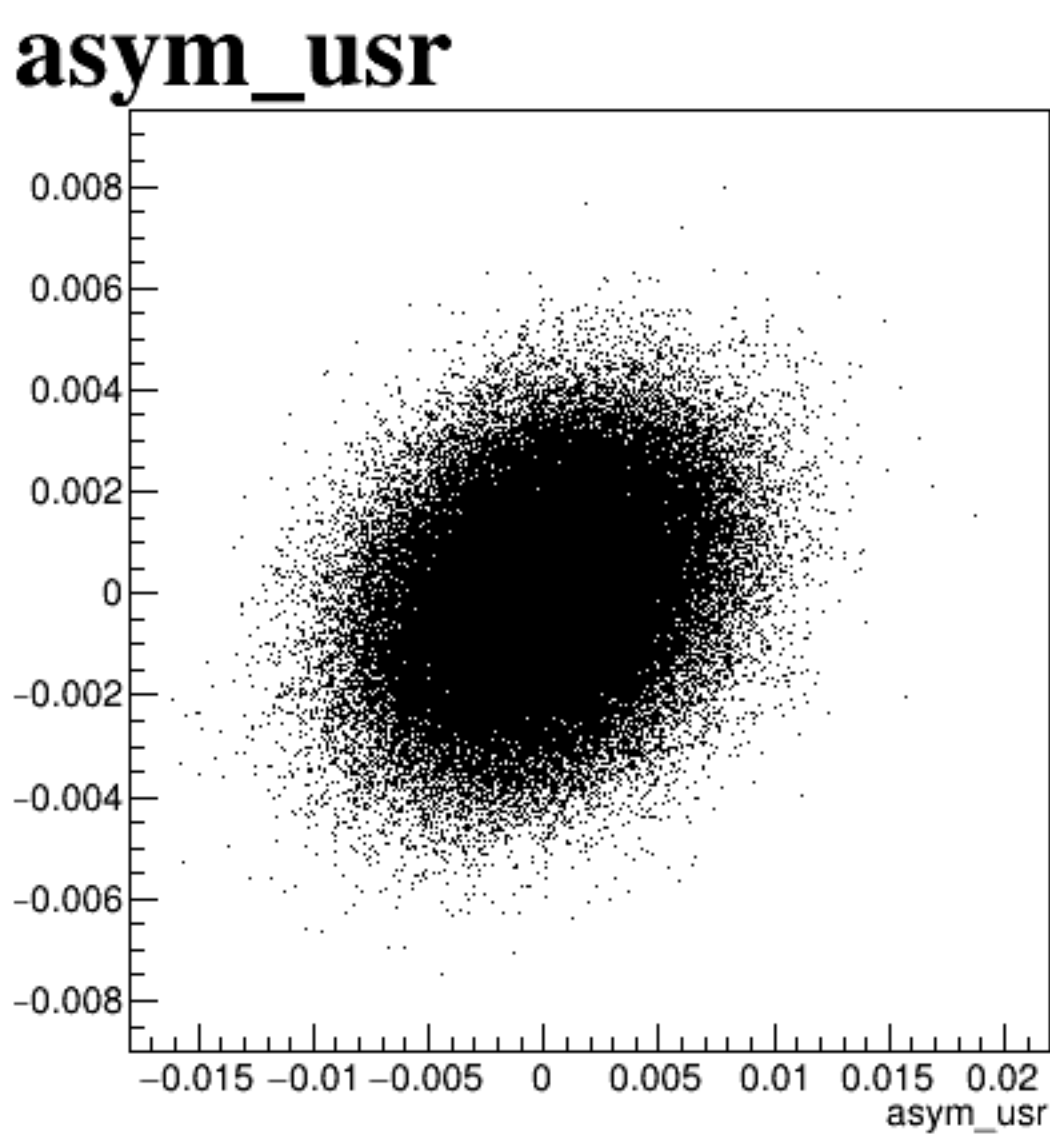
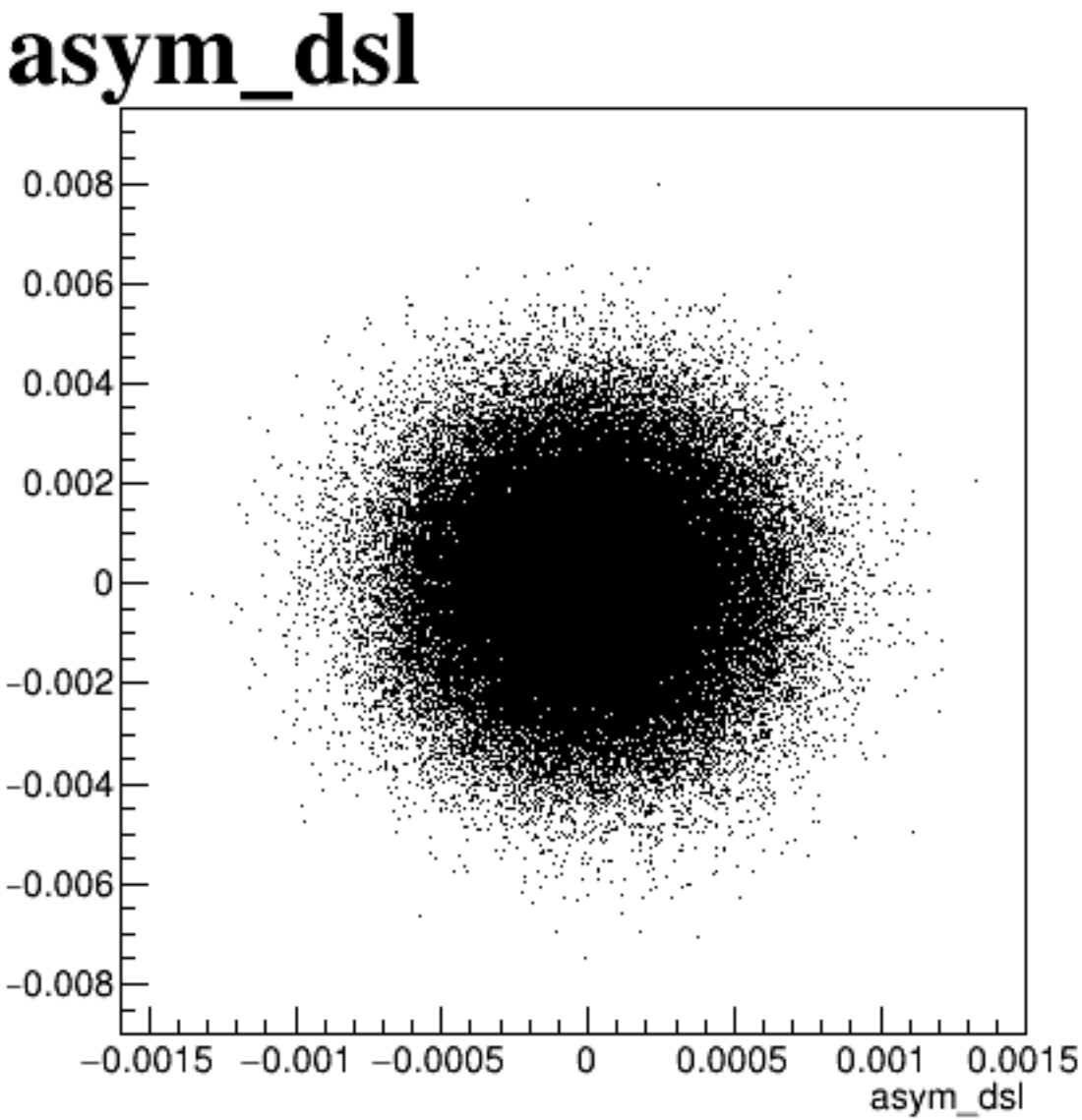
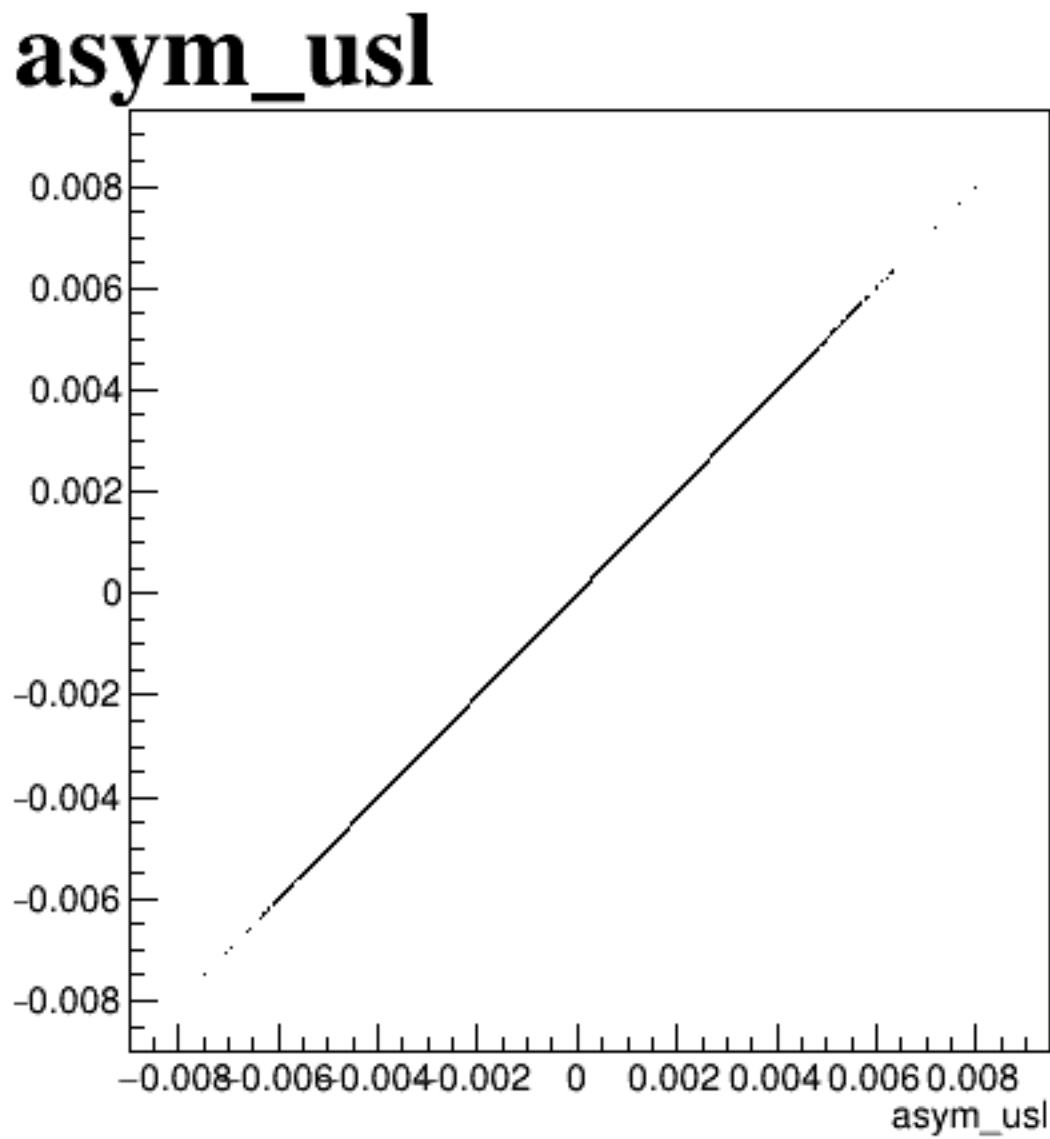
asym_usr



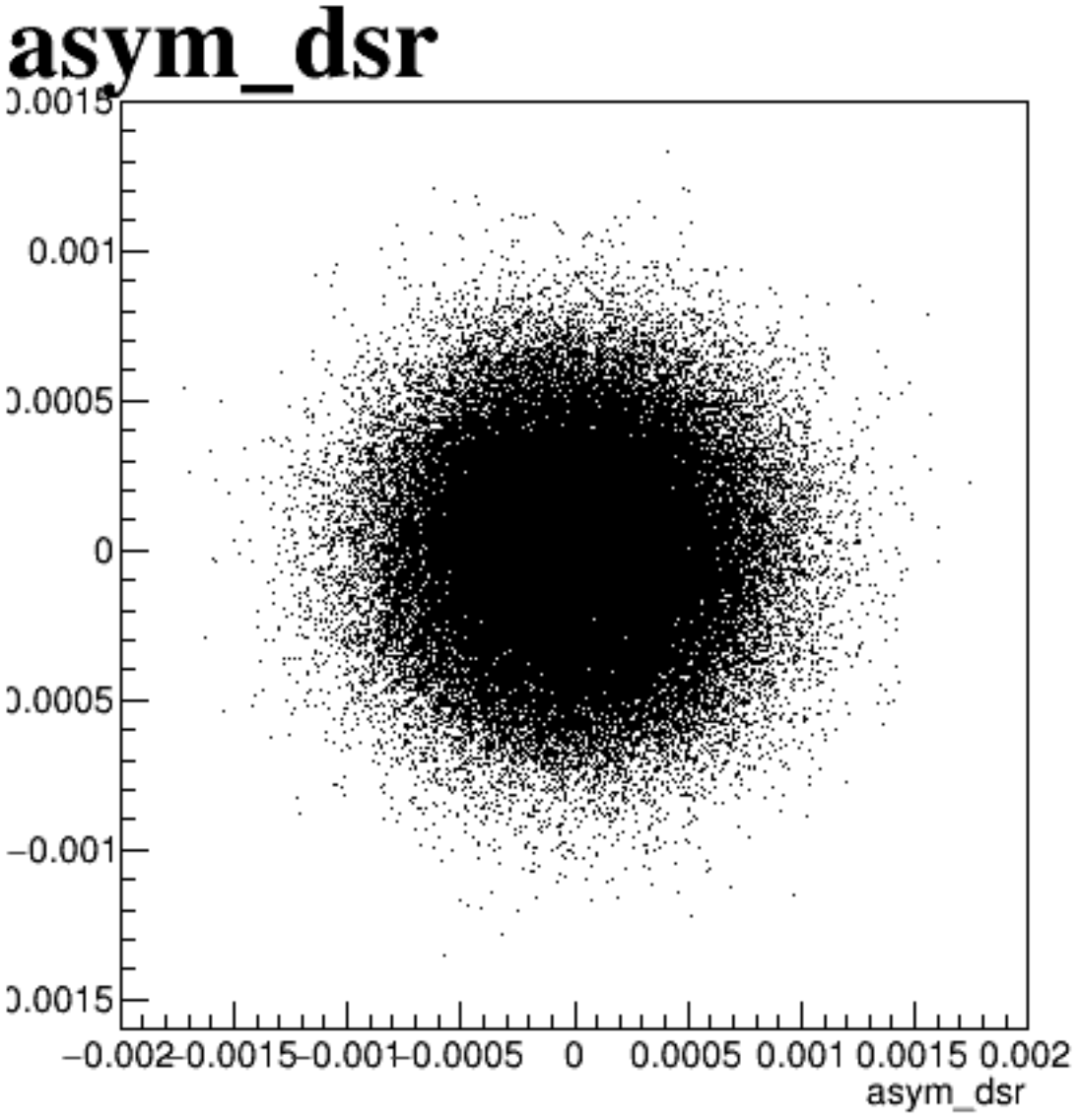
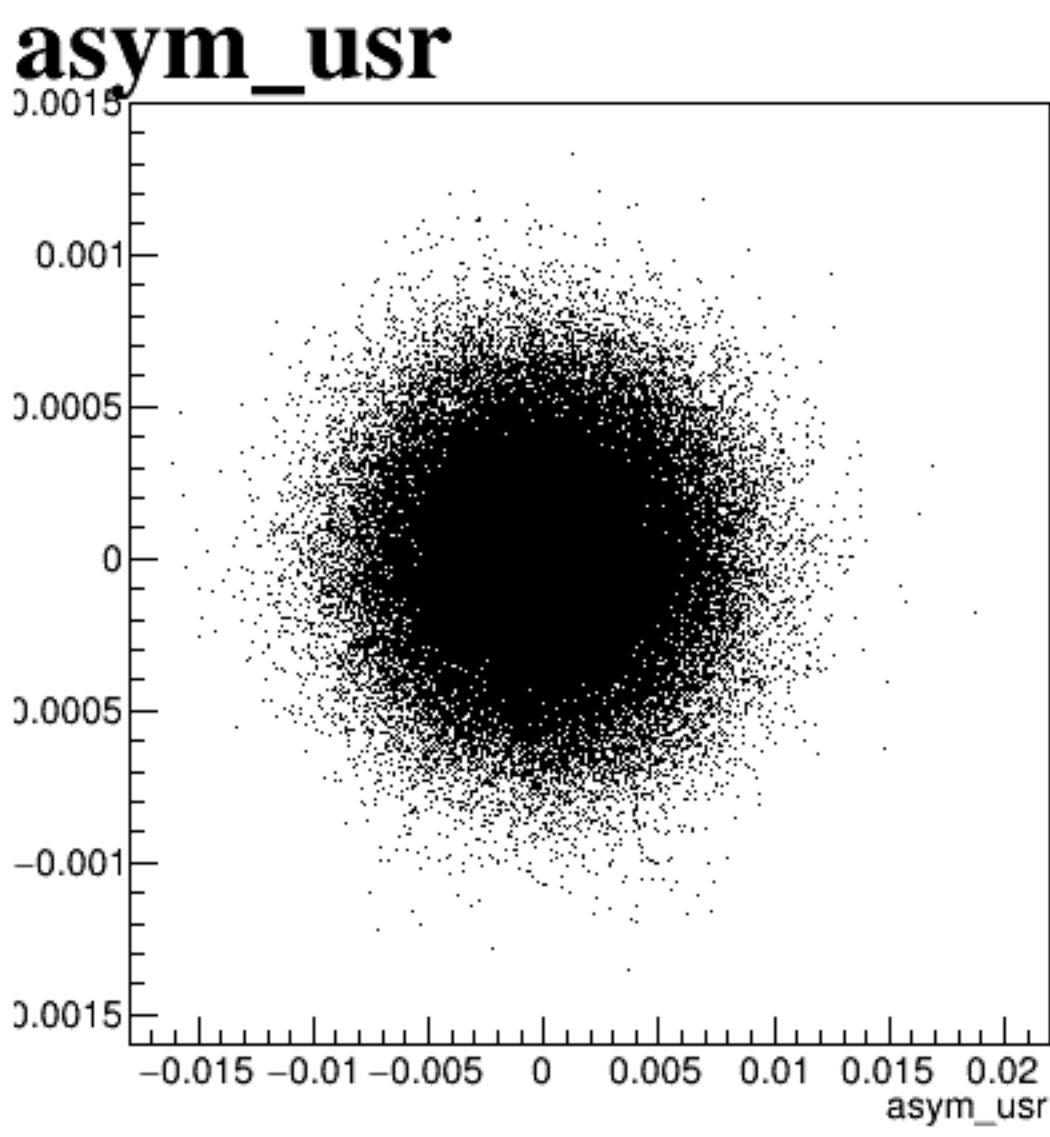
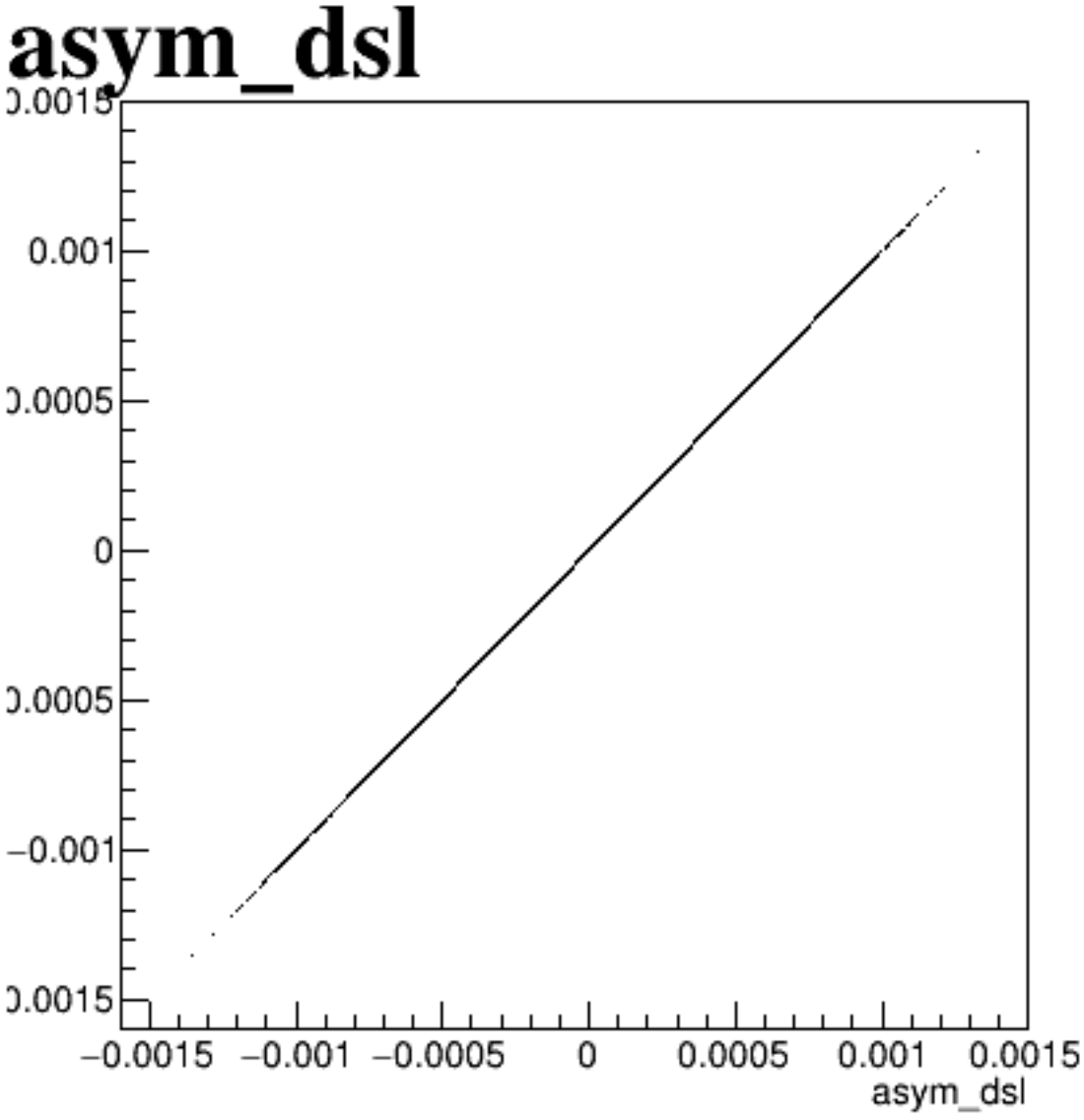
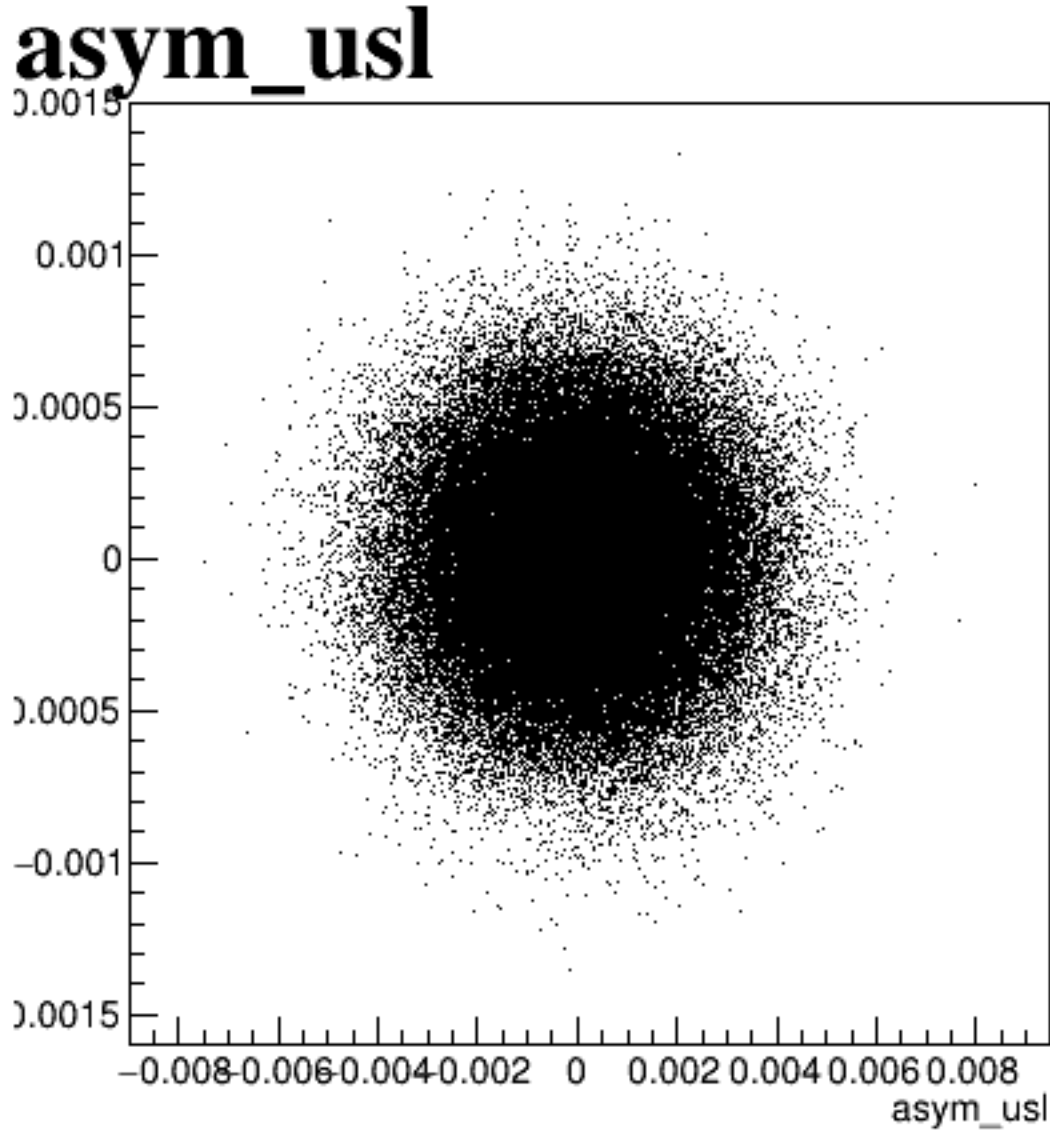
asym_dsr



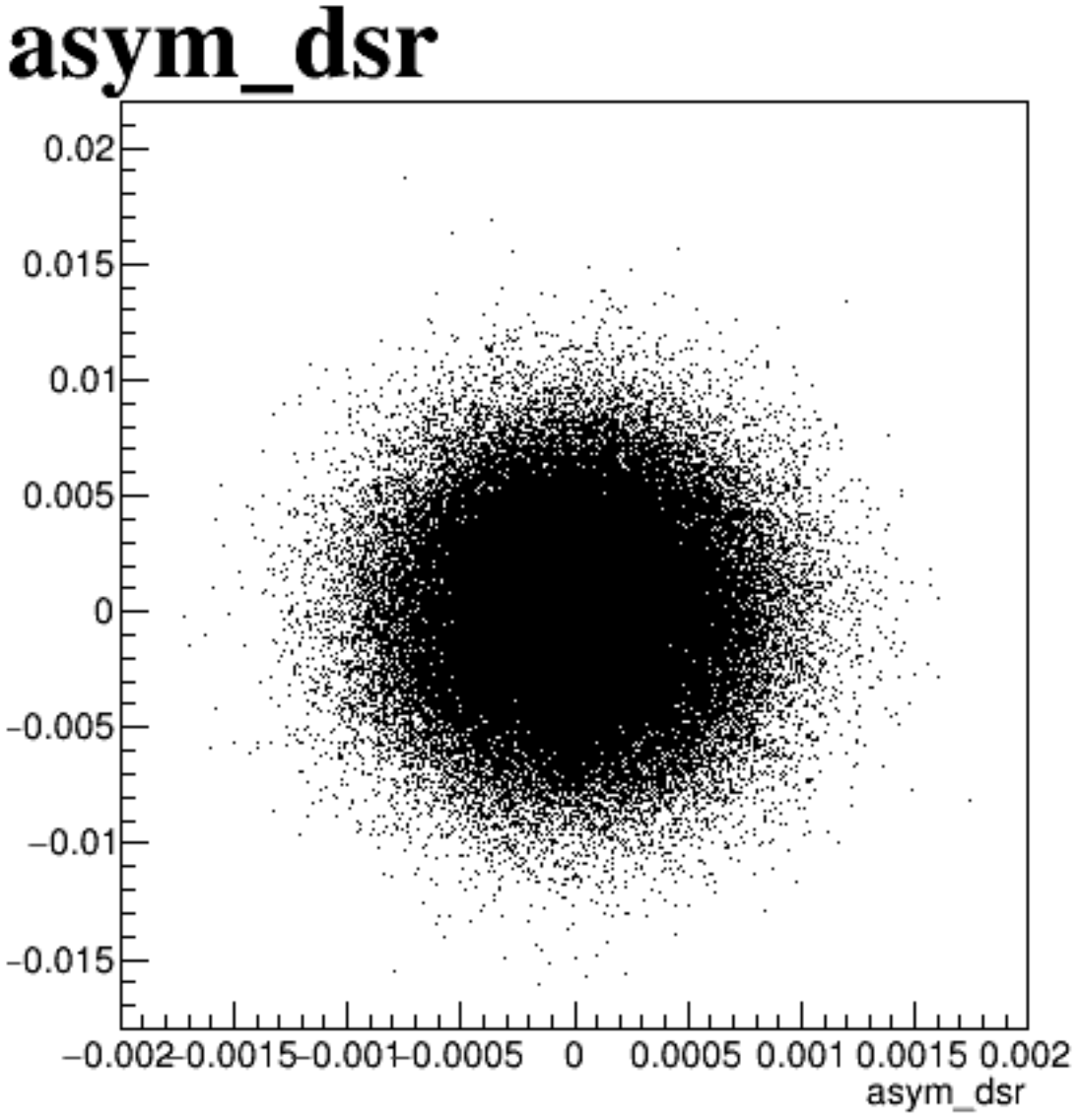
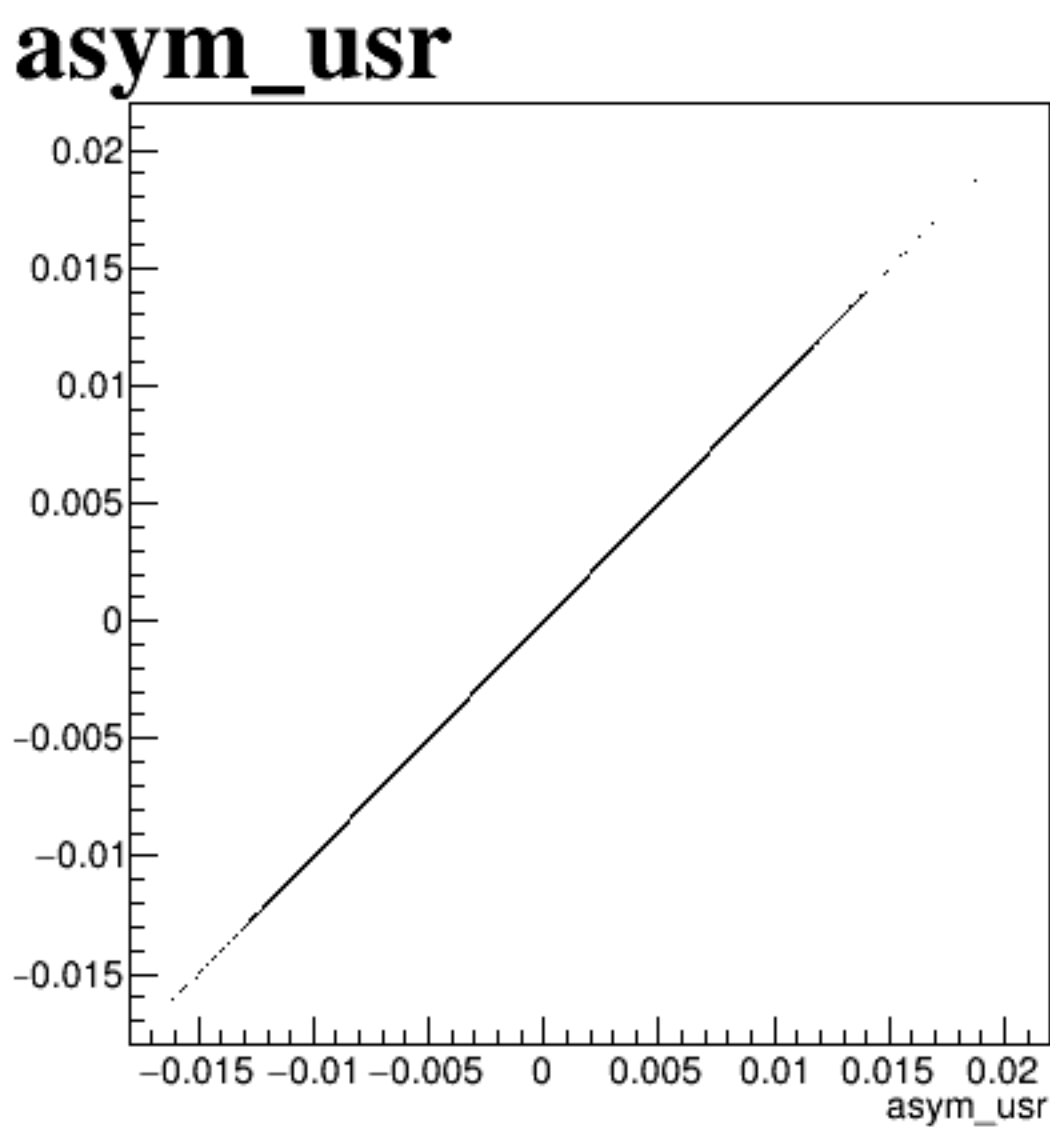
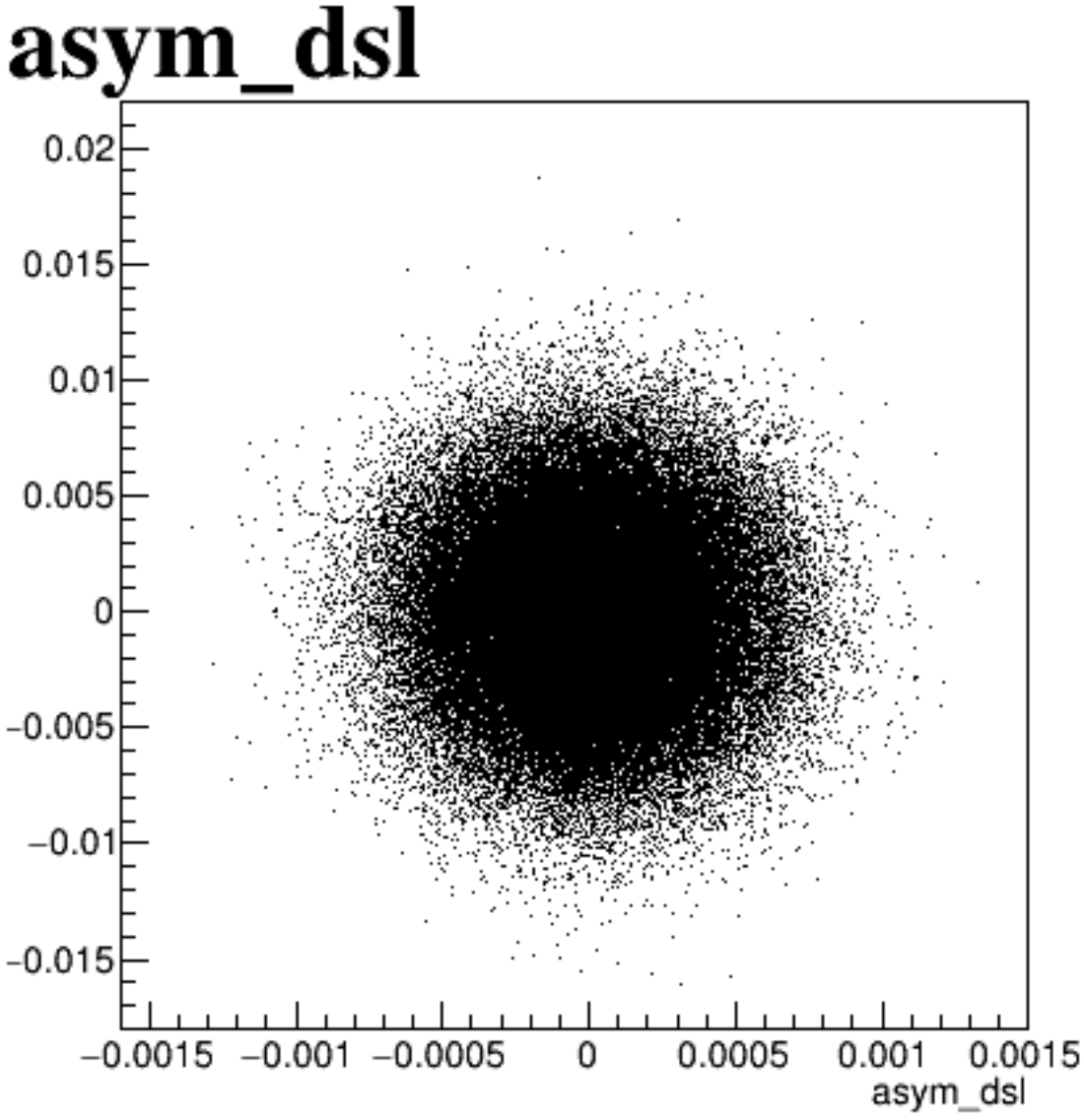
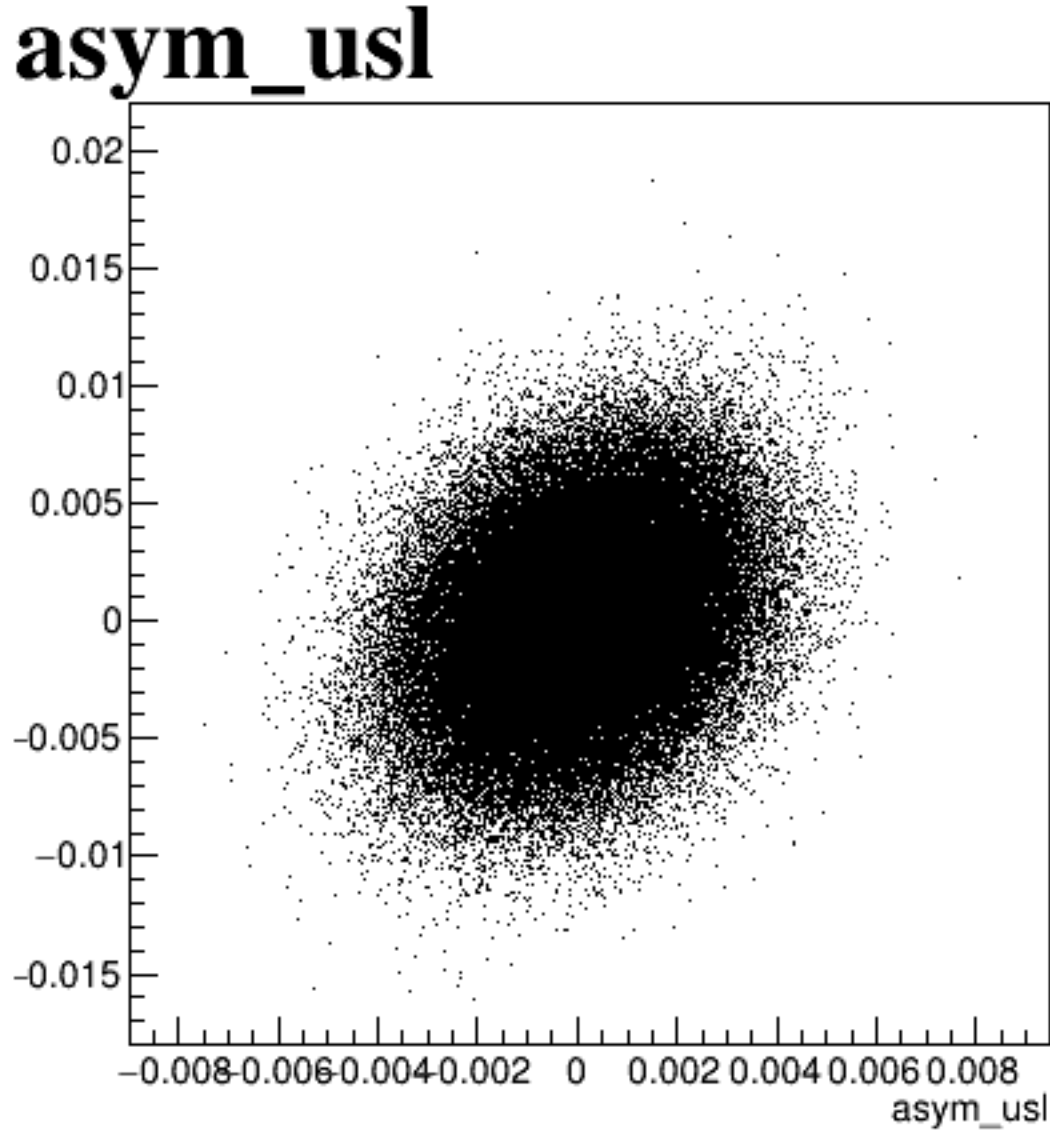
asym_usl



asym_dsl



asym_usr



asym_dsr

