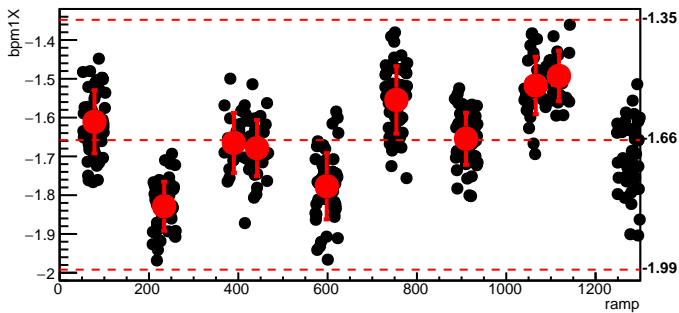
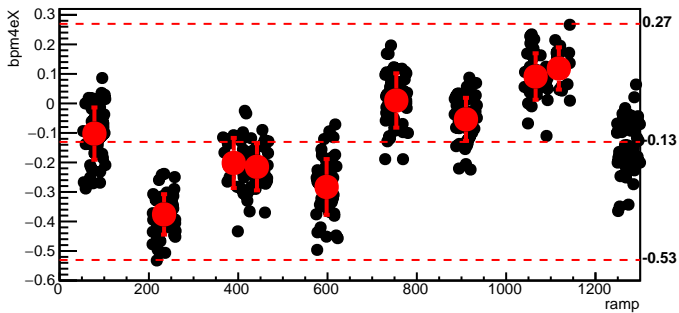


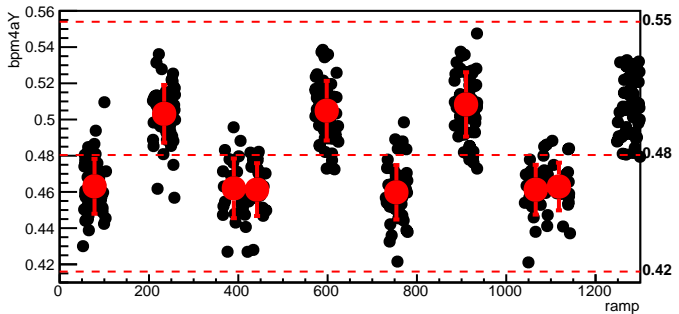
bmod\_trim1,cyc=10060



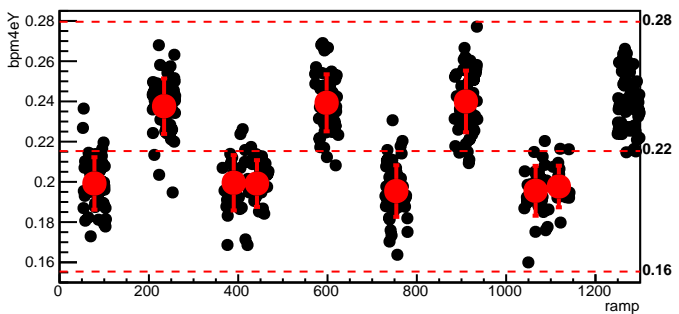
bmod\_trim1,cyc=10060



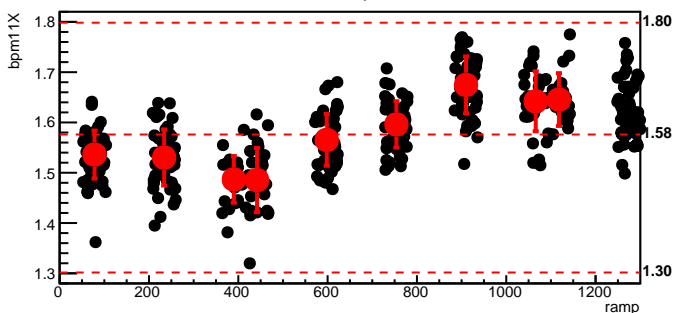
bmod\_trim1,cyc=10060



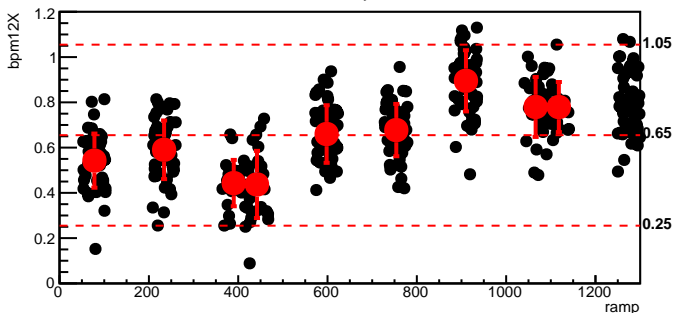
bmod\_trim1,cyc=10060



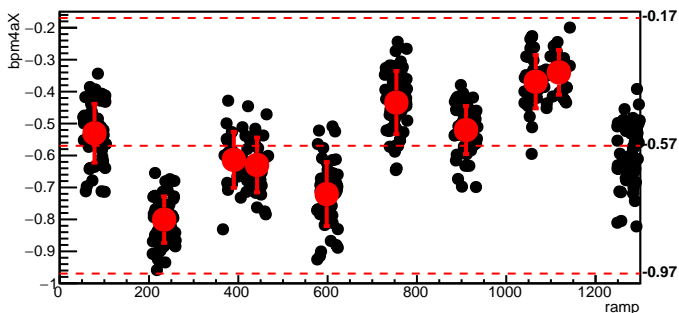
bmod\_trim1,cyc=10060



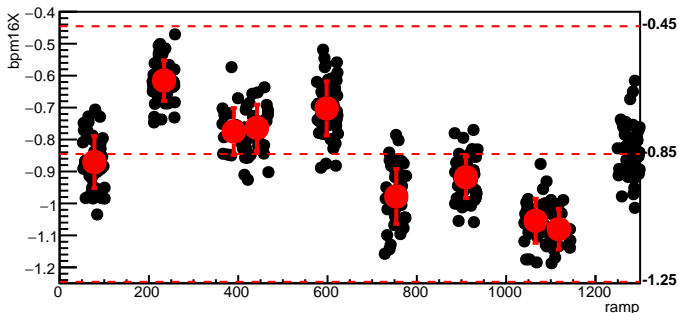
bmod\_trim1,cyc=10060



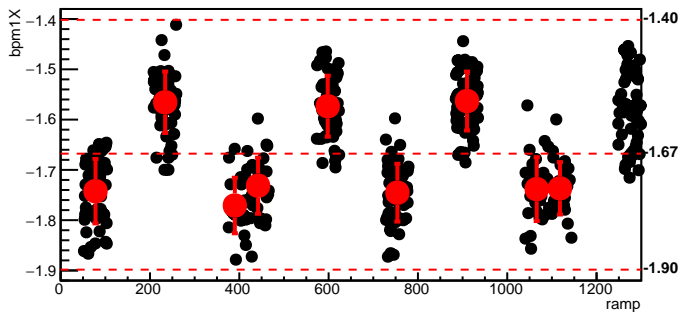
bmod\_trim1,cyc=10060



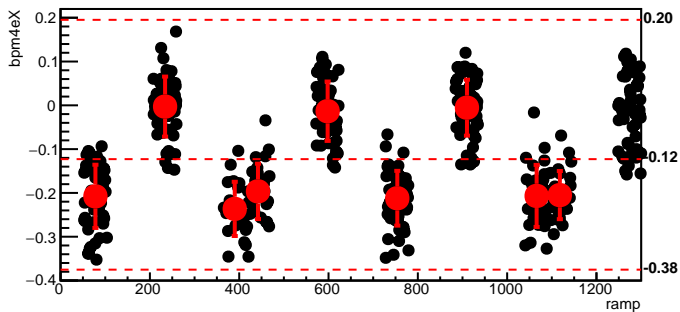
bmod\_trim1,cyc=10060



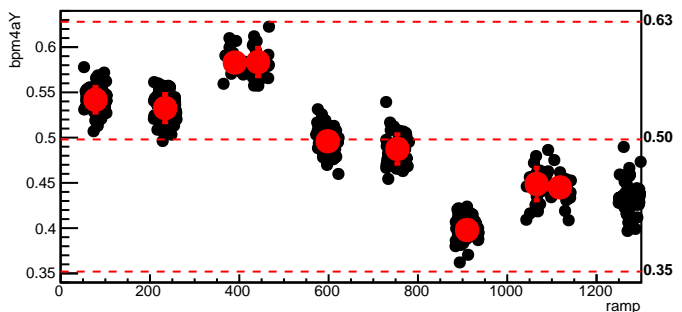
bmod\_trim2,cyc=10060



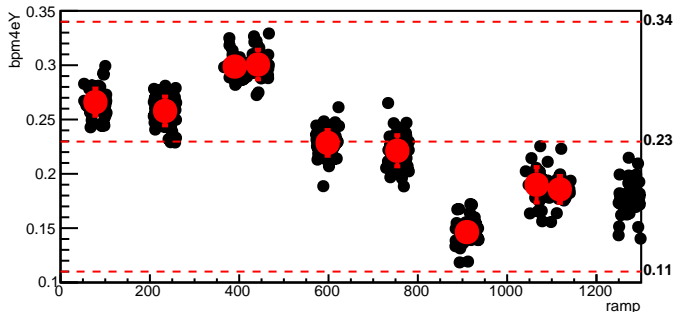
bmod\_trim2,cyc=10060



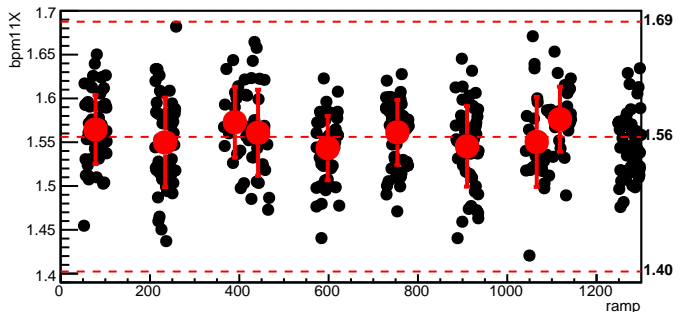
bmod\_trim2,cyc=10060



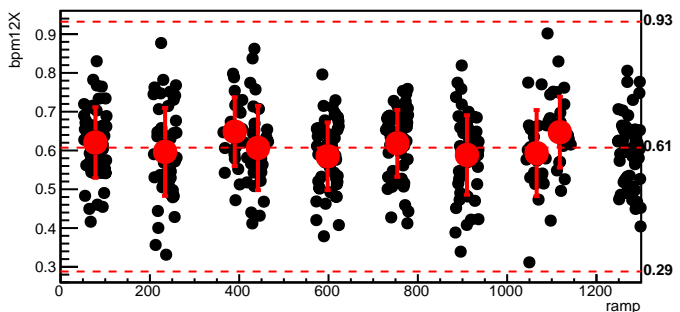
bmod\_trim2,cyc=10060



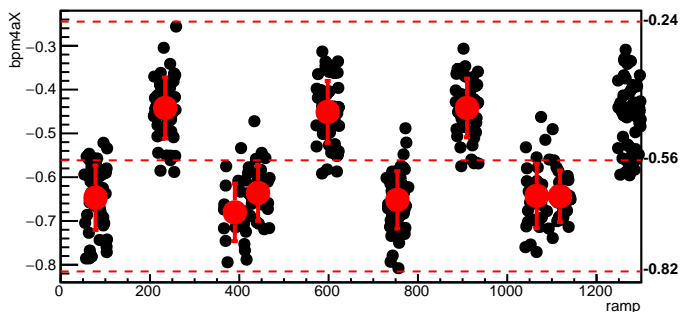
bmod\_trim2,cyc=10060



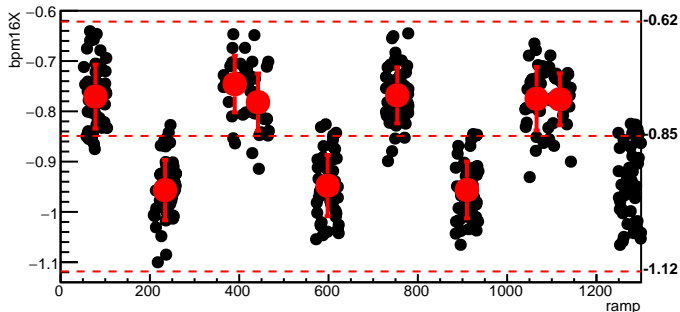
bmod\_trim2,cyc=10060



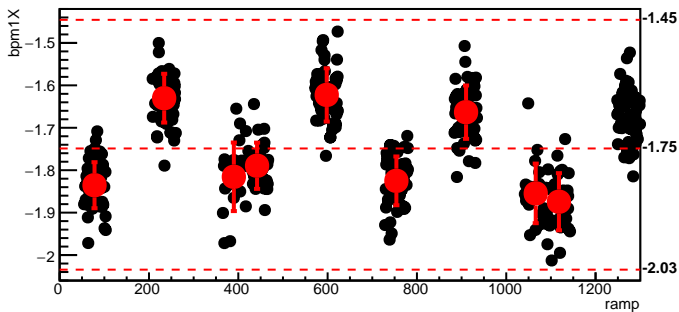
bmod\_trim2,cyc=10060



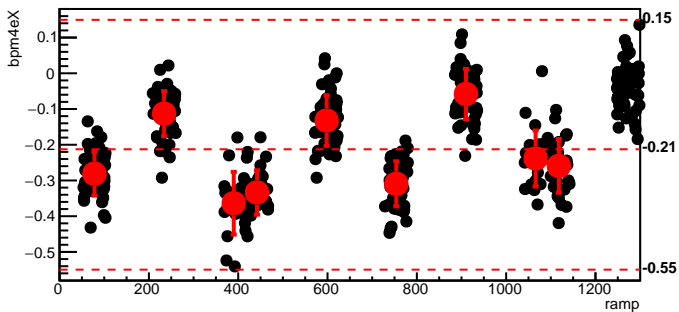
bmod\_trim2,cyc=10060



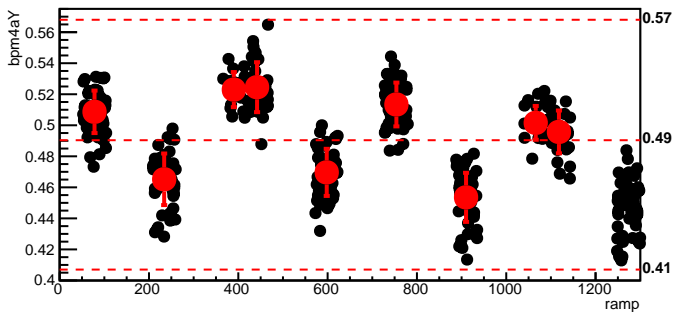
bmod\_trim3,cyc=10060



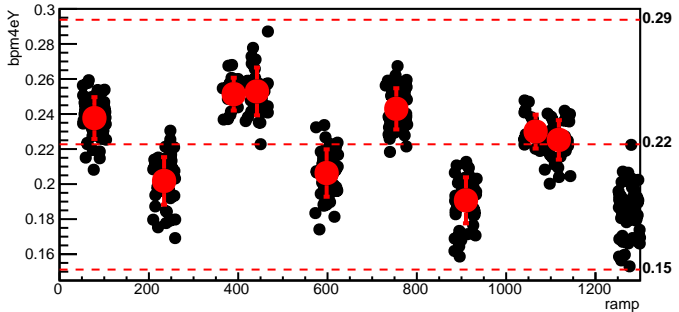
bmod\_trim3,cyc=10060



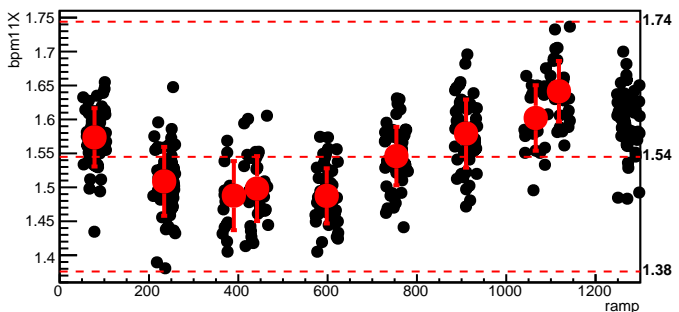
bmod\_trim3,cyc=10060



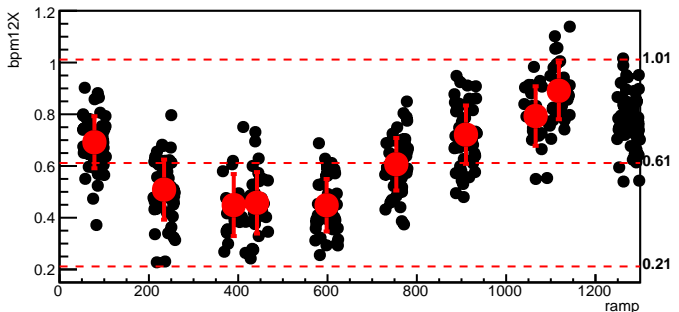
bmod\_trim3,cyc=10060



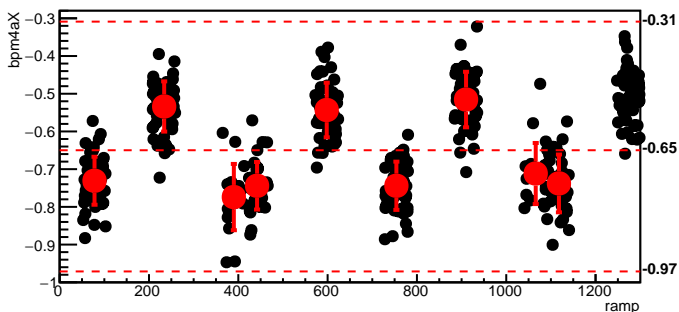
bmod\_trim3,cyc=10060



bmod\_trim3,cyc=10060



bmod\_trim3,cyc=10060



bmod\_trim3,cyc=10060

